

ESD451 采用 0201 封装的单通道 $\pm 30\text{kV}$ 双向 ESD 二极管

1 特性

- IEC 61000-4-2 4 级 ESD 保护：
 - $\pm 30\text{kV}$ 接触放电
 - $\pm 30\text{kV}$ 空气间隙放电
- IEC 61000-4-5 浪涌保护：
 - 6.2A (8 μs /20 μs)
- IO 电容：
 - 0.5pF (典型值)
- 直流击穿电压： $\pm 8\text{V}$ (典型值)
- 超低漏电流：50nA (最大值)
- 超低的 ESD 钳位电压
 - 16A TLP 时为 10.4V
 - R_{DYN} ：0.19 Ω
- 低插入损耗：3.5GHz (-3dB 带宽)
- 支持速率高达 7Gbps 的高速接口
- 工业温度范围：-55 $^{\circ}\text{C}$ 至 +150 $^{\circ}\text{C}$
- 节省空间的业界通用 0201 封装 (0.6mm \times 0.3mm \times 0.3mm)

2 应用

- 终端设备：
 - 扫地机器人
 - 可穿戴设备
 - 智能扬声器
 - 便携式电子产品
 - 小型电器
 - 零售自动化和支付
 - 便携式计算机和台式机
 - 电视和监视器
 - 扩展坞
- 接口：
 - USB 3.0
 - HDMI™ 1.4 和 2.0
 - DisplayPort™
 - SIM 卡

3 说明

ESD451 是一款双向 ESD 保护二极管，用于保护数据线路和其他 I/O 端口。ESD451 的额定 ESD 冲击消散值高达 $\pm 30\text{kV}$ ，符合 IEC 61000-4-2 国际标准（高于 4 级）。

该器件具有 0.5pF (典型值) IO 电容，可为 USB 3.0 等协议提供高速接口保护。超低的动态电阻 (0.19 Ω) 和钳位电压 (16A TLP 时为 10.4V) 可针对瞬态事件提供系统级保护。

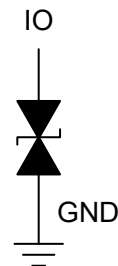
该器件采用微型封装并具有 $\pm 30\text{kV}$ ESD 等级和 6.2 A 浪涌，可提供强大的瞬态保护，用于保护便携式电子产品和其他空间受限类应用（如可穿戴设备）中的 5.5V 电源轨和数据线。

ESD451 采用业界通用的 0201 (DPL) 封装。

封装信息

器件型号	封装 ⁽¹⁾	封装尺寸 ⁽²⁾
ESD451	DPL (X2SON, 2)	0.6 mm \times 0.3 mm

- 如需了解所有可用封装，请参阅数据表末尾的可订购产品附录。
- 封装尺寸 (长 \times 宽) 为标称值，并包括引脚 (如适用)。



功能方框图



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4 Revision History

注：以前版本的页码可能与当前版本的页码不同

Changes from Revision * (April 2023) to Revision A (June 2023)	Page
• 将数据表的状态从 <i>预告信息</i> 更改为 <i>量产数据</i>	1

5 Pin Configuration and Functions

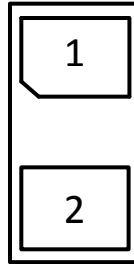


图 5-1. DPL Package, 2-Pin X2SON (Top View)

表 5-1. Pin Functions

PIN		TYPE ⁽¹⁾	DESCRIPTION
NAME	NO.		
IO	1	I/O	ESD Protected Channel. If used as ESD IO, connect pin 2 to ground
IO	2	I/O	ESD Protected Channel. If used as ESD IO, connect pin 1 to ground

(1) I = input, O = output

6 Specifications

6.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted) ⁽¹⁾

		MIN	MAX	UNIT
Peak Pulse ^{(2) (3)}	IEC 61000-4-5 power ($t_p - 8/20 \mu s$)		57	W
	IEC 61000-4-5 Current ($t_p - 8/20 \mu s$)		6	A
T_A	Ambient Operating Temperature	-55	150	°C
T_{stg}	Storage Temperature	-65	155	°C

- (1) Operation outside the *Absolute Maximum Ratings* may cause permanent device damage. Absolute maximum ratings do not imply functional operation of the device at these or any other conditions beyond those listed under *Recommended Operating Conditions*. If briefly operating outside the *Recommended Operating Conditions* but within the *Absolute Maximum Ratings*, the device may not sustain damage, but it may not be fully functional. Operating the device in this manner may affect device reliability, functionality, performance, and shorten the device lifetime.
- (2) Voltages are with respect to GND unless otherwise noted.
- (3) Measured at 25°C

6.2 ESD Ratings—JEDEC Specification

			VALUE	UNIT
$V_{(ESD)}$	Electrostatic discharge	Human body model (HBM), per ANSI/ESDA/ JEDEC JS-001 ⁽¹⁾	±2500	V
		Charged device model (CDM), per JEDEC specification JS-002 ⁽²⁾	±1000	V

- (1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
- (2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

6.3 ESD Ratings—IEC Specification

			VALUE	UNIT
$V_{(ESD)}$	Electrostatic discharge	IEC 61000-4-2 contact discharge	±30000	V
		IEC 61000-4-2 air-gap discharge	±30000	

6.4 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

			MIN	NOM	MAX	UNIT
V_{IO}	Input pin voltage	IO to GND	-5.5		5.5	V
T_A	Operating free-air temperature		-55		150	°C

6.5 Thermal Information

THERMAL METRIC ⁽¹⁾		ESD451	UNIT
		DPL (X2SON)	
		2 PINS	
$R_{\theta JA}$	Junction-to-ambient thermal resistance	356.9	°C/W
$R_{\theta JC(top)}$	Junction-to-case (top) thermal resistance	201.2	°C/W
$R_{\theta JB}$	Junction-to-board thermal resistance	136.4	°C/W
Ψ_{JT}	Junction-to-top characterization parameter	2.6	°C/W
Ψ_{JB}	Junction-to-board characterization parameter	135.9	°C/W

THERMAL METRIC ⁽¹⁾		ESD451	UNIT
		DPL (X2SON)	
		2 PINS	
$R_{\theta JC(bot)}$	Junction-to-case (bottom) thermal resistance	NA	°C/W

- (1) For more information about traditional and new thermal metrics, see the [Semiconductor and IC Package Thermal Metrics](#) application report.

6.6 Electrical Characteristics

At TA=25°C (unless otherwise noted) ⁽¹⁾

PARAMETER		TEST CONDITION	MIN	TYP	MAX	UNIT	
V _{RWM}	Reverse stand-off voltage	I _{IO} < 100 nA, across operating temperature range	-5.5		5.5	V	
I _{LEAK}	Reverse leakage current	V _{IO} = 5.5 V, IO to GND or GND to IO		5	50	nA	
V _{BRR}	Break-down voltage	I _{IO} = 1 mA, IO to GND	7	8	9	V	
V _{BRF}	Break-down voltage	I _{IO} = 1 mA, GND to IO	7	8	9	V	
V _{HOLD}	Holding voltage ⁽²⁾	TLP, IO to GND or GND to IO		7.2		V	
V _{CLAMP}	Clamping voltage with TLP	I _{PP} = 1 A, TLP, IO to GND		7.6		V	
		I _{PP} = 5 A, TLP, IO to GND		8.2		V	
		I _{PP} = 16 A, TLP, IO to GND		10.4		V	
		I _{PP} = 1 A, TLP, GND to IO		7.6		V	
		I _{PP} = 5 A, TLP, GND to IO		8.2		V	
		I _{PP} = 16 A, TLP, GND to IO		10.4		V	
	Clamping voltage with surge strike ⁽⁴⁾	I _{PP} = 6 A, t _p = 8/20 μs, IO to GND			9.5		V
		I _{PP} = 6 A, t _p = 8/20 μs, GND to IO			9.5		V
R _{DYN}	Dynamic resistance ⁽³⁾	IO to GND		0.19		Ω	
		GND to IO					
C _L	Line capacitance	V _{IO} = 0 V; f = 1 MHz, V _{pp} = 30 mV, IO to GND or IO to GND		0.5		pF	

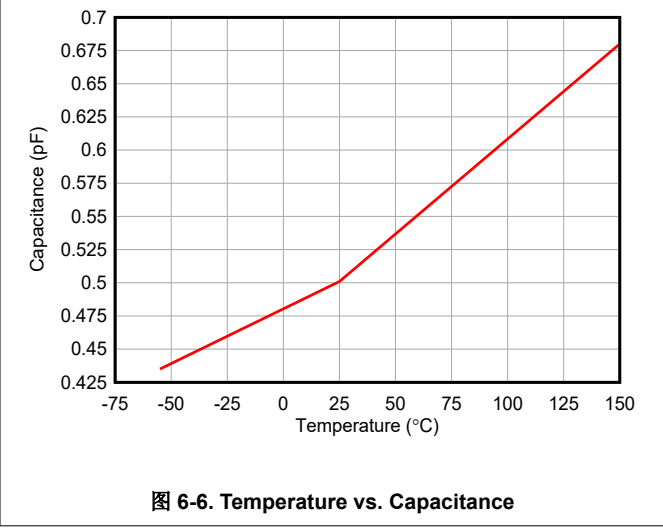
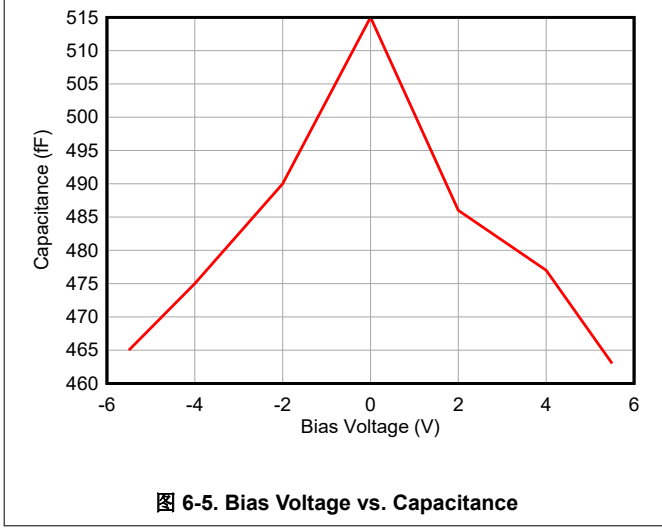
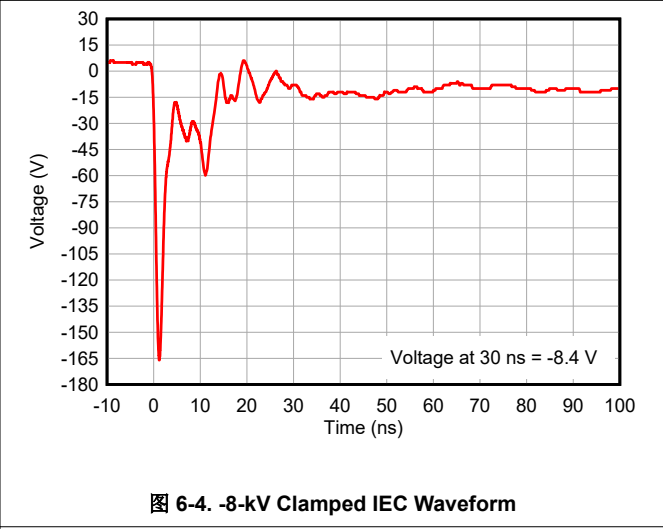
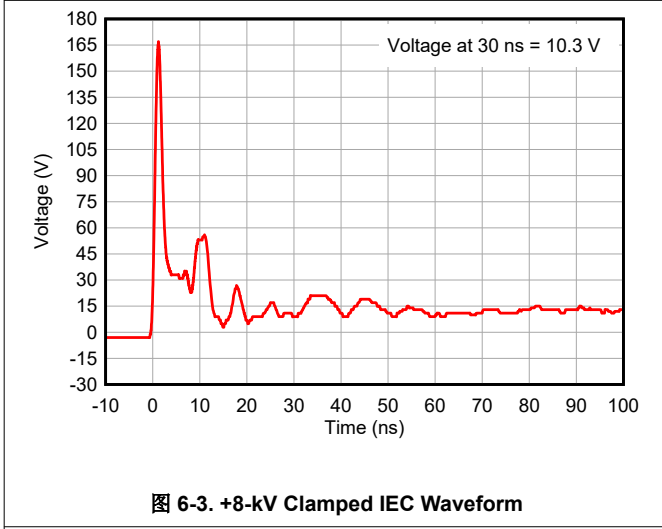
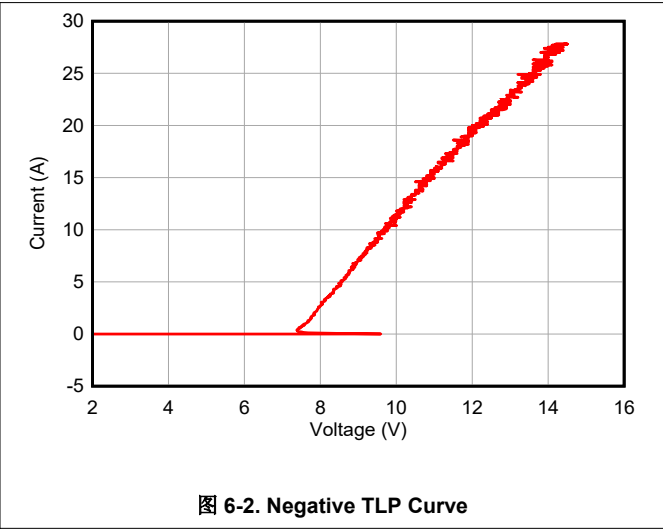
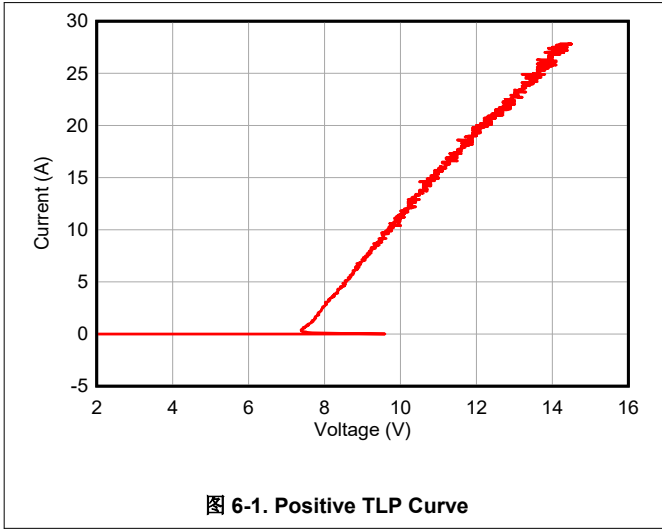
(1) Typical parameters are measured at 25°C

(2) Transition line pulse with 100 ns width and 10 ns rise and fall time

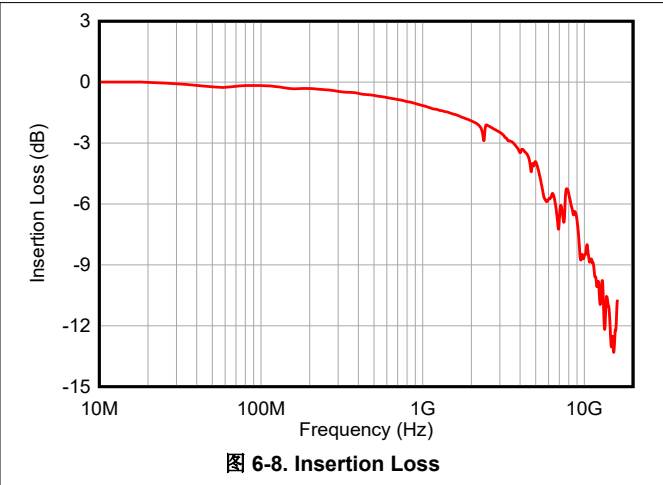
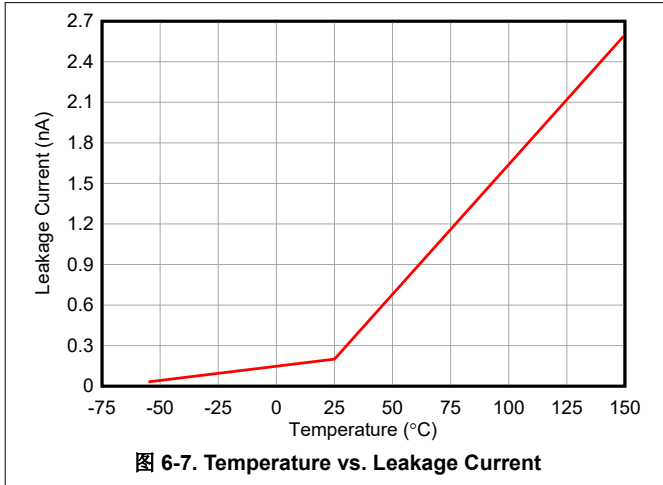
(3) Extraction of R_{DYN} using least squares fit of TLP characteristics between I = 10 A and I = 20 A

(4) Nonrepetitive current pulse 8 to 20 μs exponentially decaying waveform according to IEC 61000-4-5

6.7 Typical Characteristics



6.7 Typical Characteristics (continued)



7 Application and Implementation

备注

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes, as well as validating and testing their design implementation to confirm system functionality.

7.1 Application Information

The ESD451 is a diode type TVS which provides a path to ground for dissipating transient voltage spikes, such as ESD or surge, on signal lines and power lines. The device should be connected in parallel to the down stream circuitry it is protecting. As the current from the transient passes through the TVS, only a small voltage drop is present across the diode. This is the voltage presented to the protected IC. The low R_{DYN} of the triggered TVS holds this voltage (V_{CLAMP}) to a safe level for the protected IC. For more information on how to properly use this device, please refer to the [ESD Packaging and Layout Guide](#) for details.

8 Device and Documentation Support

8.1 Documentation Support

8.1.1 Related Documentation

For related documentation, see the following:

- Texas Instruments, [ESD Layout Guide application reports](#)
- Texas Instruments, [Generic ESD Evaluation Module user's guide](#)
- Texas Instruments, [Picking ESD Diodes for Ultra High-Speed Data Lines application reports](#)
- Texas Instruments, [Reading and Understanding an ESD Protection data sheet](#)

8.2 接收文档更新通知

要接收文档更新通知，请导航至 ti.com 上的器件产品文件夹。点击 [订阅更新](#) 进行注册，即可每周接收产品信息更改摘要。有关更改的详细信息，请查看任何已修订文档中包含的修订历史记录。

8.3 支持资源

[TI E2E™ 支持论坛](#) 是工程师的重要参考资料，可直接从专家获得快速、经过验证的解答和设计帮助。搜索现有解答或提出自己的问题可获得所需的快速设计帮助。

链接的内容由各个贡献者“按原样”提供。这些内容并不构成 TI 技术规范，并且不一定反映 TI 的观点；请参阅 TI 的 [《使用条款》](#)。

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DisplayPort™ is a trademark of Video Electronics Standards Association.

TI E2E™ is a trademark of Texas Instruments.

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8.5 静电放电警告



静电放电 (ESD) 会损坏这个集成电路。德州仪器 (TI) 建议通过适当的预防措施处理所有集成电路。如果不遵守正确的处理和安装程序，可能会损坏集成电路。

ESD 的损坏小至导致微小的性能降级，大至整个器件故障。精密的集成电路可能更容易受到损坏，这是因为非常细微的参数更改都可能会导致器件与其发布的规格不相符。

8.6 术语表

[TI 术语表](#) 本术语表列出并解释了术语、首字母缩略词和定义。

9 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
ESD451DPLR	ACTIVE	X2SON	DPL	2	15000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	G	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSELETE: TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "-" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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