



ABSTRACT

This report discusses the results of the total ionizing dose (TID) testing for TPS7H5002-SP, Texas Instruments 4-V to 14-V radiation-hardness-assured, current mode, single output PWM controller optimized for DC-DC converters in space applications. The study was done to determine TID effects under high dose rate (HDR) up to 100 krad(Si). The results show that all samples passed within the specified limits up to 100 krad(Si).

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1 Device Information

1.1 Product Description

The TPS7H5002-SP is a single output radiation-tolerant PWM controller that supports buck applications and single ended isolated topologies. The controller contains an integrated synchronous rectification output. Optimized for GaN power semiconductor based applications, the controller has configurable dead time and configurable leading edge blank time. The controller can be configured for maximum duty cycle of 75% or 100%. As such, the DCL pin can be left floating or connected to VLDO. Connection of the DCL pin to AVSS is not permissible for this device. The TPS7H5002-SP features dead-time programmability in order to target high-efficiency and high-performance topologies. The 0.613 +0.7%/-1% accurate internal reference allows design of high-current buck converters for FPGA core voltages.

The TPS7H5002-SP can be driven using an external clock through the SYNC pin or run using its internal oscillator at a frequency programmed by the user. Other programmable features include the UVLO threshold, soft start, and slope compensation. The TPS7H5002-SP is packaged in a very small 22-pin ceramic dual flat package.

1.2 Device Details

[Table 1-1](#) lists the device information used in the TID HDR characterization.

[Table 1-2](#) lists the device information used in the TID LDR characterization.

The TPS7H5002-SP is part of TPS7H500x-SP family of PWM controllers. Although each device in the TPS7H500x-SP family of PWM controllers has distinct functionality, all of the controllers share an identical device architecture, layout, and process. The differences are the features enabled in each device. As such, the radiation performance due to dose effects is expected to be similar for all of the devices in the TPS7H500x-SP family. For the full-featured device, TPS7H5001-SP, both LDR and HDR testing were performed. All electrical specifications were tested subsequent to the LDR and HDR exposure and were within datasheet specifications, as detailed in TPS7H5001-SP TID report. Examination of the TPS7H5001-SP post irradiation data indicates that the larger parametric shifts occurred after HDR exposure. As such, each device in the TPS7H500x-SP family was individually TID tested under HDR. For LDR testing information and post-LDR electrical characterization for the TPS7H5002-SP, please refer to TPS7H5001-SP TID LDR test data in Appendix C.

Table 1-1. Device and Exposure Details for HDR

TID Exposure Details	
TI Device	TPS7H5002-SP
TI Part Name	5962R1822202VXC
Package	22-pin CFP (HFT)
Technology	LBC7
Quantity Tested	HDR - 68
Lot Accept/Reject	All levels tested and passed up to 100 krad(Si) for HDR
HDR Radiation Facility	DCLAB, Texas Instruments, Dallas, TX
HDR Dose Level	3 krad(Si), 10 krad(Si), 30 krad(Si), 50 krad(Si), 100 krad(Si)
HDR Dose Rate	234.09 rads(Si)/s
HDR Radiation Source	Gammacell 220 Excel (GC-220E) Co-60
Irradiation Temperature	Ambient room temperature

Table 1-2. Device and Exposure Details for LDR

TID Exposure Details	
TI Device	TPS7H5001-SP
TI Part Name	5962R1822201VXC
Package	22-pin CFP (HFT)
Technology	LBC7
Quantity Tested	LDR - 77
Lot Accept/Reject	All levels tested and passed up to 100 krad(Si) for LDR
LDR Radiation Facility	Radiation Test Solutions (RTS) - Colorado Springs, CO
LDR Dose Rate	3 krad(Si), 10 krad(Si), 30 krad(Si), 50 krad(Si), 100 krad(Si)
LDR Radiation Source	Gammacell (JLS-81-22) Co-60
LDR Dose Rate	0.01 rad(Si)/s
Irradiation Temperature	Ambient room temperature

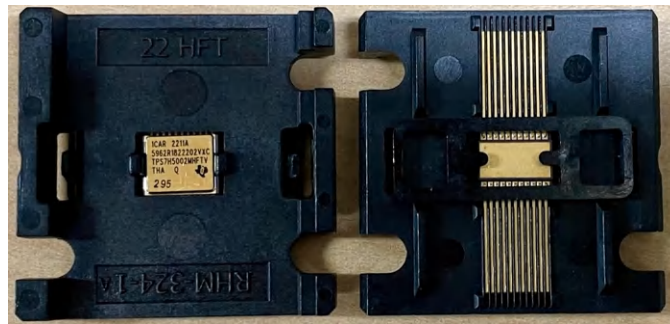


Figure 1-1. TPS7H5002-SP Device Picture

2 Total Dose Test Setup

2.1 Test Overview

The TPS7H5002-SP was tested according to MIL-STD-883, Test Method 1019.9. For this testing, Conditions A and D were used. The product was irradiated up to the target radiation level, and then put through full electrical parametric testing on the production automated test equipment (ATE). All devices remained functional passing all parametric test limits.

2.2 Test Description and Facilities

The TPS7H5002-SP HDR exposure was performed on biased and unbiased devices in a Co-60 gamma cell at TI facility in Dallas CLAB (DCLAB), Texas. The un-attenuated dose rate of this cell is 234.09 rads(Si)/s. After exposure, the devices were packed in dry ice (per MIL-STD-883 Method 1019.9 section 3.10) and bring to Junkins lab in TI Dallas for full post-radiation electrical evaluation using Texas Instruments ATE. ATE test limits are set per SMD electrical limits based on qualification and characterization data. Post-radiation measurements were taken within 30 minutes of removing the devices from the dry ice container. The devices were allowed to reach room temperature prior to electrical post-radiation measurements.

The TPS7H5001-SP LDR exposure was performed on biased and unbiased devices in a Co-60 gammacell under a 10-mrad(Si)/s exposure rate. The dose rate of the irradiator used in the exposure ranges from < 10 mrad(Si)/s to a maximum of approximately 84 rad(Si)/s, determined by the distance from the source. For the LDR (10 mrad(Si)/s) exposure, the test box was positioned approximately 2 m from the source. The exposure boards are housed in a lead-aluminum box (as specified in MIL-STD-883 TM 1019.9) to harden the gamma spectrum and minimize dose enhancement effects. The irradiator calibration is maintained by Logmire Laboratories using Thermoluminescence Dosimeters (TLDs) traceable to the National Institute of Standards and Technology (NIST) and the dosimetry was verified using TLDs prior to the radiation exposures. After exposure, devices were electrically tested at Texas Instruments. ATE test limits are set per SMD electrical limits based on qualification and characterization data.

2.3 Test Setup Details

The devices under HDR and LDR exposure were tested in two conditions, biased and unbiased, as described in the following. LDR data taken from TPS7H5001-SP.

2.3.1 Unbiased

For the unbiased HDR and LDR conditions, the exposure was performed with all pins grounded. LDR data taken from TPS7H5001-SP.

2.3.2 Biased

[Figure 2-1](#) shows the diagram for HDR and LDR exposure with biased condition. Bias diagram is same as TPS7H5001-SP as below, except pin# 11 and 12 is NC (no connect) for TPS7H5002-SP.

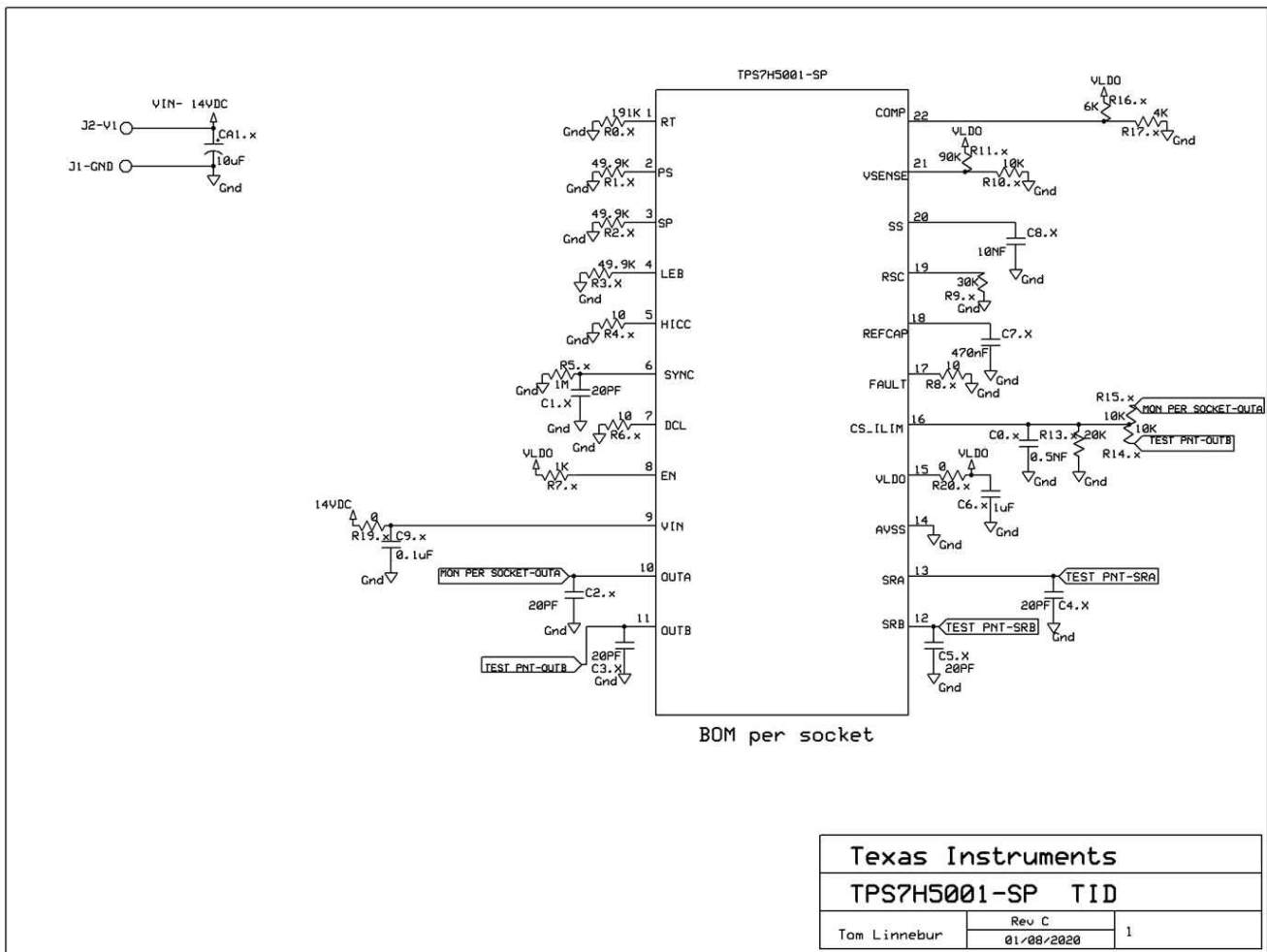


Figure 2-1. Bias Diagram Used in TID Exposure

2.4 Test Configuration and Condition

For HDR, devices were stressed at 3 Krad(Si), 10 Krad(Si), 30 Krad(Si), 50 krad(Si) and 100 krad(Si) for biased and unbiased conditions.

LDR data is from TPS7H5001-SP, devices were stressed at 3 Krad(Si), 10 Krad(Si), 30 Krad(Si), 50 krad(Si) and 100 krad(Si) for biased and unbiased conditions.

Table 2-1. HDR: 3-krad(Si) to 100-krad(Si)/s biased device information

Total Samples: 43				
Exposure Levels				
3-krad(Si)	10-krad(Si)	30-krad(Si)	50-krad(Si)	100-Krad(Si)
300, 305, 309, 311, 313	234, 235, 236, 237, 238	222, 223, 225, 226, 227	210, 211, 212, 213, 214	247, 249, 250, 251, 253, 255, 256, 257, 258, 259, 260, 261, 262, 263, 264, 265, 266, 267, 268, 269, 270, 271, 273

Table 2-2. HDR: 3-krad(Si) to 100-krad(Si)/s unbiased device information

Total Samples: 25				
Exposure Levels				
3-krad(Si)	10-krad(Si)	30-krad(Si)	50-krad(Si)	100-Krad(Si)
319, 321, 324, 327, 331	239, 240, 241, 243, 245	228, 229, 231, 232, 233	215, 216, 217, 218, 220	274, 275, 276, 277, 278

Table 2-3. LDR: 3-krad(Si) to 100-krad(Si)/s biased device information

Total Samples: 48				
Exposure Levels				
3-krad(Si)	10-krad(Si)	30-krad(Si)	50-krad(Si)	100-Krad(Si)
61, 62, 63, 64, 65, 66	73, 74, 75, 76, 77, 78	86, 89, 91, 92, 94, 95	106, 107, 108, 109, 110, 111	129, 131, 132, 133, 134, 135, 138, 139, 140, 141, 142, 143, 144, 220, 221, 224, 225, 226, 228, 232, 233, 236, 238, 243

Table 2-4. LDR: 3-krad(Si) to 100-krad(Si)/s unbiased device Information

Total Samples: 29				
Exposure Levels				
3-krad(Si)	10-krad(Si)	30-krad(Si)	50-krad(Si)	100-Krad(Si)
67, 68, 69, 70, 71, 72	79, 80, 81, 82, 83, 84	100, 101, 102, 103, 104, 105	113, 114, 115, 116, 126, 128	244, 246, 248, 374, 376

3 TI Characterization Test Results

3.1 TID Characterization Summary Results

The parametric data for the TPS7H5002-SP passes up to 100-krad(Si) HDR TID irradiation. The drifts of SMD electrical parameters through HDR were within the SMD limits.

The parametric data for the TPS7H5001-SP passes up to 100-krad(Si) LDR TID irradiation. The drifts of SMD electrical parameters through LDR were within the SMD limits. TPS7H5002-SP is spin of TPS7H5001-SP, sharing an identical device architecture, layout, and process. The differences are the features enabled in each device. As such, the radiation performance due to dose effects is expected to be similar for the full-featured device, TPS7H5001-SP and the TPS7H5002-SP under LDR TID irradiation.

Overall, the TPS7H5002-SP showed a strong degree of hardness to HDR TID irradiation up to 100 krad(Si) for both biased and unbiased exposure conditions. The measurements taken post-irradiation for each sample set showed a marginal shift for most parameters at each dose level for both biased and unbiased. The parameters that did show a greater degree of change between pre- and post-irradiation were still within the Electrical Performance Characteristics specified in Standard Microcircuit Drawing (Data Sheet Electrical Parameters). See [Table 3-1](#) for the Data Sheet Electrical Parameters and Associated Tests.

See [Appendix A](#) for Specifications Requiring Clarification.

See [Appendix B](#) for HDR report up to 100 krad(Si) of TPS7H5002-SP.

See [Appendix C](#) for LDR report up to 100 krad(Si) of TPS7H5001-SP.

3.2 Specification Compliance Matrix

Table 3-1. TPS7H5002-SP Electrical Parameters Table

PARAMETER	TEST CONDITION	TPS7H5002-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
SUPPLY VOLTAGES AND CURRENTS						
IDD Operating supply current	$f_{SW} = 500$ kHz, No load for OUTA and SRA		6.25	8	mA	5.24 __IDD_ACT_500K_NOLOAD_4V, 5.25 __IDD_ACT_500K_NOLOAD_5V, 5.26 __IDD_ACT_500K_NOLOAD_12V, 5.27 __IDD_ACT_500K_NOLOAD_14V
	$f_{SW} = 1$ MHz, No load for OUTA and SRA		6.75	9.5	mA	5.28 __IDD_ACT_1M_NOLOAD_4V, 5.29 __IDD_ACT_1M_NOLOAD_5V, 5.30 __IDD_ACT_1M_NOLOAD_12V, 5.31 __IDD_ACT_1M_NOLOAD_14V
	$f_{SW} = 2$ MHz, No load for OUTA and SRA		8.5	13.5	mA	5.36 __IDD_ACT_2M_NOLOAD_4V, 5.37 __IDD_ACT_2M_NOLOAD_5V, 5.38 __IDD_ACT_2M_NOLOAD_12V, 5.39 __IDD_ACT_2M_NOLOAD_14V
	$f_{SW} = 500$ kHz, $C_{LOAD} = 100$ pF for OUTA and SRA		7.5	9.5	mA	5.48 __IDD_ACT_500K_4V, 5.49 __IDD_ACT_500K_5V, 5.50 __IDD_ACT_500K_12V, 5.51 __IDD_ACT_500K_14V
	$f_{SW} = 1$ MHz, $C_{LOAD} = 100$ pF for OUTA and SRA		9	12	mA	5.52 __IDD_ACT_1M_4V, 5.53 __IDD_ACT_1M_5V, 5.54 __IDD_ACT_1M_12V, 5.55 __IDD_ACT_1M_14V
	$f_{SW} = 2$ MHz, $C_{LOAD} = 100$ pF for OUTA and SRA		14	19.5	mA	5.60 __IDD_ACT_2M_4V, 5.61 __IDD_ACT_2M_5V, 5.62 __IDD_ACT_2M_12V, 5.63 __IDD_ACT_2M_14V
I _{DD(dis)} Standby current	EN = 0 V			3	mA	5.1 __IDD_DIS_4V, 5.2 __IDD_DIS_5V, 5.3 __IDD_DIS_12V, 5.4 __IDD_DIS_14V
VLDO Internal linear regulator output voltage	5 V ≤ VIN ≤ 14 V, f _{sw} ≤ 1 MHz	4.75	5	5.2	V	5.65 __V_LDO_100K_5V, 5.66 __V_LDO_100K_12V, 5.67 __V_LDO_100K_14V, 5.69 __V_LDO_200K_5V, 5.70 __V_LDO_200K_12V, 5.71 __V_LDO_200K_14V, 5.73 __V_LDO_500K_5V, 5.74 __V_LDO_500K_12V, 5.75 __V_LDO_500K_14V, 5.77 __V_LDO_1M_5V, 5.78 __V_LDO_1M_12V, 5.79 __V_LDO_1M_14V
	5 V ≤ VIN ≤ 14 V, f _{sw} = 2 MHz	4.65	5	5.2	V	5.85 __V_LDO_2M_5V, 5.86 __V_LDO_2M_12V, 5.87 __V_LDO_2M_14V
ENABLE AND UNDERVOLTAGE LOCKOUT						
V _{ENR} EN threshold rising		0.57	0.6	0.65	V	6.5 __V_EN_RISE_4V, 6.8 __V_EN_RISE_5V, 6.11 __V_EN_RISE_12V, 6.14 __V_EN_RISE_14V
V _{ENF} EN threshold falling		0.47	0.5	0.55	V	6.6 __V_EN_FALL_4V, 6.9 __V_EN_FALL_5V, 6.12 __V_EN_FALL_12V, 6.15 __V_EN_FALL_14V
V _{ENH} EN hysteresis voltage		85	95	105	mV	6.7 __V_EN_HYS_4V, 6.10 __V_EN_HYS_5V, 6.13 __V_EN_HYS_12V, 6.16 __V_EN_HYS_14V
I _{EN} EN pin input leakage current	VIN = 14 V, EN = 5 V		5	50	nA	6.1 __I_EN_LEAK_4V, 6.2 __I_EN_LEAK_5V, 6.3 __I_EN_LEAK_12V, 6.4 __I_EN_LEAK_14V
VLDO _{UVLOR} VLDO UVLO rising		3.44	3.55	3.66	V	6.34 __UVLO_VLDO_RISE_1MHz, 6.37 __UVLO_VLDO_RISE_100kHz, 6.40 __UVLO_VLDO_RISE_200kHz, 6.43 __UVLO_VLDO_RISE_500kHz, 6.46 __UVLO_VLDO_RISE_2MHz
VLDO _{UVLOF} VLDO UVLO falling		3.29	3.4	3.51	V	6.35 __UVLO_VLDO_FALL_1MHz, 6.38 __UVLO_VLDO_FALL_100kHz, 6.41 __UVLO_VLDO_FALL_200kHz, 6.44 __UVLO_VLDO_FALL_500kHz, 6.47 __UVLO_VLDO_FALL_2MHz
VLDO _{UVLOH} VLDO UVLO hysteresis		115	135	160	mV	6.36 __UVLO_VLDO_HYS_1MHz, 6.39 __UVLO_VLDO_HYS_100kHz, 6.42 __UVLO_VLDO_HYS_200kHz, 6.45 __UVLO_VLDO_HYS_500kHz, 6.48 __UVLO_VLDO_HYS_2MHz
SOFT START						
I _{SS} Soft-start current	SS = 0.3 V	1.98	2.7	3.32	μA	7.1 __I_SS_4V, 7.3 __I_SS_5V, 7.5 __I_SS_12V, 7.7 __I_SS_14V
ERROR AMPLIFIER						
E _{A_{gm}} Transconductance	-2 μA < I _{COMP} < 2 μA, V _(COMP) = 1 V	1150	1800	2500	μA/V	8.9 __EA_GM_4V, 8.10 __EA_GM_5V, 8.11 __EA_GM_12V, 8.12 __EA_GM_14V
E _{A_{ISRC}} Error amplifier source current	V _(COMP) = 1 V, 100-mV input overdrive	100		190	μA	8.13 __EA_I_SOURCE_4V, 8.14 __EA_I_SOURCE_5V, 8.15 __EA_I_SOURCE_12V, 8.16 __EA_I_SOURCE_14V

Table 3-1. TPS7H5002-SP Electrical Parameters Table (continued)

PARAMETER	TEST CONDITION	TPS7H5002-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
EA _{ISNK} Error amplifier sink current	V _(COMP) = 1 V, 100-mV input overdrive	100		190	μA	8.17 __EA_I_SINK_4V, 8.18 __EA_I_SINK_5V, 8.19 __EA_I_SINK_12V, 8.20 __EA_I_SINK_14V
EA _{OS} Error amplifier offset voltage		-2		2	mV	8.5 __EA_OS_4V, 8.6 __EA_OS_5V, 8.7 __EA_OS_12V, 8.8 __EA_OS_14V
OSCILLATOR						
SYNC _{RT} SYNC out low-to-high rise time (10%/90%)	C _{LOAD} = 25 pF		6	15	ns	9.1 __T_SYNC_RISE_100kHz_4V, 9.5 __T_SYNC_RISE_200kHz_4V, 9.9 __T_SYNC_RISE_500kHz_4V, 9.13 __T_SYNC_RISE_1MHz_4V, 9.17 __T_SYNC_RISE_1p5MHz_4V, 9.21 __T_SYNC_RISE_2MHz_4V, 9.25 __T_SYNC_RISE_100kHz_5V, 9.29 __T_SYNC_RISE_200kHz_5V, 9.33 __T_SYNC_RISE_500kHz_5V, 9.37 __T_SYNC_RISE_1MHz_5V, 9.41 __T_SYNC_RISE_1p5MHz_5V, 9.45 __T_SYNC_RISE_2MHz_5V, 9.49 __T_SYNC_RISE_100kHz_12V, 9.53 __T_SYNC_RISE_200kHz_12V, 9.57 __T_SYNC_RISE_500kHz_12V, 9.61 __T_SYNC_RISE_1MHz_12V, 9.65 __T_SYNC_RISE_1p5MHz_12V, 9.69 __T_SYNC_RISE_2MHz_12V, 9.73 __T_SYNC_RISE_100kHz_14V, 9.77 __T_SYNC_RISE_200kHz_14V, 9.81 __T_SYNC_RISE_500kHz_14V, 9.85 __T_SYNC_RISE_1MHz_14V, 9.89 __T_SYNC_RISE_1p5MHz_14V, 9.93 __T_SYNC_RISE_2MHz_14V
SYNC _{FT} SYNC out high-to-low fall time (10%/90%)	C _{LOAD} = 25 pF		6	17	ns	9.2 __T_SYNC_FALL_100kHz_4V, 9.6 __T_SYNC_FALL_200kHz_4V, 9.10 __T_SYNC_FALL_500kHz_4V, 9.14 __T_SYNC_FALL_1MHz_4V, 9.18 __T_SYNC_FALL_1p5MHz_4V, 9.22 __T_SYNC_FALL_2MHz_4V, 9.26 __T_SYNC_FALL_100kHz_5V, 9.30 __T_SYNC_FALL_200kHz_5V, 9.34 __T_SYNC_FALL_500kHz_5V, 9.38 __T_SYNC_FALL_1MHz_5V, 9.42 __T_SYNC_FALL_1p5MHz_5V, 9.46 __T_SYNC_FALL_2MHz_5V, 9.50 __T_SYNC_FALL_100kHz_12V, 9.54 __T_SYNC_FALL_200kHz_12V, 9.58 __T_SYNC_FALL_500kHz_12V, 9.62 __T_SYNC_FALL_1MHz_12V, 9.66 __T_SYNC_FALL_1p5MHz_12V, 9.70 __T_SYNC_FALL_2MHz_12V, 9.74 __T_SYNC_FALL_100kHz_14V, 9.78 __T_SYNC_FALL_200kHz_14V, 9.82 __T_SYNC_FALL_500kHz_14V, 9.86 __T_SYNC_FALL_1MHz_14V, 9.90 __T_SYNC_FALL_1p5MHz_14V, 9.94 __T_SYNC_FALL_2MHz_14V
SYNC _{OL} SYNC out low level	I _{OL} = 10 mA			500	mV	9.186 __SYNC_VOL_4V, 9.187 __SYNC_VOL_5V, 9.188 __SYNC_VOL_12V, 9.189 __SYNC_VOL_14V
EXT _{DT} Externally set frequency detection time	RT = Open, f = 200 kHz			20	μs	9.185 __T_SYNC_DETECT
FSW _{EXT} Externally set frequency	RT = 1.07 MΩ	95	105	115	kHz	9.4 __FSW_EXT_RT_100kHz_4V, 9.28 __FSW_EXT_RT_100kHz_5V, 9.52 __FSW_EXT_RT_100kHz_12V, 9.76 __FSW_EXT_RT_100kHz_14V
	RT = 511 kΩ	190	210	230	kHz	9.8 __FSW_EXT_RT_200kHz_4V, 9.32 __FSW_EXT_RT_200kHz_5V, 9.56 __FSW_EXT_RT_200kHz_12V, 9.80 __FSW_EXT_RT_200kHz_14V
	RT = 90.9 kΩ	900	1000	1100	kHz	9.16 __FSW_EXT_RT_1MHz_4V, 9.40 __FSW_EXT_RT_1MHz_5V, 9.64 __FSW_EXT_RT_1MHz_12V, 9.88 __FSW_EXT_RT_1MHz_14V
	RT = 34.8 kΩ	1700	2000	2300	kHz	9.24 __FSW_EXT_RT_2MHz_4V, 9.48 __FSW_EXT_RT_2MHz_5V, 9.72 __FSW_EXT_RT_2MHz_12V, 9.96 __FSW_EXT_RT_2MHz_14V
VOLTAGE REFERENCE						
VREF Internal voltage reference initial tolerance	Measured at COMP, 25°C	0.609	0.613	0.615	V	8.1 __VREF_4V, 8.2 __VREF_5V, 8.3 __VREF_12V, 8.4 __VREF_14V

Table 3-1. TPS7H5002-SP Electrical Parameters Table (continued)

PARAMETER	TEST CONDITION	TPS7H5002-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
REFCAP voltage	REFCAP = 470 nF	1.213	1.225	1.237	V	5.88 __V_REFCAP_100K_4V, 5.89 __V_REFCAP_100K_5V, 5.90 __V_REFCAP_100K_12V, 5.91 __V_REFCAP_100K_14V, 5.92 __V_REFCAP_200K_4V, 5.93 __V_REFCAP_200K_5V, 5.94 __V_REFCAP_200K_12V, 5.95 __V_REFCAP_200K_14V, 5.96 __V_REFCAP_500K_4V, 5.97 __V_REFCAP_500K_5V, 5.98 __V_REFCAP_500K_12V, 5.99 __V_REFCAP_500K_14V, 5.100 __V_REFCAP_1M_4V, 5.101 __V_REFCAP_1M_5V, 5.102 __V_REFCAP_1M_12V, 5.103 __V_REFCAP_1M_14V, 5.104 __V_REFCAP_1P5M_4V, 5.105 __V_REFCAP_1P5M_5V, 5.106 __V_REFCAP_1P5M_12V, 5.107 __V_REFCAP_1P5M_14V, 5.108 __V_REFCAP_2M_4V, 5.109 __V_REFCAP_2M_5V, 5.110 __V_REFCAP_2M_12V, 5.111 __V_REFCAP_2M_14V
CURRENT SENSE, CURRENT LIMIT AND HICCUP						
CCSR	COMP to CS_ILIM ratio	2.00	2.06	2.12		10.49 __CCSR_Ratio
V _{CS_ILIM} Current limit (over-current) threshold			1.05	1.09	V	10.1 __V_CS_ILIM_OC_Rise_4V, 10.2 __V_CS_ILIM_OC_Fall_4V, 10.3 __V_CS_ILIM_OC_Rise_5V, 10.4 __V_CS_ILIM_OC_Fall_5V, 10.5 __V_CS_ILIM_OC_Rise_12V, 10.6 __V_CS_ILIM_OC_Fall_12V, 10.7 __V_CS_ILIM_OC_Rise_14V, 10.8 __V_CS_ILIM_OC_Fall_14V
FAULT						
V _{FLTR} FLT threshold rising		0.57	0.6	0.65	V	12.1 __V_FAULT_RISE_4V, 12.4 __V_FAULT_RISE_5V, 12.7 __V_FAULT_RISE_12V, 12.10 __V_FAULT_RISE_14V
V _{FLTF} FLT threshold falling		0.47	0.5	0.55	V	12.2 __V_FAULT_FALL_4V, 12.5 __V_FAULT_FALL_5V, 12.8 __V_FAULT_FALL_12V, 12.11 __V_FAULT_FALL_14V
V _{FLTH} FLT hysteresis voltage		90	100	110	mV	12.3 __V_FAULT_HYS_4V, 12.6 __V_FAULT_HYS_5V, 12.9 __V_FAULT_HYS_12V, 12.12 __V_FAULT_HYS_14V
T _{FLT} FLT minimum pulse width	V _{FLT} = 1 V	0.4		1.4	μs	12.14 __T_FAULT_MIN
t _{DFLT} FLT delay duration	f _{sw} = 100 kHz	140	152	169	μs	12.15 __T_FAULT_DELAY_100kHz
	f _{sw} = 200 kHz	66	78	86	μs	12.17 __T_FAULT_DELAY_200kHz
	f _{sw} = 1 MHz	14	17	21	μs	12.21 __T_FAULT_DELAY_1MHz
	f _{sw} = 2 MHz	7	11	14	μs	12.23 __T_FAULT_DELAY_2MHz
PRIMARY AND SYNCHRONOUS RECTIFIER OUTPUTS						
Rise/fall time	R _{LOAD} = 50 kΩ, C _{LOAD} = 100 pF, 10% to 90%		10	17	ns	13.1 __OUTA_RISE_1MHz_4V, 13.9 __OUTA_RISE_1MHz_5V, 13.17 __OUTA_RISE_1MHz_12V, 13.25 __OUTA_RISE_1MHz_14V, 13.3 __OUTA_FALL_1MHz_4V, 13.11 __OUTA_FALL_1MHz_5V, 13.19 __OUTA_FALL_1MHz_12V, 13.27 __OUTA_FALL_1MHz_14V, 13.13 __SRA_RISE_1MHz_5V, 13.21 __SRA_RISE_1MHz_12V, 13.29 __SRA_RISE_1MHz_14V, 13.5 __SRA_RISE_1MHz_4V, 13.15 __SRA_FALL_1MHz_5V, 13.23 __SRA_FALL_1MHz_12V, 13.31 __SRA_FALL_1MHz_14V, 13.7 __SRA_FALL_1MHz_4V
MINIMUM ON-TIME AND DEAD TIME						
t _{MIN} Minimum on-time	LEB = 10 kΩ, 5 V ≤ VIN ≤ 14 V			85	ns	13.145 __OUT_T_ON_MIN

Table 3-1. TPS7H5002-SP Electrical Parameters Table (continued)

PARAMETER	TEST CONDITION	TPS7H5002-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
TD _{PS} Primary off to secondary on dead time	PS = floating, 5 V ≤ VIN ≤ 14 V, 90% of OUTA falling edge to 10% of SRA rising edge with OUTA and SRA floating	5	8	11	ns	13.37 __PSA_DT_0ns_1M_5V, 13.41 __PSA_DT_0ns_1M_12V, 13.45 __PSA_DT_0ns_1M_14V
	PS = 49.9 kΩ, 5 V ≤ VIN ≤ 14 V, 90% of OUTA falling edge to 10% of SRA rising edge with OUTA and SRA floating	43	50	55	ns	13.53 __PSA_DT_50ns_1M_5V, 13.57 __PSA_DT_50ns_1M_12V, 13.61 __PSA_DT_50ns_1M_14V
	PS = 107 kΩ, 5 V ≤ VIN ≤ 14 V, 90% of OUTA falling edge to 10% of SRA rising edge with OUTA and SRA floating	85	100	110	ns	13.69 __PSA_DT_100ns_1M_5V, 13.73 __PSA_DT_100ns_1M_12V, 13.77 __PSA_DT_100ns_1M_14V, 13.78
TD _{SP} Secondary off to primary on dead time	SP = floating, 5 V ≤ VIN ≤ 14 V, 90% of SRA falling edge to 10% of OUTA rising edge with OUTA and SRA floating	5	8	11	ns	13.39 __SPA_DT_0ns_1M_5V, 13.43 __SPA_DT_0ns_1M_12V, 13.47 __SPA_DT_0ns_1M_14V
	SP = 49.9 kΩ, 5 V ≤ VIN ≤ 14 V, 90% of SRA falling edge to 10% of OUTA rising edge with OUTA and SRA floating	43	50	55	ns	13.55 __SPA_DT_50ns_1M_5V, 13.59 __SPA_DT_50ns_1M_12V, 13.63 __SPA_DT_50ns_1M_14V
	SP = 107 kΩ, 5 V ≤ VIN ≤ 14 V, 90% of SRA falling edge to 10% of OUTA rising edge with OUTA and SRA floating	85	100	110	ns	13.71 __SPA_DT_100ns_1M_5V, 13.75 __SPA_DT_100ns_1M_12V, 13.79 __SPA_DT_100ns_1M_14V,
DUTY CYCLE						
D _{MAX} Maximum duty cycle	DCL = floating, clock duty cycle = 50%	70	75	80	%	13.163 __MAX_DC_DCL_OPEN
	DCL = VLDO			100	%	13.166 __MAX_DC_DCL_VLDO

4 Applicable and Reference Documents

4.1 Applicable Documents

- Texas Instruments, [TPS7H500x-SP Radiation-Hardness-Assured 2-MHz Current Mode PWM Controllers datasheet \(Rev. B\)](#)
- Texas Instruments, [TPS7H500xEVM-CVAL User's Guide](#)
- Texas Instruments, [TPS7H5001-SP Total Ionizing Dose \(TID\) \(Rev. A\)](#)
- Texas Instruments, [TPS7H500x-SP Single-Event Effects \(SEE\) \(Rev. A\)](#)

4.2 Reference Documents

Texas Instruments total ionizing dose radiation (total dose) test procedure follows the standards put forth in MIL-STD-883 TM 1019. The document can be found at the DLA website.

5 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision * (March 2022) to Revision A (August 2022)	Page
• QMLV parts are used for TID exposure.....	2
• HDR TID Plots include 3/10/30/50/100 Krad(Si) irradiation in both biased and unbiased condition.....	15
• LDR TID plots of TPS7H5001-SP are added. This include 3/10/30/50/100 Krad(Si) irradiation in both biased and unbiased condition.....	16

A Appendix: Specifications Requiring Clarification

Table A-1. Specifications Requiring Clarification - Set #1

Data sheet specifications for which there is no parametric data and, therefore, not in the TID report. Functionality is assured by ATE testing.

PARAMETER	TEST CONDITION	TPS7H5002-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
V _{IN} Operating voltage		4		14	V
F _{SYNC} SYNC in frequency range		200		4000	kHz
D _{SYNC} SYNC in duty cycle	Duty cycle of external clock	40		60	%

Table A-2. Specifications Requiring Clarification - Set #2

Data sheet specifications for which no data is available in TID report as specification was verified through bench testing. TID testing is by ATE.

PARAMETER	TEST CONDITION	TPS7H5002-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
E _{A_{DC}} DC gain	V _{SENSE} = 0.6 V		10000		V/V
E _{A_{ro}} Error amplifier output resistance			7		MΩ
E _{A_{IB}} Error amplifier bias current				35	nA
E _{A_{BW}} Bandwidth			10		MHz
S _{YNCL} SYNC in low-level	V _{IN} < 5 V			0.8	V
	V _{IN} ≥ 5 V			0.8	V
S _{YNCH} SYNC in high-level	V _{IN} < 5 V		3.5		V
	V _{IN} ≥ 5 V		3.5		V
(V _{LDO} - S _{YNCOH}) SYNC out high level	I _{OH} = 10 mA			0.5	V

Table A-3. Specifications Requiring Clarification - Set #3

Data sheet specifications for which there is no 'MIN'/'MAX' value, only 'TYP', therefore, not in the TID report. Functionality is assured by ATE testing.

PARAMETER	TEST CONDITION	TPS7H5002-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
I _{HICC_DEL} Hiccup delay current	C _{S_LIM} = 1.3 V, C _{OMP} = 3 V, V _{SENSE} = REF _{CAP} /2 V, C _{HICC} = 3 nF, L _{EB} = 49.9 kΩ, f _{sw} = 100 kHz		80		μA
I _{HICC_RST} Hiccup restart current			1		μA
V _{HICC_PU} Hiccup pull-up threshold			1.0		V
V _{HICC_SD} Hiccup shut-down threshold			0.6		V
V _{HICC_RST} Hiccup restart threshold			0.3		V
Slope compensation	f _{sw} = 100 kHz, R _{SC} = 1.18 MΩ		0.033		V/μs
	f _{sw} = 200 kHz, R _{SC} = 562 kΩ		0.066		V/μs
	f _{sw} = 1000 kHz, R _{SC} = 100 kΩ		0.333		V/μs
	f _{sw} = 2000 kHz, R _{SC} = 49.9 kΩ		0.666		V/μs
PRIMARY AND SYNCHRONOUS RECTIFIER OUTPUTS Low-level threshold	I _{SINK} = 10 mA		0.5		V

Table A-3. Specifications Requiring Clarification - Set #3 (continued)

Data sheet specifications for which there is no 'MIN'/'MAX' value, only 'TYP', therefore, not in the TID report. Functionality is assured by ATE testing.

PARAMETER	TEST CONDITION	TPS7H5002-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
PRIMARY AND SYNCHRONOUS RECTIFIER OUTPUTS High-level threshold	$I_{SOURCE} = 10 \text{ mA}$		4.5		V
R_{SRC_P} Output source resistance	$I_{OUT} = 20 \text{ mA}$		15		Ω
R_{SINK_P} Output sink resistance	$I_{OUT} = 20 \text{ mA}$		15		Ω

Table A-4. Specifications Requiring Clarification - Set #4

Data sheet specifications that are not covered in TID report because specification condition is at temperature other than 25°C. TID testing is at 25°C only.

PARAMETER	TEST CONDITION	TPS7H5002-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
VREF Internal voltage reference	Measured at COMP, -55°C	0.607	0.609	0.612	V
	Measured at COMP, +125°C	0.611	0.614	0.617	V

B Appendix: HDR TID Report Data

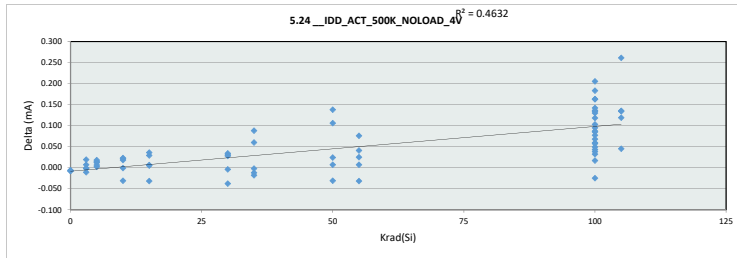
Description of identifiers for HDR TID report

IDENTIFIER - Krad(Si)	DESCRIPTION
0	Control Unit
3	3 krad(Si) biased
5	3 krad(Si) unbiased
10	10 krad(Si) biased
15	10 krad(Si) unbiased
30	30 krad(Si) biased
35	30 krad(Si) unbiased
50	50 krad(Si) biased
55	50 krad(Si) unbiased
100	100 krad(Si) biased
105	100 krad(Si) unbiased

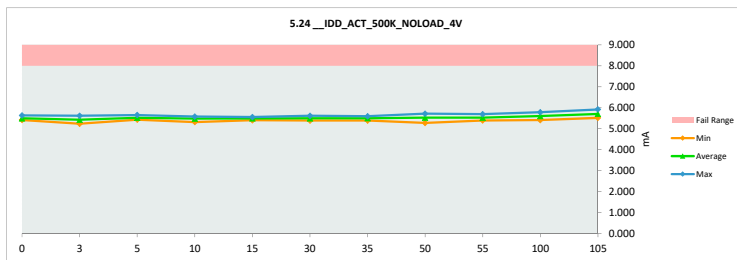
HDR TID Report
TPS7H5002-SP

HDR TID Report TPS7H5002-SP

5.24_IDD_ACT_500K_NOLOAD_4V				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		8	8	
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	5.446	5.438	-0.008
0	190	5.420	5.414	-0.006
0	191	5.636	5.628	-0.008
3	300	5.625	5.614	-0.011
3	305	5.238	5.236	-0.002
3	309	5.486	5.493	0.007
3	311	5.419	5.418	-0.001
3	313	5.344	5.363	0.019
5	319	5.648	5.650	0.002
5	321	5.522	5.540	0.018
5	324	5.428	5.440	0.012
5	327	5.415	5.429	0.014
5	331	5.524	5.530	0.006
10	234	5.511	5.510	-0.001
10	235	5.477	5.500	0.023
10	236	5.293	5.314	0.021
10	237	5.558	5.576	0.018
10	238	5.508	5.477	-0.031
15	239	5.518	5.554	0.036
15	240	5.395	5.401	0.006
15	241	5.522	5.551	0.029
15	243	5.463	5.431	-0.032
15	245	5.505	5.510	0.005
30	222	5.656	5.618	-0.038
30	223	5.428	5.458	0.030
30	225	5.444	5.478	0.034
30	226	5.361	5.389	0.028
30	227	5.537	5.533	-0.004
35	228	5.388	5.386	-0.002
35	229	5.533	5.593	0.060
35	231	5.532	5.514	-0.018
35	232	5.448	5.436	-0.012
35	233	5.465	5.553	0.088
50	210	5.456	5.594	0.138
50	211	5.265	5.272	0.007
50	212	5.609	5.715	0.106
50	213	5.480	5.504	0.024
50	214	5.576	5.545	-0.031
55	215	5.534	5.502	-0.032
55	216	5.614	5.639	0.025
55	217	5.350	5.391	0.041
55	218	5.613	5.689	0.076
55	220	5.403	5.410	0.007
100	247	5.632	5.735	0.103
100	249	5.603	5.698	0.095
100	250	5.481	5.456	-0.025
100	251	5.396	5.473	0.077
100	253	5.640	5.678	0.038
100	255	5.429	5.472	0.043
100	256	5.541	5.677	0.136
100	257	5.458	5.592	0.134
100	258	5.481	5.568	0.087
100	259	5.503	5.588	0.085
100	260	5.363	5.431	0.068
100	261	5.663	5.722	0.059
100	262	5.493	5.623	0.130
100	263	5.551	5.600	0.049
100	264	5.546	5.709	0.163
100	265	5.588	5.620	0.032
100	266	5.458	5.621	0.163
100	267	5.556	5.698	0.142
100	268	5.513	5.570	0.057
100	269	5.370	5.575	0.205
100	270	5.601	5.784	0.183
100	271	5.486	5.503	0.017
100	273	5.291	5.409	0.118
105	274	5.656	5.917	0.261
105	275	5.515	5.560	0.045
105	276	5.573	5.707	0.134
105	277	5.689	5.824	0.135
105	278	5.395	5.514	0.119
	Max	5.689	5.917	0.261
	Average	5.494	5.543	0.049
	Min	5.238	5.236	-0.038
	Std Dev	0.101	0.128	0.064

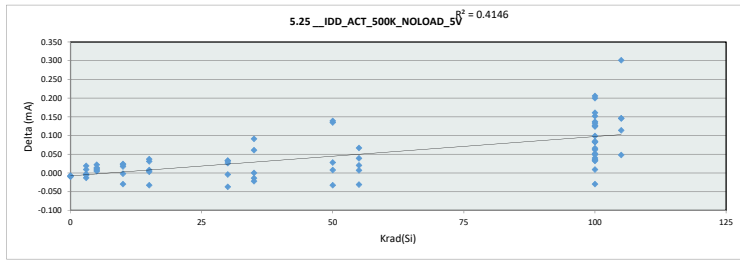


5.24_IDD_ACT_500K_NOLOAD_4V											
Test Site											
Tester											
Test Number											
Max Limit	8	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	5.414	5.236	5.429	5.314	5.401	5.389	5.386	5.272	5.391	5.409	5.514
Average	5.493	5.425	5.518	5.475	5.489	5.495	5.496	5.526	5.526	5.600	5.704
Max	5.628	5.614	5.650	5.576	5.554	5.618	5.593	5.715	5.689	5.784	5.917
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

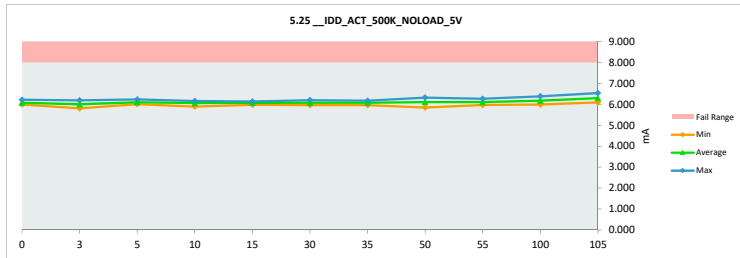


HDR TID Report
TPS7H5002-SP

5.25_IDD_ACT_500K_NOLOAD_5V				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		8	8	
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	6.028	6.019	-0.009
0	190	6.001	5.991	-0.010
0	191	6.224	6.217	-0.007
3	300	6.205	6.192	-0.013
3	305	5.807	5.804	-0.003
3	309	6.070	6.079	0.009
3	311	5.998	5.992	-0.006
3	313	5.918	5.937	0.019
5	319	6.229	6.234	0.005
5	321	6.102	6.124	0.022
5	324	6.009	6.020	0.011
5	327	5.992	6.006	0.014
5	331	6.103	6.111	0.008
10	234	6.095	6.093	-0.002
10	235	6.055	6.079	0.024
10	236	5.871	5.893	0.022
10	237	6.144	6.161	0.017
10	238	6.087	6.057	-0.030
15	239	6.094	6.131	0.037
15	240	5.971	5.979	0.008
15	241	6.102	6.133	0.031
15	243	6.043	6.010	-0.033
15	245	6.085	6.088	0.003
30	222	6.237	6.200	-0.037
30	223	6.000	6.033	0.033
30	225	6.019	6.051	0.032
30	226	5.936	5.962	0.026
30	227	6.119	6.115	-0.004
35	228	5.962	5.962	0.000
35	229	6.110	6.171	0.061
35	231	6.113	6.091	-0.022
35	232	6.022	6.008	-0.014
35	233	6.039	6.130	0.091
50	210	6.027	6.166	0.139
50	211	5.839	5.847	0.008
50	212	6.188	6.323	0.135
50	213	6.057	6.085	0.028
50	214	6.158	6.125	-0.033
55	215	6.111	6.080	-0.031
55	216	6.190	6.210	0.020
55	217	5.930	5.969	0.039
55	218	6.196	6.263	0.067
55	220	5.985	5.992	0.007
100	247	6.221	6.320	0.099
100	249	6.188	6.272	0.084
100	250	6.066	6.036	-0.030
100	251	5.977	6.039	0.062
100	253	6.222	6.254	0.032
100	255	6.013	6.053	0.040
100	256	6.120	6.257	0.137
100	257	6.036	6.163	0.127
100	258	6.058	6.142	0.084
100	259	6.079	6.161	0.082
100	260	5.937	6.003	0.066
100	261	6.250	6.299	0.049
100	262	6.069	6.195	0.126
100	263	6.129	6.163	0.034
100	264	6.131	6.292	0.161
100	265	6.172	6.207	0.035
100	266	6.039	6.191	0.152
100	267	6.135	6.268	0.133
100	268	6.090	6.142	0.052
100	269	5.946	6.152	0.206
100	270	6.180	6.380	0.200
100	271	6.061	6.070	0.009
100	273	5.864	5.988	0.124
105	274	6.236	6.537	0.301
105	275	6.096	6.144	0.048
105	276	6.155	6.301	0.146
105	277	6.265	6.411	0.146
105	278	5.976	6.090	0.114
Max		6.265	6.537	0.301
Average		6.073	6.122	0.049
Min		5.807	5.804	-0.037
Std Dev		0.104	0.132	0.067



5.25_IDD_ACT_5V											
Test Site											
Tester											
Test Number											
Max Limit	8	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	5.991	5.804	6.006	5.893	5.979	5.962	5.962	5.847	5.969	5.988	6.090
Average	6.076	6.001	6.099	6.057	6.068	6.072	6.072	6.109	6.103	6.176	6.297
Max	6.217	6.192	6.234	6.161	6.133	6.200	6.171	6.323	6.263	6.380	6.537
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

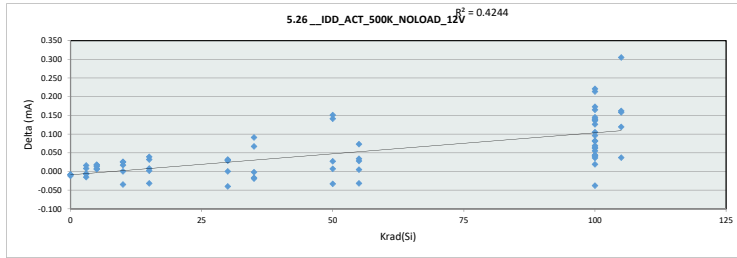


HDR TID Report
TPS7H5002-SP

5.26_IDD_ACT_500K_NOLOAD_12V

Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	8	8
Min Limit		

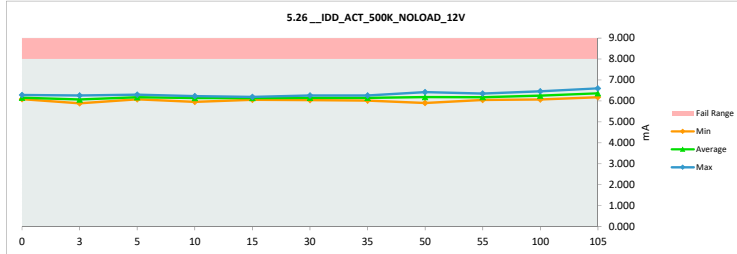
Krad(Si)	Serial #	PRE	POST	Delta
0	189	6.092	6.084	-0.008
0	190	6.099	6.087	-0.012
0	191	6.294	6.285	-0.009
3	300	6.278	6.263	-0.015
3	305	5.893	5.887	-0.006
3	309	6.129	6.137	0.008
3	311	6.067	6.060	-0.007
3	313	5.986	6.002	0.016
5	319	6.296	6.302	0.006
5	321	6.203	6.221	0.018
5	324	6.080	6.093	0.013
5	327	6.059	6.076	0.017
5	331	6.203	6.210	0.007
10	234	6.184	6.184	0.000
10	235	6.144	6.170	0.026
10	236	5.929	5.954	0.025
10	237	6.215	6.232	0.017
10	238	6.174	6.139	-0.035
15	239	6.154	6.193	0.039
15	240	6.048	6.056	0.008
15	241	6.154	6.186	0.032
15	243	6.122	6.090	-0.032
15	245	6.156	6.158	0.002
30	222	6.304	6.264	-0.040
30	223	6.064	6.094	0.030
30	225	6.102	6.134	0.032
30	226	6.011	6.040	0.029
30	227	6.191	6.191	0.000
35	228	6.021	6.019	-0.002
35	229	6.194	6.261	0.067
35	231	6.185	6.168	-0.017
35	232	6.099	6.080	-0.019
35	233	6.103	6.194	0.091
50	210	6.090	6.241	0.151
50	211	5.897	5.904	0.007
50	212	6.281	6.422	0.141
50	213	6.125	6.152	0.027
50	214	6.248	6.215	-0.033
55	215	6.191	6.159	-0.032
55	216	6.268	6.296	0.028
55	217	6.014	6.048	0.034
55	218	6.281	6.354	0.073
55	220	6.043	6.048	0.005
100	247	6.311	6.416	0.105
100	249	6.260	6.356	0.096
100	250	6.137	6.099	-0.038
100	251	6.036	6.105	0.069
100	253	6.287	6.330	0.043
100	255	6.079	6.123	0.044
100	256	6.216	6.356	0.140
100	257	6.099	6.237	0.138
100	258	6.132	6.214	0.082
100	259	6.147	6.228	0.081
100	260	6.022	6.077	0.055
100	261	6.333	6.398	0.065
100	262	6.140	6.276	0.136
100	263	6.191	6.232	0.041
100	264	6.227	6.400	0.173
100	265	6.240	6.276	0.036
100	266	6.108	6.273	0.165
100	267	6.207	6.352	0.145
100	268	6.146	6.208	0.062
100	269	6.025	6.246	0.221
100	270	6.243	6.457	0.214
100	271	6.138	6.157	0.019
100	273	5.947	6.073	0.126
105	274	6.296	6.601	0.305
105	275	6.155	6.192	0.037
105	276	6.216	6.374	0.158
105	277	6.320	6.482	0.162
105	278	6.059	6.178	0.119
Max		6.333	6.601	0.305
Average		6.146	6.198	0.052
Min		5.893	5.887	-0.040
Std Dev		0.105	0.136	0.071



5.26_IDD_ACT_500K_NOLOAD_12V

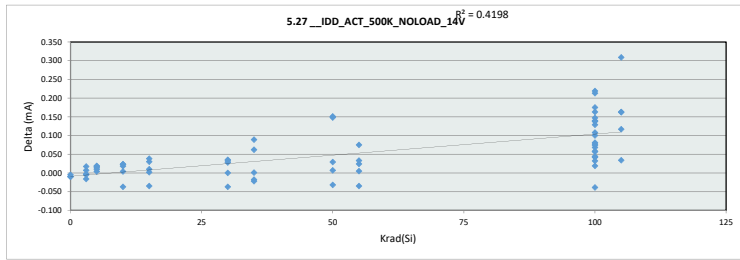
Test Site		
Tester		
Test Number		
Max Limit	8	mA
Min Limit		mA

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.084	5.887	6.076	5.954	6.056	6.040	6.019	5.904	6.048	6.073	6.178
Average	6.152	6.070	6.180	6.136	6.137	6.145	6.144	6.187	6.181	6.256	6.365
Max	6.285	6.263	6.302	6.232	6.193	6.264	6.261	6.422	6.354	6.457	6.601
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

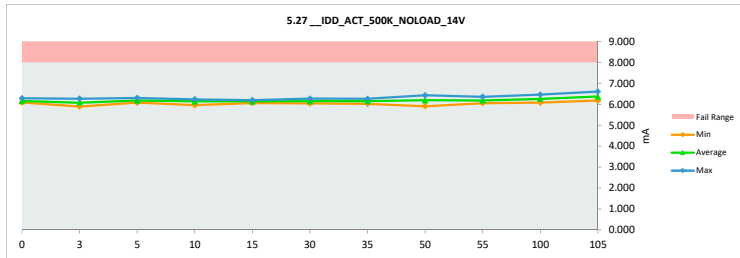


HDR TID Report
TPS7H5002-SP

5.27_IDD_ACT_500K_NOLOAD_14V				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		8	8	
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	6.094	6.089	-0.005
0	190	6.102	6.092	-0.010
0	191	6.297	6.287	-0.010
3	300	6.283	6.267	-0.016
3	305	5.895	5.892	-0.003
3	309	6.134	6.141	0.007
3	311	6.073	6.068	-0.005
3	313	5.990	6.007	0.017
5	319	6.301	6.305	0.004
5	321	6.207	6.226	0.019
5	324	6.084	6.098	0.014
5	327	6.063	6.080	0.017
5	331	6.204	6.214	0.010
10	234	6.188	6.192	0.004
10	235	6.149	6.172	0.023
10	236	5.934	5.957	0.023
10	237	6.219	6.237	0.018
10	238	6.179	6.142	-0.037
15	239	6.189	6.197	0.008
15	240	6.050	6.060	0.010
15	241	6.159	6.189	0.030
15	243	6.129	6.094	-0.035
15	245	6.161	6.163	0.002
30	222	6.309	6.272	-0.037
30	223	6.065	6.097	0.032
30	225	6.106	6.141	0.035
30	226	6.015	6.043	0.028
30	227	6.197	6.197	0.000
35	228	6.022	6.023	0.001
35	229	6.200	6.262	0.062
35	231	6.192	6.170	-0.022
35	232	6.105	6.087	-0.018
35	233	6.106	6.195	0.089
50	210	6.094	6.245	0.151
50	211	5.902	5.909	0.007
50	212	6.283	6.431	0.148
50	213	6.127	6.156	0.029
50	214	6.254	6.222	-0.032
55	215	6.195	6.160	-0.035
55	216	6.272	6.296	0.024
55	217	6.019	6.052	0.033
55	218	6.285	6.360	0.075
55	220	6.047	6.052	0.005
100	247	6.314	6.422	0.108
100	249	6.262	6.362	0.100
100	250	6.143	6.104	-0.039
100	251	6.039	6.113	0.074
100	253	6.291	6.333	0.042
100	255	6.084	6.128	0.044
100	256	6.219	6.358	0.139
100	257	6.103	6.242	0.139
100	258	6.138	6.219	0.081
100	259	6.155	6.232	0.077
100	260	6.025	6.082	0.057
100	261	6.337	6.405	0.068
100	262	6.144	6.282	0.138
100	263	6.194	6.237	0.043
100	264	6.232	6.407	0.175
100	265	6.245	6.277	0.032
100	266	6.116	6.279	0.163
100	267	6.211	6.358	0.147
100	268	6.152	6.210	0.058
100	269	6.028	6.247	0.219
100	270	6.247	6.460	0.213
100	271	6.142	6.161	0.019
100	273	5.952	6.081	0.129
105	274	6.298	6.607	0.309
105	275	6.160	6.194	0.034
105	276	6.222	6.384	0.162
105	277	6.326	6.489	0.163
105	278	6.062	6.179	0.117
Max		6.337	6.607	0.309
Average		6.151	6.203	0.052
Min		5.895	5.892	-0.039
Std Dev		0.105	0.136	0.071

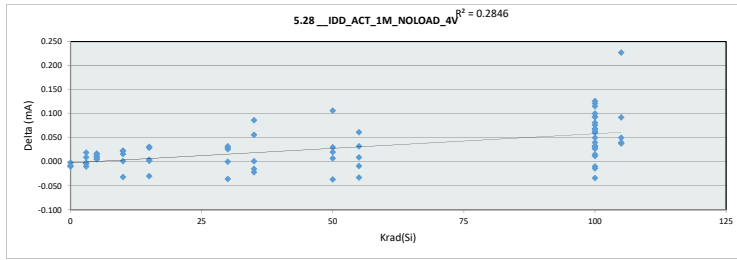


5.27_IDD_ACT_500K_NOLOAD_14V												
Test Site												
Tester												
Test Number												
Max Limit		8	mA									
Min Limit			mA									
LL		0	3	5	10	15	30	35	50	55	100	105
Min		6.089	5.892	6.080	5.957	6.060	6.043	6.023	5.909	6.052	6.081	6.179
Average		6.156	6.075	6.185	6.140	6.141	6.150	6.147	6.193	6.184	6.261	6.371
Max		6.287	6.267	6.305	6.237	6.197	6.272	6.262	6.431	6.360	6.460	6.607
UL		8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

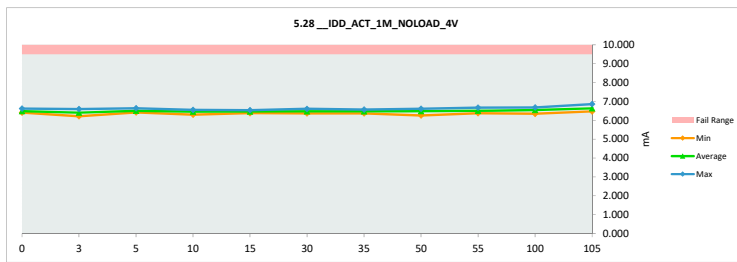


HDR TID Report
TPS7H5002-SP

5.28 IDD_ACT_1M_NOLOAD_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	6.431	6.429	-0.002
0	190	6.419	6.409	-0.010
0	191	6.625	6.617	-0.008
3	300	6.603	6.593	-0.010
3	305	6.214	6.212	-0.002
3	309	6.474	6.483	0.009
3	311	6.397	6.393	-0.004
3	313	6.324	6.343	0.019
5	319	6.633	6.638	0.005
5	321	6.506	6.523	0.017
5	324	6.416	6.427	0.011
5	327	6.401	6.416	0.015
5	331	6.510	6.518	0.008
10	234	6.496	6.497	0.001
10	235	6.458	6.480	0.022
10	236	6.278	6.301	0.023
10	237	6.538	6.554	0.016
10	238	6.490	6.458	-0.032
15	239	6.497	6.528	0.031
15	240	6.374	6.378	0.004
15	241	6.506	6.535	0.029
15	243	6.444	6.414	-0.030
15	245	6.492	6.494	0.002
30	222	6.643	6.607	-0.036
30	223	6.408	6.437	0.029
30	225	6.428	6.460	0.032
30	226	6.341	6.367	0.026
30	227	6.518	6.518	0.000
35	228	6.363	6.364	0.001
35	229	6.513	6.569	0.056
35	231	6.514	6.492	-0.022
35	232	6.425	6.410	-0.015
35	233	6.449	6.535	0.086
50	210	6.428	6.534	0.106
50	211	6.246	6.253	0.007
50	212	6.580	6.610	0.030
50	213	6.458	6.478	0.020
50	214	6.568	6.531	-0.037
55	215	6.520	6.487	-0.033
55	216	6.595	6.586	-0.009
55	217	6.338	6.370	0.032
55	218	6.607	6.668	0.061
55	220	6.387	6.396	0.009
100	247	6.616	6.676	0.060
100	249	6.591	6.623	0.032
100	250	6.479	6.445	-0.034
100	251	6.378	6.418	0.040
100	253	6.625	6.637	0.012
100	255	6.422	6.453	0.031
100	256	6.526	6.647	0.121
100	257	6.441	6.504	0.063
100	258	6.471	6.539	0.068
100	259	6.485	6.553	0.068
100	260	6.346	6.396	0.050
100	261	6.648	6.663	0.015
100	262	6.475	6.575	0.100
100	263	6.537	6.523	-0.014
100	264	6.529	6.622	0.093
100	265	6.577	6.604	0.027
100	266	6.444	6.570	0.126
100	267	6.536	6.629	0.093
100	268	6.490	6.523	0.033
100	269	6.353	6.468	0.115
100	270	6.587	6.668	0.081
100	271	6.463	6.453	-0.010
100	273	6.268	6.344	0.076
105	274	6.630	6.857	0.227
105	275	6.505	6.543	0.038
105	276	6.554	6.594	0.040
105	277	6.658	6.708	0.050
105	278	6.379	6.471	0.092
Max		6.658	6.857	0.227
Average		6.477	6.507	0.030
Min		6.214	6.212	-0.037
Std Dev		0.102	0.113	0.047

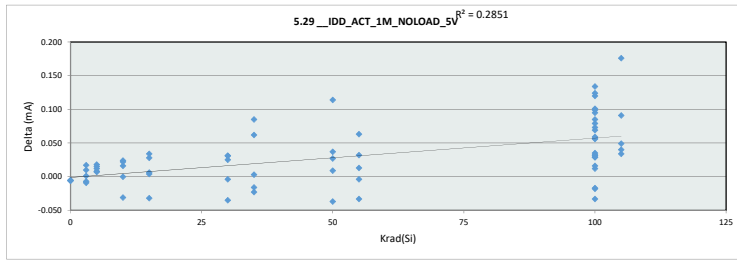


5.28 IDD_ACT											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.409	6.212	6.416	6.301	6.378	6.367	6.364	6.253	6.370	6.344	6.471
Average	6.485	6.405	6.504	6.458	6.470	6.478	6.474	6.481	6.501	6.545	6.635
Max	6.617	6.593	6.638	6.554	6.535	6.607	6.569	6.610	6.668	6.676	6.857
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

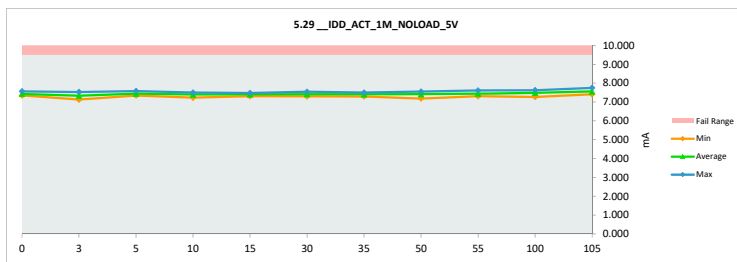


HDR TID Report
TPS7H5002-SP

5.29_IDD_ACT_1M_NOLOAD_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.374	7.369	-0.005
0	190	7.357	7.351	-0.006
0	191	7.571	7.565	-0.006
3	300	7.540	7.531	-0.009
3	305	7.129	7.130	0.001
3	309	7.414	7.424	0.010
3	311	7.330	7.323	-0.007
3	313	7.251	7.268	0.017
5	319	7.574	7.582	0.008
5	321	7.445	7.463	0.018
5	324	7.352	7.364	0.012
5	327	7.331	7.346	0.015
5	331	7.446	7.453	0.007
10	234	7.441	7.441	0.000
10	235	7.395	7.417	0.022
10	236	7.206	7.232	0.024
10	237	7.485	7.501	0.016
10	238	7.430	7.399	-0.031
15	239	7.426	7.460	0.034
15	240	7.305	7.309	0.004
15	241	7.448	7.476	0.028
15	243	7.385	7.353	-0.032
15	245	7.427	7.433	0.006
30	222	7.580	7.545	-0.035
30	223	7.338	7.369	0.031
30	225	7.361	7.392	0.031
30	226	7.271	7.296	0.025
30	227	7.461	7.457	-0.004
35	228	7.290	7.293	0.003
35	229	7.442	7.504	0.062
35	231	7.451	7.428	-0.023
35	232	7.353	7.337	-0.016
35	233	7.385	7.470	0.085
50	210	7.354	7.468	0.114
50	211	7.174	7.183	0.009
50	212	7.514	7.551	0.037
50	213	7.387	7.414	0.027
50	214	7.510	7.473	-0.037
55	215	7.458	7.425	-0.033
55	216	7.530	7.526	-0.004
55	217	7.272	7.304	0.032
55	218	7.551	7.614	0.063
55	220	7.327	7.340	0.013
100	247	7.565	7.624	0.059
100	249	7.533	7.562	0.029
100	250	7.424	7.391	-0.033
100	251	7.313	7.348	0.035
100	253	7.571	7.583	0.012
100	255	7.367	7.399	0.032
100	256	7.464	7.584	0.120
100	257	7.381	7.438	0.057
100	258	7.410	7.479	0.069
100	259	7.420	7.493	0.073
100	260	7.273	7.329	0.056
100	261	7.594	7.610	0.016
100	262	7.411	7.512	0.101
100	263	7.472	7.454	-0.018
100	264	7.469	7.564	0.095
100	265	7.522	7.554	0.032
100	266	7.379	7.513	0.134
100	267	7.473	7.572	0.099
100	268	7.427	7.456	0.029
100	269	7.280	7.404	0.124
100	270	7.523	7.608	0.085
100	271	7.396	7.379	-0.017
100	273	7.189	7.268	0.079
105	274	7.575	7.751	0.176
105	275	7.454	7.488	0.034
105	276	7.494	7.534	0.040
105	277	7.589	7.638	0.049
105	278	7.317	7.408	0.091
Max		7.594	7.751	0.176
Average		7.413	7.444	0.030
Min		7.129	7.130	-0.037
Std Dev		0.106	0.115	0.045

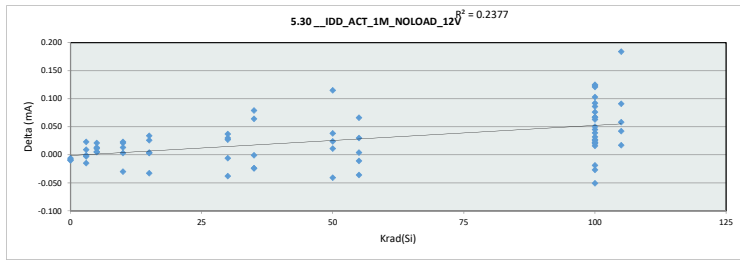


5.29_IDD_ACT											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.351	7.130	7.346	7.232	7.309	7.296	7.293	7.183	7.304	7.268	7.408
Average	7.428	7.335	7.442	7.398	7.406	7.412	7.406	7.418	7.442	7.484	7.564
Max	7.565	7.531	7.582	7.501	7.476	7.545	7.504	7.551	7.614	7.624	7.751
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

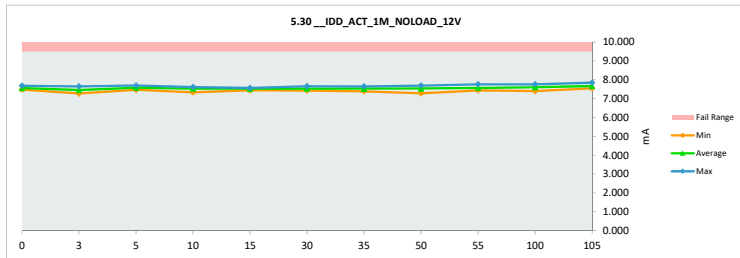


HDR TID Report
TPS7H5002-SP

		5.30_IDD_ACT_1M_NOLOAD_12V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	Min Limit	9.5	9.5	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.488	7.482	-0.006
0	190	7.527	7.518	-0.009
0	191	7.696	7.686	-0.010
3	300	7.668	7.653	-0.015
3	305	7.278	7.275	-0.003
3	309	7.520	7.529	0.009
3	311	7.450	7.449	-0.001
3	313	7.365	7.388	0.023
5	319	7.693	7.698	0.005
5	321	7.613	7.634	0.021
5	324	7.478	7.489	0.011
5	327	7.457	7.470	0.013
5	331	7.619	7.624	0.005
10	234	7.593	7.596	0.003
10	235	7.546	7.567	0.021
10	236	7.315	7.338	0.023
10	237	7.606	7.619	0.013
10	238	7.576	7.546	-0.030
15	239	7.533	7.567	0.034
15	240	7.440	7.444	0.004
15	241	7.540	7.566	0.026
15	243	7.521	7.488	-0.033
15	245	7.557	7.560	0.003
30	222	7.700	7.662	-0.038
30	223	7.447	7.477	0.030
30	225	7.502	7.539	0.037
30	226	7.404	7.431	0.027
30	227	7.594	7.588	-0.006
35	228	7.393	7.392	-0.001
35	229	7.590	7.654	0.064
35	231	7.584	7.560	-0.024
35	232	7.487	7.463	-0.024
35	233	7.498	7.577	0.079
50	210	7.460	7.575	0.115
50	211	7.274	7.285	0.011
50	212	7.659	7.697	0.038
50	213	7.508	7.532	0.024
50	214	7.670	7.629	-0.041
55	215	7.598	7.562	-0.036
55	216	7.666	7.655	-0.011
55	217	7.415	7.445	0.030
55	218	7.697	7.763	0.066
55	220	7.437	7.441	0.004
100	247	7.718	7.763	0.045
100	249	7.660	7.682	0.022
100	250	7.555	7.504	-0.051
100	251	7.418	7.438	0.020
100	253	7.687	7.703	0.016
100	255	7.486	7.513	0.027
100	256	7.627	7.748	0.121
100	257	7.494	7.544	0.050
100	258	7.542	7.605	0.063
100	259	7.539	7.606	0.067
100	260	7.421	7.460	0.039
100	261	7.741	7.763	0.022
100	262	7.532	7.635	0.103
100	263	7.584	7.557	-0.027
100	264	7.635	7.727	0.092
100	265	7.647	7.673	0.026
100	266	7.507	7.632	0.125
100	267	7.599	7.685	0.086
100	268	7.528	7.560	0.032
100	269	7.416	7.539	0.123
100	270	7.634	7.710	0.076
100	271	7.530	7.511	-0.019
100	273	7.335	7.402	0.067
105	274	7.665	7.849	0.184
105	275	7.556	7.573	0.017
105	276	7.599	7.641	0.042
105	277	7.684	7.742	0.058
105	278	7.457	7.548	0.091
	Max	7.741	7.849	0.184
	Average	7.542	7.569	0.028
	Min	7.274	7.275	-0.051
	Std Dev	0.109	0.117	0.045

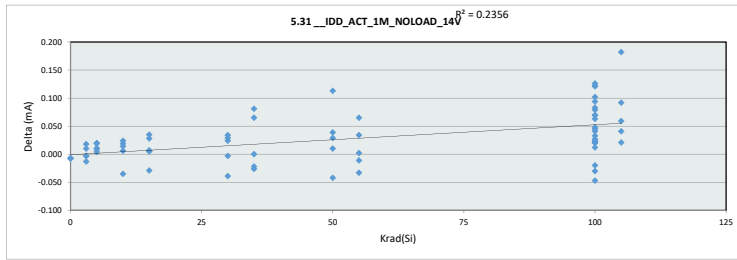


		5.30_IDD_ACT										
Test Site	Tester											
Test Number	Unit	9.5	mA									
Max Limit	Min Limit	9.5	mA									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		7.482	7.275	7.470	7.338	7.444	7.431	7.392	7.285	7.441	7.402	7.548
Average		7.562	7.459	7.583	7.533	7.525	7.539	7.529	7.544	7.573	7.607	7.671
Max		7.686	7.653	7.698	7.619	7.567	7.662	7.654	7.697	7.763	7.763	7.849
UL		9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

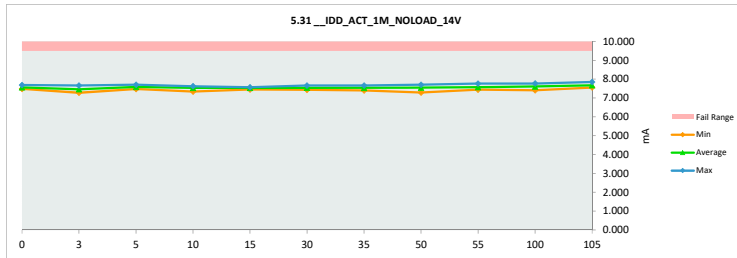


HDR TID Report
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5.31 IDD_ACT_1M_NOLOAD_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.492	7.485	-0.007
0	190	7.529	7.521	-0.008
0	191	7.697	7.690	-0.007
3	300	7.672	7.659	-0.013
3	305	7.282	7.279	-0.003
3	309	7.523	7.533	0.010
3	311	7.455	7.452	-0.003
3	313	7.371	7.389	0.018
5	319	7.698	7.702	0.004
5	321	7.616	7.635	0.019
5	324	7.485	7.496	0.011
5	327	7.457	7.477	0.020
5	331	7.622	7.631	0.009
10	234	7.596	7.602	0.006
10	235	7.550	7.574	0.024
10	236	7.321	7.340	0.019
10	237	7.610	7.624	0.014
10	238	7.582	7.547	-0.035
15	239	7.535	7.570	0.035
15	240	7.445	7.450	0.005
15	241	7.544	7.572	0.028
15	243	7.526	7.497	-0.029
15	245	7.560	7.567	0.007
30	222	7.704	7.665	-0.039
30	223	7.453	7.482	0.029
30	225	7.508	7.542	0.034
30	226	7.409	7.433	0.024
30	227	7.597	7.594	-0.003
35	228	7.397	7.397	0.000
35	229	7.595	7.660	0.065
35	231	7.589	7.563	-0.026
35	232	7.491	7.469	-0.022
35	233	7.501	7.582	0.081
50	210	7.465	7.578	0.113
50	211	7.280	7.290	0.010
50	212	7.664	7.703	0.039
50	213	7.510	7.539	0.029
50	214	7.674	7.632	-0.042
55	215	7.601	7.568	-0.033
55	216	7.668	7.657	-0.011
55	217	7.421	7.455	0.034
55	218	7.701	7.766	0.065
55	220	7.440	7.442	0.002
100	247	7.724	7.769	0.045
100	249	7.665	7.689	0.024
100	250	7.557	7.510	-0.047
100	251	7.423	7.444	0.021
100	253	7.696	7.708	0.012
100	255	7.492	7.519	0.027
100	256	7.630	7.752	0.122
100	257	7.500	7.548	0.048
100	258	7.544	7.607	0.063
100	259	7.543	7.613	0.070
100	260	7.426	7.467	0.041
100	261	7.745	7.765	0.020
100	262	7.536	7.638	0.102
100	263	7.590	7.560	-0.030
100	264	7.639	7.733	0.094
100	265	7.651	7.673	0.022
100	266	7.511	7.637	0.126
100	267	7.605	7.688	0.083
100	268	7.532	7.565	0.033
100	269	7.421	7.542	0.121
100	270	7.635	7.714	0.079
100	271	7.536	7.516	-0.020
100	273	7.341	7.410	0.069
105	274	7.670	7.852	0.182
105	275	7.560	7.581	0.021
105	276	7.604	7.645	0.041
105	277	7.688	7.747	0.059
105	278	7.464	7.556	0.092
Max		7.745	7.852	0.182
Average		7.546	7.574	0.028
Min		7.280	7.279	-0.047
Std Dev		0.108	0.116	0.045



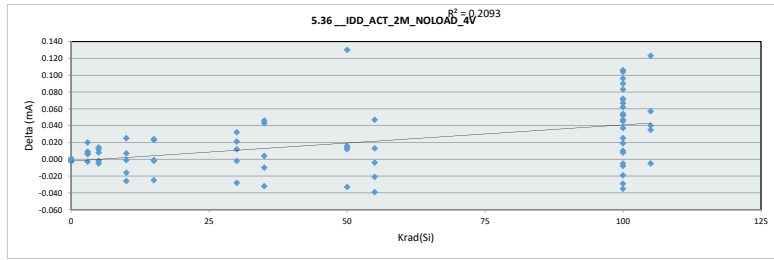
5.31 IDD_ACT											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.485	7.279	7.477	7.340	7.450	7.433	7.397	7.290	7.442	7.410	7.556
Average	7.565	7.462	7.588	7.537	7.531	7.543	7.534	7.548	7.578	7.612	7.676
Max	7.690	7.659	7.702	7.624	7.572	7.665	7.660	7.703	7.766	7.769	7.852
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



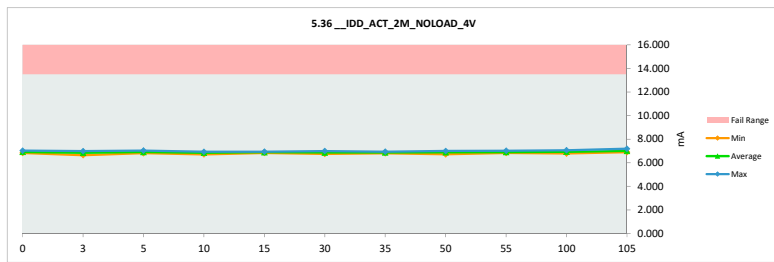
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5.36 IDD_ACT_2M_NOLOAD_4V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	13.5
Min Limit	13.5

Krad(Si)	Serial #	PRE	POST	Delta
0	189	6.870	6.869	-0.001
0	190	6.830	6.831	0.001
0	191	7.030	7.027	-0.003
3	300	6.981	6.988	0.007
3	305	6.631	6.637	0.006
3	309	6.958	6.967	0.009
3	311	6.802	6.799	-0.003
3	313	6.767	6.787	0.020
5	319	7.022	7.030	0.008
5	321	6.915	6.927	0.012
5	324	6.839	6.837	-0.002
5	327	6.797	6.811	0.014
5	331	6.914	6.909	-0.005
10	234	6.900	6.884	-0.016
10	235	6.869	6.876	0.007
10	236	6.680	6.705	0.025
10	237	6.934	6.933	-0.001
10	238	6.868	6.842	-0.026
15	239	6.918	6.941	0.023
15	240	6.835	6.834	-0.001
15	241	6.881	6.905	0.024
15	243	6.889	6.864	-0.025
15	245	6.905	6.903	-0.002
30	222	7.018	6.990	-0.028
30	223	6.793	6.814	0.021
30	225	6.841	6.873	0.032
30	226	6.737	6.749	0.012
30	227	6.904	6.902	-0.002
35	228	6.821	6.825	0.004
35	229	6.894	6.937	0.043
35	231	6.914	6.882	-0.032
35	232	6.816	6.806	-0.010
35	233	6.867	6.913	0.046
50	210	6.815	6.945	0.130
50	211	6.698	6.714	0.016
50	212	6.986	7.000	0.014
50	213	6.842	6.854	0.012
50	214	6.952	6.919	-0.033
55	215	6.898	6.859	-0.039
55	216	6.956	6.935	-0.021
55	217	6.815	6.828	0.013
55	218	6.972	7.019	0.047
55	220	6.834	6.830	-0.004
100	247	7.010	7.064	0.054
100	249	6.993	7.003	0.010
100	250	6.866	6.847	-0.019
100	251	6.846	6.854	0.008
100	253	6.963	6.982	0.019
100	255	6.816	6.811	-0.005
100	256	6.917	7.021	0.104
100	257	6.822	6.867	0.045
100	258	6.889	6.951	0.062
100	259	6.865	6.917	0.052
100	260	6.755	6.802	0.047
100	261	7.067	7.059	-0.008
100	262	6.890	6.980	0.090
100	263	6.876	6.841	-0.035
100	264	6.908	6.980	0.072
100	265	6.933	6.970	0.037
100	266	6.831	6.937	0.106
100	267	6.955	7.026	0.071
100	268	6.872	6.897	0.025
100	269	6.770	6.866	0.096
100	270	6.955	7.038	0.083
100	271	6.846	6.817	-0.029
100	273	6.714	6.781	0.067
105	274	7.064	7.187	0.123
105	275	6.912	6.907	-0.005
105	276	6.921	6.956	0.035
105	277	7.009	7.049	0.040
105	278	6.840	6.897	0.057
Max		7.067	7.187	0.130
Average		6.880	6.902	0.021
Min		6.631	6.637	-0.039
Std Dev		0.088	0.095	0.039

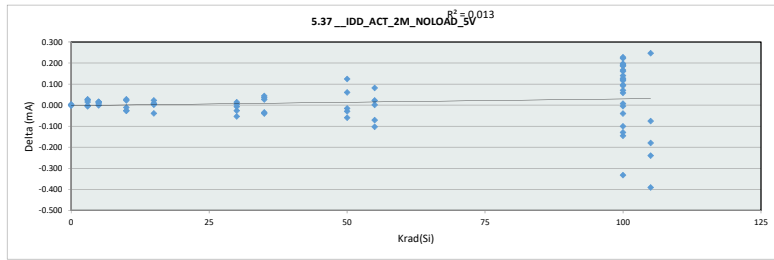


5.36 IDD_ACT												
Test Site												
Tester												
Test Number												
Max Limit	13.5	mA										
Min Limit		mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	6.831	6.637	6.811	6.705	6.834	6.749	6.806	6.714	6.828	6.781	6.897	
Average	6.909	6.836	6.903	6.848	6.889	6.866	6.873	6.886	6.894	6.927	6.999	
Max	7.027	6.988	7.030	6.933	6.941	6.990	6.937	7.000	7.019	7.064	7.187	
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	

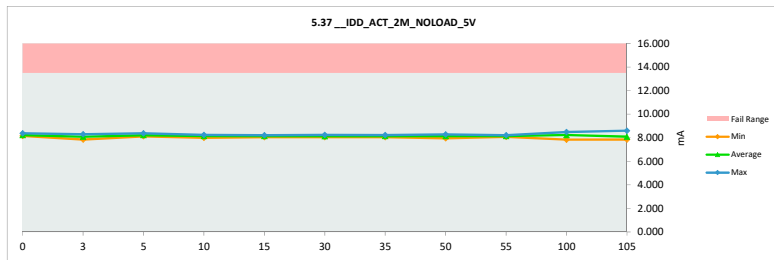


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5.37_IDD_ACT_2M_NOLOAD_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	13.5	13.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.152	8.155	0.003
0	190	8.164	8.166	0.002
0	191	8.371	8.368	-0.003
3	300	8.270	8.298	0.028
3	305	7.841	7.839	-0.002
3	309	8.201	8.216	0.015
3	311	8.071	8.065	-0.006
3	313	7.985	8.009	0.024
5	319	8.356	8.370	0.014
5	321	8.223	8.239	0.016
5	324	8.124	8.130	0.006
5	327	8.108	8.123	0.015
5	331	8.228	8.227	-0.001
10	234	8.222	8.195	-0.027
10	235	8.141	8.163	0.022
10	236	7.967	7.995	0.028
10	237	8.245	8.234	-0.011
10	238	8.169	8.143	-0.026
15	239	8.189	8.211	0.022
15	240	8.049	8.051	0.002
15	241	8.173	8.180	0.007
15	243	8.132	8.093	-0.039
15	245	8.202	8.211	0.009
30	222	8.295	8.241	-0.054
30	223	8.089	8.092	0.003
30	225	8.115	8.108	-0.007
30	226	8.028	8.042	0.014
30	227	8.204	8.178	-0.026
35	228	8.008	8.052	0.044
35	229	8.188	8.223	0.035
35	231	8.220	8.180	-0.040
35	232	8.101	8.066	-0.035
35	233	8.179	8.206	0.027
50	210	8.070	8.194	0.124
50	211	7.887	7.947	0.060
50	212	8.291	8.275	-0.016
50	213	8.129	8.100	-0.029
50	214	8.224	8.164	-0.060
55	215	8.209	8.106	-0.103
55	216	8.202	8.131	-0.071
55	217	8.047	8.069	0.022
55	218	8.216	8.217	0.001
55	220	8.081	8.163	0.082
100	247	8.320	8.487	0.167
100	249	8.309	8.163	-0.146
100	250	8.165	8.282	0.117
100	251	8.062	8.069	0.007
100	253	8.159	8.154	-0.005
100	255	8.118	7.988	-0.130
100	256	8.229	8.426	0.197
100	257	8.133	8.273	0.140
100	258	8.199	8.326	0.127
100	259	8.169	7.836	-0.333
100	260	8.034	8.156	0.122
100	261	8.378	8.470	0.092
100	262	8.186	8.372	0.186
100	263	8.080	7.979	-0.101
100	264	8.210	8.401	0.191
100	265	8.159	8.119	-0.040
100	266	8.117	8.339	0.222
100	267	8.218	8.379	0.161
100	268	8.164	8.260	0.096
100	269	8.010	8.199	0.189
100	270	8.224	8.452	0.228
100	271	8.131	8.201	0.070
100	273	7.909	7.967	0.058
105	274	8.347	8.594	0.247
105	275	8.241	7.850	-0.391
105	276	8.220	7.980	-0.240
105	277	8.265	8.085	-0.180
105	278	8.033	7.957	-0.076
Max		8.378	8.594	0.247
Average		8.157	8.172	0.015
Min		7.841	7.836	-0.391
Std Dev		0.109	0.152	0.114

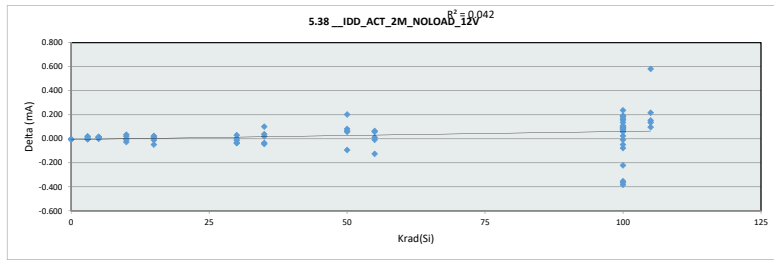


5.37_IDD_ACT_2M_NOLOAD_5V											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.155	7.839	8.123	7.995	8.051	8.042	8.052	7.947	8.069	7.836	7.850
Average	8.230	8.085	8.218	8.146	8.149	8.132	8.145	8.136	8.137	8.230	8.093
Max	8.368	8.298	8.370	8.234	8.211	8.241	8.223	8.275	8.217	8.487	8.594
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

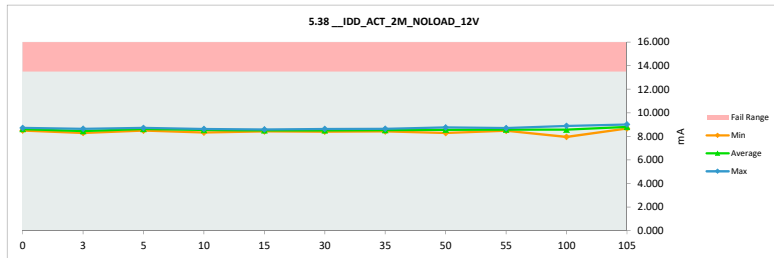


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5.38 IDD_ACT_2M_NOLOAD_12V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	13.5	13.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.497	8.495	-0.002
0	190	8.634	8.628	-0.006
0	191	8.721	8.715	-0.006
3	300	8.643	8.642	-0.001
3	305	8.294	8.291	-0.003
3	309	8.521	8.535	0.014
3	311	8.424	8.421	-0.003
3	313	8.325	8.346	0.021
5	319	8.703	8.717	0.014
5	321	8.688	8.702	0.014
5	324	8.497	8.509	0.012
5	327	8.470	8.486	0.016
5	331	8.696	8.696	0.000
10	234	8.639	8.631	-0.008
10	235	8.564	8.585	0.021
10	236	8.289	8.323	0.034
10	237	8.592	8.591	-0.001
10	238	8.607	8.580	-0.027
15	239	8.509	8.532	0.023
15	240	8.428	8.417	-0.011
15	241	8.498	8.518	0.020
15	243	8.524	8.475	-0.049
15	245	8.577	8.586	0.009
30	222	8.662	8.625	-0.037
30	223	8.423	8.425	0.002
30	225	8.518	8.548	0.030
30	226	8.409	8.397	-0.012
30	227	8.590	8.556	-0.034
35	228	8.317	8.417	0.100
35	229	8.601	8.639	0.038
35	231	8.600	8.557	-0.043
35	232	8.485	8.450	-0.035
35	233	8.525	8.544	0.019
50	210	8.393	8.595	0.202
50	211	8.197	8.279	0.082
50	212	8.699	8.757	0.058
50	213	8.474	8.539	0.065
50	214	8.736	8.642	-0.094
55	215	8.615	8.626	0.011
55	216	8.663	8.537	-0.126
55	217	8.462	8.520	0.058
55	218	8.714	8.705	-0.009
55	220	8.406	8.471	0.065
100	247	8.747	8.882	0.135
100	249	8.668	8.762	0.094
100	250	8.607	8.630	0.023
100	251	8.383	8.017	-0.366
100	253	8.549	8.500	-0.049
100	255	8.500	8.117	-0.383
100	256	8.669	8.862	0.193
100	257	8.484	8.592	0.108
100	258	8.579	8.653	0.074
100	259	8.518	8.704	0.186
100	260	8.446	8.437	-0.009
100	261	8.795	8.886	0.091
100	262	8.549	8.713	0.164
100	263	8.469	8.247	-0.222
100	264	8.657	8.842	0.185
100	265	8.574	8.496	-0.078
100	266	8.478	8.714	0.236
100	267	8.587	8.668	0.081
100	268	8.462	8.564	0.102
100	269	8.393	8.481	0.088
100	270	8.611	8.768	0.157
100	271	8.496	8.554	0.058
100	273	8.304	7.951	-0.353
105	274	8.645	8.862	0.217
105	275	8.571	8.667	0.096
105	276	8.574	8.709	0.135
105	277	8.606	8.758	0.152
105	278	8.427	9.009	0.582
Max		8.795	9.009	0.582
Average		8.538	8.568	0.030
Min		8.197	7.951	-0.383
Std Dev		0.124	0.189	0.133

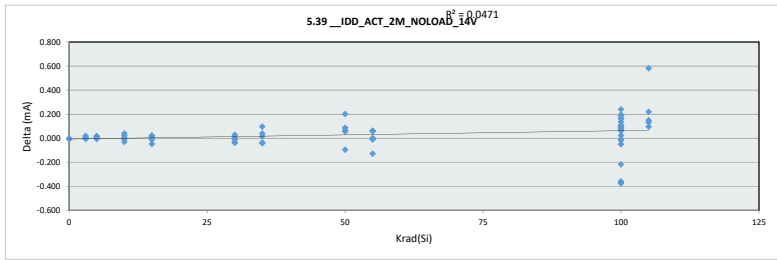


5.38 IDD_ACT											
Test Site											
Tester											
Test Number	13.5										
Max Limit	13.5	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.495	8.291	8.486	8.323	8.417	8.397	8.417	8.279	8.471	7.951	8.667
Average	8.613	8.447	8.622	8.542	8.506	8.510	8.521	8.562	8.572	8.567	8.801
Max	8.715	8.642	8.717	8.631	8.586	8.625	8.639	8.757	8.705	8.886	9.009
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

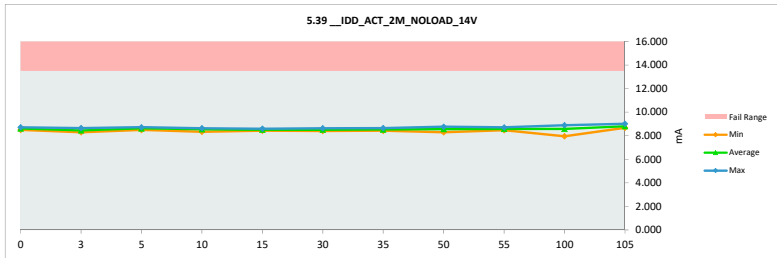


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5.39 IDD_ACT_2M_NOLOAD_14V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	13.5
			mA	13.5
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.502	8.499	-0.003
0	190	8.637	8.633	-0.004
0	191	8.725	8.718	-0.007
3	300	8.648	8.643	-0.005
3	305	8.298	8.295	-0.003
3	309	8.529	8.538	0.009
3	311	8.428	8.426	-0.002
3	313	8.331	8.350	0.019
5	319	8.707	8.719	0.012
5	321	8.690	8.706	0.016
5	324	8.504	8.513	0.009
5	327	8.472	8.489	0.017
5	331	8.703	8.697	-0.006
10	234	8.643	8.636	-0.007
10	235	8.568	8.590	0.022
10	236	8.293	8.333	0.040
10	237	8.595	8.598	0.003
10	238	8.613	8.582	-0.031
15	239	8.513	8.534	0.021
15	240	8.434	8.422	-0.012
15	241	8.502	8.524	0.022
15	243	8.529	8.481	-0.048
15	245	8.580	8.591	0.011
30	222	8.668	8.629	-0.039
30	223	8.423	8.427	0.004
30	225	8.524	8.552	0.028
30	226	8.416	8.401	-0.015
30	227	8.596	8.560	-0.036
35	228	8.324	8.420	0.096
35	229	8.604	8.643	0.039
35	231	8.605	8.562	-0.043
35	232	8.490	8.454	-0.036
35	233	8.530	8.546	0.016
50	210	8.397	8.598	0.201
50	211	8.201	8.287	0.086
50	212	8.704	8.760	0.056
50	213	8.478	8.543	0.065
50	214	8.741	8.646	-0.095
55	215	8.616	8.617	0.001
55	216	8.668	8.539	-0.129
55	217	8.467	8.524	0.057
55	218	8.716	8.706	-0.010
55	220	8.411	8.474	0.063
100	247	8.751	8.885	0.134
100	249	8.677	8.765	0.088
100	250	8.611	8.634	0.023
100	251	8.388	8.020	-0.368
100	253	8.552	8.502	-0.050
100	255	8.502	8.125	-0.377
100	256	8.672	8.867	0.195
100	257	8.488	8.593	0.105
100	258	8.584	8.663	0.079
100	259	8.521	8.715	0.194
100	260	8.451	8.442	-0.009
100	261	8.798	8.887	0.089
100	262	8.552	8.718	0.166
100	263	8.471	8.253	-0.218
100	264	8.658	8.846	0.188
100	265	8.579	8.560	-0.019
100	266	8.481	8.721	0.240
100	267	8.592	8.672	0.080
100	268	8.463	8.569	0.106
100	269	8.394	8.488	0.094
100	270	8.614	8.775	0.161
100	271	8.500	8.562	0.062
100	273	8.310	7.951	-0.359
105	274	8.647	8.867	0.220
105	275	8.576	8.672	0.096
105	276	8.579	8.711	0.132
105	277	8.612	8.761	0.149
105	278	8.431	9.012	0.581
Max		8.798	9.012	0.581
Average		8.542	8.572	0.030
Min		8.201	7.951	-0.377
Std Dev		0.124	0.189	0.133

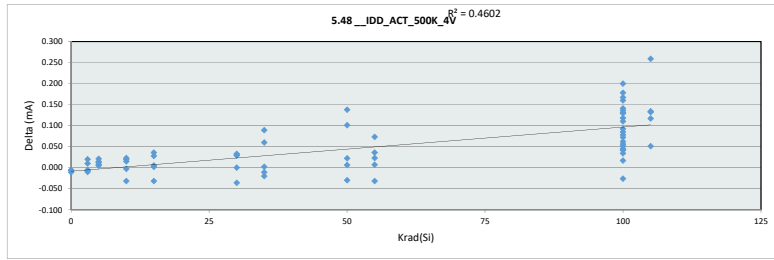


5.39 IDD_ACT_2M_NOLOAD_14V																
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
LL																
Min						8.499	8.295	8.489	8.333	8.422	8.401	8.420	8.287	8.474	7.951	8.672
Average						8.617	8.450	8.625	8.548	8.510	8.514	8.525	8.567	8.572	8.574	8.805
Max						8.718	8.643	8.719	8.636	8.591	8.629	8.643	8.760	8.706	8.887	9.012
UL						13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

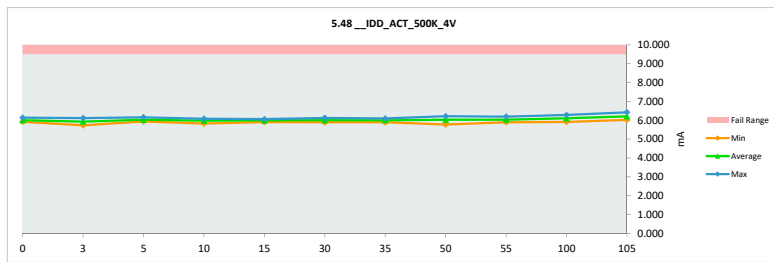


HDR TID Report
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5.48_IDD_ACT_500K_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	5.951	5.945	-0.006
0	190	5.932	5.921	-0.011
0	191	6.144	6.138	-0.006
3	300	6.126	6.116	-0.010
3	305	5.739	5.733	-0.006
3	309	5.995	6.005	0.010
3	311	5.924	5.918	-0.006
3	313	5.846	5.866	0.020
5	319	6.155	6.160	0.005
5	321	6.026	6.047	0.021
5	324	5.936	5.949	0.013
5	327	5.919	5.933	0.014
5	331	6.029	6.037	0.008
10	234	6.018	6.015	-0.003
10	235	5.979	5.999	0.020
10	236	5.801	5.824	0.023
10	237	6.067	6.082	0.015
10	238	6.012	5.980	-0.032
15	239	6.024	6.060	0.036
15	240	5.897	5.902	0.005
15	241	6.032	6.060	0.028
15	243	5.969	5.937	-0.032
15	245	6.014	6.016	0.002
30	222	6.161	6.125	-0.036
30	223	5.932	5.962	0.030
30	225	5.947	5.980	0.033
30	226	5.864	5.893	0.029
30	227	6.041	6.041	0.000
35	228	5.889	5.891	0.002
35	229	6.035	6.095	0.060
35	231	6.035	6.015	-0.020
35	232	5.947	5.936	-0.011
35	233	5.966	6.055	0.089
50	210	5.954	6.092	0.138
50	211	5.767	5.773	0.006
50	212	6.111	6.212	0.101
50	213	5.982	6.004	0.022
50	214	6.073	6.046	-0.030
55	215	6.043	6.011	-0.032
55	216	6.114	6.137	0.023
55	217	5.861	5.897	0.036
55	218	6.121	6.194	0.073
55	220	5.909	5.916	0.007
100	247	6.139	6.249	0.110
100	249	6.112	6.204	0.092
100	250	5.993	5.967	-0.026
100	251	5.903	5.975	0.072
100	253	6.144	6.186	0.042
100	255	5.939	5.984	0.045
100	256	6.045	6.181	0.136
100	257	5.964	6.095	0.131
100	258	5.987	6.072	0.085
100	259	6.005	6.083	0.078
100	260	5.863	5.925	0.062
100	261	6.167	6.223	0.056
100	262	5.994	6.123	0.129
100	263	6.056	6.098	0.042
100	264	6.048	6.208	0.160
100	265	6.094	6.128	0.034
100	266	5.963	6.130	0.167
100	267	6.058	6.199	0.141
100	268	6.020	6.071	0.051
100	269	5.872	6.072	0.200
100	270	6.103	6.281	0.178
100	271	5.986	6.003	0.017
100	273	5.794	5.912	0.118
105	274	6.163	6.422	0.259
105	275	6.025	6.076	0.051
105	276	6.078	6.210	0.132
105	277	6.190	6.324	0.134
105	278	5.904	6.021	0.117
Max		6.190	6.422	0.259
Average		5.999	6.047	0.048
Min		5.739	5.733	-0.036
Std Dev		0.102	0.127	0.063

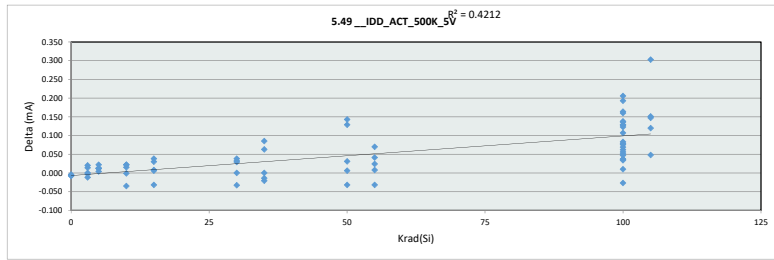


5.48_IDD_ACT_500K_4V											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	5.921	5.733	5.933	5.824	5.902	5.893	5.891	5.773	5.897	5.912	6.021
Average	6.001	5.928	6.025	5.980	5.995	6.000	5.998	6.025	6.031	6.103	6.211
Max	6.138	6.116	6.160	6.082	6.060	6.125	6.095	6.212	6.194	6.281	6.422
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

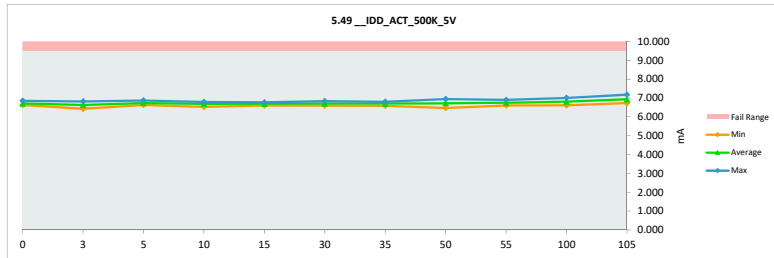


HDR TID Report TPS7H5002-SP

5.49_IDD_ACT_500K_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	6.659	6.655	-0.004
0	190	6.638	6.630	-0.008
0	191	6.861	6.854	-0.007
3	300	6.833	6.821	-0.012
3	305	6.430	6.430	0.000
3	309	6.704	6.718	0.014
3	311	6.626	6.623	-0.003
3	313	6.548	6.568	0.020
5	319	6.865	6.869	0.004
5	321	6.733	6.755	0.022
5	324	6.642	6.655	0.013
5	327	6.623	6.635	0.012
5	331	6.735	6.747	0.012
10	234	6.733	6.732	-0.001
10	235	6.686	6.708	0.022
10	236	6.505	6.527	0.022
10	237	6.782	6.797	0.015
10	238	6.721	6.686	-0.035
15	239	6.728	6.766	0.038
15	240	6.599	6.607	0.008
15	241	6.741	6.771	0.030
15	243	6.681	6.649	-0.032
15	245	6.718	6.724	0.006
30	222	6.871	6.838	-0.033
30	223	6.632	6.664	0.032
30	225	6.647	6.685	0.038
30	226	6.569	6.598	0.029
30	227	6.751	6.751	0.000
35	228	6.593	6.593	0.000
35	229	6.739	6.802	0.063
35	231	6.739	6.718	-0.021
35	232	6.645	6.631	-0.014
35	233	6.672	6.757	0.085
50	210	6.652	6.795	0.143
50	211	6.467	6.473	0.006
50	212	6.819	6.948	0.129
50	213	6.686	6.717	0.031
50	214	6.787	6.755	-0.032
55	215	6.751	6.719	-0.032
55	216	6.814	6.838	0.024
55	217	6.569	6.610	0.041
55	218	6.830	6.900	0.070
55	220	6.618	6.626	0.008
100	247	6.855	6.962	0.107
100	249	6.821	6.904	0.083
100	250	6.705	6.678	-0.027
100	251	6.609	6.678	0.069
100	253	6.853	6.890	0.037
100	255	6.649	6.696	0.047
100	256	6.751	6.887	0.136
100	257	6.668	6.797	0.129
100	258	6.694	6.770	0.076
100	259	6.708	6.787	0.079
100	260	6.564	6.626	0.062
100	261	6.881	6.937	0.056
100	262	6.697	6.825	0.128
100	263	6.761	6.798	0.037
100	264	6.761	6.925	0.164
100	265	6.805	6.839	0.034
100	266	6.670	6.830	0.160
100	267	6.766	6.904	0.138
100	268	6.724	6.775	0.051
100	269	6.575	6.781	0.206
100	270	6.811	7.004	0.193
100	271	6.689	6.699	0.010
100	273	6.496	6.619	0.123
105	274	6.873	7.176	0.303
105	275	6.736	6.784	0.048
105	276	6.787	6.934	0.147
105	277	6.893	7.044	0.151
105	278	6.610	6.730	0.120
Max		6.893	7.176	0.303
Average		6.705	6.755	0.050
Min		6.430	6.430	-0.035
Std Dev		0.104	0.132	0.067

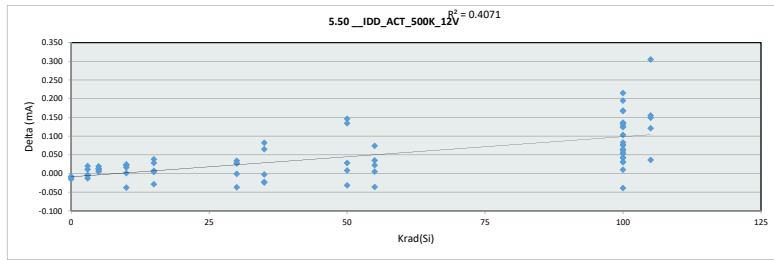


5.49_IDD_ACT_5V											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	6.630	6.430	6.635	6.527	6.607	6.598	6.593	6.473	6.610	6.619	6.730
Average	6.713	6.632	6.732	6.690	6.703	6.707	6.700	6.738	6.739	6.809	6.934
Max	6.854	6.821	6.869	6.797	6.771	6.838	6.802	6.948	6.900	7.004	7.176
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

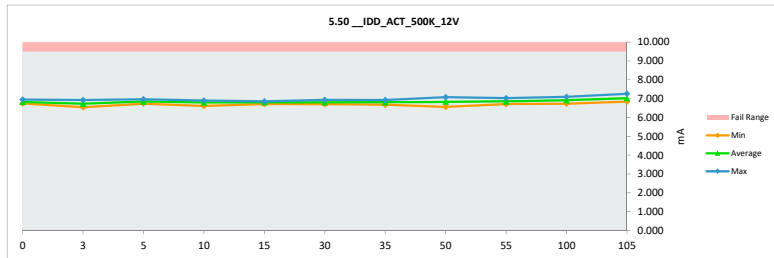


HDR TID Report TPS7H5002-SP

		5.50_IDD_ACT_500K_12V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	Min Limit	9.5	9.5	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	6.755	6.747	-0.008
0	190	6.773	6.758	-0.015
0	191	6.961	6.950	-0.011
3	300	6.938	6.925	-0.013
3	305	6.550	6.545	-0.005
3	309	6.792	6.803	0.011
3	311	6.726	6.722	-0.004
3	313	6.641	6.661	0.020
5	319	6.960	6.965	0.005
5	321	6.868	6.887	0.019
5	324	6.745	6.758	0.013
5	327	6.720	6.733	0.013
5	331	6.872	6.881	0.009
10	234	6.855	6.856	0.001
10	235	6.808	6.829	0.021
10	236	6.590	6.614	0.024
10	237	6.880	6.896	0.016
10	238	6.840	6.802	-0.038
15	239	6.816	6.854	0.038
15	240	6.711	6.715	0.004
15	241	6.818	6.846	0.028
15	243	6.790	6.761	-0.029
15	245	6.821	6.828	0.007
30	222	6.968	6.931	-0.037
30	223	6.724	6.751	0.027
30	225	6.763	6.797	0.034
30	226	6.676	6.703	0.027
30	227	6.859	6.858	-0.001
35	228	6.679	6.676	-0.003
35	229	6.859	6.924	0.065
35	231	6.848	6.824	-0.024
35	232	6.755	6.732	-0.023
35	233	6.762	6.844	0.082
50	210	6.744	6.890	0.146
50	211	6.550	6.558	0.008
50	212	6.945	7.079	0.134
50	213	6.782	6.810	0.028
50	214	6.911	6.879	-0.032
55	215	6.861	6.825	-0.036
55	216	6.925	6.947	0.022
55	217	6.686	6.721	0.035
55	218	6.949	7.023	0.074
55	220	6.704	6.709	0.005
100	247	6.982	7.085	0.103
100	249	6.928	7.011	0.083
100	250	6.807	6.768	-0.039
100	251	6.696	6.761	0.065
100	253	6.950	6.992	0.042
100	255	6.748	6.790	0.042
100	256	6.881	7.017	0.136
100	257	6.765	6.891	0.126
100	258	6.798	6.874	0.076
100	259	6.804	6.880	0.076
100	260	6.679	6.733	0.054
100	261	7.003	7.064	0.061
100	262	6.798	6.931	0.133
100	263	6.850	6.881	0.031
100	264	6.893	7.061	0.168
100	265	6.904	6.934	0.030
100	266	6.771	6.938	0.167
100	267	6.869	7.003	0.134
100	268	6.809	6.862	0.053
100	269	6.682	6.897	0.215
100	270	6.901	7.096	0.195
100	271	6.798	6.808	0.010
100	273	6.612	6.736	0.124
105	274	6.954	7.259	0.305
105	275	6.823	6.859	0.036
105	276	6.875	7.024	0.149
105	277	6.976	7.131	0.155
105	278	6.723	6.844	0.121
Max		7.003	7.259	0.305
Average		6.809	6.858	0.049
Min		6.550	6.545	-0.039
Std Dev		0.106	0.134	0.069

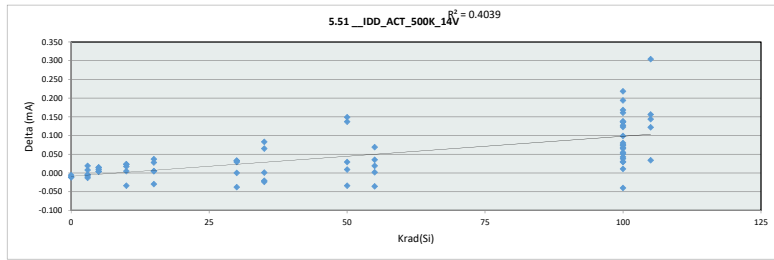


		5.50_IDD_ACT_500K_12V										
Test Site	Tester											
Test Number	Unit	9.5	mA									
Max Limit	Min Limit	9.5	mA									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		6.747	6.545	6.723	6.614	6.715	6.703	6.676	6.558	6.709	6.733	6.844
Average		6.818	6.731	6.845	6.799	6.801	6.808	6.800	6.843	6.845	6.914	7.023
Max		6.950	6.925	6.965	6.896	6.854	6.931	6.924	7.079	7.023	7.096	7.259
UL		9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

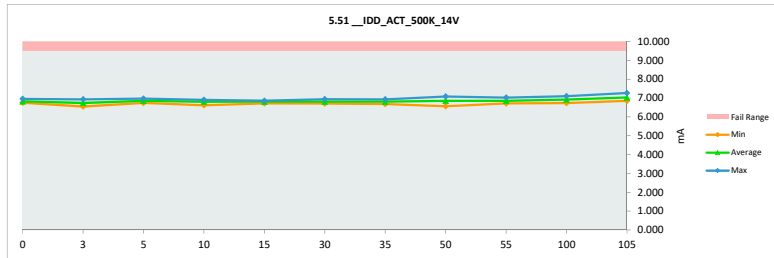


HDR TID Report
TPS7H5002-SP

5.51_IDD_ACT_500K_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	6.759	6.753	-0.006
0	190	6.777	6.765	-0.012
0	191	6.965	6.954	-0.011
3	300	6.942	6.929	-0.013
3	305	6.553	6.549	-0.004
3	309	6.797	6.805	0.008
3	311	6.734	6.726	-0.008
3	313	6.646	6.665	0.019
5	319	6.965	6.969	0.004
5	321	6.874	6.888	0.014
5	324	6.751	6.760	0.009
5	327	6.725	6.740	0.015
5	331	6.880	6.884	0.004
10	234	6.857	6.862	0.005
10	235	6.812	6.835	0.023
10	236	6.595	6.618	0.023
10	237	6.885	6.902	0.017
10	238	6.841	6.807	-0.034
15	239	6.821	6.858	0.037
15	240	6.715	6.720	0.005
15	241	6.823	6.851	0.028
15	243	6.795	6.765	-0.030
15	245	6.827	6.832	0.005
30	222	6.974	6.936	-0.038
30	223	6.726	6.758	0.032
30	225	6.767	6.800	0.033
30	226	6.680	6.709	0.029
30	227	6.862	6.862	0.000
35	228	6.682	6.683	0.001
35	229	6.864	6.929	0.065
35	231	6.852	6.828	-0.024
35	232	6.759	6.738	-0.021
35	233	6.767	6.850	0.083
50	210	6.747	6.896	0.149
50	211	6.554	6.563	0.009
50	212	6.947	7.084	0.137
50	213	6.787	6.816	0.029
50	214	6.916	6.882	-0.034
55	215	6.865	6.829	-0.036
55	216	6.930	6.949	0.019
55	217	6.690	6.725	0.035
55	218	6.955	7.024	0.069
55	220	6.711	6.713	0.002
100	247	6.986	7.085	0.099
100	249	6.934	7.014	0.080
100	250	6.812	6.772	-0.040
100	251	6.698	6.763	0.065
100	253	6.957	7.000	0.043
100	255	6.754	6.792	0.038
100	256	6.885	7.022	0.137
100	257	6.767	6.894	0.127
100	258	6.803	6.878	0.075
100	259	6.809	6.883	0.074
100	260	6.685	6.736	0.051
100	261	7.004	7.072	0.068
100	262	6.802	6.938	0.136
100	263	6.857	6.887	0.030
100	264	6.899	7.067	0.168
100	265	6.909	6.938	0.029
100	266	6.779	6.940	0.161
100	267	6.872	7.010	0.138
100	268	6.815	6.870	0.055
100	269	6.687	6.905	0.218
100	270	6.906	7.100	0.194
100	271	6.803	6.814	0.011
100	273	6.616	6.739	0.123
105	274	6.960	7.264	0.304
105	275	6.827	6.861	0.034
105	276	6.881	7.025	0.144
105	277	6.980	7.136	0.156
105	278	6.728	6.850	0.122
	Max	7.004	7.264	0.304
	Average	6.814	6.863	0.049
	Min	6.553	6.549	-0.040
	Std Dev	0.106	0.134	0.069

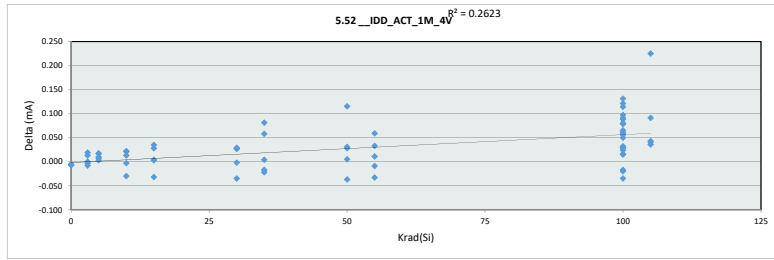


5.51_IDD_ACT_500K_14V											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.753	6.549	6.740	6.618	6.720	6.709	6.683	6.563	6.713	6.736	6.850
Average	6.824	6.735	6.848	6.805	6.805	6.813	6.806	6.848	6.848	6.918	7.027
Max	6.954	6.929	6.969	6.902	6.858	6.936	6.929	7.084	7.024	7.100	7.264
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

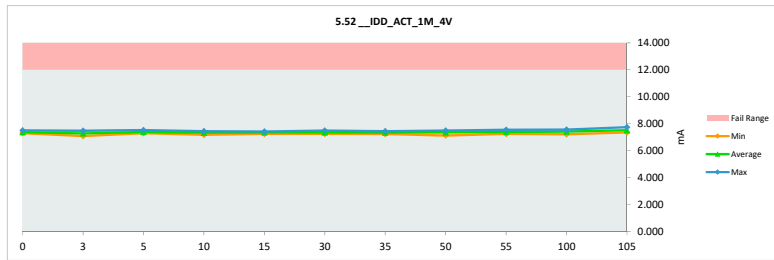


HDR TID Report TPS7H5002-SP

5.52_IDD_ACT_1M_4V				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		12	12	
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.314	7.309	-0.005
0	190	7.302	7.295	-0.007
0	191	7.509	7.502	-0.007
3	300	7.481	7.473	-0.008
3	305	7.084	7.084	0.000
3	309	7.356	7.369	0.013
3	311	7.272	7.269	-0.003
3	313	7.197	7.216	0.019
5	319	7.517	7.524	0.007
5	321	7.382	7.399	0.017
5	324	7.298	7.308	0.010
5	327	7.279	7.296	0.017
5	331	7.395	7.398	0.003
10	234	7.382	7.379	-0.003
10	235	7.331	7.353	0.022
10	236	7.158	7.179	0.021
10	237	7.422	7.435	0.013
10	238	7.365	7.335	-0.030
15	239	7.372	7.407	0.035
15	240	7.249	7.252	0.003
15	241	7.393	7.421	0.028
15	243	7.328	7.296	-0.032
15	245	7.377	7.381	0.004
30	222	7.525	7.490	-0.035
30	223	7.286	7.313	0.027
30	225	7.304	7.333	0.029
30	226	7.217	7.244	0.027
30	227	7.399	7.397	-0.002
35	228	7.234	7.238	0.004
35	229	7.384	7.442	0.058
35	231	7.390	7.368	-0.022
35	232	7.297	7.280	-0.017
35	233	7.330	7.411	0.081
50	210	7.297	7.412	0.115
50	211	7.120	7.125	0.005
50	212	7.459	7.490	0.031
50	213	7.330	7.358	0.028
50	214	7.445	7.408	-0.037
55	215	7.408	7.375	-0.033
55	216	7.471	7.462	-0.009
55	217	7.221	7.254	0.033
55	218	7.490	7.549	0.059
55	220	7.267	7.278	0.011
100	247	7.498	7.558	0.060
100	249	7.475	7.504	0.029
100	250	7.367	7.332	-0.035
100	251	7.256	7.288	0.032
100	253	7.506	7.521	0.015
100	255	7.310	7.340	0.030
100	256	7.399	7.520	0.121
100	257	7.323	7.379	0.056
100	258	7.355	7.415	0.060
100	259	7.358	7.423	0.065
100	260	7.215	7.265	0.050
100	261	7.528	7.544	0.016
100	262	7.353	7.450	0.097
100	263	7.416	7.396	-0.020
100	264	7.404	7.492	0.088
100	265	7.461	7.485	0.024
100	266	7.319	7.450	0.131
100	267	7.413	7.504	0.091
100	268	7.373	7.402	0.029
100	269	7.227	7.341	0.114
100	270	7.460	7.540	0.080
100	271	7.337	7.320	-0.017
100	273	7.141	7.219	0.078
105	274	7.513	7.738	0.225
105	275	7.392	7.428	0.036
105	276	7.430	7.473	0.043
105	277	7.533	7.574	0.041
105	278	7.258	7.349	0.091
	Max	7.533	7.738	0.225
	Average	7.356	7.385	0.030
	Min	7.084	7.084	-0.037
	Std Dev	0.104	0.114	0.046

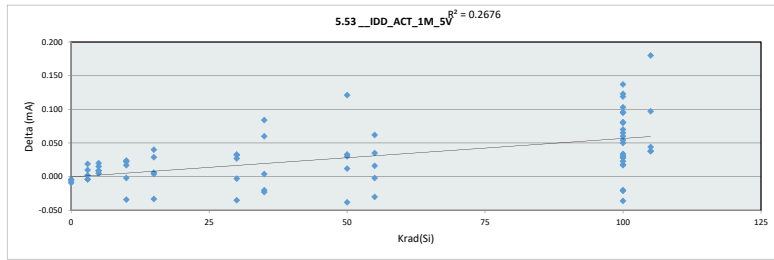


5.52_IDD_ACT											
Test Site											
Tester											
Test Number											
Max Limit	12										
Min Limit											
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.295	7.084	7.296	7.179	7.252	7.244	7.238	7.125	7.254	7.219	7.349
Average	7.369	7.282	7.385	7.336	7.351	7.355	7.348	7.359	7.384	7.421	7.512
Max	7.502	7.473	7.524	7.435	7.421	7.490	7.442	7.490	7.549	7.558	7.738
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

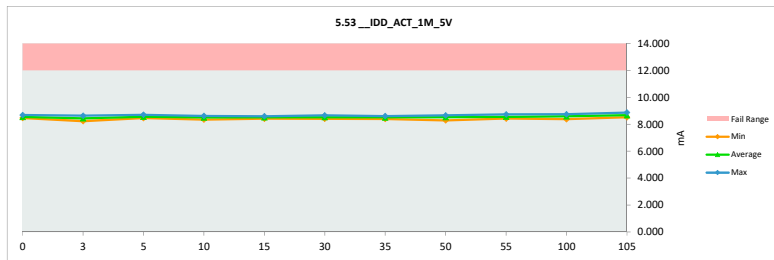


HDR TID Report TPS7H5002-SP

5.53_IDD_ACT_1M_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	12	12		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.483	8.479	-0.004
0	190	8.476	8.467	-0.009
0	191	8.691	8.685	-0.006
3	300	8.644	8.640	-0.004
3	305	8.226	8.228	0.002
3	309	8.529	8.539	0.010
3	311	8.432	8.428	-0.004
3	313	8.353	8.372	0.019
5	319	8.693	8.702	0.009
5	321	8.551	8.571	0.020
5	324	8.466	8.475	0.009
5	327	8.444	8.459	0.015
5	331	8.562	8.567	0.005
10	234	8.558	8.556	-0.002
10	235	8.498	8.520	0.022
10	236	8.321	8.345	0.024
10	237	8.601	8.618	0.017
10	238	8.540	8.506	-0.034
15	239	8.533	8.573	0.040
15	240	8.408	8.414	0.006
15	241	8.564	8.593	0.029
15	243	8.496	8.463	-0.033
15	245	8.544	8.548	0.004
30	222	8.697	8.662	-0.035
30	223	8.445	8.478	0.033
30	225	8.464	8.496	0.032
30	226	8.380	8.407	0.027
30	227	8.574	8.571	-0.003
35	228	8.390	8.394	0.004
35	229	8.549	8.609	0.060
35	231	8.556	8.533	-0.023
35	232	8.451	8.431	-0.020
35	233	8.492	8.576	0.084
50	210	8.451	8.572	0.121
50	211	8.272	8.284	0.012
50	212	8.625	8.658	0.033
50	213	8.493	8.523	0.030
50	214	8.618	8.580	-0.038
55	215	8.580	8.550	-0.030
55	216	8.633	8.631	-0.002
55	217	8.388	8.423	0.035
55	218	8.667	8.729	0.062
55	220	8.437	8.453	0.016
100	247	8.679	8.744	0.065
100	249	8.651	8.685	0.034
100	250	8.548	8.512	-0.036
100	251	8.421	8.452	0.031
100	253	8.681	8.699	0.018
100	255	8.489	8.517	0.028
100	256	8.567	8.690	0.123
100	257	8.490	8.544	0.054
100	258	8.527	8.587	0.060
100	259	8.522	8.592	0.070
100	260	8.373	8.423	0.050
100	261	8.705	8.722	0.017
100	262	8.514	8.617	0.103
100	263	8.584	8.564	-0.020
100	264	8.574	8.669	0.095
100	265	8.637	8.666	0.029
100	266	8.487	8.624	0.137
100	267	8.580	8.676	0.096
100	268	8.542	8.565	0.023
100	269	8.384	8.503	0.119
100	270	8.628	8.709	0.081
100	271	8.501	8.480	-0.021
100	273	8.297	8.377	0.080
105	274	8.688	8.868	0.180
105	275	8.570	8.608	0.038
105	276	8.603	8.641	0.038
105	277	8.697	8.741	0.044
105	278	8.420	8.517	0.097
Max		8.705	8.868	0.180
Average		8.523	8.554	0.031
Min		8.226	8.228	-0.038
Std Dev		0.109	0.117	0.045

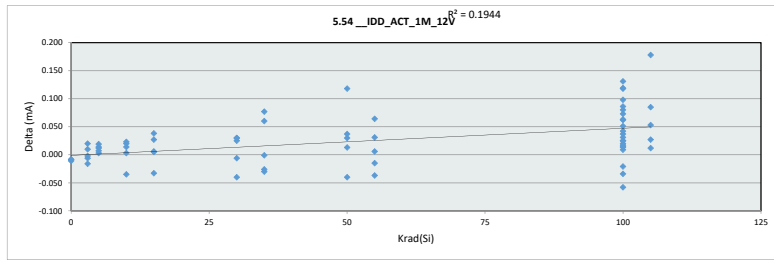


5.53_IDD_ACT											
Test Site											
Tester											
Test Number	12										
Max Limit	12	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.467	8.228	8.459	8.345	8.414	8.407	8.394	8.284	8.423	8.377	8.517
Average	8.544	8.441	8.555	8.509	8.518	8.523	8.509	8.523	8.557	8.592	8.675
Max	8.685	8.640	8.702	8.618	8.593	8.662	8.609	8.658	8.729	8.744	8.868
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

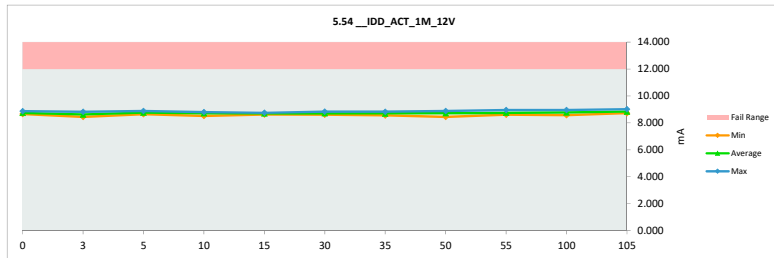


HDR TID Report
TPS7H5002-SP

5.54 IDD_ACT_1M_12V				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		12	12	
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.661	8.653	-0.008
0	190	8.721	8.710	-0.011
0	191	8.879	8.870	-0.009
3	300	8.839	8.823	-0.016
3	305	8.447	8.445	-0.002
3	309	8.694	8.704	0.010
3	311	8.621	8.615	-0.006
3	313	8.533	8.553	0.020
5	319	8.878	8.881	0.003
5	321	8.798	8.817	0.019
5	324	8.658	8.670	0.012
5	327	8.631	8.645	0.014
5	331	8.812	8.819	0.007
10	234	8.782	8.785	0.003
10	235	8.724	8.744	0.020
10	236	8.489	8.512	0.023
10	237	8.796	8.800	0.014
10	238	8.759	8.724	-0.035
15	239	8.701	8.739	0.038
15	240	8.612	8.617	0.005
15	241	8.717	8.744	0.027
15	243	8.704	8.671	-0.033
15	245	8.742	8.748	0.006
30	222	8.880	8.840	-0.040
30	223	8.619	8.649	0.030
30	225	8.679	8.709	0.030
30	226	8.582	8.607	0.025
30	227	8.774	8.768	-0.006
35	228	8.557	8.556	-0.001
35	229	8.770	8.830	0.060
35	231	8.759	8.729	-0.030
35	232	8.655	8.629	-0.026
35	233	8.668	8.745	0.077
50	210	8.621	8.739	0.118
50	211	8.434	8.447	0.013
50	212	8.843	8.880	0.037
50	213	8.675	8.705	0.030
50	214	8.853	8.813	-0.040
55	215	8.788	8.751	-0.037
55	216	8.837	8.822	-0.015
55	217	8.607	8.638	0.031
55	218	8.886	8.950	0.064
55	220	8.606	8.612	0.006
100	247	8.911	8.953	0.042
100	249	8.850	8.864	0.014
100	250	8.744	8.686	-0.058
100	251	8.588	8.597	0.009
100	253	8.867	8.884	0.017
100	255	8.675	8.700	0.025
100	256	8.805	8.924	0.119
100	257	8.672	8.709	0.037
100	258	8.724	8.775	0.051
100	259	8.705	8.768	0.063
100	260	8.589	8.620	0.031
100	261	8.925	8.945	0.020
100	262	8.706	8.804	0.098
100	263	8.761	8.727	-0.034
100	264	8.816	8.902	0.086
100	265	8.830	8.844	0.014
100	266	8.680	8.811	0.131
100	267	8.772	8.852	0.080
100	268	8.704	8.729	0.025
100	269	8.588	8.706	0.118
100	270	8.801	8.874	0.073
100	271	8.701	8.680	-0.021
100	273	8.513	8.575	0.062
105	274	8.840	9.018	0.178
105	275	8.740	8.752	0.012
105	276	8.772	8.799	0.027
105	277	8.851	8.904	0.053
105	278	8.634	8.719	0.085
	Max	8.925	9.018	0.178
	Average	8.719	8.744	0.025
	Min	8.434	8.445	-0.058
	Std Dev	0.113	0.119	0.045

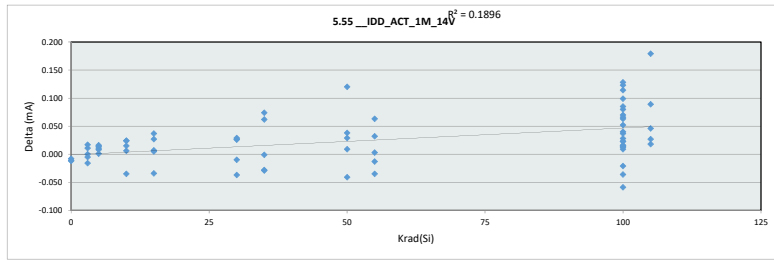


5.54 IDD_ACT											
Test Site											
Tester											
Test Number		12	mA								
Max Limit		12	mA								
Min Limit											
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.653	8.445	8.645	8.512	8.617	8.607	8.556	8.447	8.612	8.575	8.719
Average	8.744	8.628	8.766	8.713	8.704	8.715	8.698	8.717	8.755	8.780	8.838
Max	8.870	8.823	8.881	8.800	8.748	8.840	8.830	8.880	8.950	8.953	9.018
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

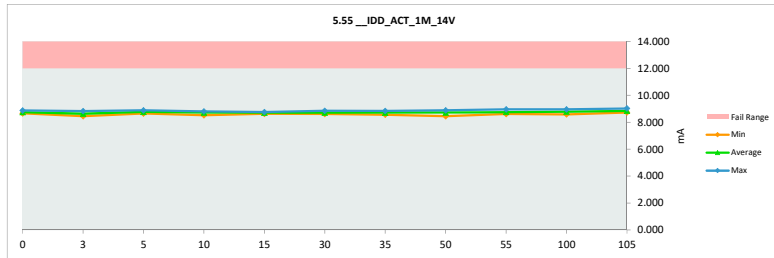


HDR TID Report
TPS7H5002-SP

5.55_IDD_ACT_1M_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	12			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.668	8.660	-0.008
0	190	8.727	8.716	-0.011
0	191	8.885	8.873	-0.012
3	300	8.842	8.826	-0.016
3	305	8.450	8.450	0.000
3	309	8.699	8.710	0.011
3	311	8.626	8.621	-0.005
3	313	8.539	8.556	0.017
5	319	8.882	8.883	0.001
5	321	8.805	8.821	0.016
5	324	8.664	8.675	0.011
5	327	8.637	8.651	0.014
5	331	8.816	8.824	0.008
10	234	8.785	8.791	0.006
10	235	8.728	8.752	0.024
10	236	8.495	8.519	0.024
10	237	8.790	8.805	0.015
10	238	8.764	8.729	-0.035
15	239	8.706	8.743	0.037
15	240	8.617	8.622	0.005
15	241	8.723	8.750	0.027
15	243	8.710	8.676	-0.034
15	245	8.748	8.755	0.007
30	222	8.884	8.847	-0.037
30	223	8.625	8.652	0.027
30	225	8.685	8.714	0.029
30	226	8.587	8.613	0.026
30	227	8.782	8.772	-0.010
35	228	8.560	8.559	-0.001
35	229	8.775	8.837	0.062
35	231	8.763	8.735	-0.028
35	232	8.661	8.632	-0.029
35	233	8.674	8.748	0.074
50	210	8.626	8.746	0.120
50	211	8.439	8.448	0.009
50	212	8.847	8.885	0.038
50	213	8.681	8.710	0.029
50	214	8.855	8.814	-0.041
55	215	8.790	8.755	-0.035
55	216	8.842	8.829	-0.013
55	217	8.611	8.643	0.032
55	218	8.891	8.954	0.063
55	220	8.612	8.615	0.003
100	247	8.917	8.957	0.040
100	249	8.855	8.868	0.013
100	250	8.751	8.692	-0.059
100	251	8.594	8.603	0.009
100	253	8.875	8.888	0.013
100	255	8.679	8.702	0.023
100	256	8.808	8.931	0.123
100	257	8.679	8.716	0.037
100	258	8.729	8.781	0.052
100	259	8.711	8.774	0.063
100	260	8.594	8.622	0.028
100	261	8.931	8.946	0.015
100	262	8.712	8.811	0.099
100	263	8.767	8.731	-0.036
100	264	8.823	8.908	0.085
100	265	8.833	8.849	0.016
100	266	8.687	8.815	0.128
100	267	8.779	8.859	0.080
100	268	8.709	8.732	0.023
100	269	8.595	8.709	0.114
100	270	8.805	8.875	0.070
100	271	8.705	8.684	-0.021
100	273	8.516	8.582	0.066
105	274	8.843	9.022	0.179
105	275	8.742	8.760	0.018
105	276	8.779	8.806	0.027
105	277	8.859	8.905	0.046
105	278	8.640	8.729	0.089
Max		8.931	9.022	0.179
Average		8.724	8.749	0.025
Min		8.439	8.448	-0.059
Std Dev		0.112	0.119	0.045

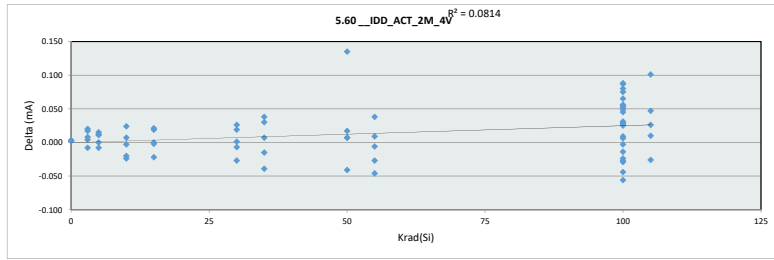


5.55_IDD_ACT											
Test Site											
Tester											
Test Number	12										
Max Limit	12	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.660	8.450	8.651	8.519	8.622	8.613	8.559	8.448	8.615	8.582	8.729
Average	8.750	8.633	8.771	8.719	8.709	8.720	8.702	8.721	8.759	8.784	8.844
Max	8.873	8.826	8.883	8.805	8.755	8.847	8.837	8.885	8.954	8.957	9.022
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

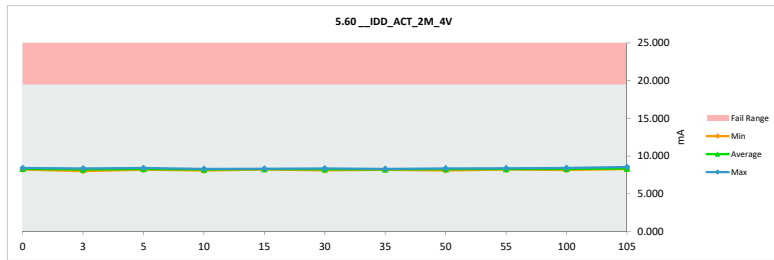


HDR TID Report
TPS7H5002-SP

5.60_IDD_ACT_2M_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	19.5	19.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.245	8.247	0.002
0	190	8.213	8.216	0.003
0	191	8.411	8.414	0.003
3	300	8.340	8.357	0.017
3	305	7.994	7.998	0.004
3	309	8.351	8.359	0.008
3	311	8.167	8.159	-0.008
3	313	8.143	8.163	0.020
5	319	8.400	8.415	0.015
5	321	8.283	8.295	0.012
5	324	8.221	8.221	0.000
5	327	8.172	8.183	0.011
5	331	8.296	8.288	-0.008
10	234	8.274	8.254	-0.020
10	235	8.228	8.235	0.007
10	236	8.054	8.078	0.024
10	237	8.305	8.302	-0.003
10	238	8.227	8.203	-0.024
15	239	8.294	8.313	0.019
15	240	8.208	8.208	0.000
15	241	8.255	8.276	0.021
15	243	8.268	8.246	-0.022
15	245	8.290	8.288	-0.002
30	222	8.388	8.361	-0.027
30	223	8.161	8.180	0.019
30	225	8.212	8.238	0.026
30	226	8.112	8.113	0.001
30	227	8.269	8.262	-0.007
35	228	8.195	8.202	0.007
35	229	8.258	8.296	0.038
35	231	8.280	8.241	-0.039
35	232	8.174	8.159	-0.015
35	233	8.247	8.277	0.030
50	210	8.175	8.310	0.135
50	211	8.065	8.082	0.017
50	212	8.362	8.369	0.007
50	213	8.201	8.208	0.007
50	214	8.319	8.278	-0.041
55	215	8.278	8.232	-0.046
55	216	8.316	8.289	-0.027
55	217	8.211	8.220	0.009
55	218	8.343	8.381	0.038
55	220	8.211	8.205	-0.006
100	247	8.384	8.429	0.045
100	249	8.378	8.375	-0.003
100	250	8.243	8.214	-0.029
100	251	8.227	8.213	-0.014
100	253	8.323	8.329	0.006
100	255	8.194	8.167	-0.027
100	256	8.278	8.366	0.088
100	257	8.191	8.216	0.025
100	258	8.272	8.321	0.049
100	259	8.218	8.249	0.031
100	260	8.115	8.143	0.028
100	261	8.439	8.415	-0.024
100	262	8.263	8.343	0.080
100	263	8.237	8.181	-0.056
100	264	8.265	8.317	0.052
100	265	8.292	8.321	0.029
100	266	8.192	8.279	0.087
100	267	8.326	8.382	0.056
100	268	8.243	8.252	0.009
100	269	8.139	8.214	0.075
100	270	8.315	8.380	0.065
100	271	8.204	8.160	-0.044
100	273	8.091	8.145	0.054
105	274	8.450	8.551	0.101
105	275	8.288	8.262	-0.026
105	276	8.285	8.295	0.010
105	277	8.365	8.391	0.026
105	278	8.217	8.264	0.047
	Max	8.450	8.551	0.135
	Average	8.251	8.265	0.013
	Min	7.994	7.998	-0.056
	Std Dev	0.089	0.095	0.036

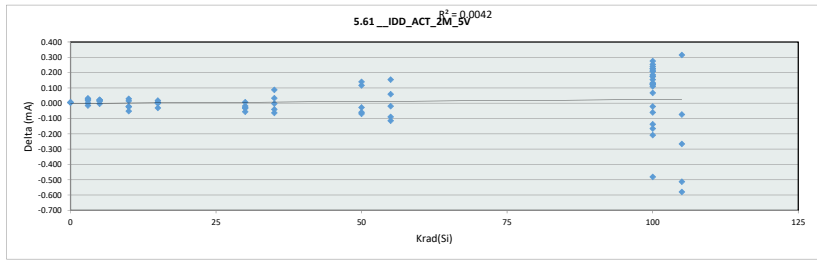


5.60_IDD_ACT_2											
Test Site											
Tester											
Test Number	19.5	mA									
Max Limit	19.5	mA									
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.216	7.998	8.183	8.078	8.208	8.113	8.159	8.082	8.205	8.143	8.262
Average	8.292	8.207	8.280	8.214	8.266	8.231	8.235	8.249	8.265	8.279	8.353
Max	8.414	8.359	8.415	8.302	8.313	8.361	8.296	8.369	8.381	8.429	8.551
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

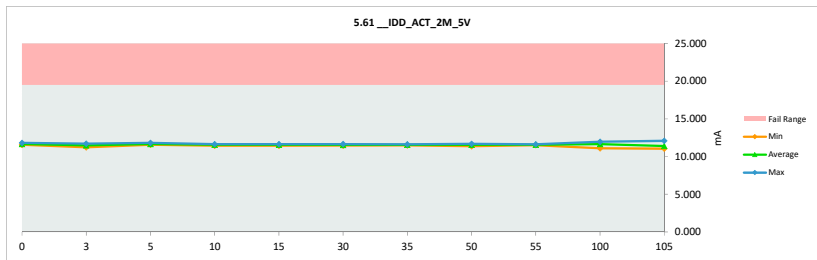


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5.61_IDD_ACT_2M_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	19.5	19.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	11.558	11.563	0.005
0	190	11.613	11.620	0.007
0	191	11.812	11.816	0.004
3	300	11.672	11.704	0.032
3	305	11.237	11.222	-0.015
3	309	11.606	11.623	0.017
3	311	11.476	11.476	0.000
3	313	11.373	11.399	0.026
5	319	11.794	11.815	0.021
5	321	11.643	11.656	0.013
5	324	11.547	11.569	0.022
5	327	11.551	11.568	0.017
5	331	11.680	11.676	-0.004
10	234	11.668	11.616	-0.052
10	235	11.540	11.555	0.015
10	236	11.401	11.430	0.029
10	237	11.661	11.639	-0.022
10	238	11.576	11.552	-0.024
15	239	11.598	11.615	0.017
15	240	11.439	11.438	-0.001
15	241	11.597	11.599	0.002
15	243	11.520	11.489	-0.031
15	245	11.640	11.646	0.006
30	222	11.708	11.652	-0.056
30	223	11.531	11.498	-0.033
30	225	11.523	11.505	-0.018
30	226	11.448	11.455	0.007
30	227	11.609	11.581	-0.028
35	228	11.378	11.465	0.087
35	229	11.596	11.629	0.033
35	231	11.651	11.588	-0.063
35	232	11.506	11.466	-0.040
35	233	11.615	11.612	-0.003
50	210	11.478	11.617	0.139
50	211	11.246	11.363	0.117
50	212	11.720	11.692	-0.028
50	213	11.564	11.505	-0.059
50	214	11.622	11.552	-0.070
55	215	11.631	11.516	-0.115
55	216	11.592	11.503	-0.089
55	217	11.450	11.509	0.059
55	218	11.617	11.598	-0.019
55	220	11.469	11.623	0.154
100	247	11.744	11.955	0.211
100	249	11.764	11.555	-0.209
100	250	11.585	11.759	0.174
100	251	11.450	11.517	0.067
100	253	11.539	11.517	-0.022
100	255	11.536	11.370	-0.166
100	256	11.645	11.871	0.226
100	257	11.540	11.726	0.186
100	258	11.633	11.809	0.176
100	259	11.568	11.087	-0.481
100	260	11.430	11.584	0.154
100	261	11.799	11.931	0.132
100	262	11.626	11.835	0.209
100	263	11.462	11.324	-0.138
100	264	11.605	11.845	0.240
100	265	11.545	11.485	-0.060
100	266	11.515	11.636	0.121
100	267	11.612	11.836	0.224
100	268	11.608	11.732	0.124
100	269	11.387	11.640	0.253
100	270	11.627	11.902	0.275
100	271	11.532	11.642	0.110
100	273	11.295	11.423	0.128
105	274	11.770	12.085	0.315
105	275	11.658	11.145	-0.513
105	276	11.624	11.044	-0.580
105	277	11.670	11.404	-0.266
105	278	11.404	11.330	-0.074
Max		11.812	12.085	0.315
Average		11.568	11.580	0.012
Min		11.237	11.044	-0.580
Std Dev		0.121	0.190	0.160

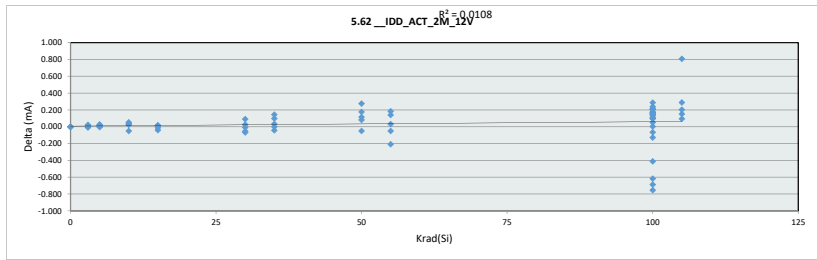


5.61_IDD_ACT											
Test Site											
Tester											
Test Number	19.5										
Max Limit	19.5	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	11.563	11.222	11.568	11.430	11.438	11.455	11.465	11.363	11.503	11.087	11.044
Average	11.666	11.485	11.657	11.558	11.557	11.538	11.552	11.546	11.550	11.651	11.402
Max	11.816	11.704	11.815	11.639	11.646	11.652	11.629	11.692	11.623	11.955	12.085
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

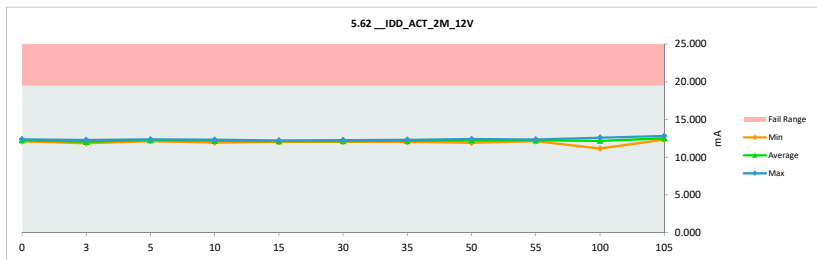


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		5.62 _IDD_ACT_2M_12V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	19.5		19.5	
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	12.095	12.096	0.001
0	190	12.299	12.295	-0.004
0	191	12.377	12.378	0.001
3	300	12.294	12.296	0.002
3	305	11.840	11.834	-0.006
3	309	12.133	12.146	0.013
3	311	12.031	12.024	-0.007
3	313	11.914	11.935	0.021
5	319	12.360	12.368	0.008
5	321	12.327	12.344	0.017
5	324	12.121	12.128	0.007
5	327	12.102	12.129	0.027
5	331	12.359	12.354	-0.005
10	234	12.288	12.322	0.034
10	235	12.165	12.218	0.053
10	236	11.913	11.937	0.024
10	237	12.211	12.240	0.029
10	238	12.294	12.243	-0.051
15	239	12.123	12.136	0.013
15	240	12.033	12.021	-0.012
15	241	12.165	12.178	0.013
15	243	12.120	12.079	-0.041
15	245	12.209	12.225	0.016
30	222	12.330	12.276	-0.054
30	223	12.053	12.044	-0.009
30	225	12.123	12.213	0.090
30	226	12.032	12.058	0.026
30	227	12.256	12.188	-0.068
35	228	11.884	12.027	0.143
35	229	12.217	12.314	0.097
35	231	12.233	12.190	-0.043
35	232	12.091	12.090	-0.001
35	233	12.144	12.177	0.033
50	210	11.987	12.261	0.274
50	211	11.740	11.915	0.175
50	212	12.332	12.412	0.080
50	213	12.095	12.210	0.115
50	214	12.414	12.363	-0.051
55	215	12.310	12.342	0.032
55	216	12.319	12.110	-0.209
55	217	12.088	12.227	0.139
55	218	12.404	12.354	-0.050
55	220	11.988	12.172	0.184
100	247	12.383	12.547	0.164
100	249	12.340	12.505	0.165
100	250	12.296	12.299	0.003
100	251	11.967	11.279	-0.688
100	253	12.175	12.046	-0.129
100	255	12.171	11.554	-0.617
100	256	12.297	12.536	0.239
100	257	12.130	12.239	0.109
100	258	12.213	12.311	0.098
100	259	12.169	12.377	0.208
100	260	12.043	11.978	-0.065
100	261	12.424	12.585	0.161
100	262	12.182	12.397	0.215
100	263	12.103	11.691	-0.412
100	264	12.276	12.481	0.205
100	265	12.225	12.401	0.176
100	266	12.069	12.356	0.287
100	267	12.184	12.317	0.133
100	268	12.102	12.259	0.157
100	269	11.973	12.023	0.050
100	270	12.260	12.406	0.146
100	271	12.135	12.231	0.096
100	273	11.906	11.151	-0.755
105	274	12.261	12.550	0.289
105	275	12.233	12.327	0.094
105	276	12.227	12.378	0.151
105	277	12.259	12.462	0.203
105	278	12.008	12.816	0.808
	Max	12.424	12.816	0.808
	Average	12.167	12.202	0.036
	Min	11.740	11.151	-0.755
	Std Dev	0.148	0.266	0.210

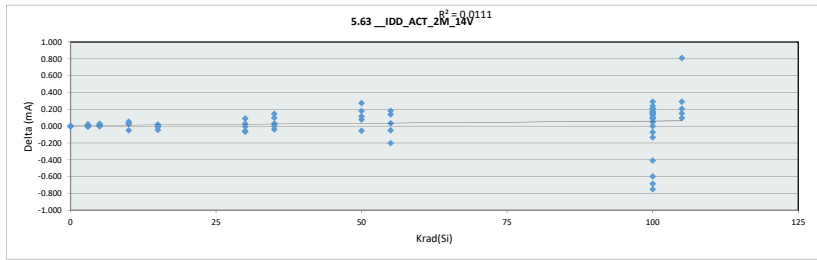


		5.62 _IDD_ACT_2										
Test Site	Tester											
Test Number	Unit	mA	mA									
Max Limit	19.5											
Min Limit												
LL	Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
Min	12.096	11.834	12.128	11.937	12.021	12.044	12.027	11.915	12.110	11.151	12.327	
Average	12.256	12.047	12.265	12.192	12.128	12.156	12.160	12.232	12.241	12.173	12.507	
Max	12.378	12.296	12.368	12.322	12.225	12.276	12.314	12.412	12.354	12.585	12.816	
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	

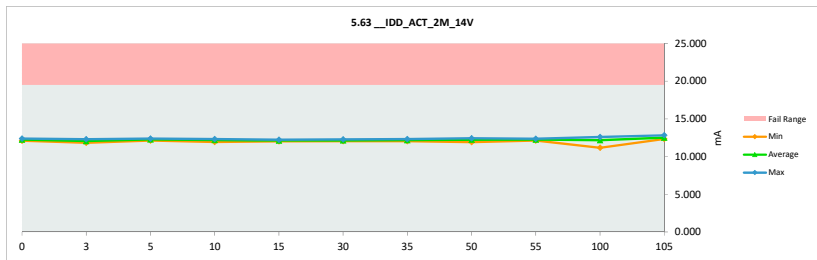


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5.63 _IDD_ACT_2M_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	19.5	19.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	12.097	12.101	0.004
0	190	12.305	12.300	-0.005
0	191	12.381	12.378	-0.003
3	300	12.299	12.300	0.001
3	305	11.844	11.841	-0.003
3	309	12.139	12.149	0.010
3	311	12.035	12.029	-0.006
3	313	11.920	11.943	0.023
5	319	12.367	12.374	0.007
5	321	12.334	12.349	0.015
5	324	12.124	12.132	0.008
5	327	12.105	12.133	0.028
5	331	12.364	12.362	-0.002
10	234	12.294	12.323	0.029
10	235	12.171	12.225	0.054
10	236	11.915	11.945	0.030
10	237	12.214	12.244	0.030
10	238	12.295	12.245	-0.050
15	239	12.126	12.140	0.014
15	240	12.037	12.025	-0.012
15	241	12.171	12.184	0.013
15	243	12.126	12.083	-0.043
15	245	12.214	12.232	0.018
30	222	12.336	12.278	-0.058
30	223	12.056	12.050	-0.006
30	225	12.127	12.218	0.091
30	226	12.035	12.063	0.028
30	227	12.259	12.193	-0.066
35	228	11.887	12.034	0.147
35	229	12.224	12.322	0.098
35	231	12.236	12.197	-0.039
35	232	12.096	12.096	0.000
35	233	12.149	12.180	0.031
50	210	11.991	12.265	0.274
50	211	11.743	11.923	0.180
50	212	12.339	12.417	0.078
50	213	12.099	12.214	0.115
50	214	12.420	12.365	-0.055
55	215	12.314	12.348	0.034
55	216	12.321	12.120	-0.201
55	217	12.093	12.232	0.139
55	218	12.408	12.359	-0.049
55	220	11.994	12.177	0.183
100	247	12.385	12.557	0.172
100	249	12.344	12.509	0.165
100	250	12.303	12.303	0.000
100	251	11.973	11.288	-0.685
100	253	12.182	12.048	-0.134
100	255	12.177	11.581	-0.596
100	256	12.301	12.541	0.240
100	257	12.133	12.240	0.107
100	258	12.219	12.310	0.091
100	259	12.176	12.382	0.206
100	260	12.045	11.973	-0.072
100	261	12.428	12.595	0.167
100	262	12.188	12.396	0.208
100	263	12.107	11.698	-0.409
100	264	12.284	12.487	0.203
100	265	12.228	12.405	0.177
100	266	12.073	12.363	0.290
100	267	12.187	12.322	0.135
100	268	12.108	12.264	0.156
100	269	11.979	12.029	0.050
100	270	12.265	12.409	0.144
100	271	12.141	12.236	0.095
100	273	11.913	11.164	-0.749
105	274	12.264	12.555	0.291
105	275	12.235	12.331	0.096
105	276	12.233	12.385	0.152
105	277	12.262	12.469	0.207
105	278	12.011	12.821	0.810
Max		12.428	12.821	0.810
Average		12.171	12.208	0.037
Min		11.743	11.164	-0.749
Std Dev		0.149	0.265	0.209

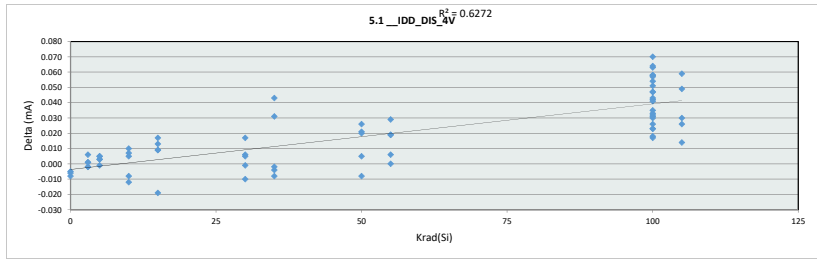


5.63 _IDD_ACT_2M_14V											
Test Site											
Tester											
Test Number											
Max Limit	19.5	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	12.101	11.841	12.132	11.945	12.025	12.050	12.034	11.923	12.120	11.164	12.331
Average	12.260	12.052	12.270	12.196	12.133	12.160	12.166	12.237	12.247	12.178	12.512
Max	12.378	12.300	12.374	12.323	12.232	12.278	12.322	12.417	12.359	12.595	12.821
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

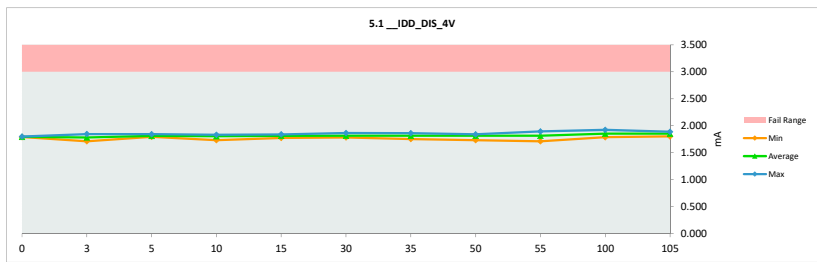


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5.1 IDD_DIS_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	3	3		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.794	1.789	-0.005
0	190	1.805	1.797	-0.008
0	191	1.798	1.792	-0.006
3	300	1.846	1.844	-0.002
3	305	1.712	1.710	-0.002
3	309	1.814	1.815	0.001
3	311	1.763	1.769	0.006
3	313	1.767	1.768	0.001
5	319	1.843	1.842	-0.001
5	321	1.794	1.797	0.003
5	324	1.789	1.794	0.005
5	327	1.796	1.799	0.003
5	331	1.811	1.816	0.005
10	234	1.831	1.823	-0.008
10	235	1.806	1.816	0.010
10	236	1.725	1.732	0.007
10	237	1.826	1.831	0.005
10	238	1.834	1.822	-0.012
15	239	1.829	1.838	0.009
15	240	1.764	1.773	0.009
15	241	1.808	1.825	0.017
15	243	1.803	1.784	-0.019
15	245	1.822	1.835	0.013
30	222	1.874	1.864	-0.010
30	223	1.790	1.789	-0.001
30	225	1.799	1.805	0.006
30	226	1.765	1.782	0.017
30	227	1.821	1.826	0.005
35	228	1.756	1.752	-0.004
35	229	1.782	1.813	0.031
35	231	1.841	1.839	-0.002
35	232	1.810	1.802	-0.008
35	233	1.818	1.861	0.043
50	210	1.797	1.817	0.020
50	211	1.740	1.732	-0.008
50	212	1.813	1.834	0.021
50	213	1.813	1.839	0.026
50	214	1.835	1.840	0.005
55	215	1.820	1.820	0.000
55	216	1.857	1.876	0.019
55	217	1.692	1.711	0.019
55	218	1.864	1.853	-0.011
55	220	1.763	1.769	0.006
100	247	1.814	1.849	0.035
100	249	1.802	1.860	0.058
100	250	1.801	1.819	0.018
100	251	1.776	1.807	0.031
100	253	1.854	1.884	0.030
100	255	1.761	1.787	0.026
100	256	1.837	1.907	0.070
100	257	1.822	1.854	0.032
100	258	1.812	1.854	0.042
100	259	1.836	1.883	0.047
100	260	1.753	1.811	0.058
100	261	1.861	1.894	0.033
100	262	1.783	1.847	0.064
100	263	1.833	1.850	0.017
100	264	1.788	1.831	0.043
100	265	1.850	1.873	0.023
100	266	1.798	1.852	0.054
100	267	1.828	1.869	0.041
100	268	1.801	1.852	0.051
100	269	1.806	1.869	0.063
100	270	1.865	1.922	0.057
100	271	1.792	1.815	0.023
100	273	1.753	1.800	0.047
105	274	1.829	1.888	0.059
105	275	1.787	1.801	0.014
105	276	1.807	1.837	0.030
105	277	1.855	1.881	0.026
105	278	1.803	1.852	0.049
Max		1.874	1.922	0.070
Average		1.805	1.824	0.020
Min		1.692	1.710	-0.019
Std Dev		0.036	0.044	0.022

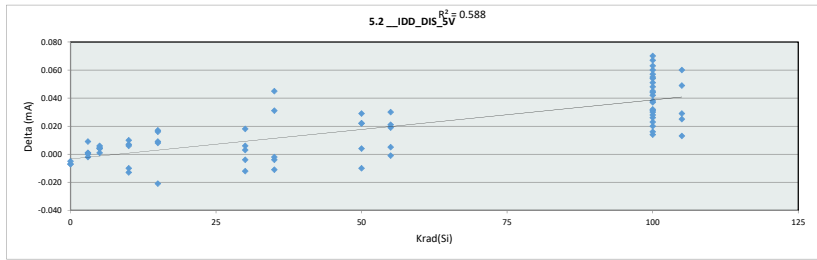


5.1 IDD DIS 4V											
Test Site											
Tester											
Test Number											
Max Limit	3	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.789	1.710	1.794	1.732	1.773	1.782	1.752	1.732	1.711	1.787	1.801
Average	1.793	1.781	1.810	1.805	1.811	1.813	1.813	1.812	1.814	1.852	1.852
Max	1.797	1.844	1.842	1.831	1.838	1.864	1.861	1.840	1.893	1.922	1.888
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

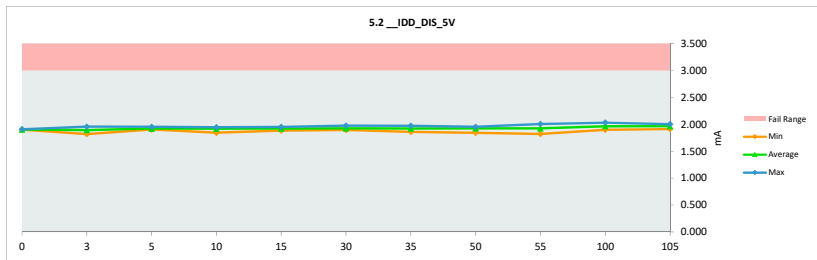


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5.2 IDD_DIS_5V				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		3	3	
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.906	1.901	-0.005
0	190	1.913	1.906	-0.007
0	191	1.910	1.903	-0.007
3	300	1.955	1.955	0.000
3	305	1.819	1.817	-0.002
3	309	1.926	1.927	0.001
3	311	1.871	1.880	0.009
3	313	1.877	1.878	0.001
5	319	1.951	1.952	0.001
5	321	1.904	1.908	0.004
5	324	1.898	1.903	0.005
5	327	1.903	1.907	0.004
5	331	1.919	1.925	0.006
10	234	1.944	1.934	-0.010
10	235	1.918	1.928	0.010
10	236	1.835	1.842	0.007
10	237	1.938	1.944	0.006
10	238	1.946	1.933	-0.013
15	239	1.941	1.950	0.009
15	240	1.875	1.883	0.008
15	241	1.918	1.935	0.017
15	243	1.913	1.892	-0.021
15	245	1.928	1.944	0.016
30	222	1.986	1.974	-0.012
30	223	1.899	1.895	-0.004
30	225	1.908	1.914	0.006
30	226	1.875	1.893	0.018
30	227	1.934	1.937	0.003
35	228	1.862	1.858	-0.004
35	229	1.890	1.921	0.031
35	231	1.951	1.949	-0.002
35	232	1.921	1.910	-0.011
35	233	1.925	1.970	0.045
50	210	1.906	1.928	0.022
50	211	1.851	1.841	-0.010
50	212	1.923	1.945	0.022
50	213	1.923	1.952	0.029
50	214	1.947	1.951	0.004
55	215	1.929	1.928	-0.001
55	216	1.964	1.985	0.021
55	217	1.801	1.820	0.019
55	218	1.973	2.003	0.030
55	220	1.874	1.879	0.005
100	247	1.924	1.961	0.037
100	249	1.912	1.972	0.060
100	250	1.911	1.927	0.016
100	251	1.887	1.918	0.031
100	253	1.962	1.992	0.030
100	255	1.872	1.898	0.026
100	256	1.950	2.020	0.070
100	257	1.931	1.959	0.028
100	258	1.920	1.962	0.042
100	259	1.949	1.997	0.048
100	260	1.862	1.919	0.057
100	261	1.974	2.006	0.032
100	262	1.892	1.959	0.067
100	263	1.942	1.956	0.014
100	264	1.900	1.944	0.044
100	265	1.963	1.986	0.023
100	266	1.909	1.963	0.054
100	267	1.938	1.976	0.038
100	268	1.913	1.964	0.051
100	269	1.915	1.978	0.063
100	270	1.975	2.030	0.055
100	271	1.903	1.923	0.020
100	273	1.864	1.909	0.045
105	274	1.942	2.002	0.060
105	275	1.898	1.911	0.013
105	276	1.919	1.948	0.029
105	277	1.968	1.993	0.025
105	278	1.914	1.963	0.049
Max		1.986	2.030	0.070
Average		1.915	1.934	0.019
Min		1.801	1.817	-0.021
Std Dev		0.037	0.045	0.023

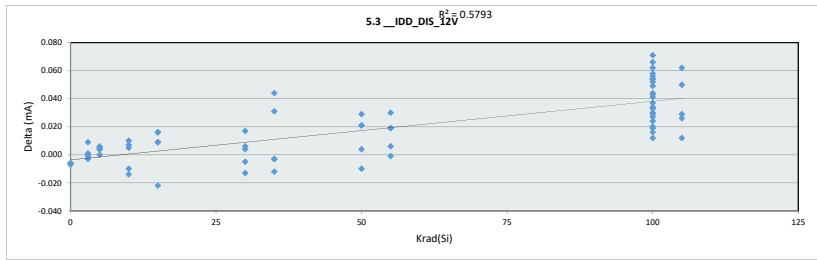


5.2 IDD DIS 5V											
Test Site											
Tester											
Test Number											
Max Limit		3									
Min Limit			3								
Unit				mA							
LL											
Min	1.901	1.817	1.903	1.842	1.883	1.893	1.858	1.841	1.820	1.898	1.911
Average	1.903	1.891	1.919	1.916	1.921	1.923	1.922	1.923	1.923	1.962	1.963
Max	1.906	1.955	1.952	1.944	1.950	1.974	1.970	1.952	2.003	2.030	2.002
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

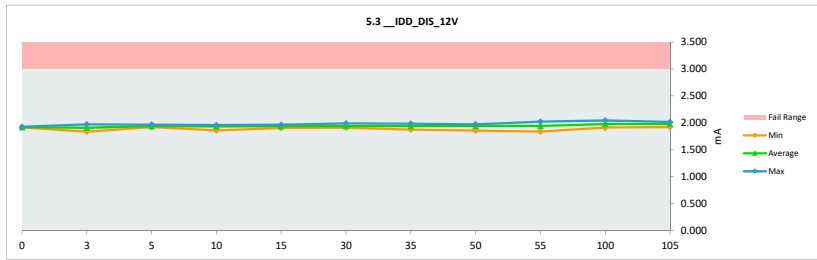


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5.3 __IDD_DIS_12V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	3			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.923	1.917	-0.006
0	190	1.935	1.928	-0.007
0	191	1.926	1.920	-0.006
3	300	1.972	1.971	-0.001
3	305	1.839	1.836	-0.003
3	309	1.942	1.942	0.000
3	311	1.889	1.898	0.009
3	313	1.894	1.895	0.001
5	319	1.967	1.967	0.000
5	321	1.928	1.932	0.004
5	324	1.915	1.921	0.006
5	327	1.920	1.924	0.004
5	331	1.943	1.948	0.005
10	234	1.964	1.954	-0.010
10	235	1.939	1.949	0.010
10	236	1.851	1.858	0.007
10	237	1.985	1.960	-0.025
10	238	1.967	1.953	-0.014
15	239	1.957	1.966	0.009
15	240	1.894	1.903	0.009
15	241	1.932	1.948	0.016
15	243	1.933	1.911	-0.022
15	245	1.946	1.962	0.016
30	222	2.003	1.990	-0.013
30	223	1.915	1.910	-0.005
30	225	1.927	1.933	0.006
30	226	1.895	1.912	0.017
30	227	1.952	1.956	0.004
35	228	1.876	1.873	-0.003
35	229	1.910	1.941	0.031
35	231	1.970	1.967	-0.003
35	232	1.940	1.928	-0.012
35	233	1.941	1.985	0.044
50	210	1.922	1.943	0.021
50	211	1.866	1.856	-0.010
50	212	1.944	1.965	0.021
50	213	1.940	1.969	0.029
50	214	1.969	1.973	0.004
55	215	1.947	1.946	-0.001
55	216	1.983	2.002	0.019
55	217	1.821	1.840	0.019
55	218	1.993	2.023	0.030
55	220	1.889	1.895	0.006
100	247	1.945	1.979	0.034
100	249	1.930	1.988	0.058
100	250	1.928	1.944	0.016
100	251	1.902	1.931	0.029
100	253	1.979	2.009	0.030
100	255	1.889	1.913	0.024
100	256	1.972	2.043	0.071
100	257	1.947	1.974	0.027
100	258	1.938	1.979	0.041
100	259	1.965	2.014	0.049
100	260	1.883	1.937	0.054
100	261	1.994	2.027	0.033
100	262	1.909	1.975	0.066
100	263	1.958	1.970	0.012
100	264	1.923	1.966	0.043
100	265	1.981	2.001	0.020
100	266	1.926	1.980	0.054
100	267	1.955	1.992	0.037
100	268	1.928	1.980	0.052
100	269	1.934	1.996	0.062
100	270	1.990	2.046	0.056
100	271	1.922	1.941	0.019
100	273	1.884	1.928	0.044
105	274	1.955	2.017	0.062
105	275	1.912	1.924	0.012
105	276	1.935	1.964	0.029
105	277	1.982	2.008	0.026
105	278	1.933	1.983	0.050
Max		2.003	2.046	0.071
Average		1.933	1.952	0.019
Min		1.821	1.836	-0.022
Std Dev		0.037	0.045	0.023

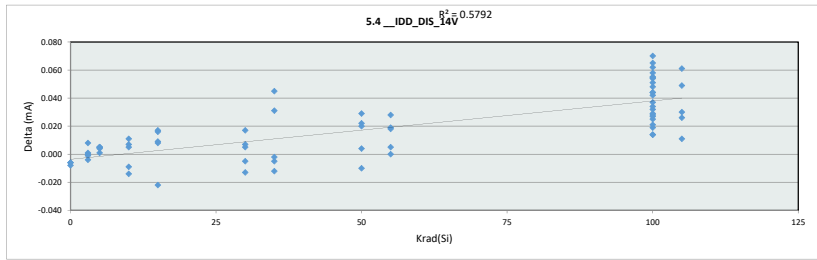


5.3 __IDD_DIS_12V											
Test Site											
Tester											
Test Number											
Max Limit	3										
Min Limit											
LL											
Min	1.917	1.836	1.921	1.858	1.903	1.910	1.873	1.856	1.840	1.913	1.924
Average	1.922	1.908	1.938	1.935	1.938	1.940	1.939	1.941	1.941	1.979	1.979
Max	1.928	1.971	1.967	1.960	1.966	1.990	1.985	1.973	2.023	2.046	2.017
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

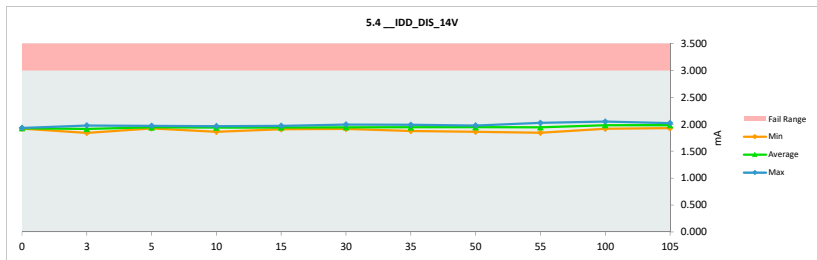


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5.4 IDD_DIS_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	3			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.927	1.921	-0.006
0	190	1.940	1.932	-0.008
0	191	1.930	1.924	-0.006
3	300	1.977	1.976	-0.001
3	305	1.844	1.840	-0.004
3	309	1.946	1.946	0.000
3	311	1.893	1.901	0.008
3	313	1.898	1.899	0.001
5	319	1.971	1.972	0.001
5	321	1.932	1.936	0.004
5	324	1.919	1.924	0.005
5	327	1.924	1.929	0.005
5	331	1.947	1.952	0.005
10	234	1.969	1.960	-0.009
10	235	1.942	1.953	0.011
10	236	1.955	1.862	0.007
10	237	1.959	1.964	0.005
10	238	1.971	1.957	-0.014
15	239	1.961	1.970	0.009
15	240	1.899	1.907	0.008
15	241	1.936	1.952	0.016
15	243	1.938	1.916	-0.022
15	245	1.949	1.966	0.017
30	222	2.007	1.994	-0.013
30	223	1.919	1.914	-0.005
30	225	1.931	1.938	0.007
30	226	1.899	1.916	0.017
30	227	1.955	1.960	0.005
35	228	1.880	1.875	-0.005
35	229	1.914	1.945	0.031
35	231	1.973	1.971	-0.002
35	232	1.944	1.932	-0.012
35	233	1.945	1.990	0.045
50	210	1.927	1.947	0.020
50	211	1.870	1.860	-0.010
50	212	1.947	1.969	0.022
50	213	1.945	1.974	0.029
50	214	1.973	1.977	0.004
55	215	1.951	1.951	0.000
55	216	1.986	2.005	0.019
55	217	1.826	1.844	0.018
55	218	1.998	2.026	0.028
55	220	1.894	1.899	0.005
100	247	1.950	1.984	0.034
100	249	1.934	1.992	0.058
100	250	1.933	1.947	0.014
100	251	1.907	1.935	0.028
100	253	1.984	2.013	0.029
100	255	1.893	1.918	0.025
100	256	1.976	2.046	0.070
100	257	1.951	1.978	0.027
100	258	1.941	1.983	0.042
100	259	1.970	2.018	0.048
100	260	1.887	1.942	0.055
100	261	1.998	2.030	0.032
100	262	1.914	1.979	0.065
100	263	1.962	1.976	0.014
100	264	1.927	1.971	0.044
100	265	1.985	2.006	0.021
100	266	1.931	1.985	0.054
100	267	1.959	1.996	0.037
100	268	1.932	1.983	0.051
100	269	1.938	2.000	0.062
100	270	1.995	2.050	0.055
100	271	1.926	1.945	0.019
100	273	1.888	1.932	0.044
105	274	1.960	2.021	0.061
105	275	1.917	1.928	0.011
105	276	1.939	1.969	0.030
105	277	1.986	2.012	0.026
105	278	1.938	1.987	0.049
Max		2.007	2.050	0.070
Average		1.937	1.956	0.019
Min		1.826	1.840	-0.022
Std Dev		0.037	0.045	0.022

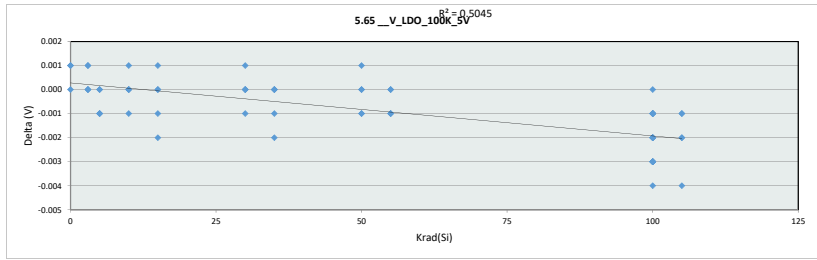


5.4 IDD DIS 14V											
Test Site											
Tester											
Test Number											
Max Limit	3	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	1.921	1.840	1.924	1.862	1.907	1.914	1.875	1.860	1.844	1.918	1.928
Average	1.926	1.912	1.943	1.939	1.942	1.944	1.943	1.945	1.945	1.983	1.983
Max	1.932	1.976	1.972	1.964	1.970	1.994	1.990	1.977	2.026	2.050	2.021
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

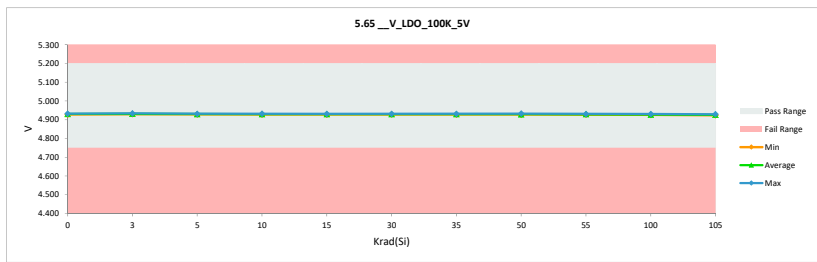


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5.65_V_LDO_100K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	4.930	4.931	0.001
0	190	4.931	4.932	0.001
0	191	4.927	4.927	0.000
3	300	4.928	4.929	0.001
3	305	4.933	4.934	0.001
3	309	4.930	4.930	0.000
3	311	4.930	4.930	0.000
3	313	4.932	4.932	0.000
5	319	4.928	4.928	0.000
5	321	4.929	4.928	-0.001
5	324	4.932	4.931	-0.001
5	327	4.931	4.931	0.000
5	331	4.930	4.929	-0.001
10	234	4.929	4.929	0.000
10	235	4.929	4.929	0.000
10	236	4.932	4.932	0.000
10	237	4.928	4.927	-0.001
10	238	4.929	4.930	0.001
15	239	4.929	4.927	-0.002
15	240	4.931	4.930	-0.001
15	241	4.929	4.929	0.000
15	243	4.930	4.931	0.001
15	245	4.930	4.930	0.000
30	222	4.927	4.928	0.001
30	223	4.931	4.931	0.000
30	225	4.930	4.930	0.000
30	226	4.931	4.930	-0.001
30	227	4.929	4.929	0.000
35	228	4.931	4.931	0.000
35	229	4.928	4.927	-0.001
35	231	4.929	4.929	0.000
35	232	4.930	4.930	0.000
35	233	4.931	4.929	-0.002
50	210	4.928	4.928	0.000
50	211	4.932	4.933	0.001
50	212	4.927	4.926	-0.001
50	213	4.929	4.928	-0.001
50	214	4.929	4.929	0.000
55	215	4.930	4.930	0.000
55	216	4.928	4.928	0.000
55	217	4.931	4.930	-0.001
55	218	4.928	4.927	-0.001
55	220	4.931	4.930	-0.001
100	247	4.927	4.926	-0.001
100	249	4.928	4.927	-0.001
100	250	4.930	4.930	0.000
100	251	4.931	4.929	-0.002
100	253	4.927	4.926	-0.001
100	255	4.931	4.928	-0.003
100	256	4.928	4.925	-0.003
100	257	4.931	4.929	-0.002
100	258	4.930	4.928	-0.002
100	259	4.929	4.926	-0.003
100	260	4.931	4.929	-0.002
100	261	4.927	4.925	-0.002
100	262	4.930	4.927	-0.003
100	263	4.929	4.928	-0.001
100	264	4.928	4.925	-0.003
100	265	4.927	4.926	-0.001
100	266	4.929	4.926	-0.003
100	267	4.929	4.926	-0.003
100	268	4.928	4.927	-0.001
100	269	4.932	4.928	-0.004
100	270	4.928	4.925	-0.003
100	271	4.929	4.928	-0.001
100	273	4.932	4.930	-0.002
105	274	4.927	4.923	-0.004
105	275	4.929	4.927	-0.002
105	276	4.927	4.926	-0.001
105	277	4.925	4.924	-0.001
105	278	4.931	4.929	-0.002
	Max	4.933	4.934	0.001
	Average	4.929	4.928	-0.001
	Min	4.925	4.923	-0.004
	Std Dev	0.002	0.002	0.001

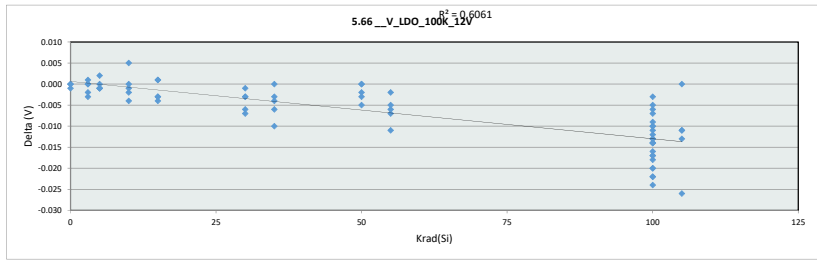


5.65_V_LDO_100K_5V											
Test Site											
Tester											
Test Number	5.2	V									
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.927	4.929	4.928	4.927	4.927	4.928	4.927	4.926	4.927	4.925	4.923
Average	4.930	4.931	4.929	4.929	4.929	4.930	4.929	4.929	4.929	4.927	4.926
Max	4.932	4.934	4.931	4.932	4.931	4.931	4.931	4.931	4.933	4.930	4.929
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

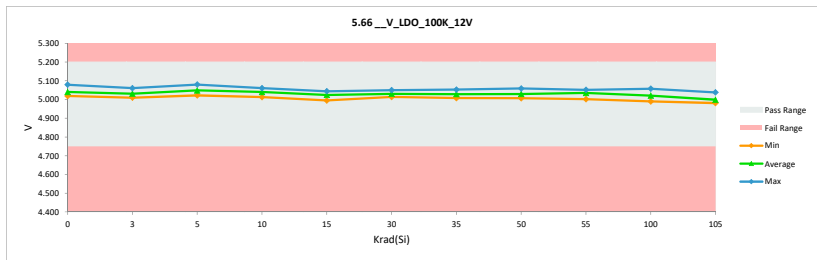


HDR TID Report
TPS7H5002-SP

5.66_V_LDO_100K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	5.020	5.019	-0.001
0	190	5.079	5.079	0.000
0	191	5.023	5.023	0.000
3	300	5.030	5.028	-0.002
3	305	5.064	5.061	-0.003
3	309	5.010	5.010	0.000
3	311	5.029	5.030	0.001
3	313	5.027	5.027	0.000
5	319	5.023	5.022	-0.001
5	321	5.075	5.074	-0.001
5	324	5.035	5.035	0.000
5	327	5.031	5.033	0.002
5	331	5.081	5.080	-0.001
10	234	5.056	5.061	0.005
10	235	5.058	5.058	0.000
10	236	5.014	5.013	-0.001
10	237	5.021	5.019	-0.002
10	238	5.054	5.050	-0.004
15	239	5.010	5.007	-0.003
15	240	5.043	5.040	-0.003
15	241	4.999	4.995	-0.004
15	243	5.043	5.044	0.001
15	245	5.036	5.037	0.001
30	222	5.021	5.018	-0.003
30	223	5.021	5.014	-0.007
30	225	5.051	5.050	-0.001
30	226	5.043	5.037	-0.006
30	227	5.036	5.033	-0.003
35	228	5.011	5.008	-0.003
35	229	5.053	5.053	0.000
35	231	5.039	5.035	-0.004
35	232	5.045	5.035	-0.010
35	233	5.019	5.013	-0.006
50	210	5.012	5.012	0.000
50	211	5.009	5.007	-0.002
50	212	5.048	5.048	0.000
50	213	5.024	5.021	-0.003
50	214	5.064	5.059	-0.005
55	215	5.046	5.041	-0.005
55	216	5.042	5.035	-0.007
55	217	5.052	5.046	-0.006
55	218	5.054	5.052	-0.002
55	220	5.013	5.002	-0.011
100	247	5.057	5.035	-0.022
100	249	5.031	5.014	-0.017
100	250	5.036	5.016	-0.020
100	251	5.012	4.990	-0.022
100	253	5.024	5.018	-0.006
100	255	5.025	5.014	-0.011
100	256	5.068	5.058	-0.010
100	257	5.024	5.008	-0.016
100	258	5.037	5.025	-0.012
100	259	5.023	5.010	-0.013
100	260	5.056	5.032	-0.024
100	261	5.048	5.045	-0.003
100	262	5.032	5.022	-0.010
100	263	5.019	5.002	-0.017
100	264	5.071	5.057	-0.014
100	265	5.025	5.011	-0.014
100	266	5.030	5.017	-0.013
100	267	5.031	5.013	-0.018
100	268	5.004	4.999	-0.005
100	269	5.045	5.036	-0.009
100	270	5.012	4.998	-0.014
100	271	5.040	5.033	-0.007
100	273	5.056	5.036	-0.020
105	274	4.994	4.983	-0.011
105	275	5.007	4.981	-0.026
105	276	5.008	4.997	-0.011
105	277	4.996	4.996	0.000
105	278	5.051	5.038	-0.013
Max		5.081	5.080	0.005
Average		5.034	5.027	-0.007
Min		4.994	4.981	-0.026
Std Dev		0.020	0.022	0.007

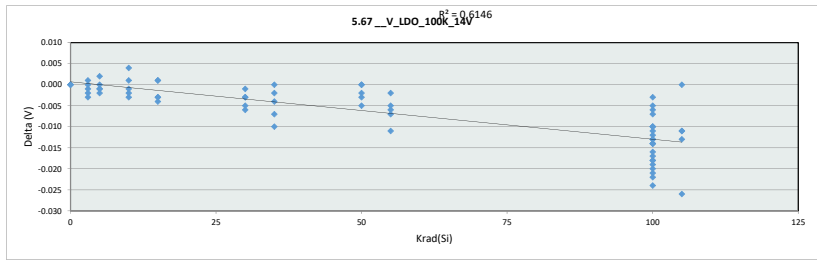


5.66_V_LDO_100K_12V											
Test Site											
Tester											
Test Number	5.2	V									
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.019	5.010	5.022	5.013	4.995	5.014	5.008	5.007	5.002	4.990	4.981
Average	5.040	5.031	5.049	5.040	5.025	5.030	5.029	5.029	5.035	5.021	4.999
Max	5.079	5.061	5.080	5.061	5.044	5.050	5.053	5.059	5.052	5.058	5.038
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

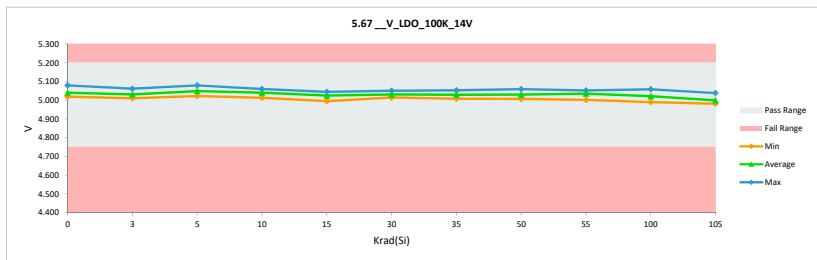


HDR TID Report
TPS7H5002-SP

5.67 __V_LDO_100K_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	5.019	5.019	0.000
0	190	5.079	5.079	0.000
0	191	5.023	5.023	0.000
3	300	5.030	5.028	-0.002
3	305	5.064	5.061	-0.003
3	309	5.010	5.010	0.000
3	311	5.029	5.030	0.001
3	313	5.027	5.026	-0.001
5	319	5.023	5.022	-0.001
5	321	5.075	5.074	-0.001
5	324	5.035	5.035	0.000
5	327	5.031	5.033	0.002
5	331	5.081	5.079	-0.002
10	234	5.056	5.060	0.004
10	235	5.057	5.058	0.001
10	236	5.014	5.013	-0.001
10	237	5.021	5.019	-0.002
10	238	5.053	5.050	-0.003
15	239	5.010	5.007	-0.003
15	240	5.043	5.040	-0.003
15	241	4.999	4.995	-0.004
15	243	5.043	5.044	0.001
15	245	5.036	5.037	0.001
30	222	5.021	5.018	-0.003
30	223	5.020	5.014	-0.006
30	225	5.051	5.050	-0.001
30	226	5.042	5.037	-0.005
30	227	5.036	5.033	-0.003
35	228	5.010	5.008	-0.002
35	229	5.053	5.053	0.000
35	231	5.039	5.035	-0.004
35	232	5.045	5.035	-0.010
35	233	5.019	5.012	-0.007
50	210	5.012	5.012	0.000
50	211	5.009	5.007	-0.002
50	212	5.048	5.048	0.000
50	213	5.024	5.021	-0.003
50	214	5.064	5.059	-0.005
55	215	5.046	5.041	-0.005
55	216	5.042	5.035	-0.007
55	217	5.052	5.046	-0.006
55	218	5.054	5.052	-0.002
55	220	5.013	5.002	-0.011
100	247	5.056	5.035	-0.021
100	249	5.031	5.014	-0.017
100	250	5.035	5.016	-0.019
100	251	5.012	4.990	-0.022
100	253	5.023	5.018	-0.005
100	255	5.025	5.014	-0.011
100	256	5.068	5.058	-0.010
100	257	5.024	5.008	-0.016
100	258	5.037	5.025	-0.012
100	259	5.023	5.010	-0.013
100	260	5.056	5.032	-0.024
100	261	5.048	5.045	-0.003
100	262	5.032	5.022	-0.010
100	263	5.019	5.001	-0.018
100	264	5.071	5.057	-0.014
100	265	5.025	5.011	-0.014
100	266	5.030	5.017	-0.013
100	267	5.031	5.013	-0.018
100	268	5.005	4.999	-0.006
100	269	5.045	5.035	-0.010
100	270	5.012	4.998	-0.014
100	271	5.040	5.033	-0.007
100	273	5.055	5.035	-0.020
105	274	4.994	4.983	-0.011
105	275	5.007	4.981	-0.026
105	276	5.008	4.997	-0.011
105	277	4.996	4.996	0.000
105	278	5.051	5.038	-0.013
Max		5.081	5.079	0.004
Average		5.034	5.027	-0.007
Min		4.994	4.981	-0.026
Std Dev		0.020	0.022	0.007

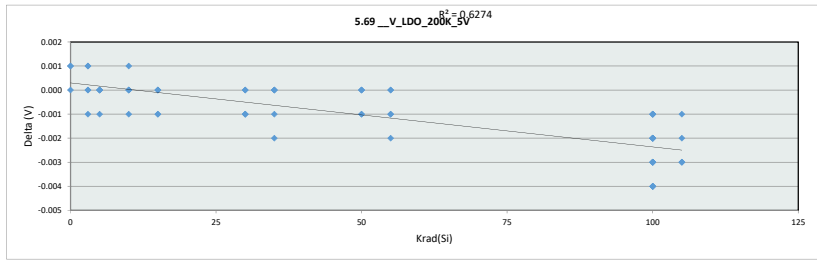


5.67 V LDO 100K 14V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.019	5.010	5.022	5.013	4.995	5.014	5.008	5.007	5.002	4.990	4.981
Average	5.040	5.031	5.049	5.040	5.025	5.030	5.029	5.029	5.035	5.021	4.999
Max	5.079	5.061	5.079	5.060	5.044	5.050	5.053	5.059	5.052	5.058	5.038
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

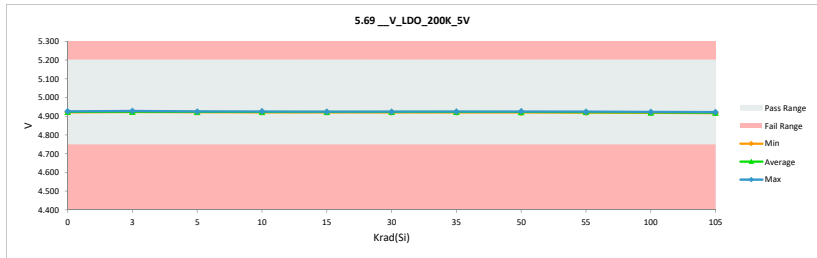


HDR TID Report
TPS7H5002-SP

5.69_V_LDO_200K_5V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			5.2	5.2
Min Limit			4.75	4.75
Krad(Si)	Serial #	PRE	POST	Delta
0	189	4.924	4.924	0.000
0	190	4.925	4.926	0.001
0	191	4.920	4.921	0.001
3	300	4.922	4.923	0.001
3	305	4.927	4.928	0.001
3	309	4.924	4.924	0.000
3	311	4.924	4.924	0.000
3	313	4.926	4.925	-0.001
5	319	4.921	4.921	0.000
5	321	4.922	4.922	0.000
5	324	4.925	4.925	0.000
5	327	4.925	4.925	0.000
5	331	4.924	4.923	-0.001
10	234	4.923	4.922	-0.001
10	235	4.923	4.923	0.000
10	236	4.926	4.926	0.000
10	237	4.921	4.921	0.000
10	238	4.923	4.924	0.001
15	239	4.922	4.921	-0.001
15	240	4.924	4.924	0.000
15	241	4.923	4.922	-0.001
15	243	4.924	4.924	0.000
15	245	4.924	4.924	0.000
30	222	4.921	4.921	0.000
30	223	4.925	4.924	-0.001
30	225	4.924	4.924	0.000
30	226	4.925	4.924	-0.001
30	227	4.923	4.922	-0.001
35	228	4.925	4.925	0.000
35	229	4.922	4.921	-0.001
35	231	4.923	4.923	0.000
35	232	4.924	4.924	0.000
35	233	4.925	4.923	-0.002
50	210	4.922	4.922	0.000
50	211	4.926	4.926	0.000
50	212	4.920	4.919	-0.001
50	213	4.923	4.922	-0.001
50	214	4.922	4.922	0.000
55	215	4.924	4.924	0.000
55	216	4.922	4.922	0.000
55	217	4.925	4.924	-0.001
55	218	4.922	4.920	-0.002
55	220	4.925	4.924	-0.001
100	247	4.920	4.919	-0.001
100	249	4.921	4.920	-0.001
100	250	4.924	4.923	-0.001
100	251	4.924	4.923	-0.001
100	253	4.921	4.920	-0.001
100	255	4.924	4.922	-0.002
100	256	4.922	4.918	-0.004
100	257	4.924	4.922	-0.002
100	258	4.924	4.921	-0.003
100	259	4.923	4.920	-0.003
100	260	4.925	4.923	-0.002
100	261	4.921	4.918	-0.003
100	262	4.924	4.920	-0.004
100	263	4.923	4.921	-0.002
100	264	4.922	4.918	-0.004
100	265	4.921	4.919	-0.002
100	266	4.923	4.919	-0.004
100	267	4.923	4.920	-0.003
100	268	4.922	4.920	-0.002
100	269	4.925	4.921	-0.004
100	270	4.921	4.918	-0.003
100	271	4.923	4.921	-0.002
100	273	4.926	4.923	-0.003
105	274	4.920	4.917	-0.003
105	275	4.923	4.920	-0.003
105	276	4.921	4.920	-0.001
105	277	4.919	4.917	-0.002
105	278	4.925	4.922	-0.003
	Max	4.927	4.928	0.001
	Average	4.923	4.922	-0.001
	Min	4.919	4.917	-0.004
	Std Dev	0.002	0.002	0.001

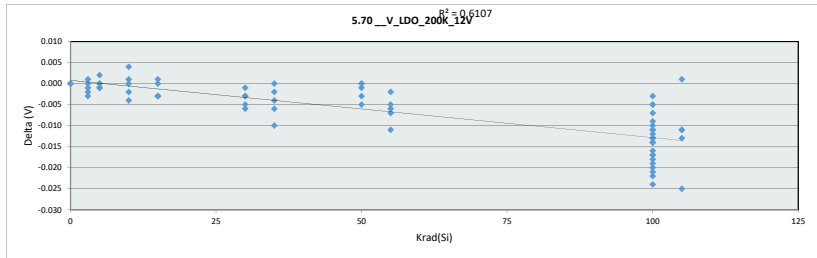


5.69_V_LDO_200K_5V											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit			5.2	5.2							
Min Limit			4.75	4.75							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.921	4.923	4.921	4.921	4.921	4.921	4.921	4.919	4.920	4.918	4.917
Average	4.924	4.925	4.923	4.923	4.923	4.923	4.923	4.922	4.923	4.920	4.919
Max	4.926	4.928	4.925	4.926	4.924	4.924	4.925	4.926	4.924	4.923	4.922
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

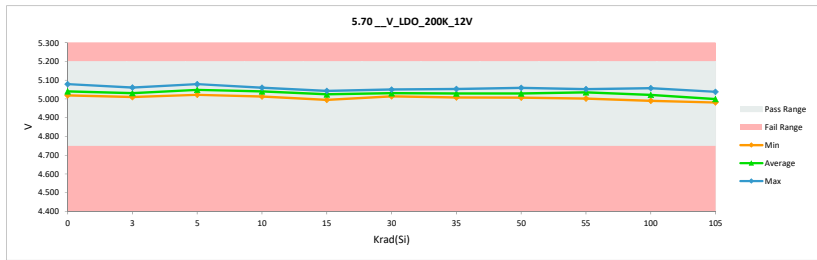


HDR TID Report
TPS7H5002-SP

5.70 __V_LDO_200K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	5.019	5.019	0.000
0	190	5.079	5.079	0.000
0	191	5.023	5.023	0.000
3	300	5.030	5.028	-0.002
3	305	5.064	5.061	-0.003
3	309	5.010	5.010	0.000
3	311	5.029	5.030	0.001
3	313	5.027	5.026	-0.001
5	319	5.022	5.022	0.000
5	321	5.075	5.074	-0.001
5	324	5.035	5.035	0.000
5	327	5.031	5.033	0.002
5	331	5.080	5.079	-0.001
10	234	5.056	5.060	0.004
10	235	5.057	5.058	0.001
10	236	5.013	5.013	0.000
10	237	5.021	5.019	-0.002
10	238	5.054	5.050	-0.004
15	239	5.010	5.007	-0.003
15	240	5.043	5.040	-0.003
15	241	4.998	4.995	-0.003
15	243	5.043	5.043	0.000
15	245	5.036	5.037	0.001
30	222	5.021	5.018	-0.003
30	223	5.020	5.014	-0.006
30	225	5.051	5.050	-0.001
30	226	5.042	5.037	-0.005
30	227	5.036	5.033	-0.003
35	228	5.010	5.008	-0.002
35	229	5.053	5.053	0.000
35	231	5.039	5.035	-0.004
35	232	5.045	5.035	-0.010
35	233	5.019	5.013	-0.006
50	210	5.012	5.012	0.000
50	211	5.008	5.007	-0.001
50	212	5.048	5.048	0.000
50	213	5.024	5.021	-0.003
50	214	5.064	5.059	-0.005
55	215	5.045	5.040	-0.005
55	216	5.042	5.035	-0.007
55	217	5.052	5.046	-0.006
55	218	5.054	5.052	-0.002
55	220	5.013	5.002	-0.011
100	247	5.056	5.035	-0.021
100	249	5.031	5.014	-0.017
100	250	5.035	5.016	-0.019
100	251	5.012	4.990	-0.022
100	253	5.023	5.018	-0.005
100	255	5.025	5.014	-0.011
100	256	5.068	5.057	-0.011
100	257	5.024	5.008	-0.016
100	258	5.037	5.025	-0.012
100	259	5.023	5.010	-0.013
100	260	5.056	5.032	-0.024
100	261	5.048	5.045	-0.003
100	262	5.032	5.022	-0.010
100	263	5.019	5.002	-0.017
100	264	5.071	5.057	-0.014
100	265	5.025	5.011	-0.014
100	266	5.030	5.017	-0.013
100	267	5.031	5.013	-0.018
100	268	5.004	4.999	-0.005
100	269	5.044	5.035	-0.009
100	270	5.012	4.998	-0.014
100	271	5.040	5.033	-0.007
100	273	5.055	5.035	-0.020
105	274	4.994	4.983	-0.011
105	275	5.006	4.981	-0.025
105	276	5.008	4.997	-0.011
105	277	4.996	4.997	0.001
105	278	5.051	5.038	-0.013
	Max	5.080	5.079	0.004
	Average	5.034	5.027	-0.007
	Min	4.994	4.981	-0.025
	Std Dev	0.020	0.022	0.007

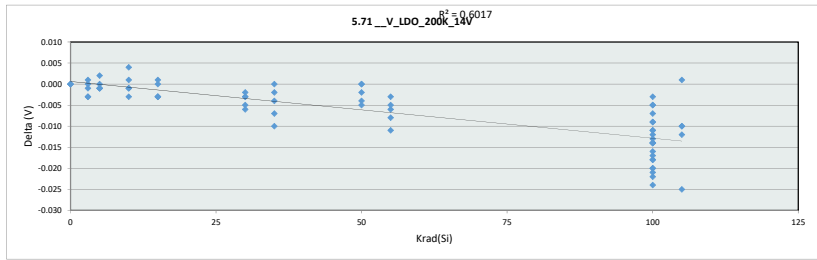


5.70 V LDO 200K											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.019	5.010	5.022	5.013	4.995	5.014	5.008	5.007	5.002	4.990	4.981
Average	5.040	5.031	5.049	5.040	5.024	5.030	5.029	5.029	5.035	5.021	4.999
Max	5.079	5.061	5.079	5.060	5.043	5.050	5.053	5.059	5.052	5.057	5.038
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

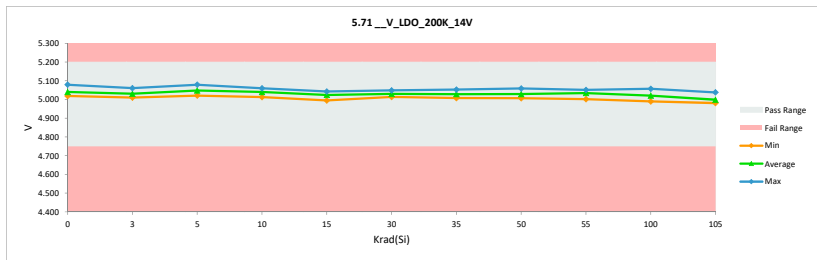


HDR TID Report
TPS7H5002-SP

5.71_V_LDO_200K_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	5.019	5.019	0.000
0	190	5.079	5.079	0.000
0	191	5.023	5.023	0.000
3	300	5.030	5.027	-0.003
3	305	5.064	5.061	-0.003
3	309	5.010	5.010	0.000
3	311	5.029	5.030	0.001
3	313	5.027	5.026	-0.001
5	319	5.022	5.021	-0.001
5	321	5.075	5.074	-0.001
5	324	5.035	5.035	0.000
5	327	5.031	5.033	0.002
5	331	5.080	5.079	-0.001
10	234	5.056	5.060	0.004
10	235	5.057	5.058	0.001
10	236	5.014	5.013	-0.001
10	237	5.020	5.019	-0.001
10	238	5.053	5.050	-0.003
15	239	5.010	5.007	-0.003
15	240	5.043	5.040	-0.003
15	241	4.998	4.995	-0.003
15	243	5.043	5.043	0.000
15	245	5.036	5.037	0.001
30	222	5.021	5.018	-0.003
30	223	5.020	5.014	-0.006
30	225	5.051	5.049	-0.002
30	226	5.042	5.037	-0.005
30	227	5.036	5.033	-0.003
35	228	5.010	5.008	-0.002
35	229	5.053	5.053	0.000
35	231	5.039	5.035	-0.004
35	232	5.045	5.035	-0.010
35	233	5.019	5.012	-0.007
50	210	5.012	5.012	0.000
50	211	5.009	5.007	-0.002
50	212	5.048	5.048	0.000
50	213	5.024	5.020	-0.004
50	214	5.064	5.059	-0.005
55	215	5.046	5.040	-0.006
55	216	5.042	5.034	-0.008
55	217	5.051	5.046	-0.005
55	218	5.054	5.051	-0.003
55	220	5.013	5.002	-0.011
100	247	5.056	5.035	-0.021
100	249	5.031	5.014	-0.017
100	250	5.035	5.015	-0.020
100	251	5.012	4.990	-0.022
100	253	5.023	5.018	-0.005
100	255	5.025	5.014	-0.011
100	256	5.068	5.057	-0.011
100	257	5.024	5.008	-0.016
100	258	5.037	5.025	-0.012
100	259	5.023	5.009	-0.014
100	260	5.056	5.032	-0.024
100	261	5.048	5.045	-0.003
100	262	5.031	5.022	-0.009
100	263	5.019	5.001	-0.018
100	264	5.071	5.057	-0.014
100	265	5.025	5.011	-0.014
100	266	5.030	5.017	-0.013
100	267	5.031	5.013	-0.018
100	268	5.004	4.999	-0.005
100	269	5.044	5.035	-0.009
100	270	5.012	4.998	-0.014
100	271	5.040	5.033	-0.007
100	273	5.056	5.036	-0.020
105	274	4.993	4.983	-0.010
105	275	5.006	4.981	-0.025
105	276	5.007	4.997	-0.010
105	277	4.995	4.996	0.001
105	278	5.050	5.038	-0.012
Max		5.080	5.079	0.004
Average		5.034	5.027	-0.007
Min		4.993	4.981	-0.025
Std Dev		0.021	0.022	0.007

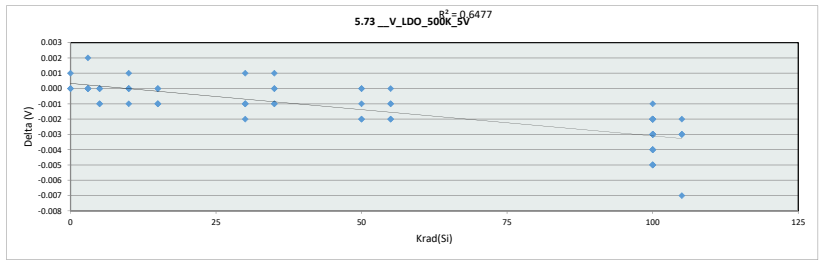


5.71_V_LDO_200K_14V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.019	5.010	5.021	5.013	4.995	5.014	5.008	5.007	5.002	4.990	4.981
Average	5.040	5.031	5.048	5.040	5.024	5.030	5.029	5.029	5.035	5.021	4.999
Max	5.079	5.061	5.079	5.060	5.043	5.049	5.053	5.059	5.051	5.057	5.038
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

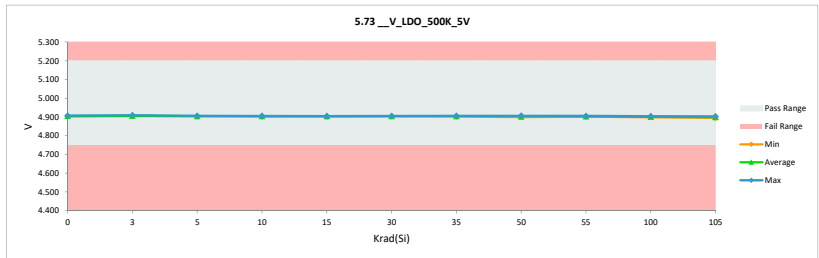


HDR TID Report
TPS7H5002-SP

5.73_V_LDO_500K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	4.905	4.905	0.000
0	190	4.906	4.907	0.001
0	191	4.902	4.902	0.000
3	300	4.903	4.905	0.002
3	305	4.909	4.909	0.000
3	309	4.905	4.905	0.000
3	311	4.905	4.905	0.000
3	313	4.906	4.906	0.000
5	319	4.903	4.903	0.000
5	321	4.903	4.903	0.000
5	324	4.907	4.906	-0.001
5	327	4.906	4.906	0.000
5	331	4.905	4.904	-0.001
10	234	4.904	4.903	-0.001
10	235	4.904	4.904	0.000
10	236	4.906	4.906	0.000
10	237	4.902	4.902	0.000
10	238	4.904	4.905	0.001
15	239	4.903	4.902	-0.001
15	240	4.905	4.904	-0.001
15	241	4.905	4.904	-0.001
15	243	4.905	4.905	0.000
15	245	4.905	4.905	0.000
30	222	4.902	4.903	0.001
30	223	4.906	4.905	-0.001
30	225	4.905	4.904	-0.001
30	226	4.906	4.904	-0.002
30	227	4.904	4.903	-0.001
35	228	4.905	4.906	0.001
35	229	4.903	4.902	-0.001
35	231	4.904	4.904	0.000
35	232	4.905	4.905	0.000
35	233	4.906	4.905	-0.001
50	210	4.903	4.903	0.000
50	211	4.907	4.907	0.000
50	212	4.901	4.899	-0.002
50	213	4.904	4.902	-0.002
50	214	4.904	4.903	-0.001
55	215	4.905	4.905	0.000
55	216	4.904	4.903	-0.001
55	217	4.906	4.904	-0.002
55	218	4.903	4.901	-0.002
55	220	4.905	4.904	-0.001
100	247	4.901	4.899	-0.002
100	249	4.902	4.900	-0.002
100	250	4.905	4.904	-0.001
100	251	4.905	4.903	-0.002
100	253	4.902	4.900	-0.002
100	255	4.905	4.902	-0.003
100	256	4.903	4.899	-0.004
100	257	4.906	4.903	-0.003
100	258	4.905	4.902	-0.003
100	259	4.903	4.900	-0.003
100	260	4.906	4.903	-0.003
100	261	4.901	4.898	-0.003
100	262	4.905	4.901	-0.004
100	263	4.904	4.901	-0.003
100	264	4.903	4.898	-0.005
100	265	4.902	4.900	-0.002
100	266	4.904	4.899	-0.005
100	267	4.904	4.900	-0.004
100	268	4.903	4.901	-0.002
100	269	4.906	4.901	-0.005
100	270	4.903	4.898	-0.005
100	271	4.904	4.902	-0.002
100	273	4.907	4.903	-0.004
105	274	4.902	4.895	-0.007
105	275	4.903	4.901	-0.002
105	276	4.902	4.899	-0.003
105	277	4.900	4.897	-0.003
105	278	4.906	4.903	-0.003
Max		4.909	4.909	0.002
Average		4.904	4.903	-0.002
Min		4.900	4.895	-0.007
Std Dev		0.002	0.003	0.002

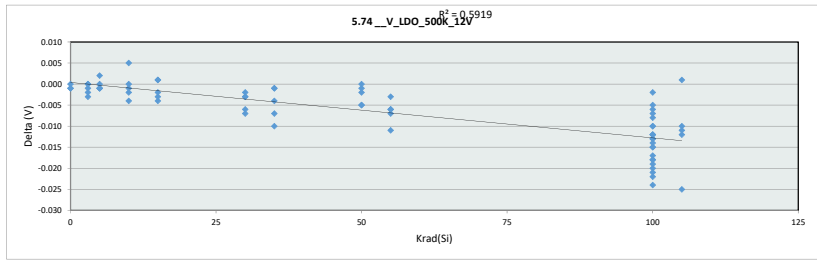


5.73_V_LDO_500K_5V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.902	4.905	4.903	4.902	4.902	4.903	4.902	4.899	4.901	4.898	4.895
Average	4.905	4.906	4.904	4.904	4.904	4.904	4.904	4.903	4.903	4.901	4.899
Max	4.907	4.909	4.906	4.906	4.905	4.905	4.906	4.907	4.905	4.904	4.903
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

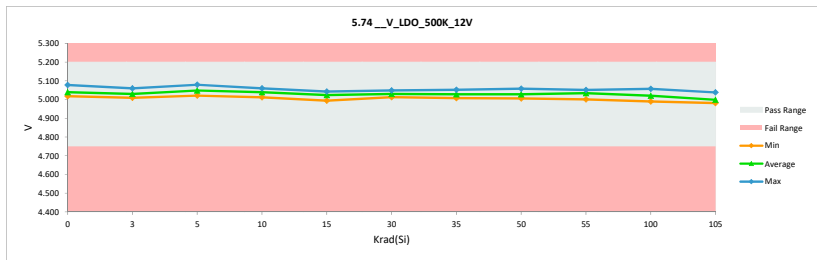


HDR TID Report
TPS7H5002-SP

5.74 _V_LDO_500K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	5.018	5.018	0.000
0	190	5.079	5.078	-0.001
0	191	5.023	5.022	-0.001
3	300	5.029	5.027	-0.002
3	305	5.063	5.060	-0.003
3	309	5.010	5.009	-0.001
3	311	5.029	5.029	0.000
3	313	5.026	5.026	0.000
5	319	5.022	5.021	-0.001
5	321	5.074	5.073	-0.001
5	324	5.035	5.035	0.000
5	327	5.031	5.033	0.002
5	331	5.080	5.079	-0.001
10	234	5.055	5.060	0.005
10	235	5.057	5.057	0.000
10	236	5.013	5.012	-0.001
10	237	5.020	5.018	-0.002
10	238	5.053	5.049	-0.004
15	239	5.010	5.007	-0.003
15	240	5.042	5.040	-0.002
15	241	4.998	4.994	-0.004
15	243	5.042	5.043	0.001
15	245	5.035	5.036	0.001
30	222	5.020	5.017	-0.003
30	223	5.020	5.013	-0.007
30	225	5.051	5.049	-0.002
30	226	5.042	5.036	-0.006
30	227	5.036	5.033	-0.003
35	228	5.009	5.008	-0.001
35	229	5.053	5.052	-0.001
35	231	5.039	5.035	-0.004
35	232	5.044	5.034	-0.010
35	233	5.019	5.012	-0.007
50	210	5.012	5.011	-0.001
50	211	5.008	5.006	-0.002
50	212	5.048	5.048	0.000
50	213	5.024	5.019	-0.005
50	214	5.063	5.058	-0.005
55	215	5.045	5.039	-0.006
55	216	5.041	5.034	-0.007
55	217	5.051	5.045	-0.006
55	218	5.054	5.051	-0.003
55	220	5.012	5.001	-0.011
100	247	5.056	5.034	-0.022
100	249	5.031	5.014	-0.017
100	250	5.034	5.015	-0.019
100	251	5.011	4.990	-0.021
100	253	5.023	5.018	-0.005
100	255	5.025	5.013	-0.012
100	256	5.067	5.057	-0.010
100	257	5.023	5.008	-0.015
100	258	5.036	5.024	-0.012
100	259	5.022	5.009	-0.013
100	260	5.056	5.032	-0.024
100	261	5.047	5.045	-0.002
100	262	5.031	5.021	-0.010
100	263	5.019	5.001	-0.018
100	264	5.070	5.056	-0.014
100	265	5.025	5.010	-0.015
100	266	5.029	5.017	-0.012
100	267	5.031	5.013	-0.018
100	268	5.004	4.998	-0.006
100	269	5.043	5.035	-0.008
100	270	5.011	4.998	-0.013
100	271	5.039	5.032	-0.007
100	273	5.055	5.035	-0.020
105	274	4.993	4.983	-0.010
105	275	5.006	4.981	-0.025
105	276	5.007	4.996	-0.011
105	277	4.995	4.996	0.001
105	278	5.050	5.038	-0.012
	Max	5.080	5.079	0.005
	Average	5.033	5.027	-0.007
	Min	4.993	4.981	-0.025
	Std Dev	0.020	0.022	0.007

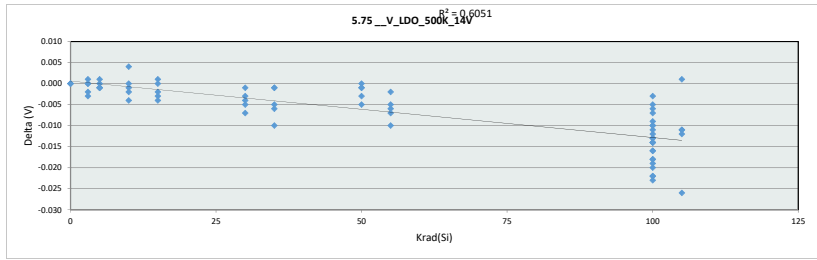


5.74 V LDO 500K											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.018	5.009	5.021	5.012	4.994	5.013	5.008	5.006	5.001	4.990	4.981
Average	5.039	5.030	5.048	5.039	5.024	5.030	5.028	5.028	5.034	5.021	4.999
Max	5.078	5.060	5.079	5.060	5.043	5.049	5.052	5.058	5.051	5.057	5.038
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

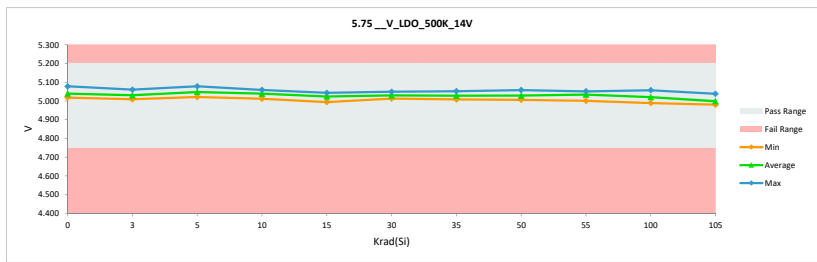


HDR TID Report
TPS7H5002-SP

5.75_V_LDO_500K_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	5.018	5.018	0.000
0	190	5.078	5.078	0.000
0	191	5.022	5.022	0.000
3	300	5.029	5.027	-0.002
3	305	5.063	5.060	-0.003
3	309	5.009	5.009	0.000
3	311	5.028	5.029	0.001
3	313	5.026	5.026	0.000
5	319	5.022	5.021	-0.001
5	321	5.074	5.073	-0.001
5	324	5.035	5.035	0.000
5	327	5.031	5.032	0.001
5	331	5.079	5.078	-0.001
10	234	5.055	5.059	0.004
10	235	5.057	5.057	0.000
10	236	5.013	5.012	-0.001
10	237	5.020	5.018	-0.002
10	238	5.053	5.049	-0.004
15	239	5.009	5.006	-0.003
15	240	5.042	5.040	-0.002
15	241	4.998	4.994	-0.004
15	243	5.042	5.043	0.001
15	245	5.036	5.036	0.000
30	222	5.020	5.017	-0.003
30	223	5.020	5.013	-0.007
30	225	5.050	5.049	-0.001
30	226	5.041	5.036	-0.005
30	227	5.036	5.032	-0.004
35	228	5.009	5.008	-0.001
35	229	5.053	5.052	-0.001
35	231	5.039	5.034	-0.005
35	232	5.044	5.034	-0.010
35	233	5.018	5.012	-0.006
50	210	5.011	5.011	0.000
50	211	5.007	5.006	-0.001
50	212	5.048	5.047	-0.001
50	213	5.023	5.020	-0.003
50	214	5.063	5.058	-0.005
55	215	5.045	5.039	-0.006
55	216	5.041	5.034	-0.007
55	217	5.051	5.046	-0.005
55	218	5.053	5.051	-0.002
55	220	5.011	5.001	-0.010
100	247	5.056	5.034	-0.022
100	249	5.030	5.014	-0.016
100	250	5.034	5.015	-0.019
100	251	5.011	4.989	-0.022
100	253	5.023	5.018	-0.005
100	255	5.024	5.013	-0.011
100	256	5.067	5.057	-0.010
100	257	5.023	5.007	-0.016
100	258	5.036	5.024	-0.012
100	259	5.022	5.009	-0.013
100	260	5.055	5.032	-0.023
100	261	5.047	5.044	-0.003
100	262	5.030	5.021	-0.009
100	263	5.019	5.001	-0.018
100	264	5.070	5.056	-0.014
100	265	5.024	5.010	-0.014
100	266	5.029	5.016	-0.013
100	267	5.030	5.012	-0.018
100	268	5.004	4.998	-0.006
100	269	5.044	5.034	-0.010
100	270	5.011	4.997	-0.014
100	271	5.039	5.032	-0.007
100	273	5.055	5.035	-0.020
105	274	4.993	4.982	-0.011
105	275	5.006	4.980	-0.026
105	276	5.007	4.996	-0.011
105	277	4.995	4.996	0.001
105	278	5.050	5.038	-0.012
Max		5.079	5.078	0.004
Average		5.033	5.027	-0.007
Min		4.993	4.980	-0.026
Std Dev		0.020	0.022	0.007

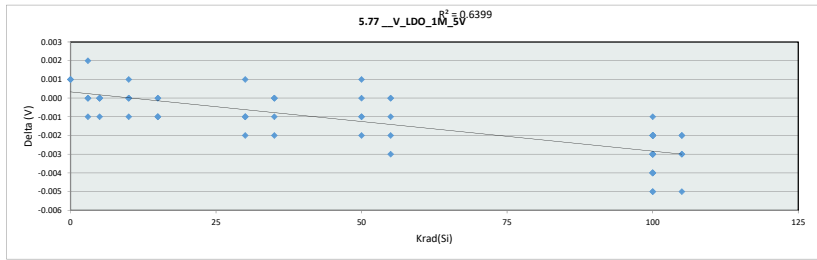


5.75_V_LDO_500K_14V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.018	5.009	5.021	5.012	4.994	5.013	5.008	5.006	5.001	4.989	4.980
Average	5.039	5.030	5.048	5.039	5.024	5.029	5.028	5.028	5.034	5.020	4.998
Max	5.078	5.060	5.078	5.059	5.043	5.049	5.052	5.058	5.051	5.057	5.038
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

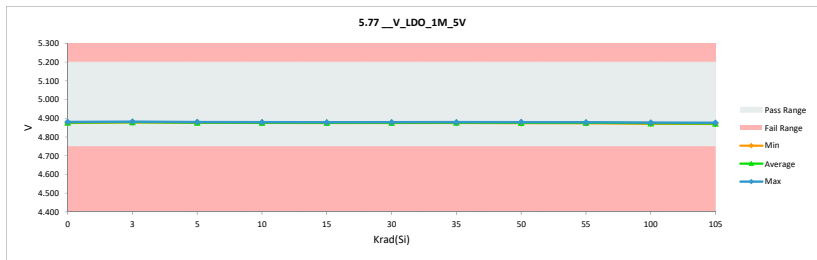


HDR TID Report
TPS7H5002-SP

		5.77_V_LDO_1M_5V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit		5.2	5.2	
Min Limit		4.75	4.75	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	4.877	4.878	0.001
0	190	4.879	4.880	0.001
0	191	4.874	4.875	0.001
3	300	4.877	4.879	0.002
3	305	4.882	4.882	0.000
3	309	4.878	4.878	0.000
3	311	4.878	4.878	0.000
3	313	4.880	4.879	-0.001
5	319	4.876	4.876	0.000
5	321	4.876	4.876	0.000
5	324	4.880	4.880	0.000
5	327	4.879	4.879	0.000
5	331	4.878	4.877	-0.001
10	234	4.877	4.877	0.000
10	235	4.877	4.877	0.000
10	236	4.879	4.879	0.000
10	237	4.875	4.875	0.000
10	238	4.877	4.878	0.001
15	239	4.876	4.875	-0.001
15	240	4.878	4.877	-0.001
15	241	4.878	4.877	-0.001
15	243	4.878	4.878	0.000
15	245	4.878	4.878	0.000
30	222	4.875	4.876	0.001
30	223	4.880	4.879	-0.001
30	225	4.878	4.877	-0.001
30	226	4.879	4.877	-0.002
30	227	4.877	4.876	-0.001
35	228	4.879	4.879	0.000
35	229	4.876	4.875	-0.001
35	231	4.878	4.878	0.000
35	232	4.878	4.878	0.000
35	233	4.880	4.878	-0.002
50	210	4.876	4.877	0.001
50	211	4.880	4.880	0.000
50	212	4.874	4.873	-0.001
50	213	4.877	4.875	-0.002
50	214	4.877	4.876	-0.001
55	215	4.878	4.878	0.000
55	216	4.877	4.877	0.000
55	217	4.879	4.877	-0.002
55	218	4.877	4.874	-0.003
55	220	4.878	4.877	-0.001
100	247	4.874	4.872	-0.002
100	249	4.875	4.873	-0.002
100	250	4.878	4.877	-0.001
100	251	4.878	4.876	-0.002
100	253	4.875	4.873	-0.002
100	255	4.877	4.875	-0.002
100	256	4.876	4.871	-0.005
100	257	4.879	4.877	-0.002
100	258	4.878	4.875	-0.003
100	259	4.876	4.873	-0.003
100	260	4.879	4.876	-0.003
100	261	4.874	4.871	-0.003
100	262	4.878	4.874	-0.004
100	263	4.877	4.875	-0.002
100	264	4.876	4.872	-0.004
100	265	4.874	4.872	-0.002
100	266	4.876	4.872	-0.004
100	267	4.877	4.873	-0.004
100	268	4.876	4.873	-0.003
100	269	4.880	4.875	-0.005
100	270	4.876	4.872	-0.004
100	271	4.877	4.875	-0.002
100	273	4.880	4.877	-0.003
105	274	4.875	4.870	-0.005
105	275	4.875	4.873	-0.002
105	276	4.875	4.873	-0.002
105	277	4.874	4.871	-0.003
105	278	4.879	4.876	-0.003
	Max	4.882	4.882	0.002
	Average	4.877	4.876	-0.001
	Min	4.874	4.870	-0.005
	Std Dev	0.002	0.003	0.002

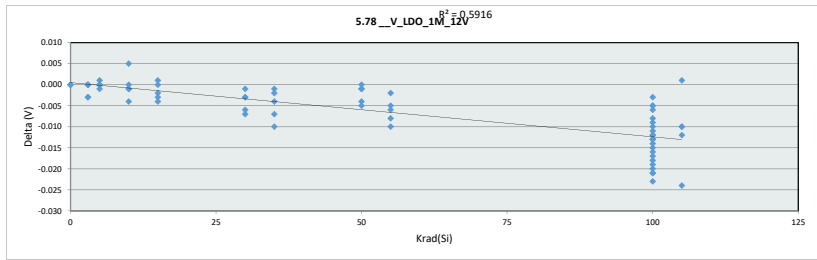


		5.77_V_LDO_1M										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit		5.2	5.2									
Min Limit		4.75	4.75									
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min		4.875	4.878	4.876	4.875	4.875	4.876	4.875	4.873	4.874	4.871	4.870
Average		4.878	4.879	4.878	4.877	4.877	4.877	4.878	4.876	4.877	4.874	4.873
Max		4.880	4.882	4.880	4.879	4.878	4.879	4.879	4.880	4.878	4.877	4.876
UL		5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

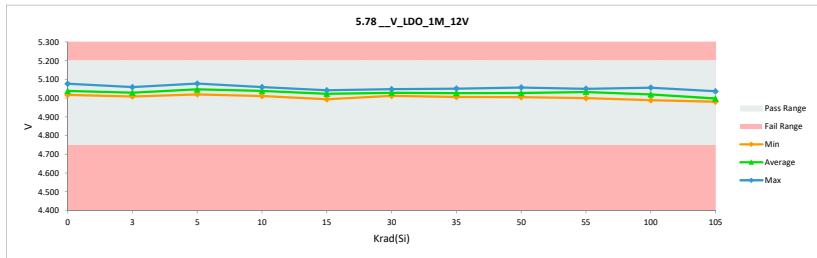


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		5.78_V_LDO_1M_12V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	Min Limit	5.2	4.75	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	5.017	5.017	0.000
0	190	5.077	5.077	0.000
0	191	5.021	5.021	0.000
3	300	5.029	5.026	-0.003
3	305	5.062	5.059	-0.003
3	309	5.009	5.009	0.000
3	311	5.028	5.028	0.000
3	313	5.025	5.025	0.000
5	319	5.021	5.020	-0.001
5	321	5.073	5.073	0.000
5	324	5.034	5.034	0.000
5	327	5.030	5.031	0.001
5	331	5.078	5.078	0.000
10	234	5.054	5.059	0.005
10	235	5.056	5.056	0.000
10	236	5.012	5.011	-0.001
10	237	5.019	5.018	-0.001
10	238	5.052	5.048	-0.004
15	239	5.009	5.005	-0.004
15	240	5.041	5.039	-0.002
15	241	4.997	4.994	-0.003
15	243	5.041	5.042	0.001
15	245	5.035	5.035	0.000
30	222	5.019	5.016	-0.003
30	223	5.019	5.012	-0.007
30	225	5.049	5.048	-0.001
30	226	5.041	5.035	-0.006
30	227	5.035	5.032	-0.003
35	228	5.008	5.006	-0.002
35	229	5.052	5.051	-0.001
35	231	5.038	5.034	-0.004
35	232	5.043	5.033	-0.010
35	233	5.018	5.011	-0.007
50	210	5.011	5.010	-0.001
50	211	5.006	5.005	-0.001
50	212	5.047	5.047	0.000
50	213	5.022	5.018	-0.004
50	214	5.062	5.057	-0.005
55	215	5.043	5.038	-0.005
55	216	5.040	5.032	-0.008
55	217	5.051	5.045	-0.006
55	218	5.052	5.050	-0.002
55	220	5.010	5.000	-0.010
100	247	5.055	5.034	-0.021
100	249	5.030	5.014	-0.016
100	250	5.033	5.014	-0.019
100	251	5.010	4.989	-0.021
100	253	5.022	5.017	-0.005
100	255	5.024	5.013	-0.011
100	256	5.066	5.056	-0.010
100	257	5.022	5.007	-0.015
100	258	5.035	5.023	-0.012
100	259	5.022	5.009	-0.013
100	260	5.054	5.031	-0.023
100	261	5.047	5.044	-0.003
100	262	5.029	5.020	-0.009
100	263	5.018	5.001	-0.017
100	264	5.069	5.056	-0.013
100	265	5.024	5.010	-0.014
100	266	5.028	5.016	-0.012
100	267	5.030	5.012	-0.018
100	268	5.003	4.998	-0.005
100	269	5.042	5.034	-0.008
100	270	5.010	4.997	-0.013
100	271	5.037	5.031	-0.006
100	273	5.054	5.034	-0.020
105	274	4.992	4.982	-0.010
105	275	5.005	4.981	-0.024
105	276	5.006	4.996	-0.010
105	277	4.994	4.995	0.001
105	278	5.049	5.037	-0.012
	Max	5.078	5.078	0.005
	Average	5.032	5.026	-0.006
	Min	4.992	4.981	-0.024
	Std Dev	0.020	0.022	0.007

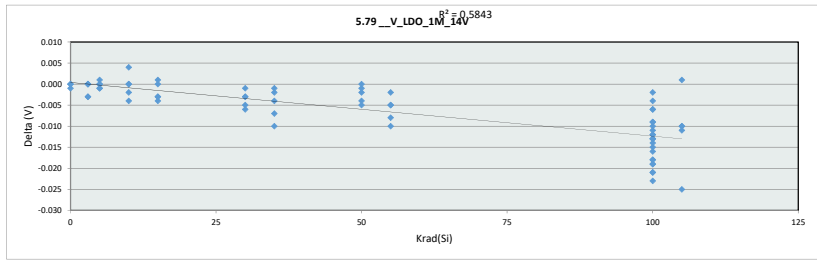


		5.78_V_LDO_1M										
Test Site	Tester											
Test Number	Unit	5.2	V									
Max Limit	Min Limit	4.75	V									
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min		5.017	5.009	5.020	5.011	4.994	5.012	5.006	5.005	5.000	4.989	4.981
Average		5.038	5.029	5.047	5.038	5.023	5.029	5.027	5.027	5.033	5.020	4.998
Max		5.077	5.059	5.078	5.059	5.042	5.048	5.051	5.057	5.050	5.056	5.037
UL		5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

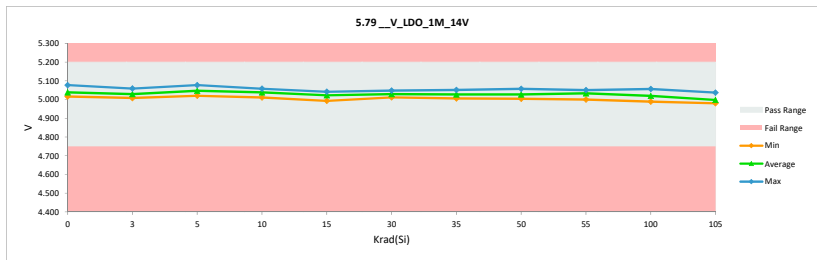


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5.79_V_LDO_1M_14V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			5.2	5.2
Min Limit			4.75	4.75
Krad(Si)	Serial #	PRE	POST	Delta
0	189	5.017	5.016	-0.001
0	190	5.077	5.077	0.000
0	191	5.021	5.021	0.000
3	300	5.029	5.026	-0.003
3	305	5.062	5.059	-0.003
3	309	5.008	5.008	0.000
3	311	5.028	5.028	0.000
3	313	5.025	5.025	0.000
5	319	5.020	5.020	0.000
5	321	5.073	5.072	-0.001
5	324	5.034	5.034	0.000
5	327	5.030	5.031	0.001
5	331	5.078	5.077	-0.001
10	234	5.054	5.058	0.004
10	235	5.056	5.056	0.000
10	236	5.011	5.011	0.000
10	237	5.019	5.017	-0.002
10	238	5.052	5.048	-0.004
15	239	5.008	5.005	-0.003
15	240	5.041	5.038	-0.003
15	241	4.997	4.993	-0.004
15	243	5.041	5.042	0.001
15	245	5.035	5.035	0.000
30	222	5.019	5.016	-0.003
30	223	5.018	5.012	-0.006
30	225	5.049	5.048	-0.001
30	226	5.040	5.035	-0.005
30	227	5.035	5.032	-0.003
35	228	5.008	5.006	-0.002
35	229	5.052	5.051	-0.001
35	231	5.038	5.034	-0.004
35	232	5.043	5.033	-0.010
35	233	5.018	5.011	-0.007
50	210	5.011	5.010	-0.001
50	211	5.006	5.004	-0.002
50	212	5.047	5.047	0.000
50	213	5.022	5.018	-0.004
50	214	5.062	5.057	-0.005
55	215	5.043	5.038	-0.005
55	216	5.040	5.032	-0.008
55	217	5.050	5.045	-0.005
55	218	5.052	5.050	-0.002
55	220	5.010	5.000	-0.010
55	247	5.054	5.033	-0.021
100	249	5.030	5.014	-0.016
100	250	5.033	5.014	-0.019
100	251	5.010	4.989	-0.021
100	253	5.021	5.017	-0.004
100	255	5.024	5.013	-0.011
100	256	5.066	5.056	-0.010
100	257	5.021	5.006	-0.015
100	258	5.035	5.023	-0.012
100	259	5.022	5.009	-0.013
100	260	5.054	5.031	-0.023
100	261	5.046	5.044	-0.002
100	262	5.029	5.020	-0.009
100	263	5.018	5.000	-0.018
100	264	5.069	5.055	-0.014
100	265	5.023	5.010	-0.013
100	266	5.028	5.016	-0.012
100	267	5.030	5.012	-0.018
100	268	5.003	4.997	-0.006
100	269	5.042	5.033	-0.009
100	270	5.010	4.997	-0.013
100	271	5.037	5.031	-0.006
100	273	5.053	5.034	-0.019
105	274	4.992	4.982	-0.010
105	275	5.005	4.980	-0.025
105	276	5.005	4.995	-0.010
105	277	4.994	4.995	0.001
105	278	5.048	5.037	-0.011
	Max	5.078	5.077	0.004
	Average	5.032	5.026	-0.007
	Min	4.992	4.980	-0.025
	Std Dev	0.020	0.022	0.007

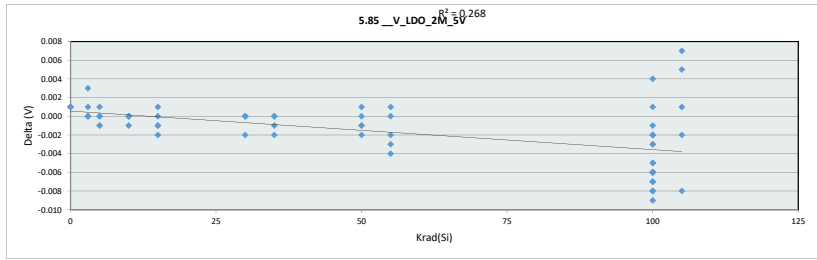


5.79_V_LDO_1M											
Test Site	Tester	Test Number	Unit								
			5.2	V							
Max Limit			5.2	V							
Min Limit			4.75	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.016	5.008	5.020	5.011	4.993	5.012	5.006	5.004	5.000	4.989	4.980
Average	5.038	5.029	5.047	5.038	5.023	5.029	5.027	5.027	5.033	5.020	4.998
Max	5.077	5.059	5.077	5.058	5.042	5.048	5.051	5.057	5.050	5.056	5.037
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

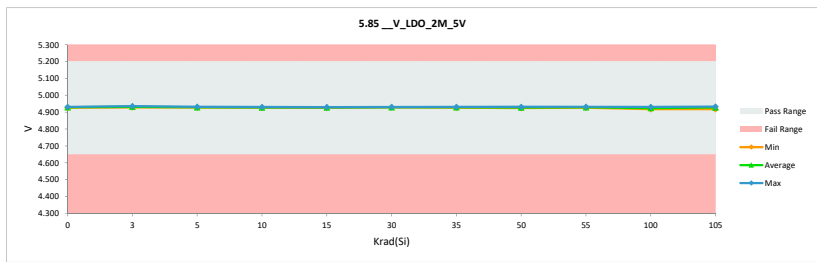


HDR TID Report TPS7H5002-SP

5.85_V_LDO_2M_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.65	4.65		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	4.929	4.930	0.001
0	190	4.930	4.931	0.001
0	191	4.925	4.926	0.001
3	300	4.929	4.932	0.003
3	305	4.935	4.936	0.001
3	309	4.930	4.930	0.000
3	311	4.929	4.929	0.000
3	313	4.932	4.932	0.000
5	319	4.927	4.927	0.000
5	321	4.927	4.927	0.000
5	324	4.933	4.932	-0.001
5	327	4.930	4.931	0.001
5	331	4.929	4.928	-0.001
10	234	4.927	4.927	0.000
10	235	4.929	4.928	-0.001
10	236	4.930	4.930	0.000
10	237	4.926	4.926	0.000
10	238	4.929	4.929	0.000
15	239	4.927	4.925	-0.002
15	240	4.929	4.928	-0.001
15	241	4.930	4.929	-0.001
15	243	4.929	4.930	0.001
15	245	4.930	4.930	0.000
30	222	4.928	4.928	0.000
30	223	4.931	4.931	0.000
30	225	4.930	4.930	0.000
30	226	4.930	4.928	-0.002
30	227	4.928	4.928	0.000
35	228	4.931	4.931	0.000
35	229	4.928	4.926	-0.002
35	231	4.929	4.929	0.000
35	232	4.930	4.930	0.000
35	233	4.932	4.931	-0.001
50	210	4.927	4.928	0.001
50	211	4.933	4.931	-0.002
50	212	4.924	4.924	0.000
50	213	4.928	4.927	-0.001
50	214	4.929	4.928	-0.001
55	215	4.931	4.932	0.001
55	216	4.931	4.931	0.000
55	217	4.930	4.927	-0.003
55	218	4.931	4.929	-0.002
55	220	4.930	4.926	-0.004
100	247	4.924	4.921	-0.003
100	249	4.926	4.927	0.001
100	250	4.930	4.925	-0.005
100	251	4.930	4.927	-0.003
100	253	4.930	4.928	-0.002
100	255	4.929	4.928	-0.001
100	256	4.927	4.918	-0.009
100	257	4.932	4.926	-0.006
100	258	4.929	4.923	-0.006
100	259	4.927	4.931	0.004
100	260	4.930	4.924	-0.006
100	261	4.925	4.918	-0.007
100	262	4.929	4.922	-0.007
100	263	4.932	4.930	-0.002
100	264	4.927	4.919	-0.008
100	265	4.927	4.925	-0.002
100	266	4.928	4.922	-0.006
100	267	4.929	4.922	-0.007
100	268	4.927	4.921	-0.006
100	269	4.932	4.924	-0.008
100	270	4.928	4.920	-0.008
100	271	4.929	4.923	-0.006
100	273	4.932	4.927	-0.005
105	274	4.926	4.918	-0.008
105	275	4.926	4.931	0.005
105	276	4.926	4.933	0.007
105	277	4.926	4.927	0.001
105	278	4.932	4.930	-0.002
	Max	4.935	4.936	0.007
	Average	4.929	4.927	-0.002
	Min	4.924	4.918	-0.009
	Std Dev	0.002	0.004	0.003

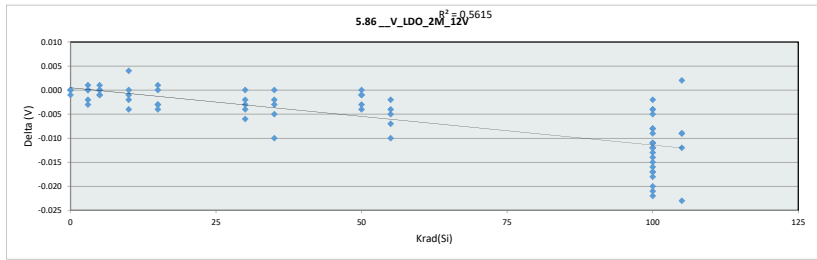


5.85_V_LDO_2M											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.65	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	4.926	4.929	4.927	4.926	4.925	4.928	4.926	4.924	4.926	4.918	4.918
Average	4.929	4.932	4.929	4.928	4.928	4.929	4.929	4.928	4.929	4.924	4.928
Max	4.931	4.936	4.932	4.930	4.930	4.931	4.931	4.931	4.931	4.932	4.933
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

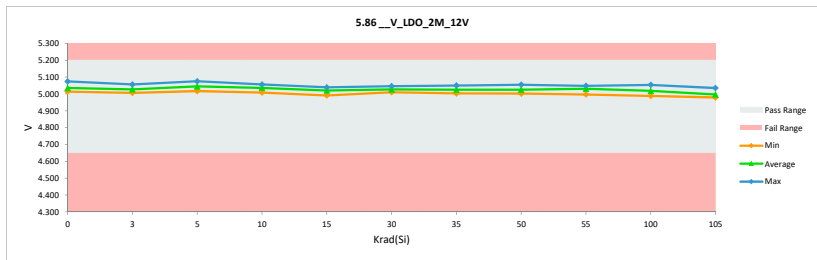


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5.86_V_LDO_2M_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.65	4.65		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	5.014	5.014	0.000
0	190	5.074	5.074	0.000
0	191	5.019	5.018	-0.001
3	300	5.026	5.024	-0.002
3	305	5.059	5.056	-0.003
3	309	5.006	5.006	0.000
3	311	5.025	5.026	0.001
3	313	5.023	5.023	0.000
5	319	5.018	5.017	-0.001
5	321	5.071	5.070	-0.001
5	324	5.033	5.033	0.000
5	327	5.028	5.029	0.001
5	331	5.075	5.075	0.000
10	234	5.052	5.056	0.004
10	235	5.054	5.054	0.000
10	236	5.009	5.008	-0.001
10	237	5.017	5.015	-0.002
10	238	5.050	5.046	-0.004
15	239	5.006	5.003	-0.003
15	240	5.039	5.036	-0.003
15	241	4.995	4.991	-0.004
15	243	5.038	5.039	0.001
15	245	5.033	5.033	0.000
30	222	5.016	5.013	-0.003
30	223	5.016	5.010	-0.006
30	225	5.046	5.046	0.000
30	226	5.038	5.034	-0.004
30	227	5.033	5.031	-0.002
35	228	5.005	5.003	-0.002
35	229	5.050	5.050	0.000
35	231	5.036	5.033	-0.003
35	232	5.041	5.031	-0.010
35	233	5.015	5.010	-0.005
50	210	5.009	5.008	-0.001
50	211	5.003	5.002	-0.001
50	212	5.045	5.045	0.000
50	213	5.019	5.016	-0.003
50	214	5.059	5.055	-0.004
55	215	5.039	5.035	-0.004
55	216	5.037	5.030	-0.007
55	217	5.048	5.043	-0.005
55	218	5.050	5.048	-0.002
55	220	5.007	4.997	-0.010
100	247	5.053	5.032	-0.021
100	249	5.028	5.013	-0.015
100	250	5.029	5.012	-0.017
100	251	5.008	4.988	-0.020
100	253	5.019	5.015	-0.004
100	255	5.022	5.011	-0.011
100	256	5.063	5.054	-0.009
100	257	5.018	5.004	-0.014
100	258	5.032	5.021	-0.011
100	259	5.020	5.008	-0.012
100	260	5.052	5.030	-0.022
100	261	5.045	5.043	-0.002
100	262	5.026	5.018	-0.008
100	263	5.015	4.999	-0.016
100	264	5.066	5.053	-0.013
100	265	5.021	5.009	-0.012
100	266	5.026	5.015	-0.011
100	267	5.028	5.011	-0.017
100	268	5.000	4.996	-0.004
100	269	5.039	5.031	-0.008
100	270	5.007	4.995	-0.012
100	271	5.034	5.029	-0.005
100	273	5.051	5.033	-0.018
105	274	4.990	4.981	-0.009
105	275	5.002	4.979	-0.023
105	276	5.003	4.994	-0.009
105	277	4.992	4.994	0.002
105	278	5.047	5.035	-0.012
	Max	5.075	5.075	0.004
	Average	5.030	5.024	-0.006
	Min	4.990	4.979	-0.023
	Std Dev	0.020	0.022	0.007

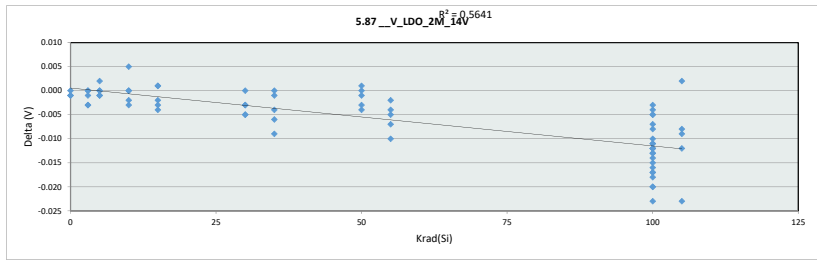


5.86_V_LDO_2M											
Test Site											
Tester											
Test Number	5.2	V									
Max Limit	5.2	V									
Min Limit	4.65	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	5.014	5.006	5.017	5.008	4.991	5.010	5.003	5.002	4.997	4.988	4.979
Average	5.035	5.027	5.045	5.036	5.020	5.027	5.025	5.025	5.031	5.018	4.997
Max	5.074	5.056	5.075	5.056	5.039	5.046	5.050	5.055	5.048	5.054	5.035
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

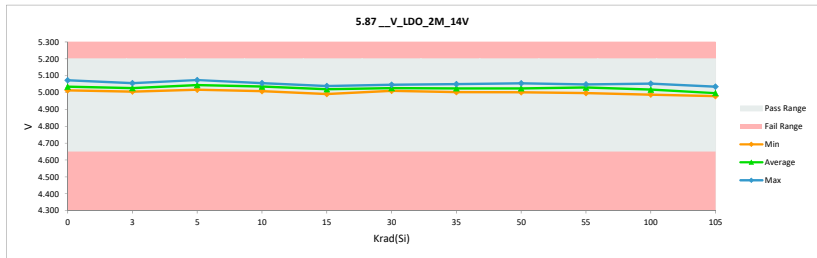


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5.87_V_LDO_2M_14V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			5.2	5.2
Min Limit			4.65	4.65
Krad(Si)	Serial #	PRE	POST	Delta
0	189	5.013	5.013	0.000
0	190	5.074	5.073	-0.001
0	191	5.019	5.018	-0.001
3	300	5.026	5.023	-0.003
3	305	5.059	5.056	-0.003
3	309	5.006	5.006	0.000
3	311	5.025	5.025	0.000
3	313	5.023	5.022	-0.001
5	319	5.017	5.017	0.000
5	321	5.071	5.070	-0.001
5	324	5.032	5.032	0.000
5	327	5.027	5.029	0.002
5	331	5.075	5.074	-0.001
10	234	5.051	5.056	0.005
10	235	5.054	5.054	0.000
10	236	5.008	5.008	0.000
10	237	5.017	5.015	-0.002
10	238	5.049	5.046	-0.003
15	239	5.006	5.002	-0.004
15	240	5.038	5.036	-0.002
15	241	4.994	4.991	-0.003
15	243	5.038	5.039	0.001
15	245	5.032	5.033	0.001
30	222	5.016	5.013	-0.003
30	223	5.015	5.010	-0.005
30	225	5.046	5.046	0.000
30	226	5.038	5.033	-0.005
30	227	5.033	5.030	-0.003
35	228	5.004	5.003	-0.001
35	229	5.050	5.050	0.000
35	231	5.036	5.032	-0.004
35	232	5.040	5.031	-0.009
35	233	5.015	5.009	-0.006
50	210	5.008	5.008	0.000
50	211	5.003	5.002	-0.001
50	212	5.044	5.045	0.001
50	213	5.019	5.015	-0.004
50	214	5.058	5.055	-0.003
55	215	5.039	5.035	-0.004
55	216	5.037	5.030	-0.007
55	217	5.048	5.043	-0.005
55	218	5.050	5.048	-0.002
55	220	5.007	4.997	-0.010
100	247	5.052	5.032	-0.020
100	249	5.027	5.013	-0.014
100	250	5.029	5.012	-0.017
100	251	5.008	4.988	-0.020
100	253	5.019	5.015	-0.004
100	255	5.022	5.011	-0.011
100	256	5.063	5.053	-0.010
100	257	5.019	5.004	-0.015
100	258	5.032	5.020	-0.012
100	259	5.020	5.008	-0.012
100	260	5.052	5.029	-0.023
100	261	5.045	5.042	-0.003
100	262	5.025	5.018	-0.007
100	263	5.015	4.998	-0.017
100	264	5.066	5.053	-0.013
100	265	5.021	5.008	-0.013
100	266	5.026	5.015	-0.011
100	267	5.027	5.011	-0.016
100	268	5.000	4.995	-0.005
100	269	5.039	5.031	-0.008
100	270	5.006	4.994	-0.012
100	271	5.034	5.029	-0.005
100	273	5.051	5.033	-0.018
105	274	4.990	4.981	-0.009
105	275	5.002	4.979	-0.023
105	276	5.002	4.994	-0.008
105	277	4.992	4.994	0.002
105	278	5.047	5.035	-0.012
	Max	5.075	5.074	0.005
	Average	5.029	5.023	-0.006
	Min	4.990	4.979	-0.023
	Std Dev	0.020	0.022	0.007

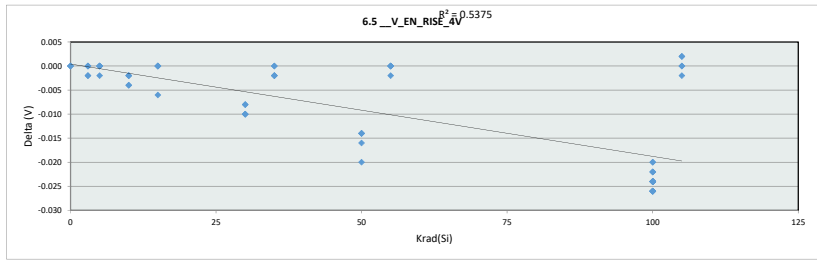


5.87_V_LDO_2M											
Test Site	Tester	Test Number	Unit								
			5.2	V							
Max Limit			5.2	V							
Min Limit			4.65	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	5.013	5.006	5.017	5.008	4.991	5.010	5.003	5.002	4.997	4.988	4.979
Average	5.035	5.026	5.044	5.036	5.020	5.026	5.025	5.025	5.031	5.018	4.997
Max	5.073	5.056	5.074	5.056	5.039	5.046	5.050	5.055	5.048	5.053	5.035
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

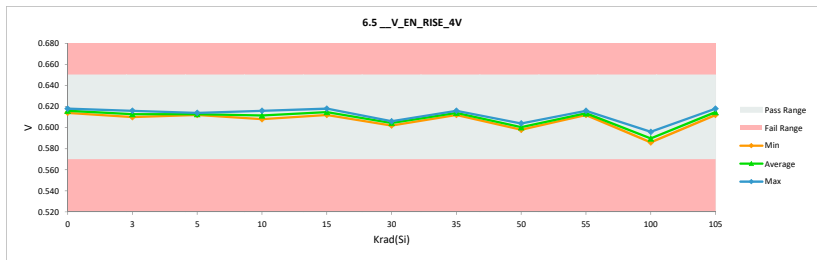


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		6.5_V_EN_RISE_4V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit		0.65	0.65	
Min Limit		0.57	0.57	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.616	0.616	0.000
0	190	0.614	0.614	0.000
0	191	0.618	0.618	0.000
3	300	0.614	0.612	-0.002
3	305	0.614	0.614	0.000
3	309	0.612	0.612	0.000
3	311	0.612	0.610	-0.002
3	313	0.616	0.616	0.000
5	319	0.614	0.614	0.000
5	321	0.614	0.612	-0.002
5	324	0.612	0.612	0.000
5	327	0.612	0.612	0.000
5	331	0.614	0.614	0.000
10	234	0.618	0.616	-0.002
10	235	0.612	0.608	-0.004
10	236	0.612	0.610	-0.002
10	237	0.614	0.612	-0.002
10	238	0.616	0.612	-0.004
15	239	0.620	0.614	-0.006
15	240	0.614	0.614	0.000
15	241	0.612	0.612	0.000
15	243	0.618	0.618	0.000
15	245	0.616	0.616	0.000
30	222	0.614	0.606	-0.008
30	223	0.614	0.606	-0.008
30	225	0.612	0.602	-0.010
30	226	0.614	0.604	-0.010
30	227	0.614	0.604	-0.010
35	228	0.612	0.612	0.000
35	229	0.616	0.614	-0.002
35	231	0.614	0.612	-0.002
35	232	0.616	0.616	0.000
35	233	0.618	0.616	-0.002
50	210	0.616	0.600	-0.016
50	211	0.618	0.604	-0.014
50	212	0.616	0.602	-0.014
50	213	0.618	0.598	-0.020
50	214	0.612	0.598	-0.014
55	215	0.612	0.612	0.000
55	216	0.614	0.612	-0.002
55	217	0.616	0.616	0.000
55	218	0.614	0.614	0.000
55	220	0.614	0.614	0.000
100	247	0.616	0.592	-0.024
100	249	0.614	0.594	-0.020
100	250	0.610	0.586	-0.024
100	251	0.616	0.592	-0.024
100	253	0.614	0.588	-0.026
100	255	0.618	0.596	-0.022
100	256	0.614	0.590	-0.024
100	257	0.612	0.592	-0.020
100	258	0.612	0.590	-0.022
100	259	0.612	0.586	-0.026
100	260	0.612	0.588	-0.024
100	261	0.616	0.592	-0.024
100	262	0.610	0.586	-0.024
100	263	0.614	0.588	-0.026
100	264	0.612	0.586	-0.026
100	265	0.616	0.590	-0.026
100	266	0.614	0.590	-0.024
100	267	0.612	0.588	-0.024
100	268	0.614	0.590	-0.024
100	269	0.618	0.592	-0.026
100	270	0.612	0.586	-0.026
100	271	0.614	0.592	-0.022
100	273	0.614	0.590	-0.024
105	274	0.612	0.612	0.000
105	275	0.616	0.618	0.002
105	276	0.612	0.614	0.002
105	277	0.618	0.616	-0.002
105	278	0.614	0.614	0.000
Max		0.620	0.618	0.002
Average		0.614	0.604	-0.010
Min		0.610	0.586	-0.026
Std Dev		0.002	0.011	0.011

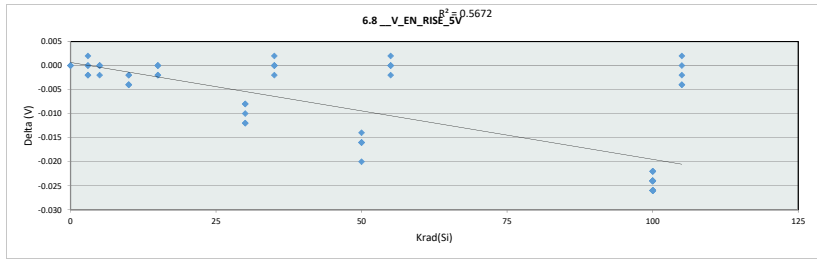


		6.5_V_EN_RISE											
Test Site	Tester												
Test Number	Unit	0.65	V										
Max Limit		0.65	V										
Min Limit		0.57	V										
Krad(Si)	LL	3	5	10	15	30	35	50	55	100	105	UL	
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	
Min	0.614	0.610	0.612	0.608	0.612	0.602	0.612	0.598	0.612	0.586	0.612	0.612	
Average	0.616	0.613	0.613	0.612	0.615	0.604	0.614	0.600	0.614	0.590	0.615	0.615	
Max	0.618	0.616	0.614	0.616	0.618	0.606	0.616	0.604	0.616	0.604	0.596	0.618	
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	

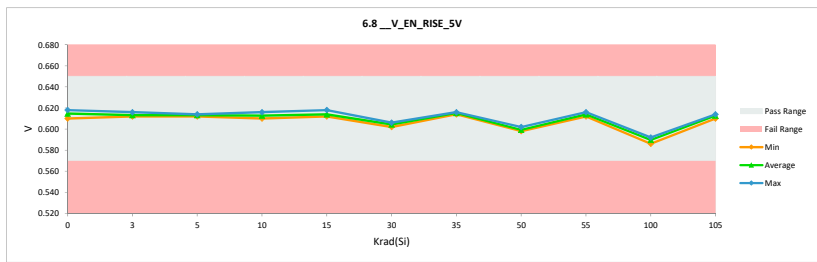


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		6.8_V_EN_RISE_5V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	0.65		0.65	
Min Limit	0.57		0.57	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.616	0.616	0.000
0	190	0.610	0.610	0.000
0	191	0.618	0.618	0.000
3	300	0.614	0.612	-0.002
3	305	0.616	0.614	-0.002
3	309	0.612	0.612	0.000
3	311	0.612	0.612	0.000
3	313	0.614	0.616	0.002
5	319	0.614	0.614	0.000
5	321	0.614	0.614	0.000
5	324	0.612	0.612	0.000
5	327	0.614	0.612	-0.002
5	331	0.614	0.614	0.000
10	234	0.620	0.616	-0.004
10	235	0.614	0.610	-0.004
10	236	0.614	0.612	-0.002
10	237	0.614	0.612	-0.002
10	238	0.618	0.614	-0.004
15	239	0.616	0.614	-0.002
15	240	0.616	0.614	-0.002
15	241	0.612	0.612	0.000
15	243	0.618	0.618	0.000
15	245	0.612	0.612	0.000
30	222	0.614	0.606	-0.008
30	223	0.616	0.606	-0.010
30	225	0.614	0.602	-0.012
30	226	0.616	0.604	-0.012
30	227	0.612	0.604	-0.008
35	228	0.612	0.614	0.002
35	229	0.616	0.616	0.000
35	231	0.614	0.614	0.000
35	232	0.616	0.616	0.000
35	233	0.618	0.616	-0.002
50	210	0.616	0.600	-0.016
50	211	0.618	0.598	-0.020
50	212	0.616	0.602	-0.014
50	213	0.614	0.598	-0.016
50	214	0.614	0.598	-0.016
55	215	0.612	0.612	0.000
55	216	0.614	0.612	-0.002
55	217	0.616	0.616	0.000
55	218	0.614	0.614	0.000
55	220	0.614	0.616	0.002
100	247	0.618	0.592	-0.026
100	249	0.614	0.592	-0.022
100	250	0.612	0.586	-0.026
100	251	0.616	0.592	-0.024
100	253	0.614	0.588	-0.026
100	255	0.618	0.592	-0.026
100	256	0.614	0.590	-0.024
100	257	0.612	0.586	-0.026
100	258	0.614	0.590	-0.024
100	259	0.614	0.588	-0.026
100	260	0.612	0.588	-0.024
100	261	0.616	0.592	-0.024
100	262	0.612	0.586	-0.026
100	263	0.614	0.588	-0.026
100	264	0.612	0.588	-0.024
100	265	0.614	0.590	-0.024
100	266	0.614	0.592	-0.022
100	267	0.612	0.588	-0.024
100	268	0.616	0.592	-0.024
100	269	0.618	0.592	-0.026
100	270	0.612	0.586	-0.026
100	271	0.614	0.592	-0.022
100	273	0.614	0.592	-0.022
105	274	0.614	0.612	-0.002
105	275	0.618	0.614	-0.004
105	276	0.612	0.614	0.002
105	277	0.614	0.610	-0.004
105	278	0.614	0.614	0.000
	Max	0.620	0.618	0.002
	Average	0.614	0.604	-0.010
	Min	0.610	0.586	-0.026
	Std Dev	0.002	0.011	0.011

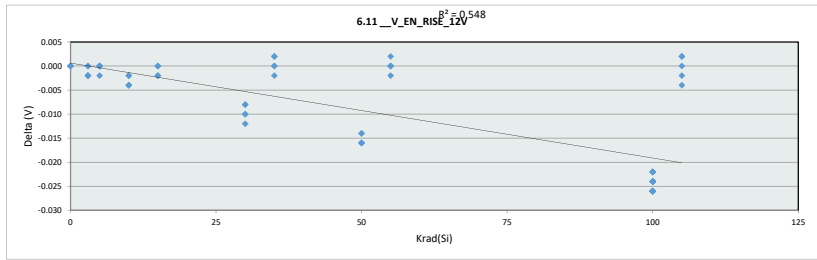


		6.8_V_EN_RISE										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit	0.65											
Min Limit	0.57											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.610	0.612	0.612	0.610	0.612	0.602	0.614	0.598	0.612	0.586	0.610	
Average	0.615	0.613	0.613	0.613	0.614	0.604	0.615	0.599	0.614	0.590	0.613	
Max	0.618	0.616	0.614	0.616	0.618	0.606	0.616	0.602	0.616	0.592	0.614	
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	

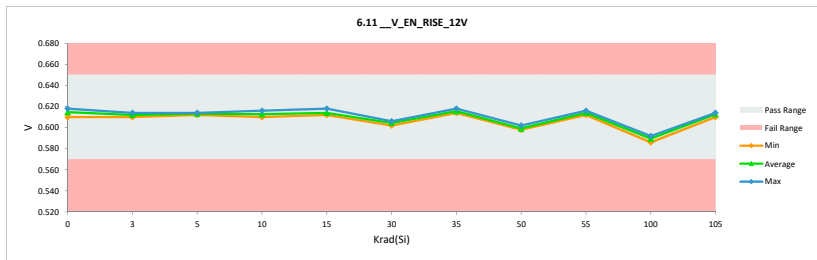


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6.11_V_EN_RISE_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	0.65	0.65		
Min Limit	0.57	0.57		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.616	0.616	0.000
0	190	0.610	0.610	0.000
0	191	0.618	0.618	0.000
3	300	0.614	0.612	-0.002
3	305	0.616	0.614	-0.002
3	309	0.612	0.612	0.000
3	311	0.612	0.610	-0.002
3	313	0.612	0.612	0.000
5	319	0.614	0.614	0.000
5	321	0.614	0.614	0.000
5	324	0.612	0.612	0.000
5	327	0.614	0.612	-0.002
5	331	0.614	0.614	0.000
10	234	0.620	0.616	-0.004
10	235	0.614	0.610	-0.004
10	236	0.614	0.612	-0.002
10	237	0.614	0.612	-0.002
10	238	0.618	0.614	-0.004
15	239	0.616	0.614	-0.002
15	240	0.616	0.614	-0.002
15	241	0.612	0.612	0.000
15	243	0.618	0.618	0.000
15	245	0.612	0.612	0.000
30	222	0.614	0.606	-0.008
30	223	0.616	0.606	-0.010
30	225	0.612	0.602	-0.010
30	226	0.616	0.604	-0.012
30	227	0.612	0.604	-0.008
35	228	0.612	0.614	0.002
35	229	0.616	0.616	0.000
35	231	0.614	0.614	0.000
35	232	0.616	0.618	0.002
35	233	0.618	0.616	-0.002
50	210	0.616	0.600	-0.016
50	211	0.614	0.598	-0.016
50	212	0.616	0.602	-0.014
50	213	0.614	0.598	-0.016
50	214	0.612	0.598	-0.014
55	215	0.612	0.612	0.000
55	216	0.614	0.612	-0.002
55	217	0.616	0.616	0.000
55	218	0.614	0.614	0.000
55	220	0.614	0.616	0.002
100	247	0.618	0.592	-0.026
100	249	0.614	0.592	-0.022
100	250	0.612	0.586	-0.026
100	251	0.616	0.592	-0.024
100	253	0.614	0.588	-0.026
100	255	0.618	0.592	-0.026
100	256	0.614	0.590	-0.024
100	257	0.612	0.586	-0.026
100	258	0.614	0.590	-0.024
100	259	0.614	0.588	-0.026
100	260	0.612	0.588	-0.024
100	261	0.616	0.592	-0.024
100	262	0.612	0.586	-0.026
100	263	0.614	0.588	-0.026
100	264	0.612	0.588	-0.024
100	265	0.612	0.590	-0.022
100	266	0.614	0.592	-0.022
100	267	0.612	0.588	-0.024
100	268	0.616	0.592	-0.024
100	269	0.618	0.592	-0.026
100	270	0.612	0.586	-0.026
100	271	0.614	0.592	-0.022
100	273	0.614	0.592	-0.022
105	274	0.614	0.612	-0.002
105	275	0.612	0.614	0.002
105	276	0.612	0.614	0.002
105	277	0.614	0.610	-0.004
105	278	0.614	0.614	0.000
Max		0.620	0.618	0.002
Average		0.614	0.604	-0.010
Min		0.610	0.586	-0.026
Std Dev		0.002	0.011	0.011

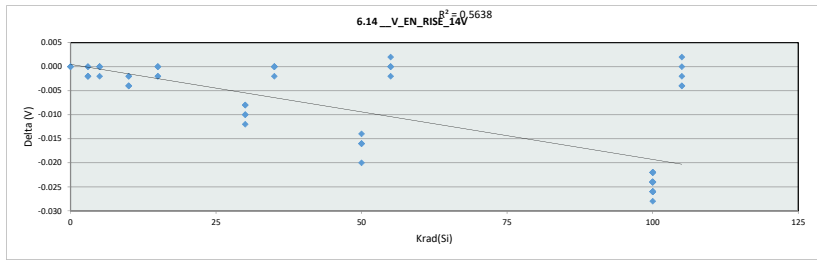


6.11_V_EN_RISE											
Test Site											
Tester											
Test Number											
Max Limit	0.65	V									
Min Limit	0.57	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.610	0.610	0.612	0.610	0.612	0.602	0.614	0.598	0.612	0.586	0.610
Average	0.615	0.612	0.613	0.613	0.614	0.604	0.616	0.599	0.614	0.590	0.613
Max	0.618	0.614	0.614	0.616	0.618	0.606	0.618	0.602	0.616	0.592	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

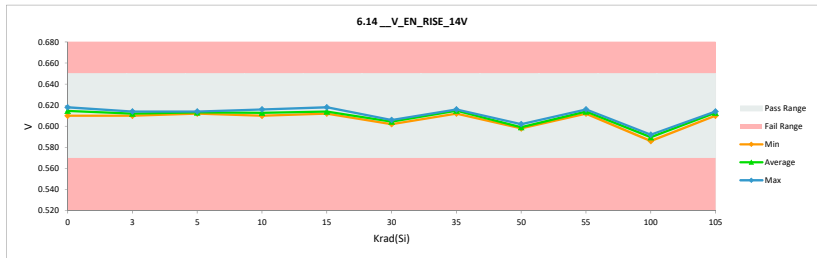


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6.14_V_EN_RISE_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	0.65	0.65		
Min Limit	0.57	0.57		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.616	0.616	0.000
0	190	0.610	0.610	0.000
0	191	0.618	0.618	0.000
3	300	0.614	0.612	-0.002
3	305	0.616	0.614	-0.002
3	309	0.612	0.612	0.000
3	311	0.612	0.610	-0.002
3	313	0.612	0.612	0.000
5	319	0.614	0.614	0.000
5	321	0.614	0.614	0.000
5	324	0.612	0.612	0.000
5	327	0.614	0.612	-0.002
5	331	0.614	0.614	0.000
10	234	0.620	0.616	-0.004
10	235	0.614	0.610	-0.004
10	236	0.614	0.612	-0.002
10	237	0.614	0.612	-0.002
10	238	0.618	0.614	-0.004
15	239	0.616	0.614	-0.002
15	240	0.616	0.614	-0.002
15	241	0.612	0.612	0.000
15	243	0.618	0.618	0.000
15	245	0.612	0.612	0.000
30	222	0.614	0.606	-0.008
30	223	0.616	0.606	-0.010
30	225	0.612	0.602	-0.010
30	226	0.616	0.604	-0.012
30	227	0.612	0.604	-0.008
35	228	0.612	0.612	0.000
35	229	0.616	0.616	0.000
35	231	0.614	0.614	0.000
35	232	0.616	0.616	0.000
35	233	0.618	0.616	-0.002
50	210	0.616	0.600	-0.016
50	211	0.618	0.598	-0.020
50	212	0.616	0.602	-0.014
50	213	0.614	0.598	-0.016
50	214	0.614	0.598	-0.016
55	215	0.612	0.612	0.000
55	216	0.614	0.612	-0.002
55	217	0.616	0.616	0.000
55	218	0.614	0.614	0.000
55	220	0.614	0.616	0.002
100	247	0.618	0.592	-0.026
100	249	0.614	0.592	-0.022
100	250	0.612	0.586	-0.026
100	251	0.616	0.592	-0.024
100	253	0.614	0.588	-0.026
100	255	0.614	0.592	-0.022
100	256	0.614	0.590	-0.024
100	257	0.612	0.588	-0.024
100	258	0.614	0.590	-0.024
100	259	0.614	0.586	-0.028
100	260	0.612	0.588	-0.024
100	261	0.616	0.592	-0.024
100	262	0.612	0.586	-0.026
100	263	0.614	0.588	-0.026
100	264	0.612	0.588	-0.024
100	265	0.612	0.590	-0.022
100	266	0.614	0.592	-0.022
100	267	0.612	0.588	-0.024
100	268	0.616	0.592	-0.024
100	269	0.618	0.592	-0.026
100	270	0.612	0.586	-0.026
100	271	0.614	0.592	-0.022
100	273	0.614	0.592	-0.022
105	274	0.614	0.612	-0.002
105	275	0.618	0.614	-0.004
105	276	0.612	0.614	0.002
105	277	0.614	0.610	-0.004
105	278	0.614	0.614	0.000
Max		0.620	0.618	0.002
Average		0.614	0.604	-0.010
Min		0.610	0.586	-0.028
Std Dev		0.002	0.011	0.011

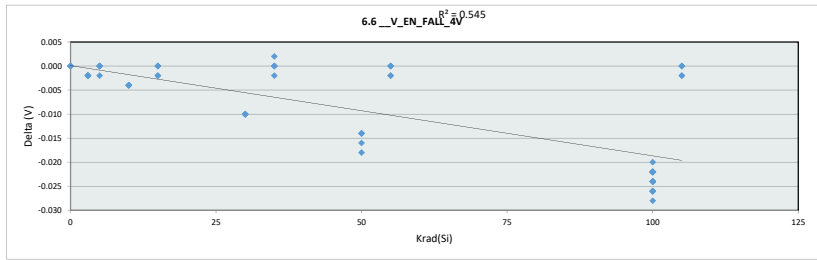


6.14_V_EN_RISE											
Test Site											
Tester											
Test Number											
Max Limit	0.65	V									
Min Limit	0.57	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.610	0.610	0.612	0.610	0.612	0.602	0.612	0.598	0.612	0.586	0.610
Average	0.615	0.612	0.613	0.613	0.614	0.604	0.615	0.599	0.614	0.590	0.613
Max	0.618	0.614	0.614	0.616	0.618	0.606	0.616	0.602	0.616	0.592	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

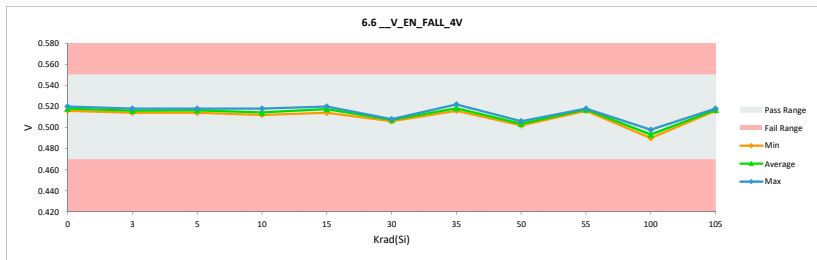


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6.6_V_EN_FALL_4V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			0.55	0.55
Min Limit			0.47	0.47
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.518	0.518	0.000
0	190	0.516	0.516	0.000
0	191	0.520	0.520	0.000
3	300	0.516	0.514	-0.002
3	305	0.520	0.518	-0.002
3	309	0.516	0.514	-0.002
3	311	0.518	0.516	-0.002
3	313	0.520	0.518	-0.002
5	319	0.518	0.518	0.000
5	321	0.516	0.516	0.000
5	324	0.514	0.514	0.000
5	327	0.516	0.516	0.000
5	331	0.520	0.518	-0.002
10	234	0.522	0.518	-0.004
10	235	0.516	0.512	-0.004
10	236	0.516	0.512	-0.004
10	237	0.518	0.514	-0.004
10	238	0.520	0.516	-0.004
15	239	0.522	0.520	-0.002
15	240	0.518	0.518	0.000
15	241	0.514	0.514	0.000
15	243	0.520	0.520	0.000
15	245	0.518	0.516	-0.002
30	222	0.518	0.508	-0.010
30	223	0.518	0.508	-0.010
30	225	0.516	0.506	-0.010
30	226	0.518	0.508	-0.010
30	227	0.516	0.506	-0.010
35	228	0.518	0.516	-0.002
35	229	0.518	0.518	0.000
35	231	0.516	0.516	0.000
35	232	0.520	0.520	0.000
35	233	0.520	0.522	0.002
50	210	0.518	0.502	-0.016
50	211	0.520	0.506	-0.014
50	212	0.520	0.506	-0.014
50	213	0.520	0.502	-0.018
50	214	0.516	0.502	-0.014
55	215	0.516	0.516	0.000
55	216	0.516	0.516	0.000
55	217	0.520	0.518	-0.002
55	218	0.518	0.518	0.000
55	220	0.520	0.518	-0.002
100	247	0.520	0.494	-0.026
100	249	0.518	0.496	-0.022
100	250	0.514	0.490	-0.024
100	251	0.520	0.494	-0.026
100	253	0.518	0.492	-0.026
100	255	0.518	0.498	-0.020
100	256	0.518	0.492	-0.026
100	257	0.516	0.494	-0.022
100	258	0.518	0.494	-0.024
100	259	0.518	0.490	-0.028
100	260	0.514	0.492	-0.022
100	261	0.518	0.496	-0.022
100	262	0.512	0.490	-0.022
100	263	0.518	0.494	-0.024
100	264	0.516	0.490	-0.026
100	265	0.518	0.496	-0.022
100	266	0.518	0.494	-0.024
100	267	0.514	0.492	-0.022
100	268	0.516	0.494	-0.022
100	269	0.520	0.496	-0.024
100	270	0.516	0.492	-0.024
100	271	0.520	0.496	-0.024
100	273	0.516	0.494	-0.022
105	274	0.518	0.516	-0.002
105	275	0.518	0.518	0.000
105	276	0.516	0.516	0.000
105	277	0.520	0.518	-0.002
105	278	0.516	0.516	0.000
Max		0.522	0.522	0.002
Average		0.518	0.508	-0.010
Min		0.512	0.490	-0.028
Std Dev		0.002	0.011	0.010

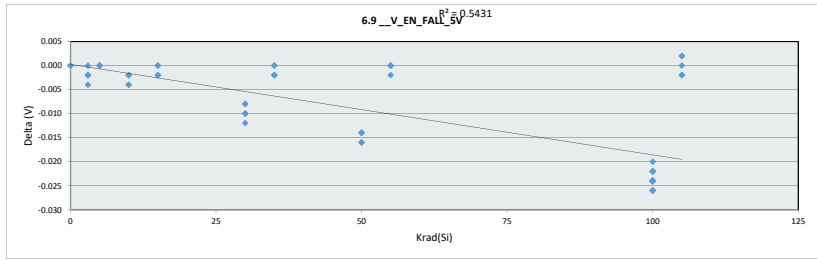


6.6_V_EN_FALL											
Test Site	Tester	Test Number	Unit								
Max Limit			0.55	V							
Min Limit			0.47	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.516	0.514	0.514	0.512	0.514	0.506	0.516	0.502	0.516	0.490	0.516
Average	0.518	0.516	0.516	0.514	0.518	0.507	0.518	0.504	0.517	0.493	0.517
Max	0.520	0.518	0.518	0.518	0.520	0.508	0.522	0.506	0.518	0.498	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

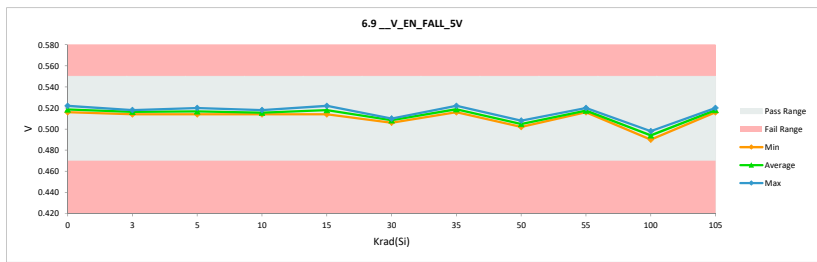


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6.9_V_EN_FALL_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	0.55	0.55		
Min Limit	0.47	0.47		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.518	0.518	0.000
0	190	0.516	0.516	0.000
0	191	0.522	0.522	0.000
3	300	0.516	0.514	-0.002
3	305	0.520	0.518	-0.002
3	309	0.516	0.516	0.000
3	311	0.520	0.516	-0.004
3	313	0.520	0.518	-0.002
5	319	0.518	0.518	0.000
5	321	0.516	0.516	0.000
5	324	0.514	0.514	0.000
5	327	0.516	0.516	0.000
5	331	0.520	0.520	0.000
10	234	0.522	0.518	-0.004
10	235	0.516	0.514	-0.002
10	236	0.516	0.514	-0.002
10	237	0.518	0.514	-0.004
10	238	0.520	0.518	-0.002
15	239	0.522	0.522	0.000
15	240	0.518	0.518	0.000
15	241	0.516	0.514	-0.002
15	243	0.522	0.520	-0.002
15	245	0.518	0.516	-0.002
30	222	0.518	0.508	-0.010
30	223	0.518	0.510	-0.008
30	225	0.516	0.508	-0.008
30	226	0.520	0.510	-0.010
30	227	0.518	0.506	-0.012
35	228	0.518	0.516	-0.002
35	229	0.520	0.518	-0.002
35	231	0.518	0.516	-0.002
35	232	0.522	0.522	0.000
35	233	0.522	0.522	0.000
50	210	0.520	0.504	-0.016
50	211	0.520	0.506	-0.014
50	212	0.522	0.508	-0.014
50	213	0.520	0.504	-0.016
50	214	0.516	0.502	-0.014
55	215	0.516	0.516	0.000
55	216	0.516	0.516	0.000
55	217	0.520	0.520	0.000
55	218	0.518	0.518	0.000
55	220	0.520	0.518	-0.002
100	247	0.520	0.494	-0.026
100	249	0.518	0.496	-0.022
100	250	0.514	0.490	-0.024
100	251	0.520	0.494	-0.026
100	253	0.518	0.494	-0.024
100	255	0.520	0.498	-0.022
100	256	0.518	0.494	-0.024
100	257	0.516	0.494	-0.022
100	258	0.518	0.494	-0.024
100	259	0.518	0.492	-0.026
100	260	0.516	0.494	-0.022
100	261	0.518	0.498	-0.020
100	262	0.514	0.490	-0.024
100	263	0.518	0.494	-0.024
100	264	0.516	0.490	-0.026
100	265	0.520	0.496	-0.024
100	266	0.518	0.494	-0.024
100	267	0.514	0.492	-0.022
100	268	0.518	0.494	-0.024
100	269	0.522	0.498	-0.024
100	270	0.516	0.492	-0.024
100	271	0.520	0.496	-0.024
100	273	0.518	0.494	-0.024
105	274	0.520	0.518	-0.002
105	275	0.518	0.520	0.002
105	276	0.516	0.518	0.002
105	277	0.520	0.518	-0.002
105	278	0.516	0.516	0.000
	Max	0.522	0.522	0.002
	Average	0.518	0.508	-0.010
	Min	0.514	0.490	-0.026
	Std Dev	0.002	0.011	0.010

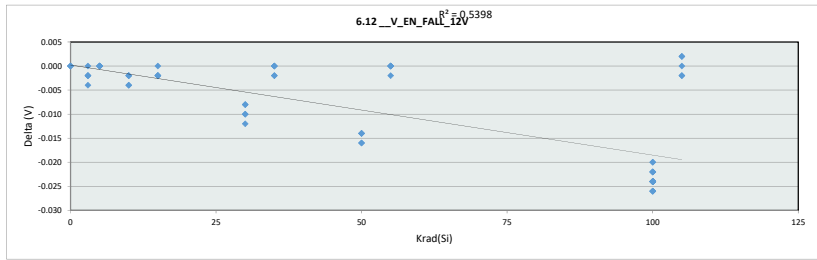


6.9_V_EN_FALL											
Test Site											
Tester											
Test Number											
Max Limit	0.55	V									
Min Limit	0.47	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.516	0.514	0.514	0.514	0.514	0.506	0.516	0.502	0.516	0.490	0.516
Average	0.519	0.516	0.517	0.516	0.518	0.508	0.519	0.505	0.518	0.494	0.518
Max	0.522	0.518	0.520	0.518	0.522	0.510	0.522	0.508	0.520	0.498	0.520
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

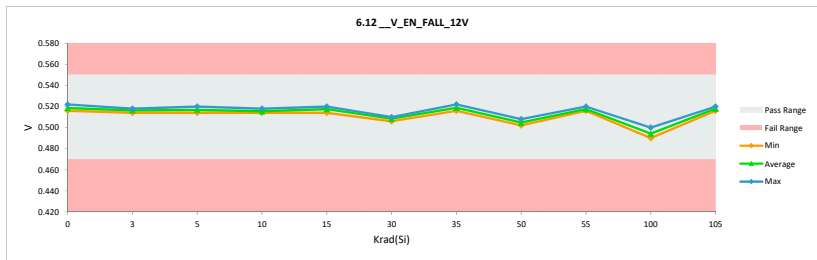


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6.12_V_EN_FALL_12V				
Test Site	Tester	Test Number	Unit	Delta
			V	
Max Limit			0.55	
Min Limit			0.47	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.518	0.518	0.000
0	190	0.516	0.516	0.000
0	191	0.522	0.522	0.000
3	300	0.516	0.514	-0.002
3	305	0.520	0.518	-0.002
3	309	0.516	0.516	0.000
3	311	0.520	0.516	-0.004
3	313	0.520	0.518	-0.002
5	319	0.518	0.518	0.000
5	321	0.516	0.516	0.000
5	324	0.514	0.514	0.000
5	327	0.516	0.516	0.000
5	331	0.520	0.520	0.000
10	234	0.522	0.518	-0.004
10	235	0.516	0.514	-0.002
10	236	0.516	0.514	-0.002
10	237	0.518	0.514	-0.004
10	238	0.520	0.518	-0.002
15	239	0.522	0.520	-0.002
15	240	0.518	0.518	0.000
15	241	0.516	0.514	-0.002
15	243	0.522	0.520	-0.002
15	245	0.518	0.516	-0.002
30	222	0.518	0.508	-0.010
30	223	0.518	0.510	-0.008
30	225	0.516	0.508	-0.008
30	226	0.520	0.510	-0.010
30	227	0.518	0.506	-0.012
35	228	0.518	0.516	-0.002
35	229	0.518	0.518	0.000
35	231	0.518	0.516	-0.002
35	232	0.522	0.522	0.000
35	233	0.522	0.522	0.000
50	210	0.520	0.504	-0.016
50	211	0.520	0.506	-0.014
50	212	0.522	0.508	-0.014
50	213	0.520	0.504	-0.016
50	214	0.516	0.502	-0.014
55	215	0.516	0.516	0.000
55	216	0.516	0.516	0.000
55	217	0.520	0.520	0.000
55	218	0.518	0.518	0.000
55	220	0.520	0.518	-0.002
100	247	0.520	0.494	-0.026
100	249	0.518	0.496	-0.022
100	250	0.514	0.490	-0.024
100	251	0.520	0.494	-0.026
100	253	0.518	0.494	-0.024
100	255	0.520	0.500	-0.020
100	256	0.518	0.494	-0.024
100	257	0.518	0.494	-0.024
100	258	0.518	0.494	-0.024
100	259	0.518	0.492	-0.026
100	260	0.516	0.494	-0.022
100	261	0.518	0.498	-0.020
100	262	0.514	0.490	-0.024
100	263	0.518	0.496	-0.022
100	264	0.516	0.490	-0.026
100	265	0.520	0.498	-0.022
100	266	0.518	0.494	-0.024
100	267	0.516	0.492	-0.024
100	268	0.518	0.494	-0.024
100	269	0.522	0.498	-0.024
100	270	0.516	0.492	-0.024
100	271	0.520	0.496	-0.024
100	273	0.518	0.494	-0.024
105	274	0.520	0.518	-0.002
105	275	0.518	0.520	0.002
105	276	0.516	0.518	0.002
105	277	0.520	0.518	-0.002
105	278	0.516	0.516	0.000
Max		0.522	0.522	0.002
Average		0.518	0.508	-0.010
Min		0.514	0.490	-0.026
Std Dev		0.002	0.011	0.010

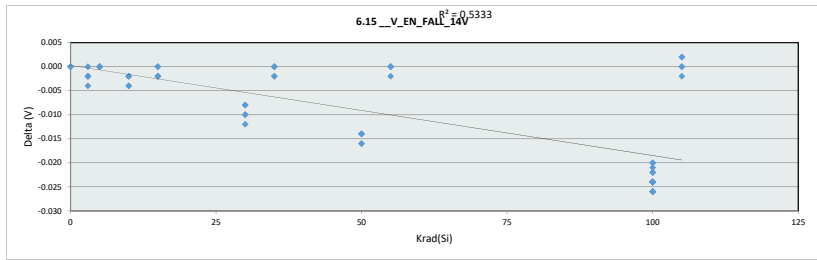


6.12_V_EN_FALL											
Test Site	Tester	Test Number	Unit	Delta							
			V								
Max Limit			0.55								
Min Limit			0.47								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.516	0.514	0.514	0.514	0.514	0.506	0.516	0.502	0.516	0.490	0.516
Average	0.519	0.516	0.517	0.516	0.518	0.508	0.519	0.505	0.518	0.494	0.518
Max	0.522	0.518	0.520	0.518	0.520	0.510	0.522	0.508	0.520	0.500	0.520
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

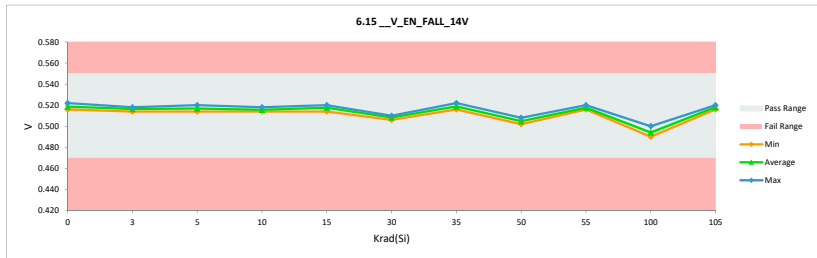


HDR TID Report
TPS7H5002-SP

6.15_V_EN_FALL_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	0.55	0.55		
Min Limit	0.47	0.47		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.518	0.518	0.000
0	190	0.516	0.516	0.000
0	191	0.522	0.522	0.000
3	300	0.516	0.514	-0.002
3	305	0.520	0.518	-0.002
3	309	0.516	0.516	0.000
3	311	0.520	0.516	-0.004
3	313	0.520	0.518	-0.002
5	319	0.518	0.518	0.000
5	321	0.516	0.516	0.000
5	324	0.514	0.514	0.000
5	327	0.516	0.516	0.000
5	331	0.520	0.520	0.000
10	234	0.522	0.518	-0.004
10	235	0.516	0.514	-0.002
10	236	0.516	0.514	-0.002
10	237	0.518	0.514	-0.004
10	238	0.520	0.518	-0.002
15	239	0.522	0.520	-0.002
15	240	0.518	0.518	0.000
15	241	0.516	0.514	-0.002
15	243	0.522	0.520	-0.002
15	245	0.516	0.516	0.000
30	222	0.518	0.508	-0.010
30	223	0.518	0.510	-0.008
30	225	0.516	0.508	-0.008
30	226	0.520	0.510	-0.010
30	227	0.518	0.506	-0.012
35	228	0.518	0.516	-0.002
35	229	0.518	0.518	0.000
35	231	0.518	0.516	-0.002
35	232	0.522	0.522	0.000
35	233	0.522	0.522	0.000
50	210	0.520	0.504	-0.016
50	211	0.520	0.506	-0.014
50	212	0.522	0.508	-0.014
50	213	0.520	0.504	-0.016
50	214	0.516	0.502	-0.014
55	215	0.516	0.516	0.000
55	216	0.516	0.516	0.000
55	217	0.520	0.520	0.000
55	218	0.518	0.518	0.000
55	220	0.520	0.518	-0.002
100	247	0.520	0.494	-0.026
100	249	0.518	0.496	-0.022
100	250	0.514	0.490	-0.024
100	251	0.520	0.494	-0.026
100	253	0.518	0.494	-0.024
100	255	0.520	0.500	-0.020
100	256	0.518	0.494	-0.024
100	257	0.515	0.494	-0.021
100	258	0.518	0.494	-0.024
100	259	0.518	0.492	-0.026
100	260	0.516	0.494	-0.022
100	261	0.518	0.498	-0.020
100	262	0.514	0.490	-0.024
100	263	0.518	0.494	-0.024
100	264	0.516	0.490	-0.026
100	265	0.520	0.496	-0.024
100	266	0.518	0.494	-0.024
100	267	0.514	0.492	-0.022
100	268	0.520	0.494	-0.026
100	269	0.522	0.498	-0.024
100	270	0.516	0.492	-0.024
100	271	0.520	0.496	-0.024
100	273	0.518	0.494	-0.024
105	274	0.518	0.518	0.000
105	275	0.518	0.520	0.002
105	276	0.516	0.518	0.002
105	277	0.520	0.518	-0.002
105	278	0.516	0.516	0.000
Max		0.522	0.522	0.002
Average		0.518	0.508	-0.010
Min		0.514	0.490	-0.026
Std Dev		0.002	0.011	0.011

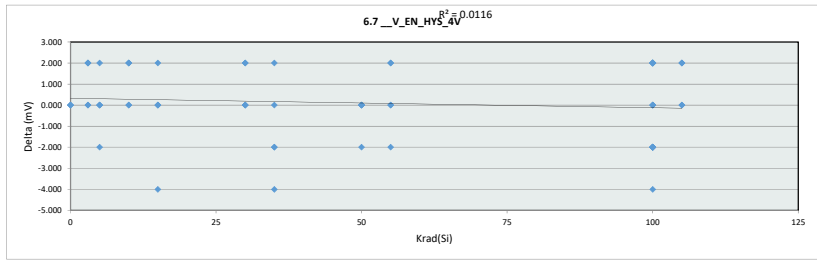


6.15_V_EN_FALL											
Test Site											
Tester											
Test Number											
Max Limit	0.55	V									
Min Limit	0.47	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.516	0.514	0.514	0.514	0.514	0.506	0.516	0.502	0.516	0.490	0.516
Average	0.519	0.516	0.517	0.516	0.518	0.508	0.519	0.505	0.518	0.494	0.518
Max	0.522	0.518	0.520	0.518	0.520	0.510	0.522	0.508	0.520	0.500	0.520
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

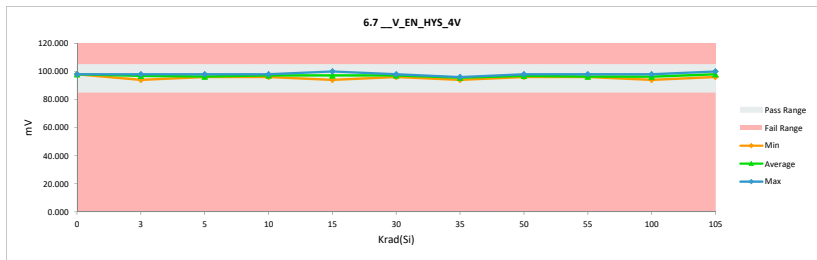


HDR TID Report TPS7H5002-SP

		6.7 __V_EN_HYS_4V		
Test Site	Tester			
Test Number	Unit	mV	mV	
Max Limit	105	105		
Min Limit	85	85		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	98.000	98.000	0.000
0	190	98.000	98.000	0.000
0	191	98.000	98.000	0.000
3	300	98.000	98.000	0.000
3	305	94.000	96.000	2.000
3	309	96.000	98.000	2.000
3	311	94.000	94.000	0.000
3	313	96.000	98.000	2.000
5	319	96.000	96.000	0.000
5	321	98.000	96.000	-2.000
5	324	98.000	98.000	0.000
5	327	96.000	96.000	0.000
5	331	94.000	96.000	2.000
10	234	96.000	98.000	2.000
10	235	96.000	96.000	0.000
10	236	96.000	98.000	2.000
10	237	96.000	98.000	2.000
10	238	96.000	96.000	0.000
15	239	98.000	94.000	-4.000
15	240	96.000	96.000	0.000
15	241	98.000	98.000	0.000
15	243	98.000	98.000	0.000
15	245	98.000	100.000	2.000
30	222	96.000	98.000	2.000
30	223	96.000	98.000	2.000
30	225	96.000	96.000	0.000
30	226	96.000	96.000	0.000
30	227	98.000	98.000	0.000
35	228	94.000	96.000	2.000
35	229	98.000	96.000	-2.000
35	231	98.000	96.000	-2.000
35	232	96.000	96.000	0.000
35	233	98.000	94.000	-4.000
50	210	98.000	98.000	0.000
50	211	98.000	98.000	0.000
50	212	96.000	96.000	0.000
50	213	98.000	96.000	-2.000
50	214	96.000	96.000	0.000
55	215	96.000	96.000	0.000
55	216	98.000	96.000	-2.000
55	217	96.000	98.000	2.000
55	218	96.000	96.000	0.000
55	220	94.000	96.000	2.000
100	247	96.000	98.000	2.000
100	249	96.000	98.000	2.000
100	250	96.000	96.000	0.000
100	251	96.000	98.000	2.000
100	253	96.000	96.000	0.000
100	255	100.000	98.000	-2.000
100	256	96.000	98.000	2.000
100	257	96.000	98.000	2.000
100	258	94.000	96.000	2.000
100	259	94.000	96.000	2.000
100	260	98.000	96.000	-2.000
100	261	98.000	96.000	-2.000
100	262	98.000	96.000	-2.000
100	263	96.000	94.000	-2.000
100	264	96.000	96.000	0.000
100	265	98.000	94.000	-4.000
100	266	96.000	96.000	0.000
100	267	98.000	96.000	-2.000
100	268	98.000	96.000	-2.000
100	269	98.000	96.000	-2.000
100	270	96.000	94.000	-2.000
100	271	94.000	96.000	2.000
100	273	98.000	96.000	-2.000
105	274	94.000	96.000	2.000
105	275	98.000	100.000	2.000
105	276	96.000	98.000	2.000
105	277	98.000	98.000	0.000
105	278	98.000	98.000	0.000
	Max	100.000	100.000	2.000
	Average	96.620	96.704	0.085
	Min	94.000	94.000	-4.000
	Std Dev	1.418	1.356	1.705

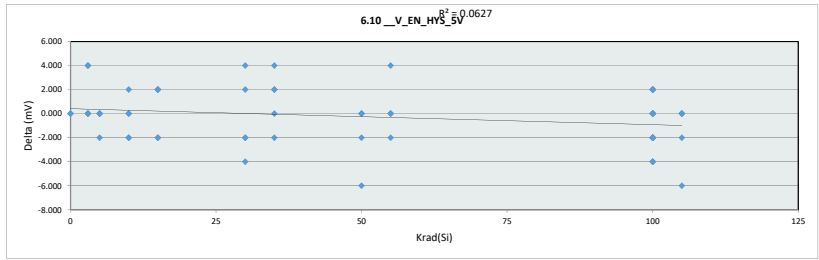


		6.7 __V_EN_HYS_4V										
Test Site	Tester											
Test Number	Unit	mV	mV									
Max Limit	105	105										
Min Limit	85	85										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	98.000	94.000	96.000	96.000	96.000	94.000	96.000	94.000	96.000	96.000	94.000	96.000
Average	98.000	96.800	96.400	97.200	97.200	97.200	95.600	96.800	96.400	96.261	98.000	
Max	98.000	98.000	98.000	98.000	100.000	98.000	96.000	98.000	98.000	98.000	100.000	
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

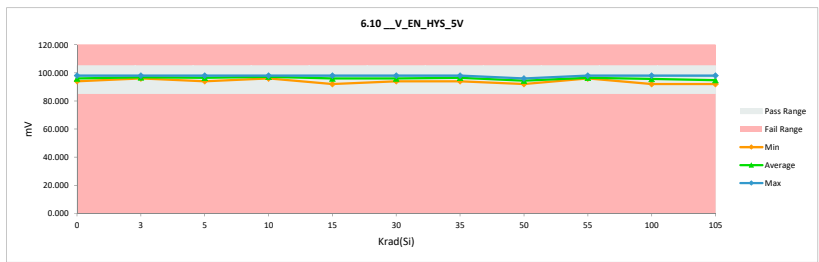


HDR TID Report
TPS7H5002-SP

6.10 __V_EN_HYS_5V				
Test Site	Tester	Test Number	Unit	
			mV	mV
Max Limit			105	105
Min Limit			85	85
Krad(Si)	Serial #	PRE	POST	Delta
0	189	98.000	98.000	0.000
0	190	94.000	94.000	0.000
0	191	96.000	96.000	0.000
3	300	98.000	98.000	0.000
3	305	96.000	96.000	0.000
3	309	96.000	96.000	0.000
3	311	92.000	96.000	4.000
3	313	94.000	98.000	4.000
5	319	96.000	96.000	0.000
5	321	98.000	98.000	0.000
5	324	98.000	98.000	0.000
5	327	98.000	96.000	-2.000
5	331	94.000	94.000	0.000
10	234	98.000	98.000	0.000
10	235	98.000	96.000	-2.000
10	236	98.000	98.000	0.000
10	237	96.000	98.000	2.000
10	238	98.000	96.000	-2.000
15	239	94.000	92.000	-2.000
15	240	98.000	96.000	-2.000
15	241	96.000	98.000	2.000
15	243	96.000	98.000	2.000
15	245	94.000	96.000	2.000
30	222	96.000	98.000	2.000
30	223	98.000	96.000	-2.000
30	225	98.000	94.000	-4.000
30	226	96.000	94.000	-2.000
30	227	94.000	98.000	4.000
35	228	94.000	98.000	4.000
35	229	96.000	98.000	2.000
35	231	96.000	98.000	2.000
35	232	94.000	94.000	0.000
35	233	96.000	94.000	-2.000
50	210	96.000	96.000	0.000
50	211	98.000	92.000	-6.000
50	212	94.000	94.000	0.000
50	213	94.000	94.000	0.000
50	214	98.000	96.000	-2.000
55	215	96.000	96.000	0.000
55	216	98.000	96.000	-2.000
55	217	96.000	96.000	0.000
55	218	96.000	96.000	0.000
55	220	94.000	98.000	4.000
100	247	98.000	98.000	0.000
100	249	96.000	96.000	0.000
100	250	98.000	96.000	-2.000
100	251	96.000	98.000	2.000
100	253	96.000	94.000	-2.000
100	255	98.000	94.000	-4.000
100	256	96.000	96.000	0.000
100	257	96.000	92.000	-4.000
100	258	96.000	96.000	0.000
100	259	96.000	96.000	0.000
100	260	96.000	94.000	-2.000
100	261	98.000	94.000	-4.000
100	262	98.000	96.000	-2.000
100	263	96.000	94.000	-2.000
100	264	96.000	98.000	2.000
100	265	94.000	94.000	0.000
100	266	96.000	98.000	2.000
100	267	98.000	96.000	-2.000
100	268	98.000	98.000	0.000
100	269	96.000	94.000	-2.000
100	270	96.000	94.000	-2.000
100	271	94.000	96.000	2.000
100	273	94.000	98.000	2.000
105	274	94.000	94.000	0.000
105	275	100.000	94.000	-6.000
105	276	96.000	96.000	0.000
105	277	94.000	92.000	-2.000
105	278	98.000	98.000	0.000
Max		100.000	98.000	4.000
Average		96.225	95.915	-0.310
Min		92.000	92.000	-6.000
Std Dev		1.605	1.803	2.182

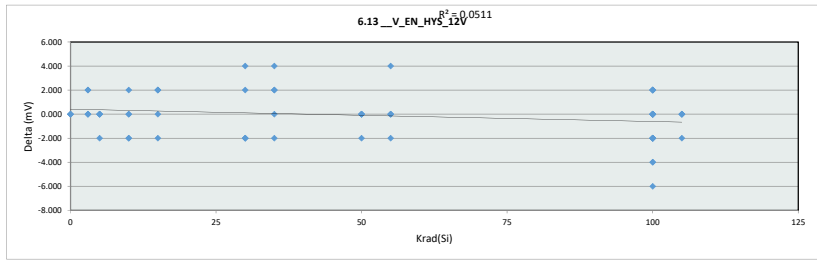


6.10 __V_EN_HYS											
Test Site	Tester	Test Number	Unit								
			mV								
Max Limit			105	mV							
Min Limit			85	mV							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	96.000	94.000	96.000	92.000	94.000	94.000	92.000	96.000	92.000	92.000
Average	96.000	96.800	96.400	97.200	96.000	96.000	96.400	94.400	96.400	95.652	94.800
Max	98.000	98.000	98.000	98.000	98.000	98.000	98.000	96.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

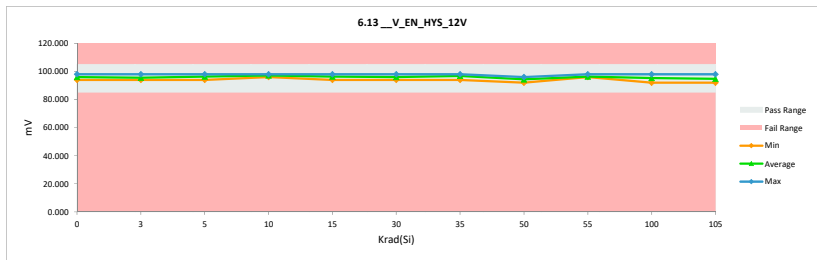


HDR TID Report
TPS7H5002-SP

6.13_V_EN_HYS_12V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	105	105		
Min Limit	85	85		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	98.000	98.000	0.000
0	190	94.000	94.000	0.000
0	191	96.000	96.000	0.000
3	300	98.000	98.000	0.000
3	305	96.000	96.000	0.000
3	309	96.000	96.000	0.000
3	311	92.000	94.000	2.000
3	313	92.000	94.000	2.000
5	319	96.000	96.000	0.000
5	321	98.000	98.000	0.000
5	324	98.000	98.000	0.000
5	327	98.000	96.000	-2.000
5	331	94.000	94.000	0.000
10	234	98.000	98.000	0.000
10	235	98.000	96.000	-2.000
10	236	98.000	98.000	0.000
10	237	96.000	98.000	2.000
10	238	98.000	96.000	-2.000
15	239	94.000	94.000	0.000
15	240	98.000	96.000	-2.000
15	241	96.000	98.000	2.000
15	243	96.000	98.000	2.000
15	245	94.000	96.000	2.000
30	222	96.000	98.000	2.000
30	223	98.000	96.000	-2.000
30	225	96.000	94.000	-2.000
30	226	96.000	94.000	-2.000
30	227	94.000	98.000	4.000
35	228	94.000	98.000	4.000
35	229	98.000	98.000	0.000
35	231	96.000	98.000	2.000
35	232	94.000	96.000	2.000
35	233	96.000	94.000	-2.000
50	210	96.000	96.000	0.000
50	211	94.000	92.000	-2.000
50	212	94.000	94.000	0.000
50	213	94.000	94.000	0.000
50	214	96.000	96.000	0.000
55	215	96.000	96.000	0.000
55	216	98.000	96.000	-2.000
55	217	96.000	96.000	0.000
55	218	96.000	96.000	0.000
55	220	94.000	98.000	4.000
100	247	98.000	98.000	0.000
100	249	96.000	96.000	0.000
100	250	98.000	96.000	-2.000
100	251	96.000	98.000	2.000
100	253	96.000	94.000	-2.000
100	255	98.000	92.000	-6.000
100	256	96.000	96.000	0.000
100	257	94.000	92.000	-2.000
100	258	96.000	96.000	0.000
100	259	96.000	96.000	0.000
100	260	96.000	94.000	-2.000
100	261	98.000	94.000	-4.000
100	262	98.000	96.000	-2.000
100	263	96.000	92.000	-4.000
100	264	96.000	98.000	2.000
100	265	92.000	92.000	0.000
100	266	96.000	98.000	2.000
100	267	96.000	96.000	0.000
100	268	98.000	98.000	0.000
100	269	96.000	94.000	-2.000
100	270	96.000	94.000	-2.000
100	271	94.000	96.000	2.000
100	273	96.000	98.000	2.000
105	274	94.000	94.000	0.000
105	275	94.000	94.000	0.000
105	276	96.000	96.000	0.000
105	277	94.000	92.000	-2.000
105	278	98.000	98.000	0.000
Max		98.000	98.000	4.000
Average		95.944	95.803	-0.141
Min		92.000	92.000	-6.000
Std Dev		1.655	1.887	1.862

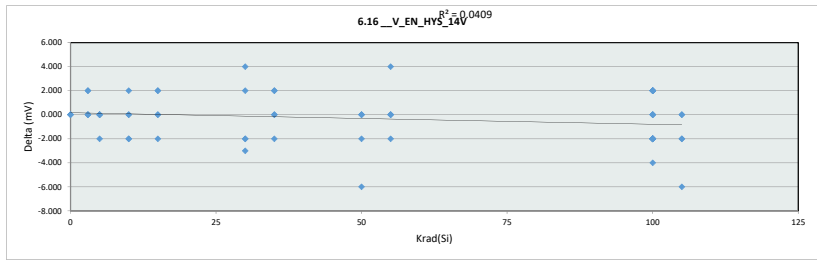


6.13_V_EN_HYS											
Test Site											
Tester											
Test Number											
Max Limit	105	mV									
Min Limit	85	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	94.000	96.000	94.000	94.000	94.000	92.000	96.000	92.000	92.000
Average	96.000	95.600	96.400	97.200	96.400	96.000	96.800	94.400	96.400	95.391	94.800
Max	98.000	98.000	98.000	98.000	98.000	98.000	98.000	96.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

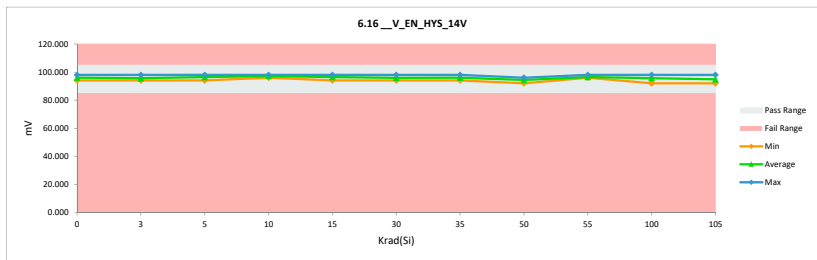


HDR TID Report
TPS7H5002-SP

6.16_V_EN_HYS_14V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	105	105		
Min Limit	85	85		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	98.000	98.000	0.000
0	190	94.000	94.000	0.000
0	191	96.000	96.000	0.000
3	300	98.000	98.000	0.000
3	305	96.000	96.000	0.000
3	309	96.000	96.000	0.000
3	311	92.000	94.000	2.000
3	313	92.000	94.000	2.000
5	319	96.000	96.000	0.000
5	321	98.000	98.000	0.000
5	324	98.000	98.000	0.000
5	327	98.000	96.000	-2.000
5	331	94.000	94.000	0.000
10	234	98.000	98.000	0.000
10	235	98.000	96.000	-2.000
10	236	98.000	98.000	0.000
10	237	96.000	98.000	2.000
10	238	98.000	96.000	-2.000
15	239	94.000	94.000	0.000
15	240	98.000	96.000	-2.000
15	241	96.000	98.000	2.000
15	243	96.000	98.000	2.000
15	245	96.000	96.000	0.000
30	222	96.000	98.000	2.000
30	223	98.000	95.000	-3.000
30	225	96.000	94.000	-2.000
30	226	96.000	94.000	-2.000
30	227	94.000	98.000	4.000
35	228	94.000	96.000	2.000
35	229	98.000	98.000	0.000
35	231	96.000	98.000	2.000
35	232	94.000	94.000	0.000
35	233	96.000	94.000	-2.000
50	210	96.000	96.000	0.000
50	211	98.000	92.000	-6.000
50	212	94.000	94.000	0.000
50	213	94.000	94.000	0.000
50	214	98.000	96.000	-2.000
55	215	96.000	96.000	0.000
55	216	98.000	96.000	-2.000
55	217	96.000	96.000	0.000
55	218	96.000	96.000	0.000
55	220	94.000	98.000	4.000
100	247	98.000	98.000	0.000
100	249	96.000	96.000	0.000
100	250	98.000	96.000	-2.000
100	251	96.000	98.000	2.000
100	253	96.000	94.000	-2.000
100	255	94.000	92.000	-2.000
100	256	96.000	96.000	0.000
100	257	96.000	94.000	-2.000
100	258	96.000	96.000	0.000
100	259	96.000	94.000	-2.000
100	260	96.000	94.000	-2.000
100	261	98.000	94.000	-4.000
100	262	98.000	96.000	-2.000
100	263	96.000	94.000	-2.000
100	264	96.000	98.000	2.000
100	265	92.000	94.000	2.000
100	266	96.000	98.000	2.000
100	267	98.000	96.000	-2.000
100	268	96.000	98.000	2.000
100	269	96.000	94.000	-2.000
100	270	96.000	94.000	-2.000
100	271	94.000	96.000	2.000
100	273	96.000	98.000	2.000
105	274	96.000	94.000	-2.000
105	275	100.000	94.000	-6.000
105	276	96.000	96.000	0.000
105	277	94.000	92.000	-2.000
105	278	98.000	98.000	0.000
Max		100.000	98.000	4.000
Average		96.141	95.789	-0.352
Min		92.000	92.000	-6.000
Std Dev		1.667	1.764	1.957

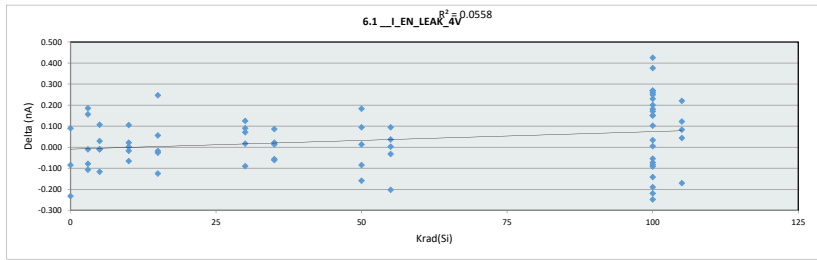


6.16_V_EN_HYS											
Test Site											
Tester											
Test Number											
Max Limit	105	mV									
Min Limit	85	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	94.000	96.000	94.000	94.000	94.000	92.000	96.000	92.000	92.000
Average	96.000	95.600	96.400	97.200	96.400	95.800	96.000	94.400	96.400	95.565	94.800
Max	98.000	98.000	98.000	98.000	98.000	98.000	98.000	96.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

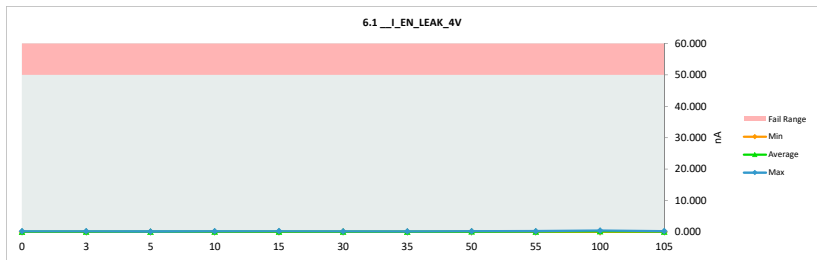


HDR TID Report
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6.1_I_EN_LEAK_4V				
Test Site				
Tester				
Test Number				
Unit	nA	nA		
Max Limit	50	50		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.237	0.005	-0.232
0	190	0.139	0.054	-0.085
0	191	0.120	0.210	0.090
3	300	0.020	0.176	0.156
3	305	0.039	0.029	-0.010
3	309	0.195	0.088	-0.107
3	311	0.020	0.205	0.185
3	313	0.225	0.146	-0.079
5	319	0.020	0.127	0.107
5	321	0.127	0.117	-0.010
5	324	0.029	0.020	-0.009
5	327	0.078	0.107	0.029
5	331	0.195	0.078	-0.117
10	234	0.120	0.142	0.022
10	235	0.105	0.210	0.105
10	236	0.129	0.053	-0.066
10	237	0.159	0.142	-0.017
10	238	0.022	0.024	0.002
15	239	0.041	0.015	-0.026
15	240	0.081	0.063	-0.018
15	241	0.046	0.102	0.056
15	243	0.012	0.259	0.247
15	245	0.198	0.073	-0.125
30	222	0.017	0.142	0.125
30	223	0.100	0.190	0.090
30	225	0.115	0.132	0.017
30	226	0.041	0.112	0.071
30	227	0.144	0.054	-0.090
35	228	0.120	0.063	-0.057
35	229	0.066	0.005	-0.061
35	231	0.085	0.171	0.086
35	232	0.002	0.015	0.013
35	233	0.041	0.063	0.022
50	210	0.066	0.249	0.183
50	211	0.007	0.102	0.095
50	212	0.207	0.122	-0.085
50	213	0.129	0.142	0.013
50	214	0.320	0.161	-0.159
55	215	0.105	0.073	-0.032
55	216	0.051	0.054	0.003
55	217	0.227	0.024	-0.203
55	218	0.144	0.239	0.095
55	220	0.163	0.200	0.037
100	247	0.307	0.059	-0.248
100	249	0.093	0.342	0.249
100	250	0.102	0.371	0.269
100	251	0.122	0.039	-0.083
100	253	0.278	0.059	-0.219
100	255	0.034	0.068	0.034
100	256	0.200	0.127	-0.073
100	257	0.015	0.273	0.258
100	258	0.054	0.156	0.102
100	259	0.034	0.410	0.376
100	260	0.239	0.185	-0.054
100	261	0.083	0.234	0.151
100	262	0.190	0.420	0.230
100	263	0.063	0.244	0.181
100	264	0.034	0.303	0.269
100	265	0.112	0.020	-0.092
100	266	0.015	0.215	0.200
100	267	0.210	0.068	-0.142
100	268	0.112	0.283	0.171
100	269	0.024	0.449	0.425
100	270	0.015	0.166	0.151
100	271	0.093	0.098	0.005
100	273	0.268	0.078	-0.190
105	274	0.112	0.234	0.122
105	275	0.015	0.059	0.044
105	276	0.015	0.098	0.083
105	277	0.200	0.029	-0.171
105	278	0.044	0.264	0.220
Max		0.320	0.449	0.425
Average		0.107	0.143	0.036
Min		0.002	0.005	-0.248
Std Dev		0.081	0.106	0.144

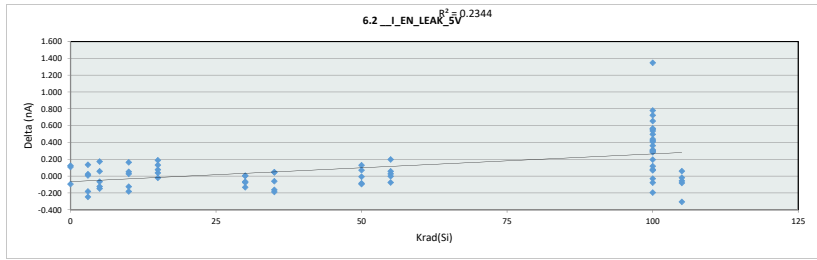


6.1_I_EN_LEAK											
Test Site											
Tester											
Test Number											
Max Limit	50	nA									
Min Limit		nA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.005	0.029	0.020	0.024	0.015	0.054	0.005	0.102	0.024	0.020	0.029
Average	0.090	0.129	0.090	0.116	0.102	0.126	0.063	0.155	0.118	0.203	0.137
Max	0.210	0.205	0.127	0.210	0.259	0.190	0.171	0.249	0.239	0.449	0.264
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000

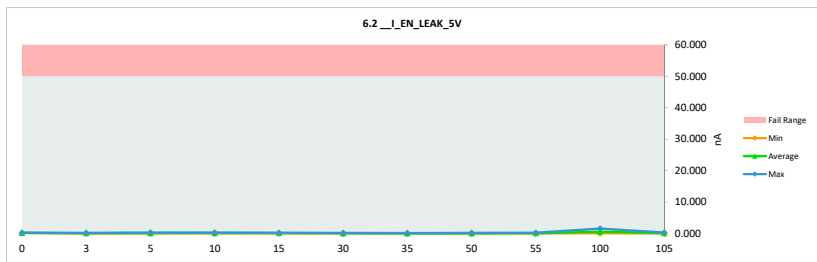


HDR TID Report
TPS7H5002-SP

6.2_I_EN_LEAK_5V				
Test Site				
Tester				
Test Number				
Unit	nA	nA		
Max Limit	50	50		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.264	0.168	-0.096
0	190	0.049	0.173	0.124
0	191	0.137	0.246	0.109
3	300	0.146	0.168	0.022
3	305	0.273	0.090	-0.183
3	309	0.098	0.105	0.007
3	311	0.010	0.144	0.134
3	313	0.254	0.007	-0.247
5	319	0.059	0.115	0.056
5	321	0.244	0.095	-0.149
5	324	0.185	0.061	-0.124
5	327	0.088	0.261	0.173
5	331	0.088	0.022	-0.066
10	234	0.234	0.051	-0.183
10	235	0.010	0.061	0.051
10	236	0.244	0.271	0.027
10	237	0.059	0.222	0.163
10	238	0.137	0.012	-0.125
15	239	0.068	0.144	0.076
15	240	0.029	0.159	0.130
15	241	0.078	0.115	0.037
15	243	0.010	0.198	0.188
15	245	0.195	0.173	-0.022
30	222	0.234	0.100	-0.134
30	223	0.088	0.022	-0.066
30	225	0.127	0.051	-0.076
30	226	0.029	0.032	0.003
30	227	0.195	0.198	0.003
35	228	0.049	0.095	0.046
35	229	0.283	0.120	-0.163
35	231	0.205	0.017	-0.188
35	232	0.137	0.076	-0.061
35	233	0.010	0.056	0.046
50	210	0.098	0.090	-0.008
50	211	0.000	0.129	0.129
50	212	0.137	0.051	-0.086
50	213	0.088	0.159	0.071
50	214	0.107	0.012	-0.095
55	215	0.078	0.076	-0.002
55	216	0.049	0.246	0.197
55	217	0.078	0.105	0.027
55	218	0.020	0.076	0.056
55	220	0.088	0.012	-0.076
100	247	0.012	0.373	0.361
100	249	0.159	0.451	0.292
100	250	0.168	0.822	0.654
100	251	0.139	0.061	-0.078
100	253	0.071	0.383	0.312
100	255	0.081	0.276	0.195
100	256	0.325	0.403	0.078
100	257	0.217	0.286	0.069
100	258	0.124	0.530	0.406
100	259	0.276	0.998	0.722
100	260	0.286	0.090	-0.196
100	261	0.139	0.256	0.117
100	262	0.198	1.545	1.347
100	263	0.295	0.715	0.420
100	264	0.344	0.783	0.439
100	265	0.286	0.256	-0.030
100	266	0.120	0.617	0.497
100	267	0.237	0.539	0.302
100	268	0.120	0.900	0.780
100	269	0.100	0.637	0.537
100	270	0.207	0.764	0.557
100	271	0.032	0.598	0.566
100	273	0.051	0.334	0.283
105	274	0.373	0.066	-0.307
105	275	0.120	0.178	0.058
105	276	0.120	0.037	-0.083
105	277	0.120	0.100	-0.020
105	278	0.344	0.286	-0.058
Max		0.373	1.545	1.347
Average		0.143	0.254	0.112
Min		0.000	0.007	-0.307
Std Dev		0.096	0.284	0.279

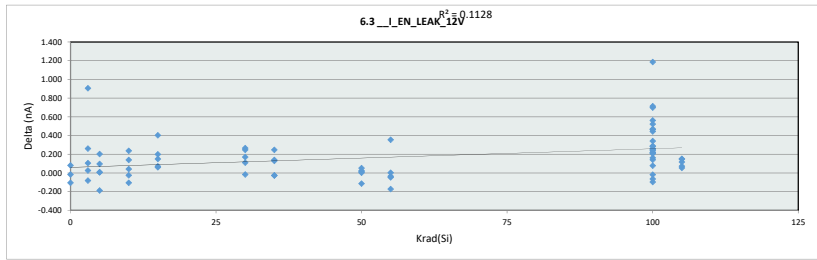


6.2_I_EN_LEAK											
Test Site											
Tester											
Test Number											
Max Limit	50	nA									
Min Limit		nA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	0.168	0.007	0.022	0.012	0.115	0.022	0.017	0.012	0.012	0.061	0.037
Average	0.196	0.103	0.111	0.123	0.158	0.081	0.073	0.088	0.103	0.549	0.133
Max	0.246	0.168	0.261	0.271	0.198	0.198	0.120	0.159	0.246	1.545	0.286
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000

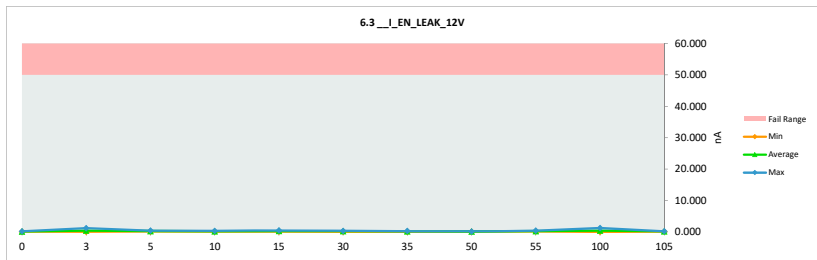


HDR TID Report
TPS7H5002-SP

6.3 I_EN_LEAK_12V				
Test Site				
Tester				
Test Number				
Unit	nA	nA		
Max Limit	50	50		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.195	0.090	-0.105
0	190	0.166	0.149	-0.017
0	191	0.117	0.198	0.081
3	300	0.293	1.198	0.905
3	305	0.176	0.281	0.105
3	309	0.039	0.300	0.261
3	311	0.020	0.046	0.026
3	313	0.303	0.222	-0.081
5	319	0.195	0.203	0.008
5	321	0.146	0.242	0.096
5	324	0.127	0.134	0.007
5	327	0.322	0.134	-0.188
5	331	0.225	0.427	0.202
10	234	0.107	0.344	0.237
10	235	0.234	0.129	-0.105
10	236	0.146	0.120	-0.026
10	237	0.078	0.120	0.042
10	238	0.117	0.256	0.139
15	239	0.078	0.481	0.403
15	240	0.225	0.295	0.070
15	241	0.146	0.295	0.149
15	243	0.059	0.120	0.061
15	245	0.000	0.198	0.198
30	222	0.146	0.315	0.169
30	223	0.020	0.266	0.246
30	225	0.078	0.061	-0.017
30	226	0.078	0.344	0.266
30	227	0.078	0.188	0.110
35	228	0.166	0.305	0.139
35	229	0.088	0.061	-0.027
35	231	0.039	0.286	0.247
35	232	0.059	0.032	-0.027
35	233	0.137	0.266	0.129
50	210	0.098	0.149	0.051
50	211	0.195	0.081	-0.114
50	212	0.098	0.100	0.002
50	213	0.088	0.110	0.022
50	214	0.127	0.139	0.012
55	215	0.195	0.159	-0.036
55	216	0.264	0.217	-0.047
55	217	0.068	0.422	0.354
55	218	0.254	0.256	0.002
55	220	0.361	0.188	-0.173
100	247	0.071	0.412	0.341
100	249	0.085	0.305	0.220
100	250	0.129	0.842	0.713
100	251	0.207	0.110	-0.097
100	253	0.110	0.188	0.078
100	255	0.002	0.286	0.284
100	256	0.037	0.178	0.141
100	257	0.076	0.012	-0.064
100	258	0.007	0.451	0.444
100	259	0.085	0.783	0.698
100	260	0.037	0.198	0.161
100	261	0.022	0.227	0.205
100	262	0.066	1.252	1.186
100	263	0.139	0.354	0.215
100	264	0.183	0.705	0.522
100	265	0.056	0.037	-0.019
100	266	0.193	0.481	0.288
100	267	0.144	0.608	0.464
100	268	0.144	0.403	0.259
100	269	0.007	0.569	0.562
100	270	0.207	0.461	0.254
100	271	0.041	0.510	0.469
100	273	0.037	0.276	0.239
105	274	0.002	0.149	0.147
105	275	0.134	0.207	0.073
105	276	0.095	0.212	0.117
105	277	0.051	0.105	0.054
105	278	0.076	0.227	0.151
	Max	0.361	1.252	1.186
	Average	0.121	0.288	0.167
	Min	0.000	0.012	-0.188
	Std Dev	0.082	0.235	0.243

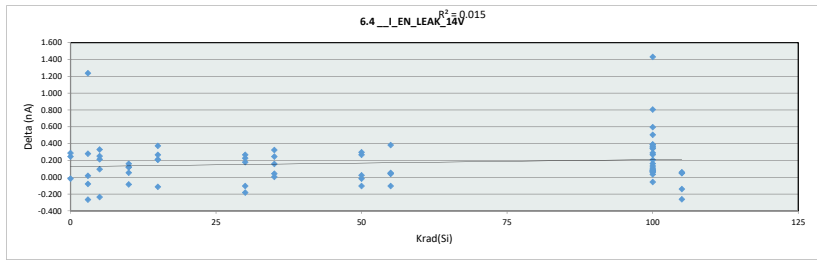


6.3 I EN LEAK											
Test Site											
Tester											
Test Number											
Max Limit	50	nA									
Min Limit		nA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.090	0.046	0.134	0.120	0.120	0.061	0.032	0.081	0.159	0.012	0.105
Average	0.146	0.409	0.228	0.194	0.278	0.235	0.190	0.116	0.248	0.419	0.180
Max	0.198	1.198	0.427	0.344	0.481	0.344	0.305	0.149	0.422	1.252	0.227
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000

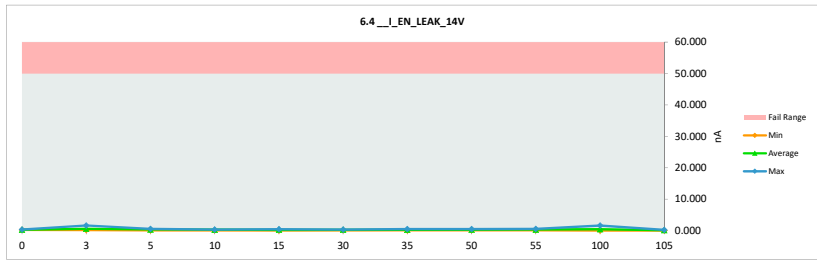


HDR TID Report
TPS7H5002-SP

6.4_I_EN_LEAK_14V				
Test Site				
Tester				
Test Number				
Unit	nA	nA		
Max Limit	50	50		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.049	0.295	0.246
0	190	0.029	0.315	0.286
0	191	0.225	0.207	-0.018
3	300	0.381	1.618	1.237
3	305	0.322	0.339	0.017
3	309	0.420	0.154	-0.266
3	311	0.176	0.456	0.280
3	313	0.332	0.251	-0.081
5	319	0.293	0.056	-0.237
5	321	0.303	0.554	0.251
5	324	0.205	0.417	0.212
5	327	0.010	0.339	0.329
5	331	0.225	0.320	0.095
10	234	0.156	0.315	0.159
10	235	0.176	0.295	0.119
10	236	0.078	0.198	0.120
10	237	0.029	0.081	0.052
10	238	0.361	0.276	-0.085
15	239	0.029	0.237	0.208
15	240	0.117	0.383	0.266
15	241	0.195	0.403	0.208
15	243	0.059	0.432	0.373
15	245	0.117	0.002	-0.115
30	222	0.312	0.129	-0.183
30	223	0.078	0.344	0.266
30	225	0.088	0.315	0.227
30	226	0.088	0.266	0.178
30	227	0.351	0.246	-0.105
35	228	0.020	0.344	0.324
35	229	0.244	0.490	0.246
35	231	0.029	0.032	0.003
35	232	0.020	0.061	0.041
35	233	0.137	0.295	0.158
50	210	0.098	0.120	0.022
50	211	0.185	0.168	-0.017
50	212	0.273	0.168	-0.105
50	213	0.107	0.403	0.296
50	214	0.195	0.461	0.266
55	215	0.244	0.286	0.042
55	216	0.225	0.120	-0.105
55	217	0.127	0.510	0.383
55	218	0.215	0.256	0.041
55	220	0.254	0.305	0.051
100	247	0.183	0.315	0.132
100	249	0.154	0.442	0.288
100	250	0.178	0.774	0.596
100	251	0.124	0.198	0.074
100	253	0.027	0.295	0.268
100	255	0.173	0.237	0.064
100	256	0.007	0.041	0.034
100	257	0.022	0.115	0.093
100	258	0.134	0.295	0.161
100	259	0.183	0.988	0.805
100	260	0.154	0.212	0.058
100	261	0.032	0.237	0.205
100	262	0.183	1.613	1.430
100	263	0.134	0.207	0.073
100	264	0.110	0.500	0.390
100	265	0.134	0.242	0.108
100	266	0.105	0.471	0.366
100	267	0.124	0.066	-0.058
100	268	0.222	0.725	0.503
100	269	0.222	0.608	0.386
100	270	0.071	0.412	0.341
100	271	0.212	0.559	0.347
100	273	0.139	0.217	0.078
105	274	0.300	0.037	-0.263
105	275	0.154	0.012	-0.142
105	276	0.183	0.232	0.049
105	277	0.066	0.124	0.058
105	278	0.046	0.095	0.049
	Max	0.420	1.618	1.430
	Average	0.160	0.331	0.172
	Min	0.007	0.002	-0.266
	Std Dev	0.100	0.286	0.280

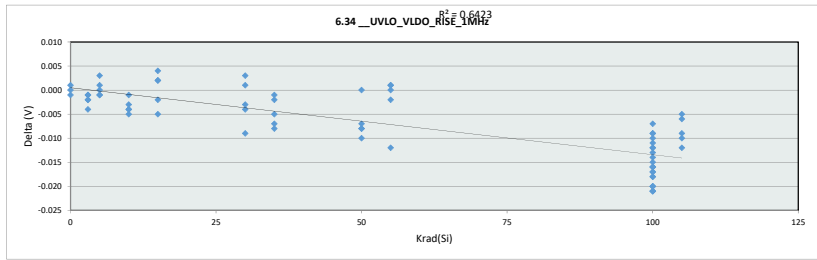


6.4_I_EN_LEAK											
Test Site											
Tester											
Test Number											
Max Limit	50	nA									
Min Limit		nA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	0.207	0.154	0.056	0.081	0.002	0.129	0.032	0.120	0.120	0.041	0.012
Average	0.272	0.564	0.337	0.233	0.291	0.260	0.244	0.264	0.295	0.425	0.100
Max	0.315	1.618	0.554	0.315	0.432	0.344	0.490	0.461	0.510	1.613	0.232
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000

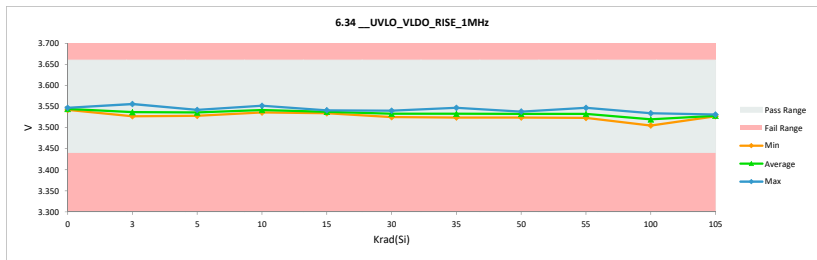


HDR TID Report
TPS7H5002-SP

6.34 UVLO_VLDO_RISE_1MHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	3.543	3.544	0.001
0	190	3.547	3.547	0.000
0	191	3.543	3.542	-0.001
3	300	3.534	3.530	-0.004
3	305	3.557	3.556	-0.001
3	309	3.529	3.527	-0.002
3	311	3.544	3.542	-0.002
3	313	3.530	3.529	-0.001
5	319	3.534	3.534	0.000
5	321	3.538	3.537	-0.001
5	324	3.540	3.539	-0.001
5	327	3.527	3.528	0.001
5	331	3.539	3.542	0.003
10	234	3.544	3.539	-0.005
10	235	3.555	3.552	-0.003
10	236	3.545	3.544	-0.001
10	237	3.542	3.538	-0.004
10	238	3.540	3.536	-0.004
15	239	3.537	3.535	-0.002
15	240	3.539	3.534	-0.005
15	241	3.535	3.539	0.004
15	243	3.539	3.541	0.002
15	245	3.536	3.538	0.002
30	222	3.532	3.533	0.001
30	223	3.529	3.525	-0.004
30	225	3.542	3.533	-0.009
30	226	3.531	3.534	0.003
30	227	3.543	3.540	-0.003
35	228	3.531	3.524	-0.007
35	229	3.548	3.547	-0.001
35	231	3.534	3.532	-0.002
35	232	3.541	3.536	-0.005
35	233	3.534	3.526	-0.008
50	210	3.545	3.538	-0.007
50	211	3.538	3.528	-0.010
50	212	3.532	3.524	-0.008
50	213	3.535	3.535	0.000
50	214	3.546	3.538	-0.008
55	215	3.538	3.536	-0.002
55	216	3.522	3.523	0.001
55	217	3.525	3.525	0.000
55	218	3.546	3.547	0.001
55	220	3.544	3.537	-0.012
100	247	3.536	3.515	-0.021
100	249	3.535	3.523	-0.012
100	250	3.534	3.524	-0.010
100	251	3.546	3.529	-0.017
100	253	3.528	3.512	-0.016
100	255	3.534	3.525	-0.009
100	256	3.526	3.515	-0.011
100	257	3.528	3.508	-0.020
100	258	3.531	3.517	-0.014
100	259	3.546	3.533	-0.013
100	260	3.536	3.519	-0.017
100	261	3.533	3.517	-0.016
100	262	3.530	3.512	-0.018
100	263	3.527	3.518	-0.009
100	264	3.534	3.522	-0.012
100	265	3.552	3.531	-0.021
100	266	3.536	3.516	-0.020
100	267	3.534	3.519	-0.015
100	268	3.534	3.514	-0.020
100	269	3.526	3.519	-0.007
100	270	3.526	3.505	-0.021
100	271	3.552	3.534	-0.018
100	273	3.540	3.524	-0.016
105	274	3.537	3.531	-0.006
105	275	3.532	3.527	-0.005
105	276	3.539	3.527	-0.012
105	277	3.537	3.527	-0.010
105	278	3.538	3.529	-0.009
	Max	3.557	3.556	0.004
	Average	3.537	3.530	-0.007
	Min	3.522	3.505	-0.021
	Std Dev	0.007	0.011	0.007

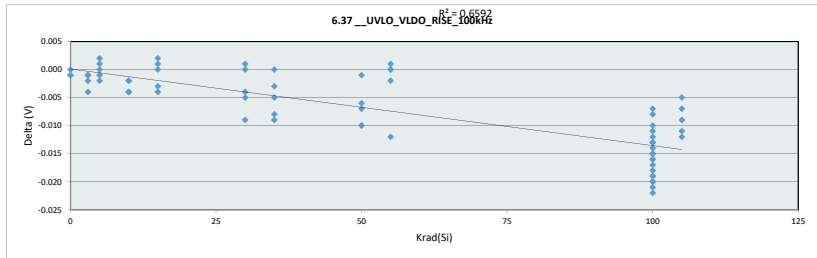


6.34 UVLO_VLDO											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.542	3.527	3.528	3.536	3.534	3.525	3.524	3.524	3.523	3.505	3.527
Average	3.544	3.537	3.536	3.542	3.537	3.533	3.533	3.533	3.533	3.520	3.528
Max	3.547	3.556	3.542	3.552	3.541	3.540	3.547	3.538	3.547	3.534	3.531
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

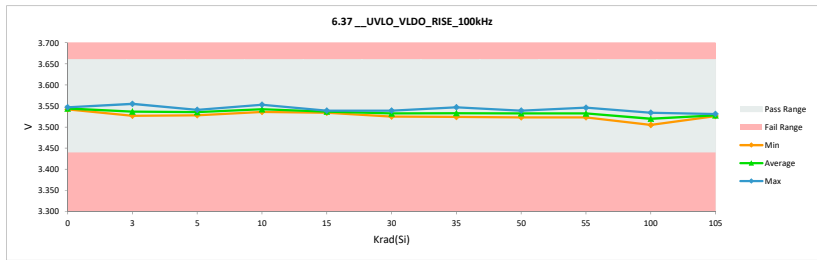


HDR TID Report
TPS7H5002-SP

6.37 UVLO_VLDO_RISE_100kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	3.543	3.543	0.000
0	190	3.548	3.547	-0.001
0	191	3.543	3.542	-0.001
3	300	3.534	3.530	-0.004
3	305	3.557	3.555	-0.002
3	309	3.528	3.527	-0.001
3	311	3.543	3.542	-0.001
3	313	3.530	3.529	-0.001
5	319	3.533	3.532	-0.001
5	321	3.539	3.537	-0.002
5	324	3.539	3.539	0.000
5	327	3.527	3.528	0.001
5	331	3.539	3.541	0.002
10	234	3.543	3.539	-0.004
10	235	3.555	3.553	-0.002
10	236	3.546	3.544	-0.002
10	237	3.543	3.539	-0.004
10	238	3.540	3.536	-0.004
15	239	3.537	3.534	-0.003
15	240	3.539	3.535	-0.004
15	241	3.537	3.539	0.002
15	243	3.539	3.539	0.000
15	245	3.536	3.537	0.001
30	222	3.533	3.533	0.000
30	223	3.530	3.525	-0.005
30	225	3.541	3.532	-0.009
30	226	3.532	3.533	0.001
30	227	3.543	3.539	-0.004
35	228	3.532	3.524	-0.008
35	229	3.547	3.547	0.000
35	231	3.535	3.532	-0.003
35	232	3.541	3.536	-0.005
35	233	3.535	3.526	-0.009
50	210	3.544	3.538	-0.006
50	211	3.539	3.529	-0.010
50	212	3.533	3.523	-0.010
50	213	3.535	3.534	-0.001
50	214	3.546	3.539	-0.007
55	215	3.538	3.536	-0.002
55	216	3.523	3.523	0.000
55	217	3.525	3.525	0.000
55	218	3.545	3.546	0.001
55	220	3.544	3.532	-0.012
100	247	3.535	3.515	-0.020
100	249	3.535	3.523	-0.012
100	250	3.534	3.523	-0.011
100	251	3.546	3.530	-0.016
100	253	3.527	3.512	-0.015
100	255	3.535	3.527	-0.008
100	256	3.528	3.515	-0.013
100	257	3.527	3.507	-0.020
100	258	3.531	3.517	-0.014
100	259	3.547	3.534	-0.013
100	260	3.534	3.519	-0.015
100	261	3.534	3.517	-0.017
100	262	3.530	3.512	-0.018
100	263	3.527	3.517	-0.010
100	264	3.535	3.522	-0.013
100	265	3.553	3.531	-0.022
100	266	3.536	3.516	-0.020
100	267	3.534	3.518	-0.016
100	268	3.534	3.515	-0.019
100	269	3.526	3.519	-0.007
100	270	3.526	3.505	-0.021
100	271	3.553	3.534	-0.019
100	273	3.539	3.524	-0.015
105	274	3.536	3.531	-0.005
105	275	3.533	3.526	-0.007
105	276	3.539	3.527	-0.012
105	277	3.537	3.526	-0.011
105	278	3.538	3.529	-0.009
Max		3.557	3.555	0.002
Average		3.537	3.530	-0.007
Min		3.523	3.505	-0.022
Std Dev		0.007	0.011	0.007

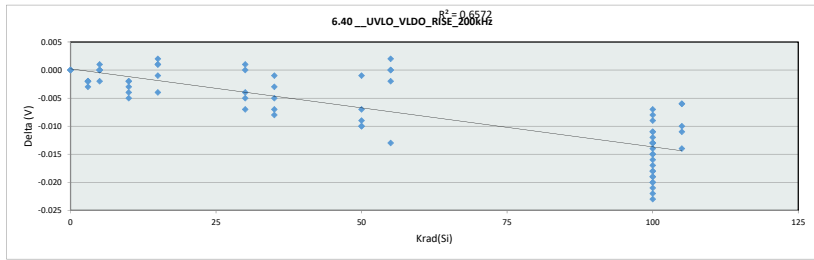


6.37 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.542	3.527	3.528	3.536	3.534	3.525	3.524	3.523	3.523	3.505	3.526
Average	3.544	3.537	3.535	3.542	3.537	3.532	3.533	3.533	3.532	3.520	3.528
Max	3.547	3.555	3.541	3.553	3.539	3.539	3.547	3.539	3.546	3.534	3.531
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

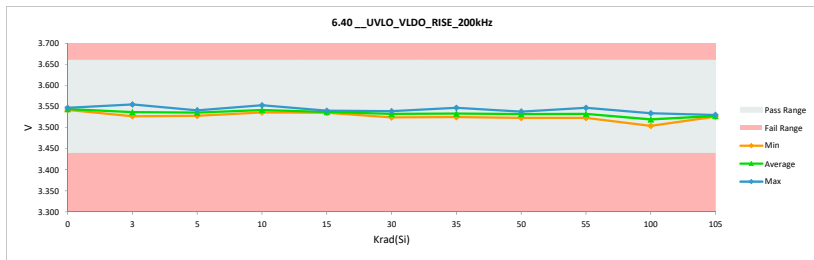


HDR TID Report TPS7H5002-SP

6.40_UVLO_VLDO_RISE_200kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	3.543	3.543	0.000
0	190	3.547	3.547	0.000
0	191	3.542	3.542	0.000
3	300	3.534	3.531	-0.003
3	305	3.557	3.555	-0.002
3	309	3.529	3.527	-0.002
3	311	3.544	3.542	-0.002
3	313	3.530	3.528	-0.002
5	319	3.534	3.534	0.000
5	321	3.538	3.536	-0.002
5	324	3.539	3.539	0.000
5	327	3.528	3.528	0.000
5	331	3.540	3.541	0.001
10	234	3.543	3.538	-0.005
10	235	3.555	3.553	-0.002
10	236	3.545	3.543	-0.002
10	237	3.543	3.539	-0.004
10	238	3.539	3.536	-0.003
15	239	3.536	3.535	-0.001
15	240	3.539	3.535	-0.004
15	241	3.536	3.538	0.002
15	243	3.539	3.540	0.001
15	245	3.536	3.537	0.001
30	222	3.533	3.533	0.000
30	223	3.529	3.524	-0.005
30	225	3.540	3.533	-0.007
30	226	3.532	3.533	0.001
30	227	3.543	3.539	-0.004
35	228	3.532	3.525	-0.007
35	229	3.548	3.547	-0.001
35	231	3.535	3.532	-0.003
35	232	3.541	3.536	-0.005
35	233	3.534	3.526	-0.008
50	210	3.544	3.537	-0.007
50	211	3.538	3.528	-0.010
50	212	3.533	3.523	-0.010
50	213	3.535	3.534	-0.001
50	214	3.547	3.538	-0.009
55	215	3.538	3.536	-0.002
55	216	3.523	3.523	0.000
55	217	3.525	3.525	0.000
55	218	3.545	3.547	0.002
55	220	3.544	3.531	-0.013
100	247	3.535	3.515	-0.020
100	249	3.535	3.522	-0.013
100	250	3.534	3.523	-0.011
100	251	3.546	3.529	-0.017
100	253	3.527	3.513	-0.014
100	255	3.535	3.526	-0.009
100	256	3.526	3.515	-0.011
100	257	3.527	3.508	-0.019
100	258	3.530	3.517	-0.013
100	259	3.547	3.534	-0.013
100	260	3.535	3.519	-0.016
100	261	3.533	3.518	-0.015
100	262	3.530	3.511	-0.019
100	263	3.527	3.519	-0.008
100	264	3.534	3.522	-0.012
100	265	3.553	3.530	-0.023
100	266	3.536	3.515	-0.021
100	267	3.539	3.519	-0.020
100	268	3.533	3.515	-0.018
100	269	3.526	3.519	-0.007
100	270	3.526	3.504	-0.022
100	271	3.551	3.533	-0.018
100	273	3.539	3.524	-0.015
105	274	3.536	3.530	-0.006
105	275	3.532	3.526	-0.006
105	276	3.540	3.526	-0.014
105	277	3.538	3.527	-0.011
105	278	3.538	3.528	-0.010
	Max	3.557	3.555	0.002
	Average	3.537	3.530	-0.007
	Min	3.523	3.504	-0.023
	Std Dev	0.007	0.010	0.007

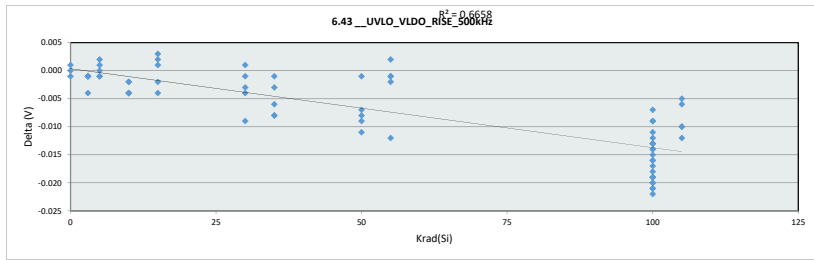


6.40_UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.542	3.527	3.528	3.536	3.535	3.524	3.525	3.523	3.523	3.504	3.526
Average	3.544	3.537	3.536	3.542	3.537	3.532	3.533	3.532	3.532	3.520	3.527
Max	3.547	3.555	3.541	3.553	3.540	3.539	3.547	3.538	3.547	3.534	3.530
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

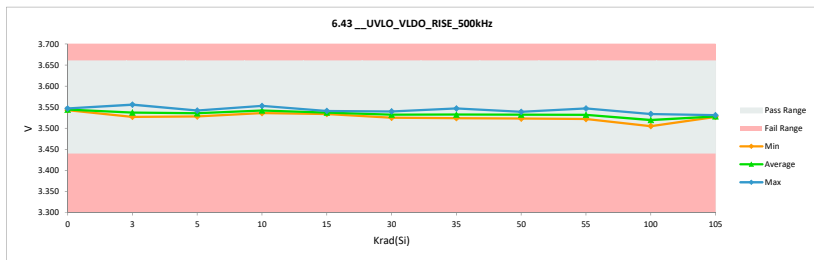


HDR TID Report
TPS7H5002-SP

6.43 UVLO_VLDO_RISE_500kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	3.543	3.543	0.000
0	190	3.548	3.547	-0.001
0	191	3.542	3.543	0.001
3	300	3.535	3.531	-0.004
3	305	3.557	3.556	-0.001
3	309	3.528	3.527	-0.001
3	311	3.544	3.543	-0.001
3	313	3.530	3.529	-0.001
5	319	3.534	3.533	-0.001
5	321	3.538	3.537	-0.001
5	324	3.538	3.538	0.000
5	327	3.527	3.528	0.001
5	331	3.540	3.542	0.002
10	234	3.543	3.539	-0.004
10	235	3.555	3.553	-0.002
10	236	3.546	3.544	-0.002
10	237	3.543	3.539	-0.004
10	238	3.540	3.536	-0.004
15	239	3.536	3.534	-0.002
15	240	3.539	3.535	-0.004
15	241	3.536	3.539	0.003
15	243	3.539	3.541	0.002
15	245	3.536	3.537	0.001
30	222	3.533	3.532	-0.001
30	223	3.529	3.525	-0.004
30	225	3.541	3.532	-0.009
30	226	3.532	3.533	0.001
30	227	3.543	3.540	-0.003
35	228	3.532	3.524	-0.008
35	229	3.548	3.547	-0.001
35	231	3.534	3.531	-0.003
35	232	3.541	3.535	-0.006
35	233	3.534	3.526	-0.008
50	210	3.545	3.537	-0.008
50	211	3.538	3.529	-0.009
50	212	3.534	3.523	-0.011
50	213	3.535	3.534	-0.001
50	214	3.546	3.539	-0.007
55	215	3.537	3.535	-0.002
55	216	3.523	3.522	-0.001
55	217	3.525	3.524	-0.001
55	218	3.545	3.547	0.002
55	220	3.544	3.532	-0.012
100	247	3.535	3.516	-0.019
100	249	3.535	3.523	-0.012
100	250	3.534	3.523	-0.011
100	251	3.547	3.529	-0.018
100	253	3.526	3.512	-0.014
100	255	3.535	3.526	-0.009
100	256	3.528	3.515	-0.013
100	257	3.527	3.508	-0.019
100	258	3.530	3.517	-0.013
100	259	3.547	3.533	-0.014
100	260	3.535	3.520	-0.015
100	261	3.534	3.518	-0.016
100	262	3.530	3.511	-0.019
100	263	3.527	3.518	-0.009
100	264	3.535	3.522	-0.013
100	265	3.552	3.532	-0.020
100	266	3.535	3.515	-0.020
100	267	3.539	3.517	-0.022
100	268	3.534	3.513	-0.021
100	269	3.526	3.519	-0.007
100	270	3.526	3.505	-0.021
100	271	3.551	3.534	-0.017
100	273	3.540	3.524	-0.016
105	274	3.537	3.531	-0.006
105	275	3.532	3.527	-0.005
105	276	3.539	3.527	-0.012
105	277	3.538	3.528	-0.010
105	278	3.538	3.528	-0.010
	Max	3.557	3.556	0.003
	Average	3.537	3.530	-0.007
	Min	3.523	3.505	-0.022
	Std Dev	0.007	0.011	0.007

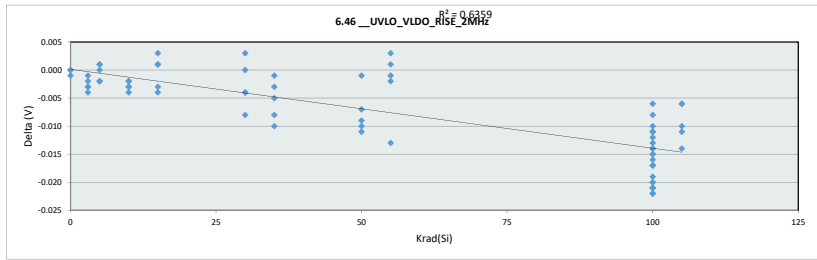


6.43 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.543	3.527	3.528	3.536	3.534	3.525	3.524	3.523	3.522	3.505	3.527
Average	3.544	3.537	3.536	3.542	3.537	3.532	3.533	3.532	3.532	3.520	3.528
Max	3.547	3.556	3.542	3.553	3.541	3.540	3.547	3.539	3.547	3.534	3.531
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

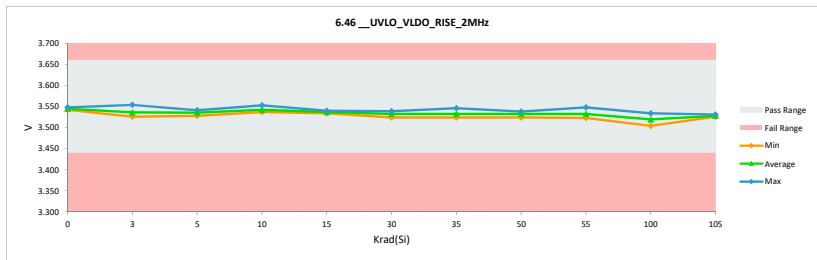


HDR TID Report
TPS7H5002-SP

6.46 _UVLO_VLDO_RISE_2MHZ				
Test Site				
Tester				
Test Number				
Unit		V	V	
Max Limit		3.66	3.66	
Min Limit		3.44	3.44	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	3.544	3.544	0.000
0	190	3.548	3.548	0.000
0	191	3.543	3.542	-0.001
3	300	3.535	3.531	-0.004
3	305	3.557	3.554	-0.003
3	309	3.529	3.526	-0.003
3	311	3.543	3.542	-0.001
3	313	3.530	3.528	-0.002
5	319	3.534	3.532	-0.002
5	321	3.539	3.537	-0.002
5	324	3.539	3.539	0.000
5	327	3.527	3.528	0.001
5	331	3.540	3.541	0.001
10	234	3.542	3.539	-0.003
10	235	3.555	3.553	-0.002
10	236	3.546	3.544	-0.002
10	237	3.543	3.539	-0.004
10	238	3.540	3.537	-0.003
15	239	3.537	3.534	-0.003
15	240	3.539	3.535	-0.004
15	241	3.536	3.539	0.003
15	243	3.539	3.540	0.001
15	245	3.536	3.537	0.001
30	222	3.533	3.533	0.000
30	223	3.528	3.524	-0.004
30	225	3.541	3.533	-0.008
30	226	3.531	3.534	0.003
30	227	3.543	3.539	-0.004
35	228	3.532	3.524	-0.008
35	229	3.547	3.546	-0.001
35	231	3.534	3.531	-0.003
35	232	3.541	3.536	-0.005
35	233	3.535	3.525	-0.010
50	210	3.545	3.538	-0.007
50	211	3.539	3.528	-0.011
50	212	3.534	3.524	-0.010
50	213	3.536	3.535	-0.001
50	214	3.547	3.538	-0.009
55	215	3.538	3.536	-0.002
55	216	3.522	3.523	0.001
55	217	3.525	3.524	-0.001
55	218	3.545	3.548	0.003
55	220	3.544	3.531	-0.013
100	247	3.536	3.515	-0.021
100	249	3.534	3.522	-0.012
100	250	3.535	3.524	-0.011
100	251	3.546	3.529	-0.017
100	253	3.527	3.511	-0.016
100	255	3.535	3.527	-0.008
100	256	3.527	3.516	-0.011
100	257	3.528	3.508	-0.020
100	258	3.531	3.517	-0.014
100	259	3.547	3.533	-0.014
100	260	3.535	3.520	-0.015
100	261	3.534	3.517	-0.017
100	262	3.530	3.511	-0.019
100	263	3.527	3.517	-0.010
100	264	3.535	3.522	-0.013
100	265	3.553	3.531	-0.022
100	266	3.536	3.516	-0.020
100	267	3.539	3.518	-0.021
100	268	3.534	3.513	-0.021
100	269	3.526	3.520	-0.006
100	270	3.526	3.504	-0.022
100	271	3.551	3.534	-0.017
100	273	3.539	3.524	-0.015
105	274	3.537	3.531	-0.006
105	275	3.533	3.527	-0.006
105	276	3.540	3.526	-0.014
105	277	3.538	3.527	-0.011
105	278	3.538	3.528	-0.010
Max		3.557	3.554	0.003
Average		3.537	3.530	-0.007
Min		3.522	3.504	-0.022
Std Dev		0.007	0.011	0.007

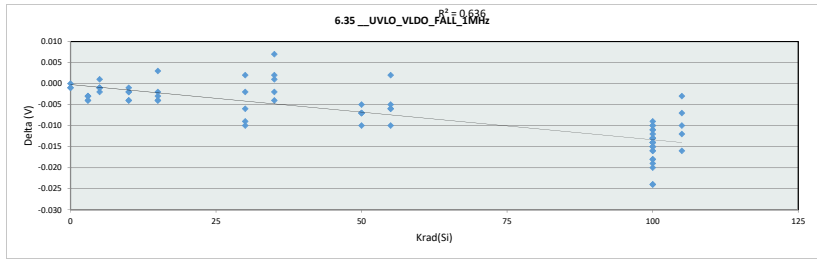


6.46 _UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit		3.66	V								
Min Limit		3.44	V								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.542	3.526	3.528	3.537	3.534	3.524	3.524	3.524	3.523	3.504	3.526
Average	3.545	3.536	3.535	3.542	3.537	3.533	3.532	3.533	3.532	3.520	3.528
Max	3.548	3.554	3.541	3.553	3.540	3.539	3.546	3.538	3.548	3.534	3.531
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

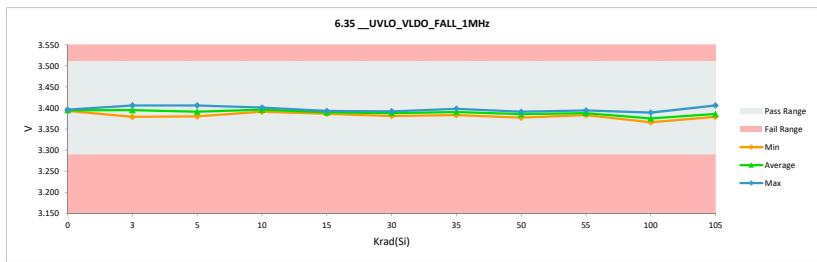


HDR TID Report
TPS7H5002-SP

6.35_UVLO_VLDO_FALL_1MHz				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit		3.51		3.51
Min Limit		3.29		3.29
Krad(Si)	Serial #	PRE	POST	Delta
0	189	3.394	3.393	-0.001
0	190	3.396	3.396	0.000
0	191	3.396	3.395	-0.001
3	300	3.382	3.379	-0.003
3	305	3.409	3.406	-0.003
3	309	3.399	3.395	-0.004
3	311	3.397	3.394	-0.003
3	313	3.404	3.400	-0.004
5	319	3.388	3.386	-0.002
5	321	3.393	3.392	-0.001
5	324	3.393	3.392	-0.001
5	327	3.381	3.380	-0.001
5	331	3.405	3.406	0.001
10	234	3.395	3.393	-0.002
10	235	3.405	3.401	-0.004
10	236	3.397	3.395	-0.002
10	237	3.395	3.391	-0.004
10	238	3.401	3.400	-0.001
15	239	3.393	3.389	-0.004
15	240	3.395	3.393	-0.002
15	241	3.394	3.390	-0.004
15	243	3.387	3.390	0.003
15	245	3.389	3.386	-0.003
30	222	3.390	3.381	-0.009
30	223	3.391	3.389	-0.002
30	225	3.397	3.391	-0.006
30	226	3.384	3.386	0.002
30	227	3.402	3.392	-0.010
35	228	3.384	3.391	0.007
35	229	3.396	3.398	0.002
35	231	3.387	3.383	-0.004
35	232	3.397	3.395	-0.002
35	233	3.384	3.385	0.001
50	210	3.394	3.389	-0.005
50	211	3.387	3.380	-0.007
50	212	3.387	3.377	-0.010
50	213	3.398	3.391	-0.007
50	214	3.397	3.390	-0.007
55	215	3.394	3.388	-0.006
55	216	3.388	3.383	-0.005
55	217	3.396	3.390	-0.006
55	218	3.392	3.394	0.002
55	220	3.394	3.384	-0.010
100	247	3.400	3.376	-0.024
100	249	3.391	3.376	-0.015
100	250	3.389	3.377	-0.012
100	251	3.395	3.385	-0.010
100	253	3.381	3.367	-0.014
100	255	3.396	3.383	-0.013
100	256	3.383	3.367	-0.016
100	257	3.383	3.373	-0.010
100	258	3.382	3.366	-0.016
100	259	3.397	3.382	-0.015
100	260	3.393	3.378	-0.015
100	261	3.388	3.374	-0.014
100	262	3.379	3.366	-0.013
100	263	3.393	3.369	-0.024
100	264	3.391	3.380	-0.011
100	265	3.403	3.383	-0.020
100	266	3.398	3.389	-0.009
100	267	3.387	3.368	-0.019
100	268	3.382	3.371	-0.011
100	269	3.385	3.372	-0.013
100	270	3.383	3.369	-0.014
100	271	3.403	3.385	-0.018
100	273	3.392	3.374	-0.018
105	274	3.396	3.380	-0.016
105	275	3.386	3.379	-0.007
105	276	3.395	3.385	-0.010
105	277	3.409	3.406	-0.003
105	278	3.391	3.379	-0.012
Max		3.409	3.406	0.007
Average		3.393	3.385	-0.007
Min		3.379	3.366	-0.024
Std Dev		0.007	0.010	0.007

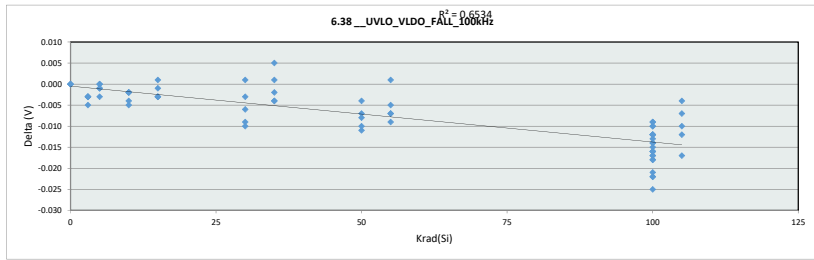


6.35_UVLO_VLD											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit		3.51		3.51							
Min Limit		3.29		3.29							
Krad(Si)	LL	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.393	3.379	3.380	3.391	3.386	3.381	3.383	3.377	3.383	3.366	3.379
Average	3.395	3.395	3.391	3.396	3.390	3.388	3.390	3.385	3.388	3.375	3.386
Max	3.396	3.406	3.406	3.401	3.393	3.392	3.398	3.391	3.394	3.389	3.406
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

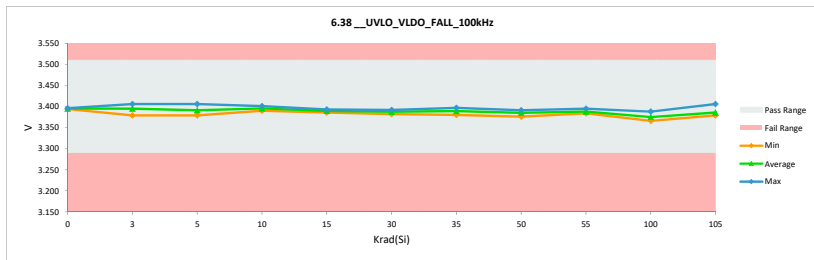


HDR TID Report TPS7H5002-SP

6.38 UVLO_VLDO_FALL_100kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	3.394	3.394	0.000
0	190	3.396	3.396	0.000
0	191	3.396	3.396	0.000
3	300	3.382	3.379	-0.003
3	305	3.409	3.406	-0.003
3	309	3.399	3.396	-0.003
3	311	3.397	3.394	-0.003
3	313	3.404	3.399	-0.005
5	319	3.387	3.386	-0.001
5	321	3.392	3.392	0.000
5	324	3.393	3.392	-0.001
5	327	3.382	3.379	-0.003
5	331	3.406	3.406	0.000
10	234	3.395	3.393	-0.002
10	235	3.405	3.401	-0.004
10	236	3.397	3.395	-0.002
10	237	3.395	3.390	-0.005
10	238	3.401	3.399	-0.002
15	239	3.392	3.389	-0.003
15	240	3.394	3.393	-0.001
15	241	3.394	3.391	-0.003
15	243	3.388	3.389	0.001
15	245	3.389	3.386	-0.003
30	222	3.391	3.382	-0.009
30	223	3.391	3.388	-0.003
30	225	3.397	3.391	-0.006
30	226	3.385	3.386	0.001
30	227	3.402	3.392	-0.010
35	228	3.386	3.391	0.005
35	229	3.396	3.397	0.001
35	231	3.388	3.384	-0.004
35	232	3.397	3.395	-0.002
35	233	3.384	3.380	-0.004
50	210	3.394	3.390	-0.004
50	211	3.389	3.379	-0.010
50	212	3.387	3.376	-0.011
50	213	3.399	3.391	-0.008
50	214	3.397	3.390	-0.007
55	215	3.394	3.387	-0.007
55	216	3.389	3.384	-0.005
55	217	3.397	3.390	-0.007
55	218	3.394	3.395	0.001
55	220	3.394	3.385	-0.009
100	247	3.399	3.377	-0.022
100	249	3.391	3.375	-0.016
100	250	3.390	3.378	-0.012
100	251	3.395	3.386	-0.009
100	253	3.381	3.369	-0.012
100	255	3.396	3.381	-0.015
100	256	3.384	3.366	-0.018
100	257	3.383	3.374	-0.009
100	258	3.383	3.366	-0.017
100	259	3.397	3.383	-0.014
100	260	3.393	3.377	-0.016
100	261	3.389	3.373	-0.016
100	262	3.379	3.367	-0.012
100	263	3.394	3.369	-0.025
100	264	3.391	3.381	-0.010
100	265	3.403	3.381	-0.022
100	266	3.398	3.388	-0.010
100	267	3.388	3.367	-0.021
100	268	3.382	3.370	-0.012
100	269	3.385	3.372	-0.013
100	270	3.383	3.369	-0.014
100	271	3.402	3.385	-0.017
100	273	3.392	3.374	-0.018
105	274	3.397	3.380	-0.017
105	275	3.386	3.379	-0.007
105	276	3.395	3.385	-0.010
105	277	3.410	3.406	-0.004
105	278	3.391	3.379	-0.012
Max		3.410	3.406	0.005
Average		3.393	3.385	-0.008
Min		3.379	3.366	-0.025
Std Dev		0.007	0.010	0.007

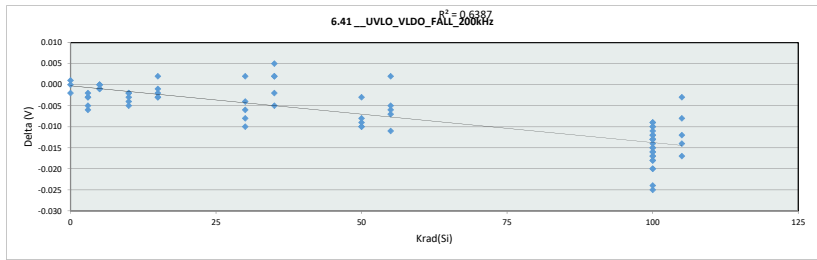


6.38 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.394	3.379	3.379	3.390	3.386	3.382	3.380	3.376	3.384	3.366	3.379
Average	3.395	3.395	3.391	3.396	3.390	3.388	3.389	3.385	3.388	3.375	3.386
Max	3.396	3.406	3.406	3.401	3.393	3.392	3.397	3.391	3.395	3.388	3.406
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

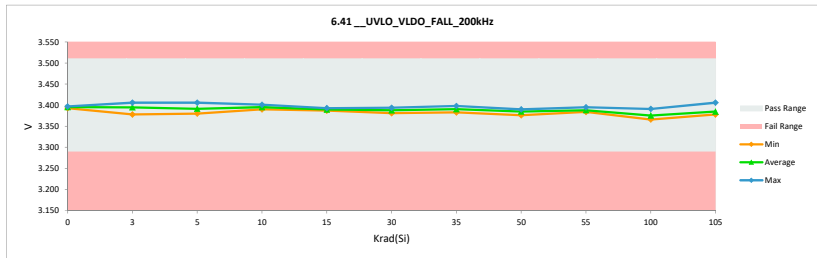


HDR TID Report
TPS7H5002-SP

6.41 UVLO_VLDO_FALL_200kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	3.395	3.393	-0.002
0	190	3.396	3.397	0.001
0	191	3.396	3.396	0.000
3	300	3.383	3.378	-0.005
3	305	3.409	3.406	-0.003
3	309	3.399	3.396	-0.003
3	311	3.397	3.395	-0.002
3	313	3.405	3.399	-0.006
5	319	3.386	3.386	0.000
5	321	3.393	3.393	0.000
5	324	3.393	3.392	-0.001
5	327	3.381	3.380	-0.001
5	331	3.406	3.406	0.000
10	234	3.395	3.393	-0.002
10	235	3.405	3.401	-0.004
10	236	3.397	3.394	-0.003
10	237	3.395	3.390	-0.005
10	238	3.401	3.399	-0.002
15	239	3.392	3.389	-0.003
15	240	3.394	3.393	-0.001
15	241	3.394	3.391	-0.003
15	243	3.387	3.389	0.002
15	245	3.389	3.387	-0.002
30	222	3.391	3.381	-0.010
30	223	3.392	3.388	-0.004
30	225	3.396	3.390	-0.006
30	226	3.384	3.386	0.002
30	227	3.402	3.394	-0.008
35	228	3.386	3.391	0.005
35	229	3.396	3.398	0.002
35	231	3.388	3.383	-0.005
35	232	3.397	3.395	-0.002
35	233	3.383	3.385	0.002
50	210	3.393	3.390	-0.003
50	211	3.389	3.379	-0.010
50	212	3.386	3.376	-0.010
50	213	3.399	3.390	-0.009
50	214	3.398	3.390	-0.008
55	215	3.394	3.387	-0.007
55	216	3.389	3.384	-0.005
55	217	3.396	3.390	-0.006
55	218	3.393	3.395	0.002
55	220	3.395	3.384	-0.011
100	247	3.399	3.375	-0.024
100	249	3.392	3.374	-0.018
100	250	3.390	3.376	-0.014
100	251	3.396	3.386	-0.010
100	253	3.381	3.368	-0.013
100	255	3.396	3.383	-0.013
100	256	3.384	3.367	-0.017
100	257	3.382	3.373	-0.009
100	258	3.383	3.366	-0.017
100	259	3.397	3.382	-0.015
100	260	3.393	3.377	-0.016
100	261	3.390	3.374	-0.016
100	262	3.379	3.366	-0.013
100	263	3.394	3.369	-0.025
100	264	3.391	3.381	-0.010
100	265	3.403	3.383	-0.020
100	266	3.398	3.389	-0.009
100	267	3.387	3.369	-0.018
100	268	3.382	3.370	-0.012
100	269	3.385	3.373	-0.012
100	270	3.384	3.369	-0.015
100	271	3.402	3.391	-0.011
100	273	3.394	3.374	-0.020
105	274	3.397	3.380	-0.017
105	275	3.386	3.378	-0.008
105	276	3.396	3.382	-0.014
105	277	3.409	3.406	-0.003
105	278	3.390	3.378	-0.012
Max		3.409	3.406	0.005
Average		3.393	3.385	-0.008
Min		3.379	3.366	-0.025
Std Dev		0.007	0.010	0.007

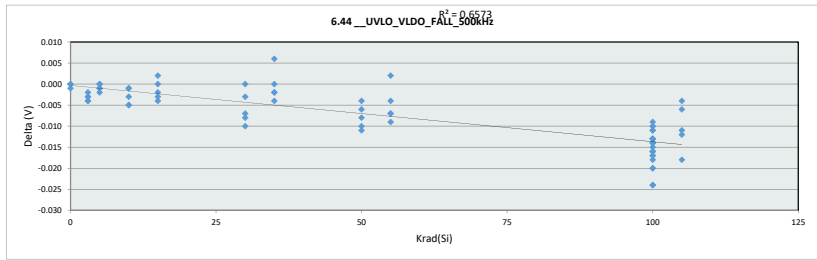


6.41 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.393	3.378	3.380	3.390	3.387	3.381	3.383	3.376	3.384	3.366	3.378
Average	3.395	3.395	3.391	3.395	3.390	3.388	3.390	3.385	3.388	3.375	3.385
Max	3.397	3.406	3.406	3.401	3.393	3.394	3.398	3.390	3.395	3.391	3.406
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

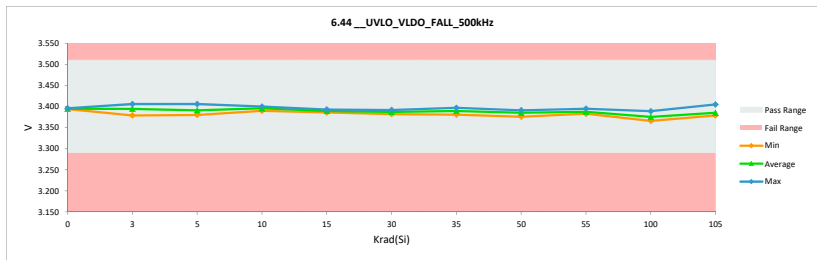


HDR TID Report
TPS7H5002-SP

6.44 UVLO_VLDO_FALL_500kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	3.394	3.394	0.000
0	190	3.396	3.396	0.000
0	191	3.396	3.395	-0.001
3	300	3.382	3.379	-0.003
3	305	3.408	3.406	-0.002
3	309	3.399	3.395	-0.004
3	311	3.397	3.394	-0.003
3	313	3.403	3.399	-0.004
5	319	3.387	3.386	-0.001
5	321	3.392	3.392	0.000
5	324	3.393	3.391	-0.002
5	327	3.381	3.380	-0.001
5	331	3.406	3.406	0.000
10	234	3.395	3.394	-0.001
10	235	3.405	3.400	-0.005
10	236	3.398	3.395	-0.003
10	237	3.395	3.390	-0.005
10	238	3.401	3.400	-0.001
15	239	3.392	3.389	-0.003
15	240	3.393	3.393	0.000
15	241	3.393	3.391	-0.002
15	243	3.387	3.389	0.002
15	245	3.390	3.386	-0.004
30	222	3.390	3.382	-0.008
30	223	3.391	3.388	-0.003
30	225	3.397	3.390	-0.007
30	226	3.385	3.385	0.000
30	227	3.402	3.392	-0.010
35	228	3.385	3.391	0.006
35	229	3.397	3.397	0.000
35	231	3.388	3.384	-0.004
35	232	3.397	3.395	-0.002
35	233	3.383	3.381	-0.002
50	210	3.394	3.390	-0.004
50	211	3.389	3.378	-0.011
50	212	3.386	3.376	-0.010
50	213	3.399	3.391	-0.008
50	214	3.397	3.391	-0.006
55	215	3.394	3.387	-0.007
55	216	3.387	3.383	-0.004
55	217	3.396	3.389	-0.007
55	218	3.393	3.395	0.002
55	220	3.394	3.385	-0.009
100	247	3.400	3.376	-0.024
100	249	3.392	3.375	-0.017
100	250	3.390	3.377	-0.013
100	251	3.396	3.386	-0.010
100	253	3.382	3.368	-0.014
100	255	3.396	3.382	-0.014
100	256	3.383	3.366	-0.017
100	257	3.383	3.373	-0.010
100	258	3.382	3.366	-0.016
100	259	3.396	3.383	-0.013
100	260	3.394	3.378	-0.016
100	261	3.389	3.374	-0.015
100	262	3.379	3.366	-0.013
100	263	3.393	3.369	-0.024
100	264	3.392	3.381	-0.011
100	265	3.403	3.383	-0.020
100	266	3.398	3.389	-0.009
100	267	3.387	3.369	-0.018
100	268	3.382	3.371	-0.011
100	269	3.385	3.372	-0.013
100	270	3.383	3.369	-0.014
100	271	3.402	3.386	-0.016
100	273	3.393	3.373	-0.020
105	274	3.397	3.379	-0.018
105	275	3.386	3.380	-0.006
105	276	3.395	3.383	-0.012
105	277	3.409	3.405	-0.004
105	278	3.390	3.379	-0.011
Max		3.409	3.406	0.006
Average		3.393	3.385	-0.008
Min		3.379	3.366	-0.024
Std Dev		0.007	0.010	0.007

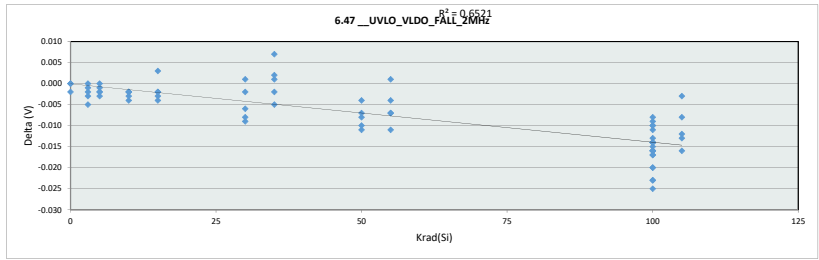


6.44 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.394	3.379	3.380	3.390	3.386	3.382	3.381	3.376	3.383	3.366	3.379
Average	3.395	3.395	3.391	3.396	3.390	3.387	3.390	3.385	3.388	3.375	3.385
Max	3.396	3.406	3.406	3.400	3.393	3.392	3.397	3.391	3.395	3.389	3.405
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

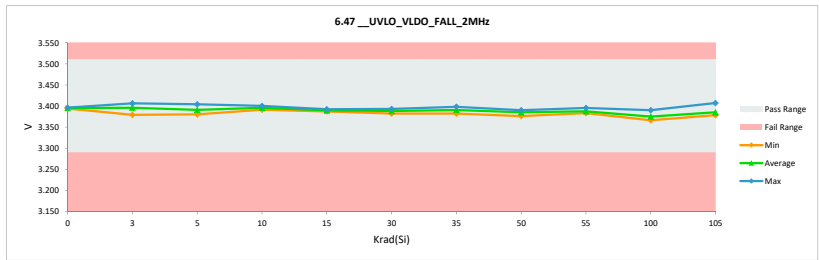


HDR TID Report
TPS7H5002-SP

6.47_UVLO_VLDO_FALL_2MHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	3.394	3.394	0.000
0	190	3.397	3.395	-0.002
0	191	3.396	3.396	0.000
3	300	3.382	3.379	-0.003
3	305	3.407	3.406	-0.001
3	309	3.400	3.395	-0.005
3	311	3.397	3.395	-0.002
3	313	3.404	3.404	0.000
5	319	3.386	3.386	0.000
5	321	3.394	3.392	-0.002
5	324	3.394	3.391	-0.003
5	327	3.381	3.380	-0.001
5	331	3.406	3.404	-0.002
10	234	3.395	3.393	-0.002
10	235	3.404	3.400	-0.004
10	236	3.397	3.395	-0.002
10	237	3.394	3.391	-0.003
10	238	3.401	3.399	-0.002
15	239	3.392	3.389	-0.003
15	240	3.394	3.392	-0.002
15	241	3.394	3.390	-0.004
15	243	3.386	3.389	0.003
15	245	3.389	3.387	-0.002
30	222	3.390	3.382	-0.008
30	223	3.391	3.389	-0.002
30	225	3.396	3.390	-0.006
30	226	3.385	3.386	0.001
30	227	3.402	3.393	-0.009
35	228	3.384	3.391	0.007
35	229	3.397	3.398	0.001
35	231	3.387	3.382	-0.005
35	232	3.397	3.395	-0.002
35	233	3.384	3.386	0.002
50	210	3.394	3.390	-0.004
50	211	3.389	3.378	-0.011
50	212	3.386	3.376	-0.010
50	213	3.398	3.390	-0.008
50	214	3.397	3.390	-0.007
55	215	3.394	3.387	-0.007
55	216	3.387	3.383	-0.004
55	217	3.396	3.389	-0.007
55	218	3.394	3.393	0.001
55	220	3.394	3.383	-0.011
100	247	3.399	3.376	-0.023
100	249	3.392	3.375	-0.017
100	250	3.390	3.376	-0.014
100	251	3.395	3.385	-0.010
100	253	3.381	3.367	-0.014
100	255	3.396	3.382	-0.014
100	256	3.383	3.367	-0.016
100	257	3.382	3.373	-0.009
100	258	3.382	3.366	-0.016
100	259	3.397	3.382	-0.015
100	260	3.393	3.377	-0.016
100	261	3.389	3.373	-0.016
100	262	3.379	3.366	-0.013
100	263	3.394	3.369	-0.025
100	264	3.391	3.381	-0.010
100	265	3.404	3.381	-0.023
100	266	3.398	3.390	-0.008
100	267	3.387	3.367	-0.020
100	268	3.382	3.371	-0.011
100	269	3.385	3.371	-0.014
100	270	3.383	3.369	-0.014
100	271	3.403	3.386	-0.017
100	273	3.393	3.373	-0.020
105	274	3.396	3.380	-0.016
105	275	3.386	3.378	-0.008
105	276	3.394	3.382	-0.012
105	277	3.410	3.407	-0.003
105	278	3.391	3.378	-0.013
Max		3.410	3.407	0.007
Average		3.393	3.385	-0.008
Min		3.379	3.366	-0.025
Std Dev		0.007	0.010	0.007

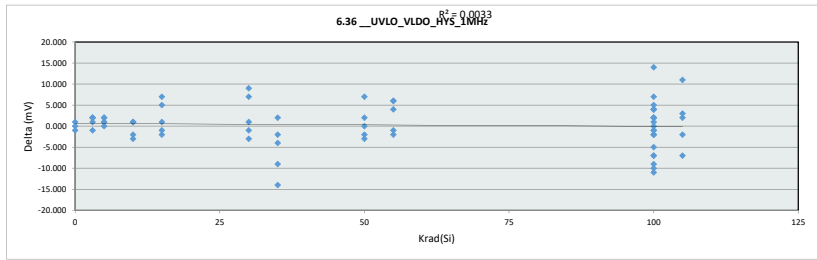


6.47_UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.394	3.379	3.380	3.391	3.387	3.382	3.382	3.376	3.383	3.366	3.378
Average	3.395	3.396	3.391	3.396	3.389	3.388	3.390	3.385	3.387	3.375	3.385
Max	3.396	3.406	3.404	3.400	3.392	3.393	3.398	3.390	3.395	3.390	3.407
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

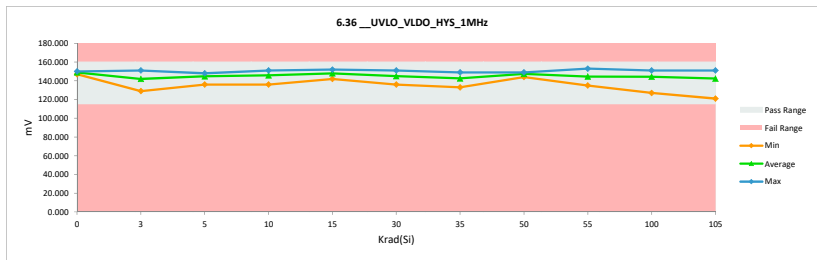


HDR TID Report TPS7H5002-SP

6.36 UVLO_VLDO_HYS_1MHz				
Test Site	Tester	PRE	POST	Delta
0	189	149.000	150.000	1.000
0	190	151.000	150.000	-1.000
0	191	147.000	147.000	0.000
3	300	152.000	151.000	-1.000
3	305	148.000	150.000	2.000
3	309	130.000	132.000	2.000
3	311	147.000	148.000	1.000
3	313	127.000	129.000	2.000
5	319	146.000	148.000	2.000
5	321	145.000	145.000	0.000
5	324	146.000	147.000	1.000
5	327	146.000	148.000	2.000
5	331	135.000	136.000	1.000
10	234	149.000	146.000	-3.000
10	235	150.000	151.000	1.000
10	236	148.000	149.000	1.000
10	237	146.000	147.000	1.000
10	238	138.000	136.000	-2.000
15	239	145.000	146.000	1.000
15	240	144.000	142.000	-2.000
15	241	142.000	149.000	7.000
15	243	152.000	151.000	-1.000
15	245	147.000	152.000	5.000
30	222	142.000	151.000	9.000
30	223	137.000	136.000	-1.000
30	225	145.000	142.000	-3.000
30	226	147.000	148.000	1.000
30	227	141.000	148.000	7.000
35	228	147.000	133.000	-14.000
35	229	151.000	149.000	-2.000
35	231	147.000	149.000	2.000
35	232	145.000	141.000	-4.000
35	233	150.000	141.000	-9.000
50	210	151.000	149.000	-2.000
50	211	151.000	148.000	-3.000
50	212	145.000	147.000	2.000
50	213	137.000	144.000	7.000
50	214	149.000	149.000	0.000
55	215	143.000	147.000	4.000
55	216	134.000	140.000	6.000
55	217	129.000	135.000	6.000
55	218	154.000	153.000	-1.000
55	220	150.000	148.000	-2.000
100	247	136.000	138.000	2.000
100	249	144.000	148.000	4.000
100	250	145.000	147.000	2.000
100	251	151.000	144.000	-7.000
100	253	146.000	145.000	-1.000
100	255	138.000	143.000	5.000
100	256	144.000	148.000	4.000
100	257	145.000	135.000	-10.000
100	258	149.000	151.000	2.000
100	259	150.000	151.000	1.000
100	260	143.000	141.000	-2.000
100	261	145.000	143.000	-2.000
100	262	151.000	146.000	-5.000
100	263	134.000	148.000	14.000
100	264	143.000	141.000	-2.000
100	265	149.000	148.000	-1.000
100	266	138.000	127.000	-11.000
100	267	147.000	151.000	4.000
100	268	152.000	143.000	-9.000
100	269	140.000	147.000	7.000
100	270	143.000	136.000	-7.000
100	271	149.000	149.000	0.000
100	273	148.000	150.000	2.000
105	274	140.000	151.000	11.000
105	275	146.000	148.000	2.000
105	276	144.000	142.000	-2.000
105	277	128.000	121.000	-7.000
105	278	147.000	150.000	3.000
Max		154.000	153.000	14.000
Average		144.507	144.789	0.282
Min		127.000	121.000	-14.000
Std Dev		6.075	6.487	4.897

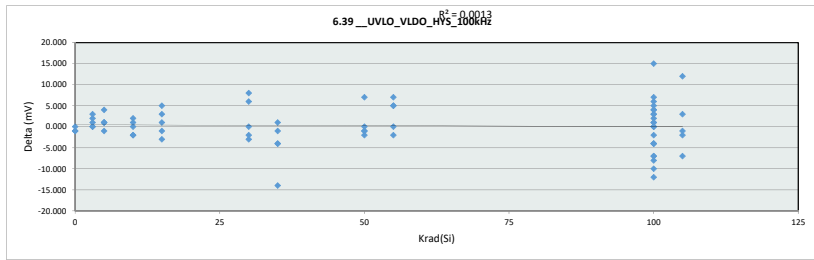


6.36 UVLO_VLD											
Test Site	Tester	PRE	POST	Delta	LL	Min	Average	Max	UL		
0	3	115.000	115.000	0.000	115.000	115.000	115.000	115.000	115.000		
5	10	115.000	115.000	0.000	115.000	115.000	115.000	115.000	115.000		
10	15	115.000	115.000	0.000	115.000	115.000	115.000	115.000	115.000		
30	35	115.000	115.000	0.000	115.000	115.000	115.000	115.000	115.000		
50	55	115.000	115.000	0.000	115.000	115.000	115.000	115.000	115.000		
100	105	115.000	115.000	0.000	115.000	115.000	115.000	115.000	115.000		

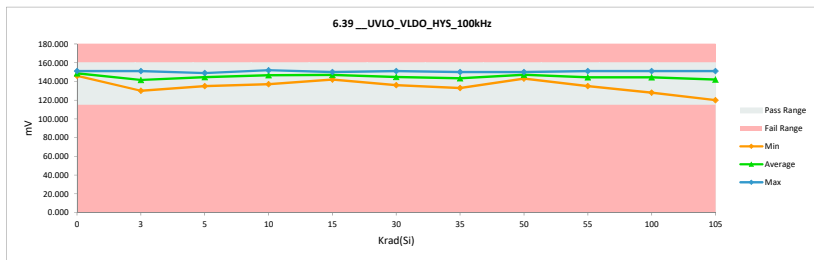


HDR TID Report TPS7H5002-SP

6.39 UVLO_VLDO_HYS_100kHz				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	160	160		
Min Limit	115	115		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	149.000	149.000	0.000
0	190	152.000	151.000	-1.000
0	191	147.000	146.000	-1.000
3	300	151.000	151.000	0.000
3	305	148.000	148.000	0.000
3	309	129.000	131.000	2.000
3	311	147.000	148.000	1.000
3	313	127.000	130.000	3.000
5	319	146.000	147.000	1.000
5	321	146.000	145.000	-1.000
5	324	146.000	147.000	1.000
5	327	145.000	149.000	4.000
5	331	134.000	135.000	1.000
10	234	148.000	146.000	-2.000
10	235	150.000	152.000	2.000
10	236	149.000	149.000	0.000
10	237	148.000	149.000	1.000
10	238	139.000	137.000	-2.000
15	239	144.000	145.000	1.000
15	240	145.000	142.000	-3.000
15	241	143.000	148.000	5.000
15	243	151.000	150.000	-1.000
15	245	147.000	150.000	3.000
30	222	143.000	151.000	8.000
30	223	139.000	136.000	-3.000
30	225	144.000	142.000	-2.000
30	226	148.000	148.000	0.000
30	227	141.000	147.000	6.000
35	228	147.000	133.000	-14.000
35	229	151.000	150.000	-1.000
35	231	147.000	148.000	1.000
35	232	144.000	140.000	-4.000
35	233	150.000	146.000	-4.000
50	210	150.000	148.000	-2.000
50	211	150.000	150.000	0.000
50	212	147.000	146.000	-1.000
50	213	136.000	143.000	7.000
50	214	150.000	149.000	-1.000
55	215	144.000	149.000	5.000
55	216	134.000	139.000	5.000
55	217	128.000	135.000	7.000
55	218	151.000	151.000	0.000
55	220	150.000	148.000	-2.000
100	247	136.000	138.000	2.000
100	249	144.000	148.000	4.000
100	250	144.000	145.000	1.000
100	251	151.000	144.000	-7.000
100	253	147.000	143.000	-4.000
100	255	139.000	146.000	7.000
100	256	144.000	148.000	4.000
100	257	145.000	133.000	-12.000
100	258	148.000	151.000	3.000
100	259	151.000	151.000	0.000
100	260	141.000	142.000	1.000
100	261	144.000	144.000	0.000
100	262	150.000	146.000	-4.000
100	263	133.000	148.000	15.000
100	264	144.000	140.000	-4.000
100	265	150.000	150.000	0.000
100	266	138.000	128.000	-10.000
100	267	146.000	151.000	5.000
100	268	152.000	144.000	-8.000
100	269	141.000	147.000	6.000
100	270	143.000	136.000	-7.000
100	271	151.000	149.000	-2.000
100	273	147.000	150.000	3.000
105	274	139.000	151.000	12.000
105	275	148.000	147.000	-1.000
105	276	144.000	142.000	-2.000
105	277	127.000	120.000	-7.000
105	278	147.000	150.000	3.000
Max		152.000	152.000	15.000
Average		144.493	144.732	0.239
Min		127.000	120.000	-14.000
Std Dev		6.166	6.509	4.851

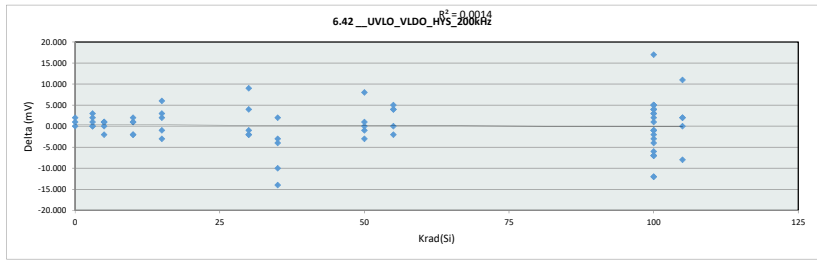


6.39 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	160	mV									
Min Limit	115	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	146.000	130.000	135.000	137.000	142.000	136.000	133.000	143.000	135.000	128.000	120.000
Average	148.667	141.600	144.600	146.600	147.000	144.800	143.400	147.200	144.400	144.435	142.000
Max	151.000	151.000	149.000	152.000	150.000	151.000	150.000	150.000	151.000	151.000	151.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

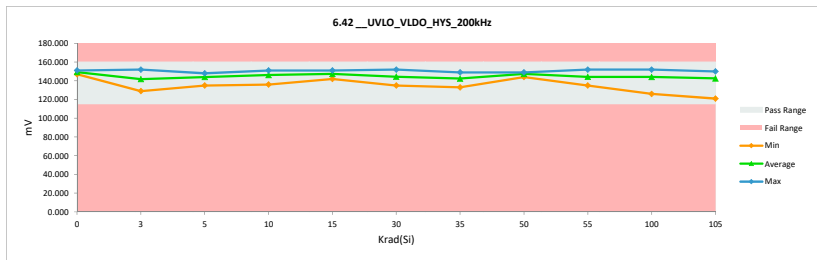


HDR TID Report TPS7H5002-SP

6.42 UVLO_VLDO_HYS_200kHz				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	160	160		
Min Limit	115	115		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	148.000	150.000	2.000
0	190	151.000	151.000	0.000
0	191	146.000	147.000	1.000
3	300	151.000	152.000	1.000
3	305	149.000	149.000	0.000
3	309	130.000	132.000	2.000
3	311	147.000	147.000	0.000
3	313	126.000	129.000	3.000
5	319	147.000	148.000	1.000
5	321	145.000	143.000	-2.000
5	324	146.000	146.000	0.000
5	327	147.000	148.000	1.000
5	331	134.000	135.000	1.000
10	234	148.000	146.000	-2.000
10	235	149.000	151.000	2.000
10	236	148.000	149.000	1.000
10	237	148.000	149.000	1.000
10	238	138.000	136.000	-2.000
15	239	144.000	146.000	2.000
15	240	145.000	142.000	-3.000
15	241	142.000	148.000	6.000
15	243	152.000	151.000	-1.000
15	245	147.000	150.000	3.000
30	222	143.000	152.000	9.000
30	223	137.000	135.000	-2.000
30	225	144.000	143.000	-1.000
30	226	149.000	147.000	-2.000
30	227	141.000	145.000	4.000
35	228	147.000	133.000	-14.000
35	229	152.000	149.000	-3.000
35	231	147.000	149.000	2.000
35	232	144.000	140.000	-4.000
35	233	151.000	141.000	-10.000
50	210	151.000	148.000	-3.000
50	211	149.000	149.000	0.000
50	212	147.000	148.000	1.000
50	213	136.000	144.000	8.000
50	214	149.000	148.000	-1.000
55	215	144.000	148.000	4.000
55	216	135.000	139.000	4.000
55	217	130.000	135.000	5.000
55	218	152.000	152.000	0.000
55	220	149.000	147.000	-2.000
100	247	136.000	140.000	4.000
100	249	143.000	148.000	5.000
100	250	144.000	147.000	3.000
100	251	150.000	144.000	-6.000
100	253	146.000	145.000	-1.000
100	255	139.000	143.000	4.000
100	256	143.000	148.000	5.000
100	257	146.000	134.000	-12.000
100	258	148.000	151.000	3.000
100	259	150.000	152.000	2.000
100	260	141.000	142.000	1.000
100	261	144.000	143.000	-1.000
100	262	150.000	146.000	-4.000
100	263	133.000	150.000	17.000
100	264	143.000	140.000	-3.000
100	265	149.000	148.000	-1.000
100	266	138.000	126.000	-12.000
100	267	152.000	150.000	-2.000
100	268	151.000	144.000	-7.000
100	269	142.000	147.000	5.000
100	270	142.000	135.000	-7.000
100	271	149.000	142.000	-7.000
100	273	146.000	150.000	4.000
105	274	139.000	150.000	11.000
105	275	146.000	148.000	2.000
105	276	144.000	144.000	0.000
105	277	129.000	121.000	-8.000
105	278	148.000	150.000	2.000
Max		152.000	152.000	17.000
Average		144.451	144.577	0.127
Min		126.000	121.000	-14.000
Std Dev		6.042	6.555	5.065

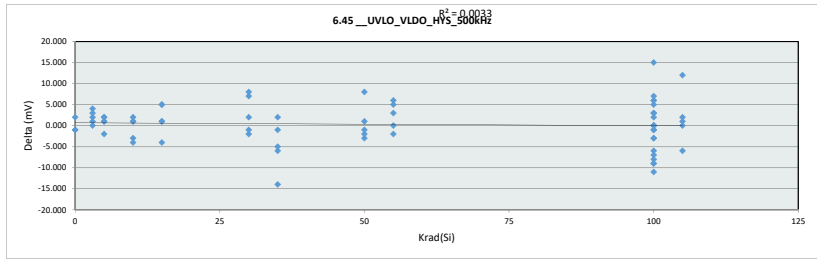


6.42 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	160	mV									
Min Limit	115	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	147.000	129.000	135.000	136.000	142.000	135.000	133.000	144.000	135.000	126.000	121.000
Average	149.333	141.800	144.000	146.200	147.400	144.400	142.400	147.400	144.200	144.130	142.600
Max	151.000	152.000	148.000	151.000	151.000	152.000	149.000	149.000	152.000	152.000	150.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

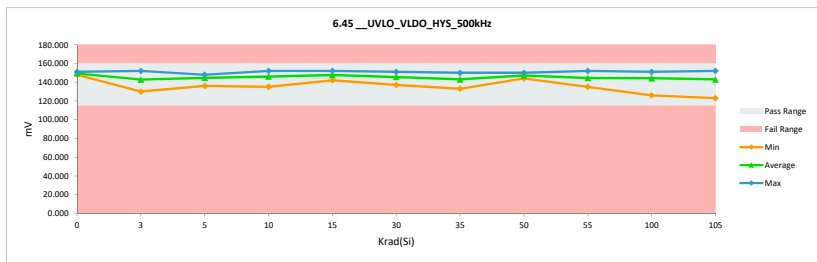


HDR TID Report TPS7H5002-SP

6.45_UVLO_VLDO_HYS_500kHz				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	160	160		
Min Limit	115	115		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	150.000	149.000	-1.000
0	190	152.000	151.000	-1.000
0	191	146.000	148.000	2.000
3	300	152.000	152.000	0.000
3	305	149.000	150.000	1.000
3	309	129.000	133.000	4.000
3	311	147.000	149.000	2.000
3	313	127.000	130.000	3.000
5	319	147.000	148.000	1.000
5	321	146.000	144.000	-2.000
5	324	146.000	147.000	1.000
5	327	146.000	148.000	2.000
5	331	134.000	136.000	2.000
10	234	148.000	145.000	-3.000
10	235	150.000	152.000	2.000
10	236	148.000	149.000	1.000
10	237	148.000	149.000	1.000
10	238	139.000	135.000	-4.000
15	239	144.000	145.000	1.000
15	240	146.000	142.000	-4.000
15	241	143.000	148.000	5.000
15	243	151.000	152.000	1.000
15	245	146.000	151.000	5.000
30	222	143.000	151.000	8.000
30	223	138.000	137.000	-1.000
30	225	144.000	142.000	-2.000
30	226	147.000	149.000	2.000
30	227	141.000	148.000	7.000
35	228	147.000	133.000	-14.000
35	229	151.000	150.000	-1.000
35	231	145.000	147.000	2.000
35	232	145.000	140.000	-5.000
35	233	151.000	145.000	-6.000
50	210	150.000	147.000	-3.000
50	211	149.000	150.000	1.000
50	212	149.000	147.000	-2.000
50	213	136.000	144.000	8.000
50	214	149.000	148.000	-1.000
55	215	143.000	148.000	5.000
55	216	136.000	139.000	3.000
55	217	129.000	135.000	6.000
55	218	152.000	152.000	0.000
55	220	150.000	148.000	-2.000
100	247	134.000	140.000	6.000
100	249	143.000	148.000	5.000
100	250	145.000	145.000	0.000
100	251	151.000	143.000	-8.000
100	253	145.000	144.000	-1.000
100	255	138.000	144.000	6.000
100	256	145.000	148.000	3.000
100	257	144.000	135.000	-9.000
100	258	149.000	151.000	2.000
100	259	150.000	150.000	0.000
100	260	141.000	141.000	0.000
100	261	144.000	144.000	0.000
100	262	151.000	145.000	-6.000
100	263	134.000	149.000	15.000
100	264	143.000	140.000	-3.000
100	265	149.000	149.000	0.000
100	266	137.000	126.000	-11.000
100	267	152.000	149.000	-3.000
100	268	152.000	143.000	-9.000
100	269	141.000	148.000	7.000
100	270	143.000	136.000	-7.000
100	271	149.000	148.000	-1.000
100	273	147.000	150.000	3.000
105	274	140.000	152.000	12.000
105	275	146.000	147.000	1.000
105	276	144.000	144.000	0.000
105	277	129.000	123.000	-6.000
105	278	147.000	149.000	2.000
Max		152.000	152.000	15.000
Average		144.535	144.845	0.310
Min		127.000	123.000	-14.000
Std Dev		6.120	6.355	4.941

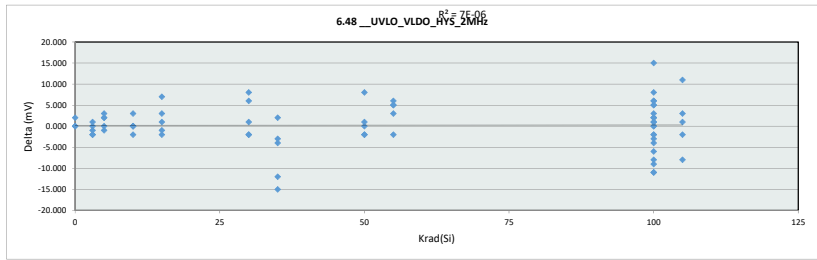


6.45_UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	160	mV									
Min Limit	115	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	148.000	130.000	136.000	135.000	142.000	137.000	144.000	135.000	126.000	123.000	123.000
Average	149.333	142.800	144.600	146.000	147.600	145.400	143.000	147.200	144.400	144.174	143.000
Max	151.000	152.000	148.000	152.000	152.000	151.000	150.000	150.000	152.000	151.000	152.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

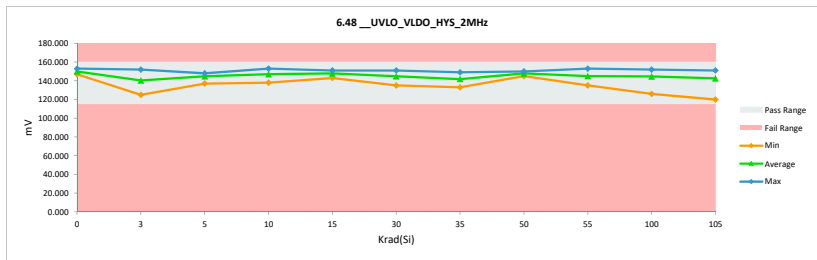


HDR TID Report TPS7H5002-SP

6.48 UVLO_VLDO_HYS_2MHz				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	160	160		
Min Limit	115	115		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	150.000	150.000	0.000
0	190	151.000	153.000	2.000
0	191	147.000	147.000	0.000
3	300	153.000	152.000	-1.000
3	305	150.000	148.000	-2.000
3	309	129.000	130.000	1.000
3	311	147.000	147.000	0.000
3	313	127.000	125.000	-2.000
5	319	148.000	147.000	-1.000
5	321	145.000	145.000	0.000
5	324	145.000	147.000	2.000
5	327	146.000	148.000	2.000
5	331	134.000	137.000	3.000
10	234	148.000	146.000	-2.000
10	235	150.000	153.000	3.000
10	236	149.000	149.000	0.000
10	237	149.000	149.000	0.000
10	238	138.000	138.000	0.000
15	239	144.000	145.000	1.000
15	240	145.000	143.000	-2.000
15	241	142.000	149.000	7.000
15	243	152.000	151.000	-1.000
15	245	148.000	151.000	3.000
30	222	143.000	151.000	8.000
30	223	137.000	135.000	-2.000
30	225	145.000	143.000	-2.000
30	226	147.000	148.000	1.000
30	227	141.000	147.000	6.000
35	228	148.000	133.000	-15.000
35	229	151.000	148.000	-3.000
35	231	147.000	149.000	2.000
35	232	144.000	140.000	-4.000
35	233	151.000	139.000	-12.000
50	210	150.000	148.000	-2.000
50	211	150.000	150.000	0.000
50	212	147.000	148.000	1.000
50	213	137.000	145.000	8.000
50	214	150.000	148.000	-2.000
55	215	144.000	149.000	5.000
55	216	135.000	140.000	5.000
55	217	129.000	135.000	6.000
55	218	150.000	153.000	3.000
55	220	150.000	148.000	-2.000
100	247	136.000	138.000	2.000
100	249	142.000	147.000	5.000
100	250	145.000	148.000	3.000
100	251	151.000	145.000	-6.000
100	253	146.000	144.000	-2.000
100	255	139.000	145.000	6.000
100	256	144.000	149.000	5.000
100	257	146.000	135.000	-11.000
100	258	149.000	151.000	2.000
100	259	149.000	151.000	2.000
100	260	142.000	143.000	1.000
100	261	144.000	144.000	0.000
100	262	150.000	146.000	-4.000
100	263	133.000	148.000	15.000
100	264	144.000	141.000	-3.000
100	265	149.000	150.000	1.000
100	266	137.000	126.000	-11.000
100	267	152.000	150.000	-2.000
100	268	151.000	142.000	-9.000
100	269	141.000	149.000	8.000
100	270	143.000	135.000	-8.000
100	271	148.000	148.000	0.000
100	273	146.000	152.000	6.000
105	274	140.000	151.000	11.000
105	275	147.000	148.000	1.000
105	276	146.000	144.000	-2.000
105	277	128.000	120.000	-8.000
105	278	147.000	150.000	3.000
Max		153.000	153.000	15.000
Average		144.620	144.887	0.268
Min		127.000	120.000	-15.000
Std Dev		6.165	6.886	5.171

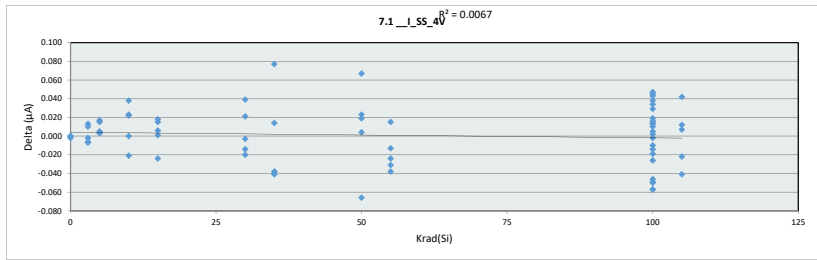


6.48 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	160	mV									
Min Limit	115	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	147.000	125.000	137.000	138.000	143.000	135.000	133.000	145.000	135.000	126.000	120.000
Average	150.000	140.400	144.800	147.000	147.800	144.800	141.800	147.800	145.000	144.652	142.600
Max	153.000	152.000	148.000	153.000	151.000	149.000	150.000	150.000	153.000	152.000	151.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

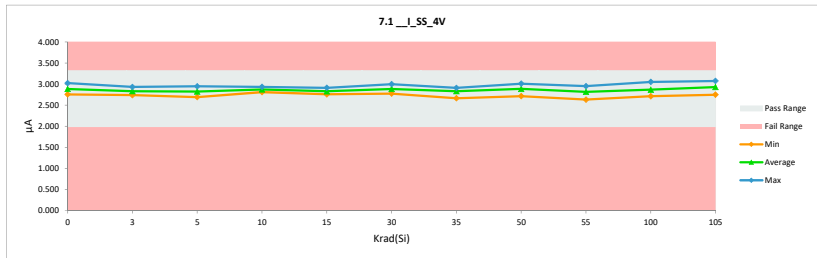


HDR TID Report
TPS7H5002-SP

7.1_I_SS_4V				
Test Site				
Tester				
Test Number				
Unit		µA	µA	
Max Limit		3.32	3.32	
Min Limit		1.98	1.98	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	2.875	2.875	0.000
0	190	2.760	2.758	-0.002
0	191	3.025	3.025	0.000
3	300	2.936	2.934	-0.002
3	305	2.747	2.740	-0.007
3	309	2.897	2.910	0.013
3	311	2.791	2.785	-0.006
3	313	2.780	2.790	0.010
5	319	2.883	2.887	0.004
5	321	2.944	2.949	0.005
5	324	2.726	2.741	0.015
5	327	2.676	2.693	0.017
5	331	2.850	2.853	0.003
10	234	2.886	2.886	0.000
10	235	2.914	2.936	0.022
10	236	2.789	2.812	0.023
10	237	2.891	2.929	0.038
10	238	2.832	2.811	-0.021
15	239	2.906	2.912	0.006
15	240	2.790	2.791	0.001
15	241	2.853	2.871	0.018
15	243	2.783	2.759	-0.024
15	245	2.805	2.820	0.015
30	222	3.019	2.999	-0.020
30	223	2.774	2.813	0.039
30	225	2.790	2.776	-0.014
30	226	2.854	2.875	0.021
30	227	2.966	2.963	-0.003
35	228	2.707	2.666	-0.041
35	229	2.896	2.910	0.014
35	231	2.939	2.901	-0.038
35	232	2.849	2.811	-0.038
35	233	2.799	2.876	0.077
50	210	2.830	2.897	0.067
50	211	2.710	2.714	0.004
50	212	2.989	3.012	0.023
50	213	2.901	2.920	0.019
50	214	2.964	2.898	-0.066
55	215	2.878	2.840	-0.038
55	216	2.907	2.883	-0.024
55	217	2.805	2.774	-0.031
55	218	2.937	2.952	0.015
55	220	2.646	2.633	-0.013
100	247	2.917	2.898	-0.019
100	249	3.011	3.054	0.043
100	250	2.822	2.765	-0.057
100	251	2.769	2.779	0.010
100	253	2.976	2.926	-0.050
100	255	2.915	2.913	-0.002
100	256	2.968	3.015	0.047
100	257	2.746	2.765	0.019
100	258	2.792	2.826	0.034
100	259	2.941	2.931	-0.010
100	260	2.819	2.835	0.016
100	261	3.020	2.963	-0.057
100	262	2.949	2.962	0.013
100	263	2.826	2.777	-0.049
100	264	2.836	2.865	0.029
100	265	2.940	2.945	0.005
100	266	2.848	2.886	0.038
100	267	2.802	2.847	0.045
100	268	2.897	2.871	-0.026
100	269	2.702	2.716	0.014
100	270	2.899	2.885	-0.014
100	271	2.890	2.844	-0.046
100	273	2.711	2.713	0.002
105	274	2.974	3.016	0.042
105	275	2.883	2.861	-0.022
105	276	2.940	2.952	0.012
105	277	3.068	3.075	0.007
105	278	2.790	2.749	-0.041
Max		3.068	3.075	0.007
Average		2.861	2.862	0.001
Min		2.646	2.633	-0.013
Std Dev		0.093	0.095	0.030

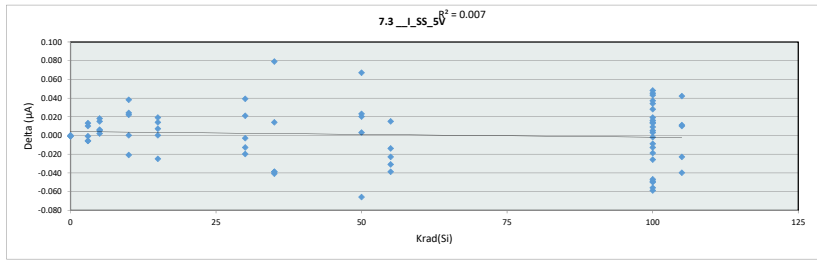


7.1_I_SS_4V											
Test Site											
Tester											
Test Number											
Max Limit		3.32		µA							
Min Limit		1.98		µA							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.758	2.740	2.693	2.811	2.759	2.776	2.666	2.714	2.633	2.713	2.749
Average	2.886	2.832	2.825	2.875	2.831	2.885	2.833	2.888	2.816	2.869	2.931
Max	3.025	2.934	2.949	2.936	2.912	2.999	2.910	3.012	2.952	3.054	3.075
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

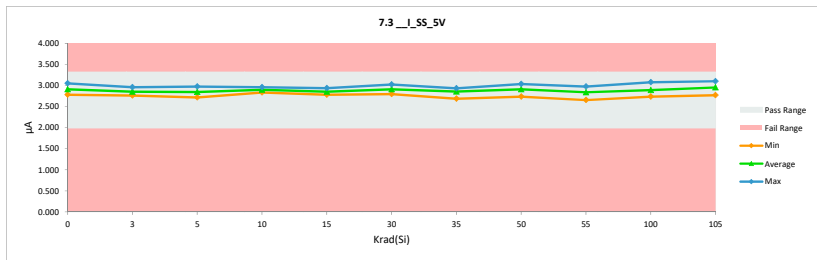


HDR TID Report
TPS7H5002-SP

7.3_I_SS_5V				
Test Site				
Tester				
Test Number				
Unit		µA	µA	
Max Limit		3.32	3.32	
Min Limit		1.98	1.98	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	2.897	2.896	-0.001
0	190	2.781	2.780	-0.001
0	191	3.049	3.049	0.000
3	300	2.959	2.958	-0.001
3	305	2.767	2.761	-0.006
3	309	2.921	2.934	0.013
3	311	2.812	2.806	-0.006
3	313	2.802	2.812	0.010
5	319	2.905	2.910	0.005
5	321	2.967	2.973	0.006
5	324	2.746	2.761	0.015
5	327	2.697	2.715	0.018
5	331	2.871	2.873	0.002
10	234	2.910	2.910	0.000
10	235	2.938	2.960	0.022
10	236	2.910	2.834	-0.076
10	237	2.913	2.951	0.038
10	238	2.854	2.833	-0.021
15	239	2.928	2.935	0.007
15	240	2.812	2.812	0.000
15	241	2.876	2.895	0.019
15	243	2.806	2.781	-0.025
15	245	2.827	2.841	0.014
30	222	3.043	3.023	-0.020
30	223	2.796	2.835	0.039
30	225	2.810	2.797	-0.013
30	226	2.877	2.898	0.021
30	227	2.990	2.987	-0.003
35	228	2.728	2.687	-0.041
35	229	2.918	2.932	0.014
35	231	2.962	2.923	-0.039
35	232	2.872	2.833	-0.039
35	233	2.821	2.900	0.079
50	210	2.852	2.919	0.067
50	211	2.731	2.734	0.003
50	212	3.012	3.035	0.023
50	213	2.924	2.944	0.020
50	214	2.987	2.921	-0.066
55	215	2.900	2.861	-0.039
55	216	2.929	2.906	-0.023
55	217	2.827	2.796	-0.031
55	218	2.959	2.974	0.015
55	220	2.667	2.653	-0.014
100	247	2.940	2.921	-0.019
100	249	3.035	3.078	0.043
100	250	2.844	2.785	-0.059
100	251	2.790	2.799	0.009
100	253	3.000	2.950	-0.050
100	255	2.938	2.936	-0.002
100	256	2.991	3.039	0.048
100	257	2.767	2.786	0.019
100	258	2.814	2.848	0.034
100	259	2.964	2.955	-0.009
100	260	2.840	2.856	0.016
100	261	3.042	2.986	-0.056
100	262	2.973	2.987	0.014
100	263	2.847	2.798	-0.049
100	264	2.860	2.888	0.028
100	265	2.963	2.968	0.005
100	266	2.870	2.907	0.037
100	267	2.824	2.869	0.045
100	268	2.920	2.894	-0.026
100	269	2.723	2.736	0.013
100	270	2.920	2.907	-0.013
100	271	2.913	2.866	-0.047
100	273	2.733	2.736	0.003
105	274	2.998	3.040	0.042
105	275	2.907	2.884	-0.023
105	276	2.963	2.974	0.011
105	277	3.091	3.101	0.010
105	278	2.810	2.770	-0.040
Max		3.091	3.101	0.079
Average		2.884	2.885	0.001
Min		2.667	2.653	-0.066
Std Dev		0.094	0.096	0.030

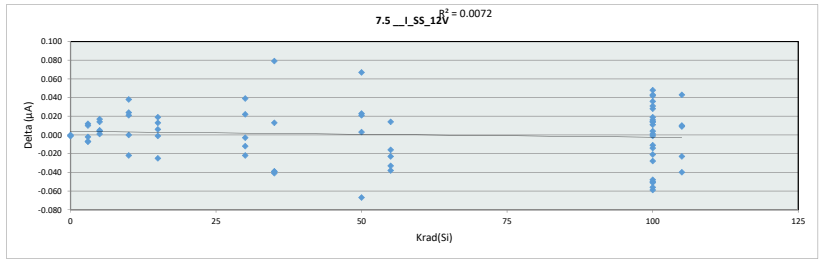


7.3_I_SS_5V											
Test Site											
Tester											
Test Number											
Max Limit		3.32	µA								
Min Limit		1.98	µA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.780	2.761	2.715	2.833	2.781	2.797	2.687	2.734	2.653	2.736	2.770
Average	2.908	2.854	2.846	2.898	2.853	2.908	2.855	2.911	2.838	2.891	2.954
Max	3.049	2.958	2.973	2.960	2.935	3.023	2.932	3.035	2.974	3.078	3.101
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

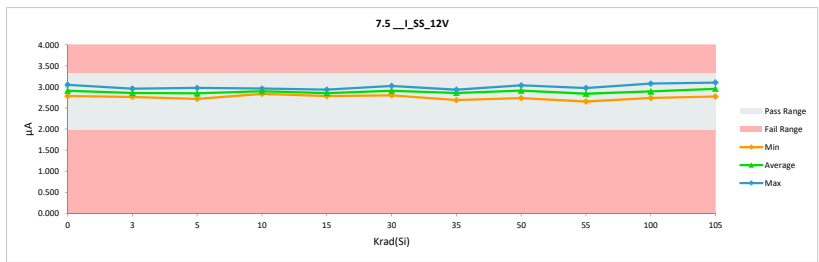


HDR TID Report TPS7H5002-SP

7.5 I SS 12V				
Test Site				
Tester				
Test Number				
Unit		µA	µA	
Max Limit		3.32	3.32	
Min Limit		1.98	1.98	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	2.897	2.896	-0.001
0	190	2.783	2.782	-0.001
0	191	3.050	3.050	0.000
3	300	2.961	2.959	-0.002
3	305	2.769	2.762	-0.007
3	309	2.922	2.934	0.012
3	311	2.813	2.806	-0.007
3	313	2.803	2.813	0.010
5	319	2.906	2.910	0.004
5	321	2.970	2.975	0.005
5	324	2.747	2.761	0.014
5	327	2.698	2.715	0.017
5	331	2.875	2.876	0.001
10	234	2.911	2.911	0.000
10	235	2.940	2.961	0.021
10	236	2.811	2.835	0.024
10	237	2.914	2.952	0.038
10	238	2.856	2.834	-0.022
15	239	2.929	2.935	0.006
15	240	2.814	2.813	-0.001
15	241	2.876	2.895	0.019
15	243	2.807	2.782	-0.025
15	245	2.829	2.842	0.013
30	222	3.045	3.023	-0.022
30	223	2.796	2.835	0.039
30	225	2.812	2.800	-0.012
30	226	2.878	2.900	0.022
30	227	2.991	2.988	-0.003
35	228	2.728	2.687	-0.041
35	229	2.920	2.933	0.013
35	231	2.963	2.924	-0.039
35	232	2.872	2.833	-0.039
35	233	2.821	2.900	0.079
50	210	2.852	2.919	0.067
50	211	2.732	2.735	0.003
50	212	3.014	3.037	0.023
50	213	2.925	2.946	0.021
50	214	2.988	2.921	-0.067
55	215	2.901	2.863	-0.038
55	216	2.930	2.907	-0.023
55	217	2.830	2.797	-0.033
55	218	2.960	2.974	0.014
55	220	2.668	2.652	-0.016
100	247	2.944	2.923	-0.021
100	249	3.037	3.079	0.042
100	250	2.846	2.787	-0.059
100	251	2.789	2.800	0.011
100	253	3.000	2.949	-0.051
100	255	2.938	2.937	-0.001
100	256	2.993	3.041	0.048
100	257	2.767	2.786	0.019
100	258	2.816	2.847	0.031
100	259	2.965	2.954	-0.011
100	260	2.842	2.858	0.016
100	261	3.043	2.987	-0.056
100	262	2.974	2.988	0.014
100	263	2.849	2.799	-0.050
100	264	2.862	2.890	0.028
100	265	2.964	2.968	0.004
100	266	2.871	2.907	0.036
100	267	2.825	2.868	0.043
100	268	2.922	2.894	-0.028
100	269	2.724	2.738	0.014
100	270	2.921	2.907	-0.014
100	271	2.913	2.865	-0.048
100	273	2.735	2.736	0.001
105	274	2.998	3.041	0.043
105	275	2.907	2.884	-0.023
105	276	2.963	2.972	0.009
105	277	3.092	3.102	0.010
105	278	2.811	2.771	-0.040
	Max	3.092	3.102	0.079
	Average	2.885	2.885	0.000
	Min	2.668	2.652	-0.067
	Std Dev	0.094	0.096	0.030

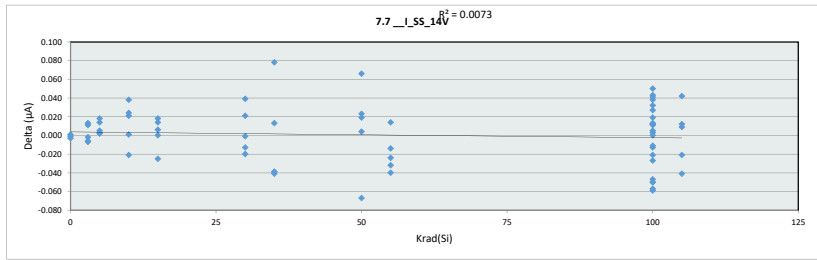


7.5 I SS 12V											
Test Site											
Tester											
Test Number											
Max Limit			3.32	µA							
Min Limit			1.98	µA							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.782	2.762	2.715	2.834	2.782	2.800	2.687	2.735	2.652	2.736	2.771
Average	2.909	2.855	2.847	2.899	2.853	2.909	2.855	2.912	2.839	2.892	2.954
Max	3.050	2.959	2.975	2.961	2.935	3.023	2.933	3.037	2.974	3.079	3.102
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

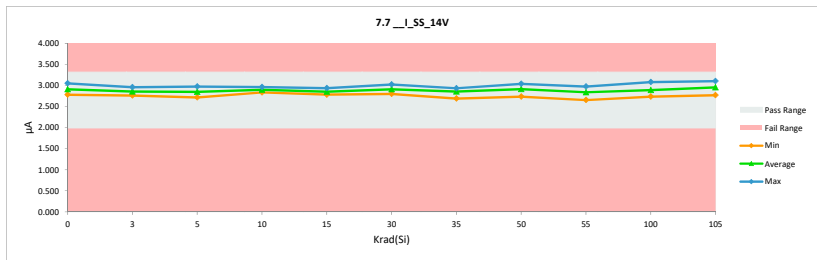


HDR TID Report TPS7H5002-SP

7.7_I_SS_14V				
Test Site				
Tester				
Test Number				
Unit		µA	µA	
Max Limit		3.32	3.32	
Min Limit		1.98	1.98	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	2.897	2.898	0.001
0	190	2.784	2.781	-0.003
0	191	3.050	3.049	-0.001
3	300	2.961	2.959	-0.002
3	305	2.769	2.762	-0.007
3	309	2.922	2.935	0.013
3	311	2.813	2.807	-0.006
3	313	2.802	2.813	0.011
5	319	2.907	2.910	0.003
5	321	2.970	2.975	0.005
5	324	2.747	2.761	0.014
5	327	2.698	2.716	0.018
5	331	2.874	2.876	0.002
10	234	2.911	2.912	0.001
10	235	2.940	2.961	0.021
10	236	2.811	2.835	0.024
10	237	2.914	2.952	0.038
10	238	2.856	2.835	-0.021
15	239	2.929	2.935	0.006
15	240	2.813	2.813	0.000
15	241	2.876	2.894	0.018
15	243	2.807	2.782	-0.025
15	245	2.828	2.842	0.014
30	222	3.044	3.024	-0.020
30	223	2.796	2.835	0.039
30	225	2.812	2.799	-0.013
30	226	2.879	2.900	0.021
30	227	2.991	2.990	-0.001
35	228	2.729	2.688	-0.041
35	229	2.921	2.934	0.013
35	231	2.963	2.924	-0.039
35	232	2.873	2.834	-0.039
35	233	2.821	2.899	0.078
50	210	2.853	2.919	0.066
50	211	2.731	2.735	0.004
50	212	3.014	3.037	0.023
50	213	2.925	2.944	0.019
50	214	2.989	2.922	-0.067
55	215	2.901	2.861	-0.040
55	216	2.930	2.906	-0.024
55	217	2.830	2.798	-0.032
55	218	2.961	2.975	0.014
55	220	2.667	2.653	-0.014
100	247	2.943	2.922	-0.021
100	249	3.036	3.079	0.043
100	250	2.845	2.786	-0.059
100	251	2.789	2.800	0.011
100	253	3.001	2.951	-0.050
100	255	2.938	2.938	0.000
100	256	2.992	3.042	0.050
100	257	2.767	2.786	0.019
100	258	2.816	2.848	0.032
100	259	2.966	2.955	-0.011
100	260	2.843	2.855	0.012
100	261	3.044	2.987	-0.057
100	262	2.974	2.987	0.013
100	263	2.848	2.798	-0.050
100	264	2.862	2.889	0.027
100	265	2.964	2.969	0.005
100	266	2.871	2.909	0.038
100	267	2.826	2.867	0.041
100	268	2.921	2.894	-0.027
100	269	2.724	2.736	0.012
100	270	2.920	2.907	-0.013
100	271	2.914	2.867	-0.047
100	273	2.734	2.737	0.003
105	274	2.989	3.041	0.042
105	275	2.907	2.886	-0.021
105	276	2.963	2.972	0.009
105	277	3.091	3.103	0.012
105	278	2.811	2.770	-0.041
Max		3.091	3.103	0.078
Average		2.885	2.885	0.001
Min		2.667	2.653	-0.067
Std Dev		0.094	0.096	0.030

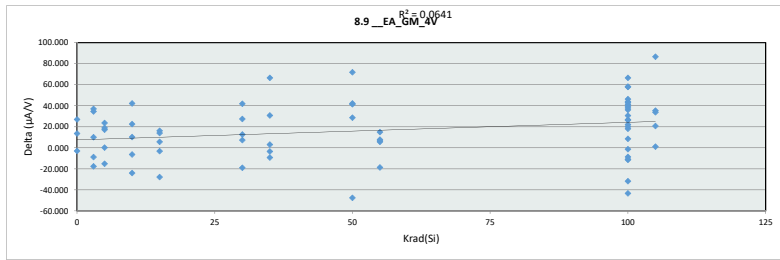


7.7_I_SS_14V											
Test Site											
Tester											
Test Number											
Max Limit		3.32		µA							
Min Limit		1.98		µA							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.781	2.762	2.716	2.835	2.782	2.799	2.688	2.735	2.653	2.736	2.770
Average	2.909	2.855	2.848	2.899	2.853	2.910	2.856	2.911	2.839	2.892	2.954
Max	3.049	2.959	2.975	2.961	2.935	3.024	2.934	3.037	2.975	3.079	3.103
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

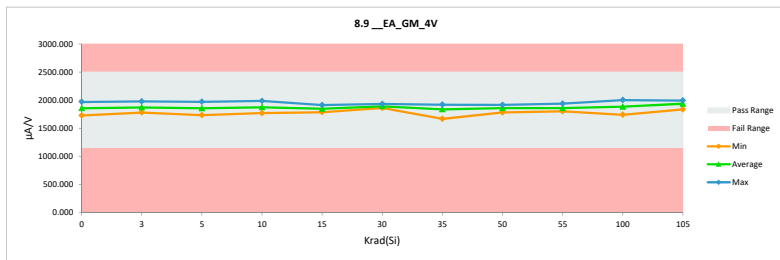


HDR TID Report TPS7H5002-SP

8.9_EA_GM_4V				
Test Site				
Tester				
Test Number				
Unit	µA/V	µA/V		
Max Limit	2500	2500		
Min Limit	1150	1150		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1847.965	1874.706	26.741
0	190	1729.761	1726.540	-3.221
0	191	1953.679	1967.090	13.411
3	300	1881.909	1864.042	-17.867
3	305	1742.707	1779.566	36.859
3	309	1966.745	1976.639	9.894
3	311	1868.339	1859.342	-8.997
3	313	1840.737	1875.084	34.347
5	319	1946.595	1970.024	23.429
5	321	1848.617	1867.247	18.630
5	324	1747.891	1732.459	-15.432
5	327	1829.307	1829.307	0.000
5	331	1858.889	1875.912	17.023
10	234	1869.974	1845.800	-24.174
10	235	1852.321	1874.602	22.281
10	236	1758.839	1768.881	10.042
10	237	1944.024	1985.975	41.951
10	238	1901.327	1894.778	-6.549
15	239	1896.459	1912.457	15.998
15	240	1880.179	1894.008	13.829
15	241	1844.035	1840.737	-3.298
15	243	1790.391	1795.853	5.462
15	245	1813.047	1784.962	-28.085
30	222	1886.936	1894.019	7.083
30	223	1852.321	1894.008	41.687
30	225	1834.999	1862.283	27.284
30	226	1858.168	1870.620	12.452
30	227	1950.454	1931.268	-19.186
35	228	1676.113	1666.638	-9.475
35	229	1805.905	1836.425	30.520
35	231	1916.914	1913.242	-3.672
35	232	1843.630	1846.521	2.891
35	233	1852.321	1918.690	66.369
50	210	1810.201	1881.909	71.708
50	211	1752.418	1780.907	28.489
50	212	1864.032	1905.214	41.182
50	213	1872.938	1914.934	41.996
50	214	1854.646	1806.867	-47.779
55	215	1795.843	1801.338	5.495
55	216	1881.909	1889.443	7.534
55	217	1840.737	1821.848	-18.889
55	218	1931.268	1937.625	6.357
55	220	1820.257	1835.009	14.752
100	247	1942.403	1983.298	40.895
100	249	1921.822	1930.191	8.369
100	250	1799.136	1835.009	35.873
100	251	1771.533	1801.900	30.367
100	253	1963.465	1954.623	-8.842
100	255	1848.678	1886.437	37.759
100	256	1882.584	1948.845	66.261
100	257	1875.912	1918.690	42.778
100	258	1864.042	1885.907	21.865
100	259	1823.987	1881.909	57.922
100	260	1712.800	1739.284	26.484
100	261	1932.169	1900.121	-32.048
100	262	1983.275	2001.266	17.991
100	263	1813.361	1802.180	-11.181
100	264	1849.993	1869.963	19.970
100	265	1900.703	1889.135	-11.568
100	266	1880.907	1907.011	26.104
100	267	1880.569	1938.207	57.638
100	268	1795.843	1841.899	46.056
100	269	1801.338	1840.747	39.409
100	270	1825.257	1823.650	-1.607
100	271	1963.465	1919.952	-43.513
100	273	1784.962	1828.569	43.607
105	274	1937.636	1958.159	20.523
105	275	1906.274	1902.674	-3.600
105	276	1927.740	1928.571	0.831
105	277	1934.542	1969.700	35.158
105	278	1801.329	1835.009	33.680
	Max	1983.275	2001.266	86.400
	Average	1856.922	1873.489	16.567
	Min	1676.113	1666.638	-47.779
	Std Dev	66.029	68.818	26.990

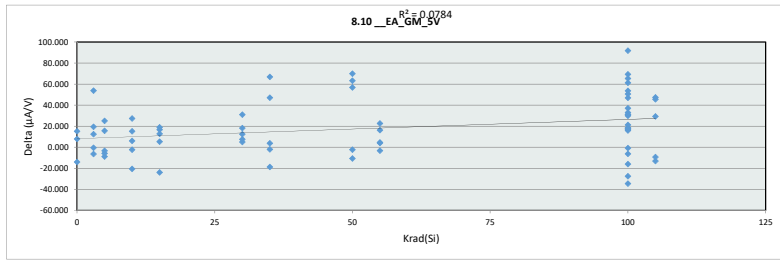


8.9_EA_GM_4V											
Test Site											
Tester											
Test Number											
Max Limit	2500	µA/V									
Min Limit	1150	µA/V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1726.540	1779.566	1732.459	1768.881	1784.962	1862.283	1666.638	1780.907	1801.338	1729.284	1835.009
Average	1856.112	1870.935	1854.990	1874.007	1845.603	1890.440	1836.303	1857.966	1857.053	1883.861	1936.823
Max	1967.090	1976.639	1970.024	1985.975	1912.457	1931.268	1918.690	1914.934	1937.625	2001.266	1992.674
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000

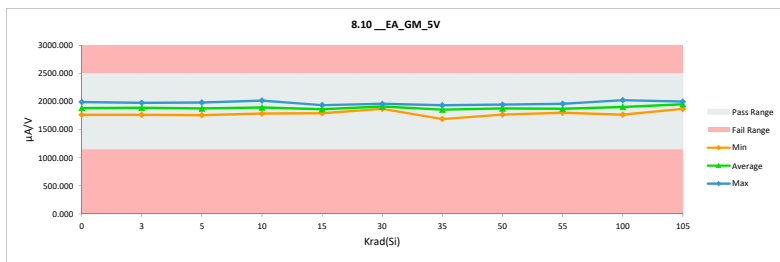


HDR TID Report
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8.10_EA_GM_5V				
Test Site				
Tester				
Test Number				
Unit	µA/V	µA/V		
Max Limit	2500	2500		
Min Limit	1150	1150		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1905.030	1890.955	-14.075
0	190	1755.995	1763.823	7.828
0	191	1974.836	1989.991	15.155
3	300	1858.179	1911.890	53.711
3	305	1768.881	1762.396	-6.485
3	309	1976.627	1976.045	-0.582
3	311	1881.666	1894.008	12.342
3	313	1869.953	1889.294	19.341
5	319	1988.317	1982.455	-5.862
5	321	1866.977	1891.989	25.012
5	324	1742.716	1758.314	15.598
5	327	1867.413	1864.042	-3.371
5	331	1888.550	1879.653	-8.897
10	234	1890.646	1869.974	-20.672
10	235	1881.909	1887.944	6.035
10	236	1770.402	1785.570	15.168
10	237	1989.991	2017.244	27.253
10	238	1916.345	1914.006	-2.339
15	239	1920.975	1937.625	16.650
15	240	1881.233	1900.121	18.888
15	241	1858.827	1864.042	5.215
15	243	1814.292	1790.391	-23.901
15	245	1805.905	1818.649	12.744
30	222	1900.121	1912.467	12.346
30	223	1894.008	1924.953	30.945
30	225	1852.321	1870.505	18.184
30	226	1876.930	1881.909	4.979
30	227	1950.454	1958.022	7.568
35	228	1706.658	1687.908	-18.750
35	229	1824.156	1871.277	47.121
35	231	1924.169	1922.174	-1.995
35	232	1860.373	1864.042	3.669
35	233	1866.998	1933.806	66.808
50	210	1818.018	1887.944	69.926
50	211	1779.576	1768.881	-10.695
50	212	1880.812	1944.012	63.200
50	213	1885.928	1942.752	56.824
50	214	1837.300	1835.009	-2.291
55	215	1804.089	1800.778	-3.311
55	216	1896.041	1900.121	4.080
55	217	1840.737	1845.222	4.485
55	218	1936.026	1958.559	22.533
55	220	1829.307	1845.455	16.148
100	247	1948.822	2002.425	53.603
100	249	1924.953	1918.690	-6.263
100	250	1818.018	1868.641	50.623
100	251	1763.591	1824.781	61.190
100	253	1963.465	1979.286	15.821
100	255	1886.299	1904.597	18.298
100	256	1890.071	1955.330	65.259
100	257	1900.111	1931.268	31.157
100	258	1887.944	1918.690	30.746
100	259	1842.911	1912.151	69.240
100	260	1728.389	1765.344	36.955
100	261	1928.460	1912.467	-15.993
100	262	2004.662	2022.923	18.261
100	263	1811.628	1832.864	21.236
100	264	1848.821	1881.793	32.972
100	265	1937.625	1902.966	-34.659
100	266	1883.229	1900.111	16.882
100	267	1852.321	1944.024	91.703
100	268	1823.640	1870.547	46.907
100	269	1831.844	1849.402	17.558
100	270	1867.819	1866.904	-0.915
100	271	1978.107	1950.454	-27.653
100	273	1800.894	1830.714	29.820
105	274	1959.898	1950.476	-9.422
105	275	1931.279	1978.574	47.295
105	276	1970.024	1956.926	-13.098
105	277	1952.292	1997.870	45.578
105	278	1840.737	1869.953	29.216
	Max	2004.662	2022.923	91.703
	Average	1873.486	1891.498	18.012
	Min	1706.658	1687.908	-34.659
	Std Dev	66.859	68.259	26.757

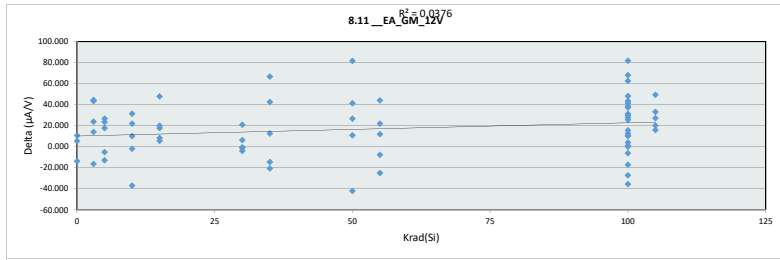


8.10_EA_GM_5V											
Test Site											
Tester											
Test Number											
Max Limit	2500	µA/V									
Min Limit	1150	µA/V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1763.823	1762.396	1758.314	1785.570	1790.391	1870.505	1687.908	1768.881	1800.778	1765.344	1869.953
Average	1881.590	1886.727	1875.291	1894.948	1862.166	1909.571	1855.841	1875.720	1870.027	1902.016	1950.760
Max	1989.991	1976.045	1982.455	2017.244	1937.625	1958.022	1933.806	1944.012	1958.559	2022.923	1997.870
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000

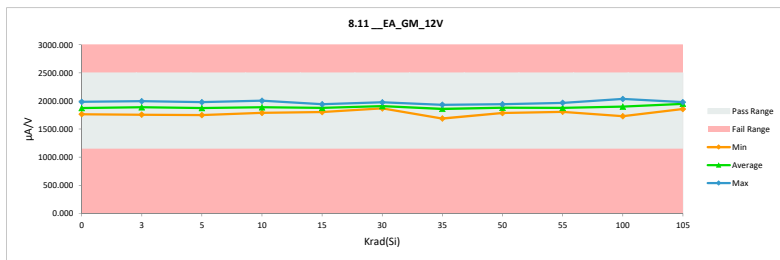


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8.11_EA_GM_12V				
Test Site				
Tester				
Test Number				
Unit	µA/V	µA/V		
Max Limit	2500	2500		
Min Limit	1150	1150		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1883.906	1869.963	-13.943
0	190	1752.510	1763.100	10.590
0	191	1977.967	1983.298	5.331
3	300	1864.052	1908.486	44.434
3	305	1771.243	1754.838	-16.405
3	309	1952.224	1995.601	43.377
3	311	1881.255	1895.227	13.972
3	313	1858.158	1881.909	23.751
5	319	1952.610	1979.297	26.687
5	321	1870.568	1894.008	23.440
5	324	1753.087	1747.891	-5.196
5	327	1852.321	1869.953	17.632
5	331	1889.284	1876.384	-12.900
10	234	1889.284	1852.311	-36.973
10	235	1875.922	1885.917	9.995
10	236	1765.697	1787.663	21.966
10	237	1972.212	2003.525	31.313
10	238	1906.274	1904.208	-2.066
15	239	1933.628	1941.886	8.258
15	240	1881.909	1929.691	47.782
15	241	1856.072	1875.912	19.840
15	243	1795.843	1801.338	5.495
15	245	1809.020	1826.728	17.708
30	222	1900.121	1906.274	6.153
30	223	1912.457	1908.334	-4.123
30	225	1846.521	1867.278	20.757
30	226	1875.325	1874.120	-1.205
30	227	1976.639	1975.964	-0.675
35	228	1707.978	1687.297	-20.681
35	229	1827.504	1869.963	42.459
35	231	1943.393	1928.748	-14.645
35	232	1862.304	1874.717	12.413
35	233	1864.632	1931.268	66.636
50	210	1812.421	1894.008	81.587
50	211	1774.212	1784.953	10.741
50	212	1886.851	1928.116	41.265
50	213	1915.240	1941.897	26.657
50	214	1888.603	1846.430	-42.173
55	215	1814.096	1806.255	-7.841
55	216	1897.553	1909.355	11.802
55	217	1869.963	1844.755	-25.208
55	218	1921.371	1965.328	43.957
55	220	1836.275	1858.158	21.883
100	247	1959.371	1900.605	-58.766
100	249	1924.953	1937.625	12.672
100	250	1816.345	1844.187	27.842
100	251	1802.345	1812.411	10.066
100	253	1981.050	1981.963	0.913
100	255	1871.110	1912.446	41.336
100	256	1893.858	1956.344	62.486
100	257	1877.707	1907.629	29.922
100	258	1878.474	1889.156	10.682
100	259	1845.252	1913.231	67.979
100	260	1727.375	1727.384	0.009
100	261	1935.178	1917.900	-17.278
100	262	1997.573	2035.983	38.410
100	263	1819.704	1823.640	3.936
100	264	1862.376	1887.944	25.568
100	265	1931.268	1904.068	-27.200
100	266	1886.437	1934.452	48.015
100	267	1859.342	1941.077	81.735
100	268	1830.255	1867.330	37.075
100	269	1843.620	1837.300	-6.320
100	270	1851.790	1867.278	15.488
100	271	1989.991	1954.327	-35.664
100	273	1803.178	1846.521	43.343
105	274	1956.344	1972.166	15.822
105	275	1909.974	1959.326	49.352
105	276	1950.454	1977.571	27.117
105	277	1956.937	1976.930	19.993
105	278	1823.640	1856.575	32.935
	Max	1997.573	2035.983	81.735
	Average	1874.147	1891.066	16.920
	Min	1707.978	1687.297	-42.173
	Std Dev	65.083	69.916	26.607



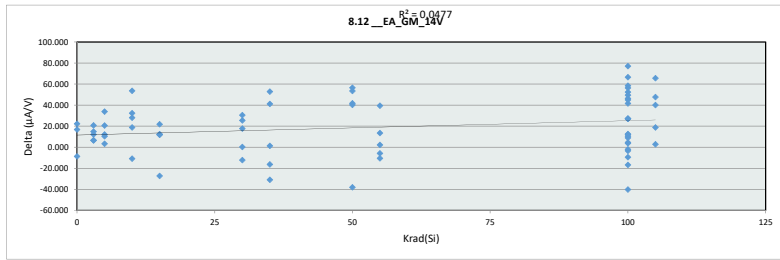
8.11_EA_GM_12											
Test Site											
Tester											
Test Number											
Max Limit	2500	µA/V									
Min Limit	1150	µA/V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1763.100	1754.838	1747.891	1787.663	1801.338	1867.278	1687.297	1784.953	1806.255	1727.384	1856.575
Average	1872.120	1887.212	1873.507	1886.725	1875.111	1906.394	1858.399	1879.081	1876.770	1899.600	1948.514
Max	1983.298	1995.601	1979.297	2003.525	1941.886	1975.964	1931.268	1941.897	1965.328	2035.983	1977.571
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



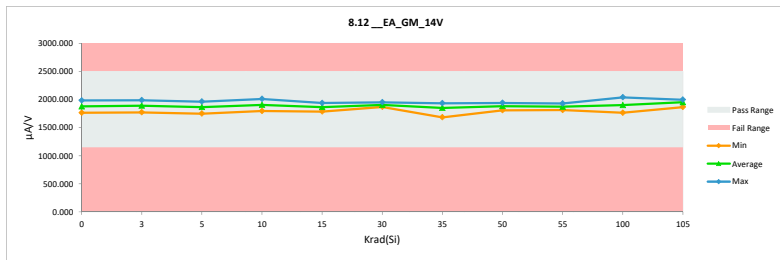
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8.12_EA_GM_14V				
Test Site	Tester	Unit	Max Limit	Min Limit
8.12	EA	GM	14V	
Serial #	PRE	POST	Delta	
0	189	1864.777	1886.967	22.190
0	190	1747.882	1764.472	16.590
0	191	1992.236	1983.298	-8.938
3	300	1895.377	1916.017	20.640
3	305	1764.296	1770.823	6.527
3	309	1976.627	1988.493	11.866
3	311	1888.422	1894.682	6.260
3	313	1869.953	1884.646	14.693
5	319	1952.451	1962.649	10.198
5	321	1869.963	1881.909	11.946
5	324	1727.384	1747.891	20.507
5	327	1818.018	1851.739	33.721
5	331	1882.458	1885.557	3.099
10	234	1886.914	1875.912	-11.002
10	235	1906.274	1924.964	18.690
10	236	1767.912	1795.843	27.931
10	237	1956.937	2010.367	53.430
10	238	1887.944	1920.095	32.151
15	239	1926.003	1937.625	11.622
15	240	1894.008	1906.274	12.266
15	241	1864.052	1875.912	11.860
15	243	1812.421	1784.962	-27.459
15	245	1801.929	1823.640	21.711
30	222	1912.457	1900.121	-12.336
30	223	1875.912	1906.264	30.352
30	225	1842.901	1868.276	25.375
30	226	1876.468	1894.008	17.540
30	227	1950.306	1950.454	0.148
35	228	1713.569	1682.424	-31.145
35	229	1824.136	1865.119	40.983
35	231	1933.282	1934.441	1.159
35	232	1867.330	1850.871	-16.459
35	233	1866.001	1918.679	52.678
50	210	1840.737	1894.019	53.282
50	211	1766.227	1807.870	41.643
50	212	1881.233	1937.625	56.392
50	213	1898.380	1938.476	40.096
50	214	1875.335	1837.079	-38.256
55	215	1823.095	1812.421	-10.674
55	216	1904.197	1906.274	2.077
55	217	1840.737	1834.698	-6.039
55	218	1916.203	1929.469	13.266
55	220	1842.668	1881.909	39.241
100	247	1949.026	2005.225	56.199
100	249	1936.697	1927.076	-9.621
100	250	1837.159	1845.932	8.773
100	251	1790.391	1817.221	26.830
100	253	1968.394	1978.632	10.238
100	255	1889.677	1894.019	4.342
100	256	1888.486	1954.828	66.342
100	257	1881.909	1926.666	44.757
100	258	1887.934	1885.663	-2.271
100	259	1841.778	1900.111	58.333
100	260	1712.310	1764.546	52.236
100	261	1929.469	1925.859	-3.610
100	262	1996.717	2038.210	41.493
100	263	1834.047	1846.511	12.464
100	264	1880.401	1883.916	3.515
100	265	1930.579	1913.613	-16.966
100	266	1888.550	1934.742	46.192
100	267	1861.219	1938.039	76.820
100	268	1812.421	1861.829	49.408
100	269	1819.566	1846.511	26.945
100	270	1860.115	1871.935	11.820
100	271	1971.668	1931.268	-40.400
100	273	1805.817	1832.864	27.047
105	274	1931.268	1986.729	65.461
105	275	1905.041	1952.599	47.558
105	276	1967.402	1970.024	2.622
105	277	1938.274	1978.235	39.961
105	278	1846.511	1865.202	18.691
Max		1996.717	2038.210	76.820
Average		1872.792	1891.736	18.944
Min		1712.310	1682.424	-40.400
Std Dev		64.767	67.269	25.735

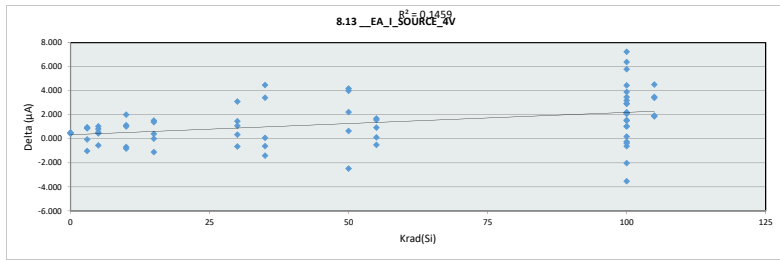


8.12_EA_GM_14											
Test Site	Tester	Unit	Max Limit	Min Limit							
8.12	EA	GM	14								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1764.472	1770.823	1747.891	1795.843	1784.962	1868.276	1682.424	1807.870	1812.421	1764.546	1865.202
Average	1878.246	1890.932	1865.949	1905.436	1865.683	1903.825	1850.307	1883.014	1872.954	1901.096	1952.558
Max	1983.298	1988.493	1962.649	2010.367	1937.625	1950.454	1934.441	1938.476	1929.469	2038.210	1996.729
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000

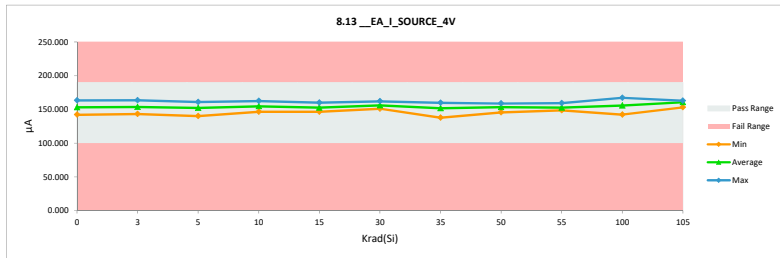


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8.13_EA_I_SOURCE_4V				
Test Site	Tester	Test Number	Unit	
			µA	µA
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	153.771	154.238	0.467
0	190	141.687	142.088	0.401
0	191	162.759	163.265	0.506
3	300	153.544	154.406	0.862
3	305	143.147	143.070	-0.077
3	309	162.671	163.610	0.939
3	311	153.659	152.621	-1.038
3	313	153.864	154.738	0.874
5	319	160.263	161.059	0.796
5	321	153.396	153.922	0.526
5	324	139.572	140.010	0.438
5	327	150.281	151.298	1.017
5	331	155.114	154.544	-0.570
10	234	154.330	153.495	-0.835
10	235	155.316	156.422	1.106
10	236	144.473	146.467	1.994
10	237	161.488	162.499	1.011
10	238	155.174	154.474	-0.700
15	239	160.101	160.100	-0.001
15	240	155.456	156.819	1.363
15	241	151.099	151.488	0.389
15	243	147.517	146.395	-1.122
15	245	147.395	148.887	1.492
30	222	156.453	155.791	-0.662
30	223	153.557	156.645	3.088
30	225	150.680	151.008	0.328
30	226	152.717	154.167	1.450
30	227	160.949	162.014	1.065
35	228	139.111	137.695	-1.416
35	229	147.774	151.172	3.398
35	231	159.696	159.750	0.054
35	232	151.992	151.359	-0.633
35	233	153.929	158.385	4.456
50	210	150.362	154.525	4.163
50	211	144.794	145.423	0.629
50	212	155.606	157.806	2.200
50	213	154.764	158.740	3.976
50	214	152.465	149.964	-2.501
55	215	148.452	148.552	0.100
55	216	154.961	155.876	0.915
55	217	150.434	149.909	-0.525
55	218	157.634	159.311	1.677
55	220	147.604	149.160	1.556
100	247	161.117	163.172	2.055
100	249	159.571	159.744	0.173
100	250	148.227	151.142	2.915
100	251	146.204	147.745	1.541
100	253	162.334	161.699	-0.635
100	255	153.631	155.823	2.192
100	256	154.255	161.478	7.223
100	257	153.472	156.647	3.175
100	258	154.972	155.990	1.018
100	259	151.845	157.614	5.769
100	260	140.832	142.311	1.479
100	261	159.500	159.126	-0.374
100	262	163.352	167.228	3.876
100	263	148.080	147.822	-0.258
100	264	149.797	152.689	2.892
100	265	158.378	156.337	-2.041
100	266	153.799	158.230	4.431
100	267	151.089	157.467	6.378
100	268	150.126	152.268	2.142
100	269	148.571	150.716	2.145
100	270	152.425	153.441	1.016
100	271	165.124	161.580	-3.544
100	273	146.890	150.340	3.450
105	274	159.090	162.564	3.474
105	275	158.026	162.525	4.499
105	276	160.147	162.059	1.912
105	277	159.378	162.766	3.388
105	278	151.222	153.071	1.849
Max		165.124	167.228	7.223
Average		153.260	154.602	1.342
Min		139.111	137.695	-3.544
Std Dev		5.923	6.187	2.009

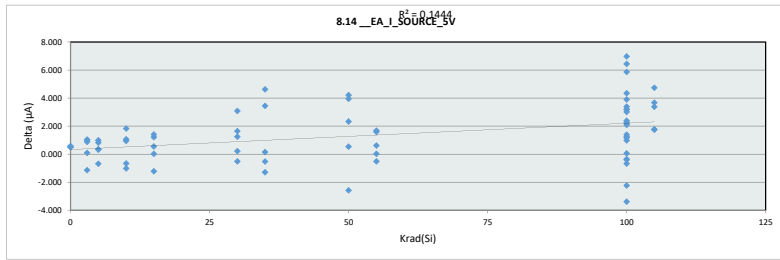


8.13_EA_I_SOURCE_4V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.088	143.070	140.010	146.467	146.395	151.008	137.695	145.423	148.552	142.311	153.071
Average	153.197	153.689	152.167	154.671	152.738	155.925	151.672	153.292	152.562	155.679	160.597
Max	163.265	163.610	161.059	162.499	162.499	160.100	162.014	159.750	158.740	159.311	167.228
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

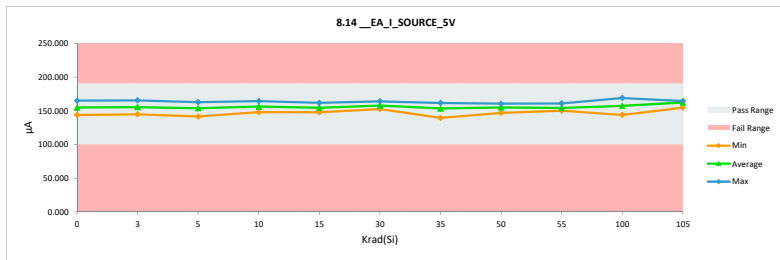


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8.14_EA_I_SOURCE_5V				
Test Site				
Tester				
Test Number				
Unit		µA	µA	
Max Limit		190	190	
Min Limit		100	100	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	155.515	156.019	0.504
0	190	143.229	143.741	0.512
0	191	164.496	165.081	0.585
3	300	155.219	156.237	1.018
3	305	144.608	144.701	0.093
3	309	164.367	165.404	1.037
3	311	155.342	154.203	-1.139
3	313	155.458	156.339	0.881
5	319	161.988	162.810	0.822
5	321	155.107	155.446	0.339
5	324	141.215	141.568	0.353
5	327	151.963	152.956	0.993
5	331	156.788	156.108	-0.680
10	234	156.042	155.030	-1.012
10	235	157.133	158.086	0.953
10	236	146.088	147.918	1.830
10	237	163.237	164.306	1.069
10	238	156.852	156.197	-0.655
15	239	161.652	161.680	0.028
15	240	157.267	158.476	1.209
15	241	152.699	153.245	0.546
15	243	149.057	147.843	-1.214
15	245	149.039	150.441	1.402
30	222	158.139	157.626	-0.513
30	223	155.124	158.212	3.088
30	225	152.401	152.627	0.226
30	226	154.405	156.056	1.651
30	227	162.676	163.922	1.246
35	228	140.678	139.389	-1.289
35	229	149.427	152.872	3.445
35	231	161.386	161.543	0.157
35	232	153.588	153.064	-0.524
35	233	155.609	160.228	4.619
50	210	152.144	156.090	3.946
50	211	146.371	146.906	0.535
50	212	157.238	159.565	2.327
50	213	156.323	160.534	4.211
50	214	154.149	151.581	-2.568
55	215	150.153	150.179	0.026
55	216	156.712	157.327	0.615
55	217	152.099	151.589	-0.510
55	218	159.228	160.913	1.685
55	220	149.312	150.899	1.587
100	247	162.848	164.942	2.094
100	249	161.329	161.390	0.061
100	250	149.761	152.960	3.199
100	251	147.910	149.300	1.390
100	253	164.001	163.336	-0.665
100	255	155.218	157.464	2.246
100	256	155.714	162.687	6.973
100	257	155.092	158.278	3.186
100	258	156.541	157.722	1.181
100	259	153.383	159.248	5.865
100	260	142.597	143.841	1.244
100	261	161.061	160.662	-0.399
100	262	165.041	168.943	3.902
100	263	149.731	149.384	-0.347
100	264	151.422	154.435	3.013
100	265	160.240	158.007	-2.233
100	266	155.494	159.840	4.346
100	267	152.868	159.303	6.435
100	268	151.603	153.869	2.266
100	269	150.072	152.460	2.388
100	270	154.003	154.986	0.983
100	271	166.743	163.357	-3.386
100	273	148.569	151.957	3.388
105	274	160.808	164.482	3.674
105	275	159.577	164.315	4.738
105	276	161.998	163.786	1.788
105	277	161.064	164.443	3.379
105	278	152.912	154.663	1.751
	Max	166.743	168.943	6.973
	Average	154.917	156.268	1.351
	Min	140.678	139.389	-3.386
	Std Dev	5.951	6.232	2.026

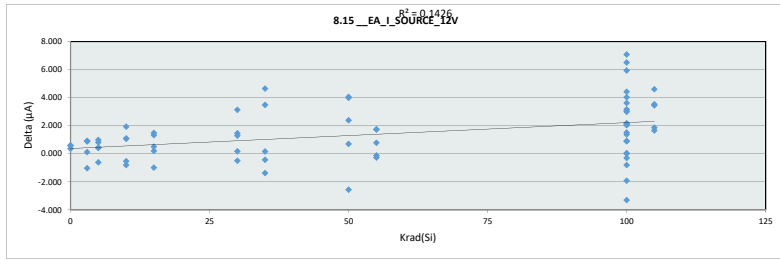


8.14_EA_I_SOURCE											
Test Site											
Tester											
Test Number											
Max Limit		190	µA								
Min Limit		100	µA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	143.741	144.701	141.568	147.918	147.843	152.627	139.389	146.906	150.179	143.841	154.663
Average	154.947	155.377	153.778	156.307	154.337	157.689	153.419	154.935	154.181	157.320	162.338
Max	165.081	165.404	162.810	164.306	161.680	163.922	161.543	160.534	160.913	168.943	164.482
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

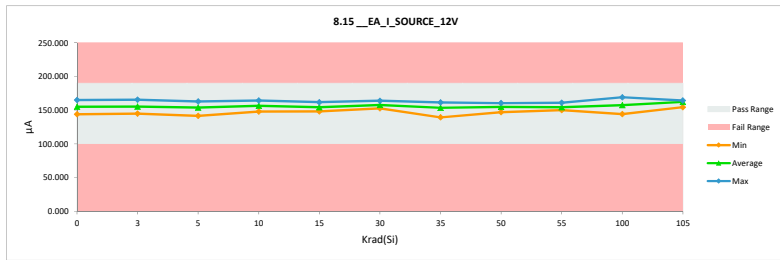


HDR TID Report TPS7H5002-SP

8.15_EA_I_SOURCE_12V				
Test Site	Tester	Test Number	Unit	
			µA	µA
Max Limit			190	190
Min Limit			100	100
Krad(Si)	Serial #	PRE	POST	Delta
0	189	155.511	156.074	0.563
0	190	143.573	143.926	0.353
0	191	164.551	165.150	0.599
3	300	155.341	156.252	0.911
3	305	144.682	144.789	0.107
3	309	164.551	165.422	0.871
3	311	155.351	154.306	-1.045
3	313	155.510	156.409	0.899
5	319	162.043	162.854	0.811
5	321	155.163	155.566	0.403
5	324	141.159	141.598	0.439
5	327	152.043	153.016	0.973
5	331	156.872	156.250	-0.622
10	234	156.078	155.259	-0.819
10	235	157.189	158.259	1.070
10	236	146.102	148.023	1.921
10	237	163.235	164.321	1.086
10	238	156.892	155.247	-0.545
15	239	161.668	161.855	0.187
15	240	157.204	158.528	1.324
15	241	152.665	153.161	0.496
15	243	149.126	148.130	-0.996
15	245	149.141	150.623	1.482
30	222	158.167	157.664	-0.503
30	223	155.150	158.275	3.125
30	225	152.491	152.660	0.169
30	226	154.541	155.996	1.455
30	227	162.700	163.983	1.283
35	228	140.755	139.370	-1.385
35	229	149.538	153.011	3.473
35	231	161.402	161.553	0.151
35	232	153.593	153.153	-0.440
35	233	155.632	160.277	4.645
50	210	152.197	156.175	3.978
50	211	146.267	146.954	0.687
50	212	157.290	159.669	2.379
50	213	156.444	160.490	4.046
50	214	154.115	151.543	-2.572
55	215	150.295	150.167	-0.128
55	216	156.773	157.541	0.768
55	217	152.198	151.922	-0.276
55	218	159.336	161.081	1.745
55	220	149.292	151.016	1.724
100	247	163.029	165.072	2.043
100	249	161.363	161.354	-0.009
100	250	149.956	152.941	2.985
100	251	147.941	149.443	1.502
100	253	164.162	163.353	-0.809
100	255	155.336	157.483	2.147
100	256	155.982	163.054	7.072
100	257	155.214	158.247	3.033
100	258	156.581	157.473	0.892
100	259	153.457	159.390	5.933
100	260	142.711	144.077	1.366
100	261	161.132	160.821	-0.311
100	262	165.163	169.187	4.024
100	263	149.659	149.696	0.037
100	264	151.531	154.686	3.155
100	265	160.129	158.191	-1.938
100	266	155.486	159.901	4.415
100	267	152.874	159.377	6.503
100	268	151.673	153.838	2.165
100	269	150.225	152.279	2.054
100	270	154.023	154.920	0.897
100	271	166.767	163.451	-3.316
100	273	148.661	152.277	3.616
105	274	160.819	164.337	3.518
105	275	159.578	164.169	4.591
105	276	161.993	163.846	1.853
105	277	160.975	164.415	3.440
105	278	152.854	154.506	1.652
	Max	166.767	169.187	7.072
	Average	154.973	156.344	1.370
	Min	140.755	139.370	-3.316
	Std Dev	5.945	6.217	2.005

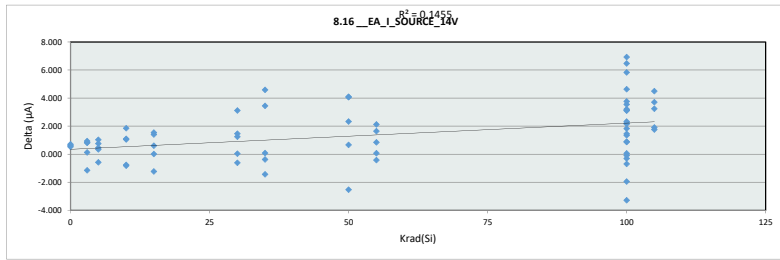


8.15_EA_I_SOURCE_12V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	143.926	144.789	141.598	148.023	148.130	152.660	139.370	146.954	150.167	144.077	154.506
Average	155.050	155.436	153.857	156.442	154.459	157.716	153.473	154.966	154.345	157.414	162.255
Max	165.150	165.422	162.854	164.321	161.855	163.983	161.553	160.490	161.081	169.187	164.415
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

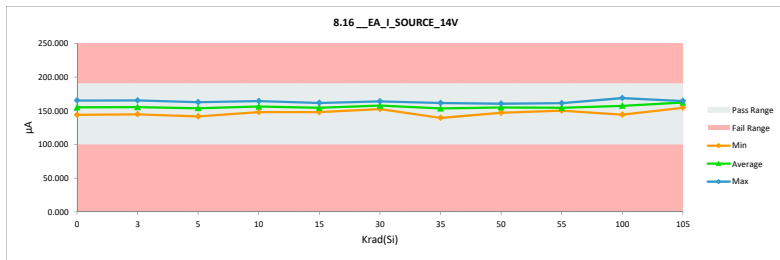


HDR TID Report
TPS7H5002-SP

8.16_EA_I_SOURCE_14V				
Test Site				
Tester				
Test Number				
Unit		µA	µA	
Max Limit		190	190	
Min Limit		100	100	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	155.543	156.058	0.515
0	190	143.311	143.908	0.597
0	191	164.446	165.141	0.695
3	300	155.276	156.100	0.824
3	305	144.610	144.731	0.121
3	309	164.448	165.248	0.800
3	311	155.346	154.191	-1.155
3	313	155.417	156.342	0.925
5	319	161.927	162.676	0.749
5	321	155.144	155.483	0.339
5	324	141.079	141.528	0.449
5	327	151.967	152.980	1.013
5	331	156.779	156.185	-0.594
10	234	156.005	155.171	-0.834
10	235	157.081	158.120	1.039
10	236	146.072	147.915	1.843
10	237	163.160	164.243	1.083
10	238	156.883	156.113	-0.770
15	239	161.563	161.570	0.007
15	240	157.090	158.489	1.399
15	241	152.572	153.169	0.597
15	243	149.097	147.859	-1.238
15	245	149.098	150.627	1.529
30	222	158.105	157.481	-0.624
30	223	155.056	158.165	3.109
30	225	152.510	152.534	0.024
30	226	154.470	155.920	1.450
30	227	162.618	163.858	1.240
35	228	140.813	139.368	-1.445
35	229	149.465	152.903	3.438
35	231	161.373	161.442	0.069
35	232	153.549	153.162	-0.387
35	233	155.562	160.143	4.581
50	210	152.084	156.140	4.056
50	211	146.325	146.979	0.654
50	212	157.254	159.573	2.319
50	213	156.378	160.465	4.087
50	214	154.045	151.504	-2.541
55	215	150.078	150.138	0.060
55	216	156.728	157.559	0.831
55	217	152.126	151.698	-0.428
55	218	159.104	161.219	2.115
55	220	149.206	150.835	1.629
100	247	162.854	165.062	2.208
100	249	161.353	161.406	0.053
100	250	149.891	153.021	3.130
100	251	147.845	149.187	1.342
100	253	164.069	163.368	-0.701
100	255	155.229	157.047	1.818
100	256	155.957	162.876	6.919
100	257	155.106	158.298	3.192
100	258	156.664	157.528	0.864
100	259	153.355	159.177	5.822
100	260	142.652	144.087	1.435
100	261	161.010	160.690	-0.320
100	262	165.046	168.797	3.751
100	263	149.734	149.632	-0.102
100	264	151.505	154.595	3.090
100	265	160.050	158.097	-1.953
100	266	155.384	160.007	4.623
100	267	152.811	159.272	6.461
100	268	151.592	153.905	2.313
100	269	150.079	152.231	2.152
100	270	153.881	154.767	0.886
100	271	166.716	163.429	-3.287
100	273	148.581	152.124	3.543
105	274	160.735	164.430	3.695
105	275	159.530	164.011	4.481
105	276	161.921	163.817	1.896
105	277	160.908	164.137	3.229
105	278	152.726	154.473	1.747
Max		166.716	168.797	6.919
Average		154.901	156.259	1.359
Min		140.813	139.368	-3.287
Std Dev		5.936	6.201	2.006

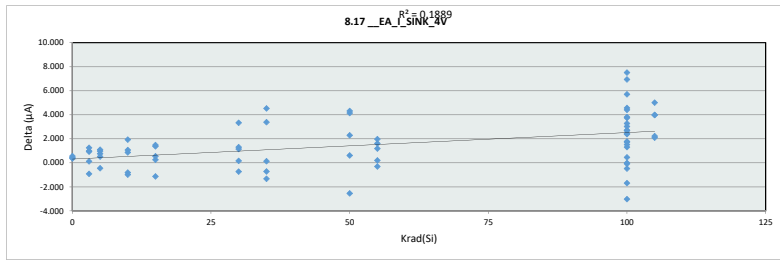


8.16_EA_I_SOURCE_14V											
Test Site											
Tester											
Test Number											
Max Limit		190	µA								
Min Limit		100	µA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	143.908	144.731	141.528	147.915	147.859	152.534	139.368	146.979	150.138	144.087	154.473
Average	155.036	155.322	153.770	156.312	154.343	157.592	153.404	154.932	154.290	157.331	162.174
Max	165.141	165.248	162.676	164.243	161.570	163.858	161.442	160.465	161.219	168.797	164.430
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

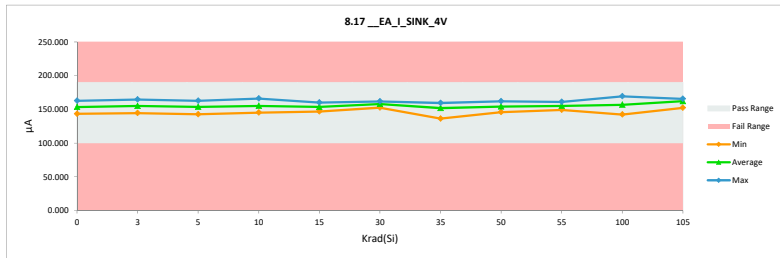


HDR TID Report
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8.17_EA_I_SINK_4V				
Test Site				
Tester				
Test Number				
Unit		µA	µA	
Max Limit		190	190	
Min Limit		100	100	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	153.978	154.402	0.424
0	190	143.157	143.493	0.336
0	191	162.224	162.763	0.539
3	300	155.653	156.887	1.234
3	305	144.485	144.591	0.106
3	309	163.838	164.806	0.968
3	311	156.124	155.183	-0.941
3	313	153.038	153.952	0.914
5	319	161.953	162.912	0.959
5	321	154.311	155.051	0.740
5	324	142.263	142.765	0.502
5	327	152.028	153.107	1.079
5	331	155.329	154.864	-0.465
10	234	153.681	152.683	-0.998
10	235	154.921	155.968	1.047
10	236	143.316	145.233	1.917
10	237	165.291	166.133	0.842
10	238	157.389	156.561	-0.828
15	239	160.044	160.278	0.234
15	240	156.617	158.074	1.457
15	241	153.642	154.165	0.523
15	243	148.019	146.876	-1.143
15	245	147.686	149.044	1.358
30	222	159.201	158.453	-0.748
30	223	156.895	160.212	3.317
30	225	152.468	152.612	0.144
30	226	155.940	157.232	1.292
30	227	160.704	161.828	1.124
35	228	137.673	136.323	-1.350
35	229	150.297	153.669	3.372
35	231	159.391	159.507	0.116
35	232	152.714	151.979	-0.735
35	233	153.737	158.254	4.517
50	210	149.746	153.871	4.125
50	211	145.373	145.971	0.598
50	212	156.088	158.358	2.270
50	213	157.749	162.043	4.294
50	214	154.101	151.535	-2.566
55	215	149.165	149.342	0.177
55	216	158.304	159.471	1.167
55	217	151.658	151.341	-0.317
55	218	159.271	161.230	1.959
55	220	153.441	155.040	1.599
100	247	164.299	166.660	2.361
100	249	159.532	159.970	0.438
100	250	148.766	152.019	3.253
100	251	146.433	148.183	1.750
100	253	164.290	163.797	-0.493
100	255	153.677	156.370	2.693
100	256	155.803	163.300	7.497
100	257	154.918	158.646	3.728
100	258	155.464	156.960	1.496
100	259	150.657	156.341	5.684
100	260	140.737	142.457	1.720
100	261	159.325	159.222	-0.103
100	262	165.023	169.416	4.393
100	263	151.740	151.706	-0.034
100	264	153.703	156.704	3.001
100	265	160.258	158.545	-1.713
100	266	154.349	158.896	4.547
100	267	154.624	161.551	6.927
100	268	149.097	151.515	2.418
100	269	150.502	153.162	2.660
100	270	151.542	152.824	1.282
100	271	161.631	158.593	-3.038
100	273	148.194	151.990	3.796
105	274	160.612	164.579	3.967
105	275	159.351	164.342	4.991
105	276	161.550	163.762	2.212
105	277	161.743	165.702	3.959
105	278	150.370	152.444	2.074
	Max	165.291	169.416	7.497
	Average	154.382	155.883	1.502
	Min	137.673	136.323	-3.038
	Std Dev	6.079	6.488	2.080

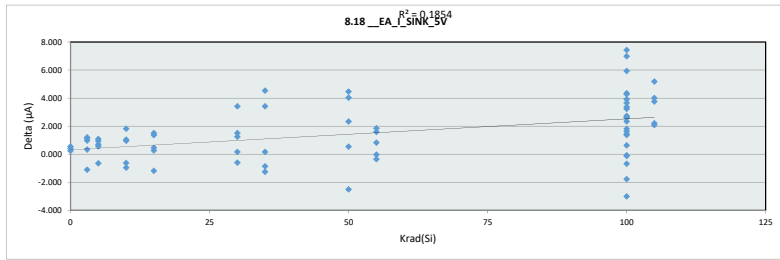


8.17_EA_I_SINK											
Test Site											
Tester											
Test Number											
Max Limit		190	µA								
Min Limit		100	µA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	143.493	144.591	142.765	145.233	146.876	152.612	136.323	145.971	149.342	142.457	152.444
Average	153.553	155.084	153.740	155.316	153.687	158.067	151.946	154.356	155.285	156.906	162.166
Max	162.763	164.806	162.912	166.133	160.278	161.828	159.507	162.043	161.230	169.416	165.702
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

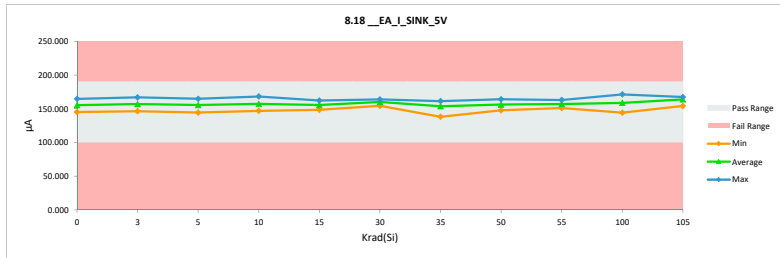


HDR TID Report
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8.18_EA_I_SINK_5V				
Test Site				
Tester				
Test Number				
Unit		µA	µA	
Max Limit		190	190	
Min Limit		100	100	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	155.932	156.342	0.410
0	190	144.888	145.135	0.247
0	191	164.115	164.665	0.550
3	300	157.573	158.773	1.200
3	305	146.050	146.388	0.338
3	309	165.710	166.836	1.126
3	311	157.946	156.841	-1.105
3	313	154.721	155.689	0.968
5	319	163.906	164.846	0.940
5	321	156.087	156.781	0.694
5	324	143.810	144.372	0.562
5	327	153.750	154.835	1.085
5	331	157.165	156.517	-0.648
10	234	155.316	154.359	-0.957
10	235	156.739	157.786	1.047
10	236	145.021	146.839	1.818
10	237	167.245	168.209	0.964
10	238	159.193	158.568	-0.625
15	239	161.805	162.074	0.269
15	240	158.472	159.828	1.356
15	241	155.474	155.935	0.461
15	243	149.654	148.470	-1.184
15	245	149.303	150.807	1.504
30	222	160.996	160.394	-0.602
30	223	158.614	162.031	3.417
30	225	154.209	154.376	0.167
30	226	157.630	159.139	1.509
30	227	162.575	163.820	1.245
35	228	139.366	138.114	-1.252
35	229	152.180	155.599	3.419
35	231	161.191	161.352	0.161
35	232	154.496	153.634	-0.862
35	233	155.668	160.197	4.529
50	210	151.584	155.609	4.025
50	211	147.059	147.602	0.543
50	212	157.879	160.208	2.329
50	213	159.501	163.965	4.464
50	214	155.797	153.294	-2.503
55	215	151.011	150.979	-0.032
55	216	160.218	161.040	0.822
55	217	153.561	153.215	-0.346
55	218	161.122	162.972	1.850
55	220	155.351	156.940	1.589
100	247	166.034	168.627	2.593
100	249	161.346	161.978	0.632
100	250	150.615	153.981	3.366
100	251	148.184	149.985	1.801
100	253	166.267	165.584	-0.683
100	255	155.572	158.302	2.730
100	256	157.575	165.001	7.426
100	257	156.745	160.396	3.651
100	258	157.294	158.723	1.429
100	259	152.360	158.291	5.931
100	260	142.581	144.204	1.623
100	261	161.049	160.925	-0.124
100	262	167.022	171.298	4.276
100	263	153.571	153.498	-0.073
100	264	155.392	158.628	3.236
100	265	162.046	160.266	-1.780
100	266	156.158	160.509	4.351
100	267	156.543	163.521	6.978
100	268	150.710	153.049	2.339
100	269	152.322	154.977	2.655
100	270	153.321	154.696	1.375
100	271	163.444	160.441	-3.003
100	273	149.907	153.827	3.920
105	274	162.443	166.452	4.009
105	275	161.203	166.379	5.176
105	276	163.354	165.558	2.204
105	277	163.571	167.327	3.756
105	278	152.087	154.166	2.079
	Max	167.245	171.298	7.426
	Average	156.177	157.690	1.512
	Min	139.366	138.114	-3.003
	Std Dev	6.131	6.547	2.093



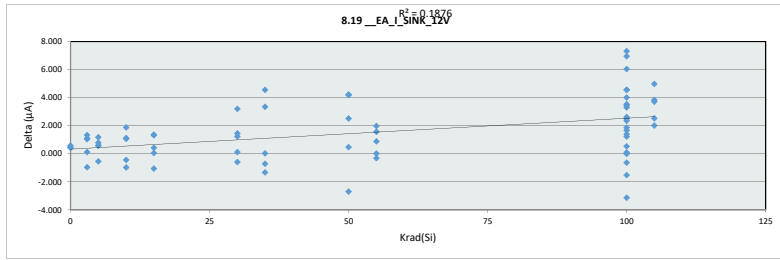
8.18_EA_I_SINK											
Test Site											
Tester											
Test Number											
Max Limit		190	µA								
Min Limit		100	µA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	145.135	146.388	144.372	146.839	148.470	154.376	138.114	147.602	150.979	144.204	154.166
Average	155.381	156.905	155.470	157.152	155.423	159.952	153.779	156.136	157.029	158.726	163.976
Max	164.665	166.836	164.846	168.209	162.074	163.820	161.352	163.965	162.972	171.298	167.327
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



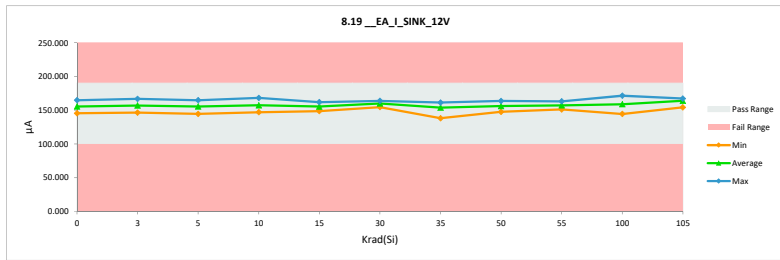
HDR TID Report

TPS7H5002-SP

8.19_EA_I_SINK_12V				
Test Site	Tester	Test Number	Unit	
			µA	µA
Max Limit	190		190	
Min Limit	100		100	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	155.994	156.398	0.404
0	190	145.002	145.458	0.456
0	191	164.165	164.730	0.565
3	300	157.571	158.889	1.318
3	305	146.257	146.370	0.113
3	309	165.721	166.811	1.090
3	311	157.941	156.965	-0.976
3	313	154.750	155.786	1.036
5	319	163.974	164.759	0.785
5	321	156.212	156.778	0.566
5	324	143.920	144.504	0.584
5	327	153.847	155.017	1.170
5	331	157.251	156.702	-0.549
10	234	155.486	154.504	-0.982
10	235	156.865	157.985	1.120
10	236	145.063	146.930	1.867
10	237	167.188	168.240	1.052
10	238	159.188	158.739	-0.449
15	239	161.936	161.980	0.044
15	240	158.537	159.848	1.311
15	241	155.510	155.928	0.418
15	243	149.759	148.697	-1.062
15	245	149.416	150.773	1.357
30	222	161.040	160.443	-0.597
30	223	158.826	162.020	3.194
30	225	154.449	154.554	0.105
30	226	157.813	159.258	1.445
30	227	162.631	163.864	1.233
35	228	139.459	138.123	-1.336
35	229	152.462	155.802	3.340
35	231	161.300	161.316	0.016
35	232	154.551	153.827	-0.724
35	233	155.693	160.245	4.552
50	210	151.622	155.839	4.217
50	211	147.101	147.563	0.462
50	212	157.844	160.353	2.509
50	213	159.556	163.730	4.174
50	214	155.936	153.227	-2.709
55	215	151.114	151.121	0.007
55	216	160.297	161.174	0.877
55	217	153.638	153.319	-0.319
55	218	161.202	163.170	1.968
55	220	155.332	156.892	1.560
100	247	166.222	168.725	2.503
100	249	161.485	162.007	0.522
100	250	150.710	154.001	3.291
100	251	148.282	150.139	1.857
100	253	166.268	165.623	-0.645
100	255	155.537	158.148	2.611
100	256	157.767	165.079	7.312
100	257	156.803	160.329	3.526
100	258	157.349	158.738	1.389
100	259	152.310	158.340	6.030
100	260	142.759	144.424	1.665
100	261	161.175	161.155	-0.020
100	262	166.934	171.483	4.549
100	263	153.647	153.733	0.086
100	264	155.477	158.920	3.443
100	265	162.098	160.561	-1.537
100	266	156.108	160.662	4.554
100	267	156.596	163.544	6.948
100	268	150.729	153.080	2.351
100	269	152.451	154.967	2.516
100	270	153.334	154.557	1.223
100	271	163.491	160.346	-3.145
100	273	150.050	154.048	3.998
105	274	162.590	166.427	3.837
105	275	161.185	166.156	4.971
105	276	163.350	165.866	2.516
105	277	163.583	167.293	3.710
105	278	152.214	154.221	2.007
	Max	167.188	171.483	7.312
	Average	156.252	157.763	1.511
	Min	139.459	138.123	-3.145
	Std Dev	6.109	6.522	2.086

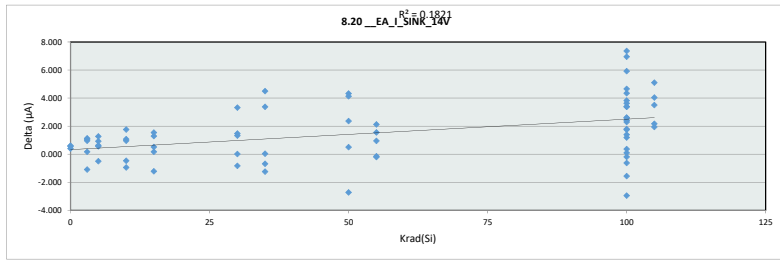


8.19_EA_I_SINK											
Test Site	Tester	Test Number	Unit								
			µA								
Max Limit	190		µA								
Min Limit	100		µA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	145.458	146.370	144.504	146.930	148.697	154.554	138.123	147.563	151.121	144.424	154.221
Average	155.529	156.964	155.552	157.280	155.445	160.028	153.863	156.142	157.135	158.809	163.993
Max	164.730	166.811	164.759	168.240	161.980	163.864	161.316	163.730	163.170	171.483	167.293
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

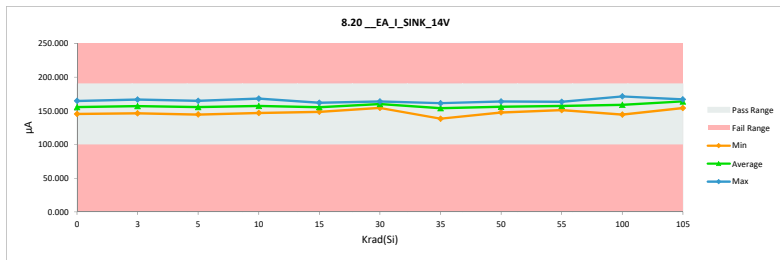


HDR TID Report TPS7H5002-SP

8.20_EA_I_SINK_14V				
Test Site				
Tester				
Test Number				
Unit		µA	µA	
Max Limit		190	190	
Min Limit		100	100	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	155.808	156.416	0.608
0	190	144.882	145.288	0.406
0	191	164.069	164.635	0.566
3	300	157.673	158.751	1.078
3	305	146.139	146.312	0.173
3	309	165.611	166.744	1.133
3	311	157.885	156.794	-1.091
3	313	154.768	155.725	0.957
5	319	163.909	164.838	0.929
5	321	156.101	156.737	0.636
5	324	143.898	144.457	0.559
5	327	153.768	155.036	1.268
5	331	157.179	156.676	-0.503
10	234	155.347	154.404	-0.943
10	235	156.765	157.835	1.070
10	236	145.026	146.784	1.758
10	237	167.052	168.011	0.959
10	238	159.047	158.580	-0.467
15	239	161.705	161.881	0.176
15	240	158.462	159.756	1.294
15	241	155.333	155.850	0.517
15	243	149.708	148.495	-1.213
15	245	149.343	150.885	1.542
30	222	160.970	160.137	-0.833
30	223	158.721	162.043	3.322
30	225	154.264	154.275	0.011
30	226	157.743	159.219	1.476
30	227	162.515	163.836	1.321
35	228	139.421	138.187	-1.234
35	229	152.303	155.685	3.382
35	231	161.225	161.259	0.034
35	232	154.453	153.770	-0.683
35	233	155.649	160.146	4.497
50	210	151.568	155.698	4.130
50	211	147.043	147.545	0.502
50	212	157.874	160.238	2.364
50	213	159.490	163.816	4.326
50	214	155.809	153.083	-2.726
55	215	151.062	150.919	-0.143
55	216	160.258	161.209	0.951
55	217	153.551	153.349	-0.202
55	218	161.124	163.244	2.120
55	220	155.264	156.815	1.551
100	247	166.168	168.785	2.617
100	249	161.613	161.979	0.366
100	250	150.628	154.016	3.388
100	251	148.182	149.979	1.797
100	253	166.187	165.560	-0.627
100	255	155.584	157.879	2.295
100	256	157.669	165.028	7.359
100	257	156.765	160.400	3.635
100	258	157.303	158.695	1.392
100	259	152.273	158.196	5.923
100	260	142.555	144.307	1.752
100	261	161.114	160.923	-0.191
100	262	166.984	171.328	4.344
100	263	153.529	153.629	0.100
100	264	155.437	158.820	3.383
100	265	162.032	160.470	-1.562
100	266	156.097	160.755	4.658
100	267	156.555	163.506	6.951
100	268	150.600	153.014	2.414
100	269	152.237	154.822	2.585
100	270	153.256	154.441	1.185
100	271	163.440	160.486	-2.954
100	273	149.944	153.764	3.820
105	274	162.493	166.530	4.037
105	275	161.152	166.252	5.100
105	276	163.244	165.410	2.166
105	277	163.451	166.953	3.502
105	278	152.139	154.081	1.942
Max		167.052	171.328	7.359
Average		156.175	157.681	1.506
Min		139.421	138.187	-2.954
Std Dev		6.116	6.527	2.084

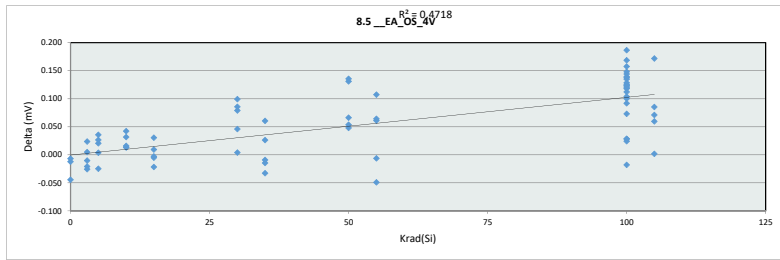


8.20_EA_I_SINK											
Test Site											
Tester											
Test Number											
Max Limit		190	µA								
Min Limit		100	µA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	145.288	146.312	144.457	146.784	148.495	154.275	138.187	147.545	150.919	144.307	154.081
Average	155.446	156.865	155.549	157.123	155.373	159.902	153.809	156.076	157.107	158.730	163.845
Max	164.635	166.744	164.838	168.011	161.881	163.836	161.259	163.816	163.244	171.328	166.953
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

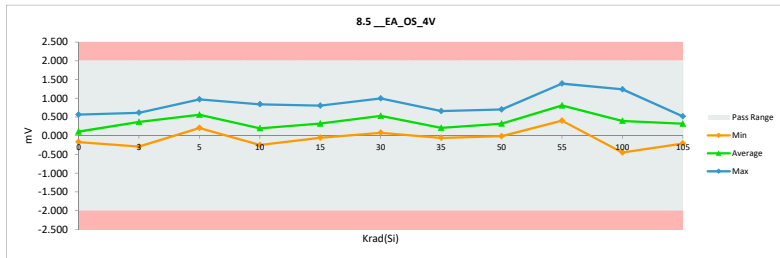


HDR TID Report
TPS7H5002-SP

8.5_EA_OS_4V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	2	2		
Min Limit	-2	-2		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	-0.055	-0.068	-0.013
0	190	0.569	0.562	-0.007
0	191	-0.125	-0.170	-0.045
3	300	0.551	0.555	0.004
3	305	0.486	0.465	-0.021
3	309	0.497	0.486	-0.011
3	311	0.589	0.612	0.023
3	313	-0.266	-0.292	-0.026
5	319	0.501	0.521	0.020
5	321	0.425	0.400	-0.025
5	324	0.932	0.968	0.035
5	327	0.694	0.697	0.003
5	331	0.179	0.206	0.026
10	234	-0.291	-0.249	0.042
10	235	0.002	0.015	0.013
10	236	-0.176	-0.163	0.013
10	237	0.823	0.838	0.016
10	238	0.508	0.539	0.031
15	239	-0.051	-0.054	-0.003
15	240	0.370	0.379	0.009
15	241	0.824	0.802	-0.022
15	243	0.210	0.205	-0.005
15	245	0.253	0.283	0.030
30	222	0.423	0.426	0.003
30	223	0.917	0.995	0.079
30	225	0.302	0.348	0.046
30	226	0.692	0.791	0.099
30	227	-0.007	0.078	0.085
35	228	-0.085	-0.059	0.026
35	229	0.672	0.658	-0.015
35	231	-0.033	-0.042	-0.010
35	232	0.200	0.167	-0.033
35	233	0.251	0.311	0.060
50	210	-0.145	-0.014	0.131
50	211	0.156	0.204	0.048
50	212	0.397	0.451	0.053
50	213	0.564	0.700	0.135
50	214	0.204	0.270	0.066
55	215	0.637	0.631	-0.007
55	216	1.038	1.098	0.061
55	217	0.400	0.507	0.107
55	218	0.337	0.401	0.064
55	220	1.441	1.391	-0.049
100	247	0.809	0.934	0.125
100	249	0.049	0.193	0.144
100	250	0.357	0.505	0.148
100	251	0.056	0.168	0.112
100	253	0.544	0.567	0.024
100	255	0.038	0.206	0.168
100	256	0.160	0.297	0.137
100	257	0.483	0.622	0.139
100	258	0.538	0.638	0.100
100	259	-0.355	-0.327	0.028
100	260	-0.089	0.098	0.186
100	261	0.082	0.205	0.122
100	262	0.518	0.591	0.073
100	263	1.116	1.239	0.123
100	264	0.660	0.751	0.092
100	265	0.216	0.373	0.157
100	266	0.141	0.123	-0.018
100	267	1.046	1.181	0.135
100	268	-0.195	-0.167	0.028
100	269	0.608	0.711	0.103
100	270	-0.066	0.052	0.118
100	271	-0.570	-0.450	0.119
100	273	0.413	0.541	0.128
105	274	0.350	0.409	0.059
105	275	0.361	0.446	0.085
105	276	0.260	0.431	0.171
105	277	0.513	0.514	0.001
105	278	-0.280	-0.209	0.071
	Max	1.441	1.391	0.186
	Average	0.332	0.387	0.055
	Min	-0.570	-0.450	-0.049
	Std Dev	0.387	0.388	0.061

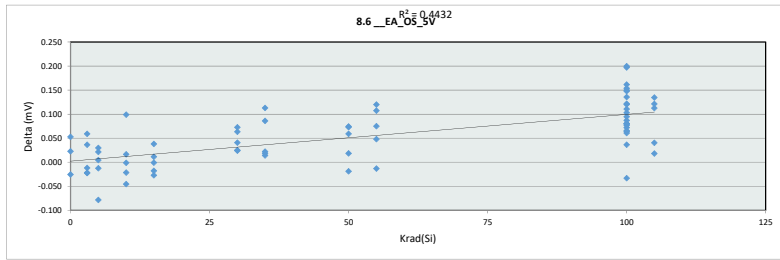


8.5_EA_OS_4V											
Test Site											
Tester											
Test Number											
Max Limit	2	mV									
Min Limit	-2	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.170	-0.292	0.206	-0.249	-0.054	0.078	-0.059	-0.014	0.401	-0.450	-0.209
Average	0.108	0.365	0.558	0.196	0.323	0.528	0.207	0.322	0.806	0.394	0.318
Max	0.562	0.612	0.968	0.838	0.802	0.995	0.658	0.700	1.391	1.239	0.514
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

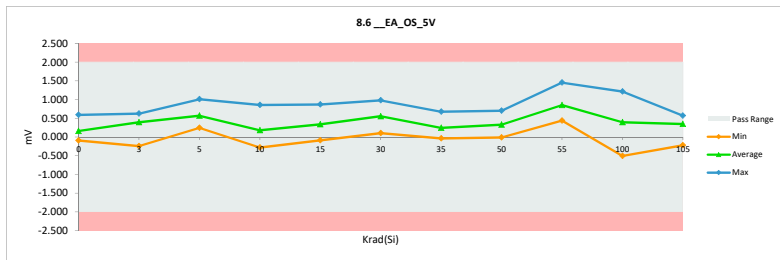


HDR TID Report
TPS7H5002-SP

8.6_EA_OS_5V				
Test Site				
Tester				
Test Number				
Unit		mV	mV	
Max Limit		2	2	
Min Limit		-2	-2	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	-0.040	-0.017	0.022
0	190	0.540	0.592	0.052
0	191	-0.066	-0.091	-0.026
3	300	0.586	0.564	-0.023
3	305	0.459	0.495	0.036
3	309	0.463	0.522	0.059
3	311	0.649	0.627	-0.022
3	313	-0.231	-0.243	-0.012
5	319	0.497	0.484	-0.013
5	321	0.460	0.381	-0.079
5	324	1.008	1.012	0.004
5	327	0.698	0.727	0.030
5	331	0.223	0.244	0.021
10	234	-0.258	-0.279	-0.022
10	235	0.013	-0.032	-0.046
10	236	-0.162	-0.163	-0.002
10	237	0.759	0.858	0.099
10	238	0.515	0.521	0.016
15	239	-0.072	-0.090	-0.018
15	240	0.392	0.403	0.011
15	241	0.832	0.870	0.038
15	243	0.233	0.232	-0.001
15	245	0.316	0.288	-0.027
30	222	0.474	0.498	0.024
30	223	0.957	0.981	0.025
30	225	0.351	0.392	0.040
30	226	0.738	0.801	0.063
30	227	0.031	0.103	0.072
35	228	-0.121	-0.036	0.086
35	229	0.656	0.677	0.022
35	231	0.007	0.021	0.014
35	232	0.207	0.225	0.018
35	233	0.230	0.343	0.113
50	210	-0.084	-0.010	0.074
50	211	0.203	0.221	0.018
50	212	0.424	0.483	0.059
50	213	0.630	0.703	0.073
50	214	0.279	0.259	-0.019
55	215	0.612	0.686	0.075
55	216	1.100	1.147	0.048
55	217	0.417	0.537	0.120
55	218	0.332	0.439	0.107
55	220	1.467	1.454	-0.013
100	247	0.820	0.955	0.135
100	249	0.065	0.142	0.077
100	250	0.310	0.509	0.199
100	251	0.076	0.156	0.080
100	253	0.500	0.610	0.110
100	255	0.039	0.236	0.197
100	256	0.170	0.234	0.063
100	257	0.479	0.633	0.153
100	258	0.512	0.662	0.149
100	259	-0.339	-0.373	-0.034
100	260	-0.046	0.101	0.148
100	261	0.118	0.213	0.094
100	262	0.494	0.655	0.161
100	263	1.095	1.215	0.120
100	264	0.684	0.745	0.060
100	265	0.283	0.363	0.080
100	266	0.129	0.165	0.036
100	267	1.084	1.187	0.103
100	268	-0.169	-0.090	0.079
100	269	0.623	0.744	0.121
100	270	-0.068	0.004	0.072
100	271	-0.592	-0.505	0.087
100	273	0.447	0.512	0.065
105	274	0.364	0.498	0.134
105	275	0.339	0.441	0.112
105	276	0.330	0.451	0.121
105	277	0.531	0.571	0.040
105	278	-0.240	-0.222	0.018
	Max	1.467	1.454	0.199
	Average	0.349	0.404	0.055
	Min	-0.592	-0.505	-0.079
	Std Dev	0.387	0.396	0.060

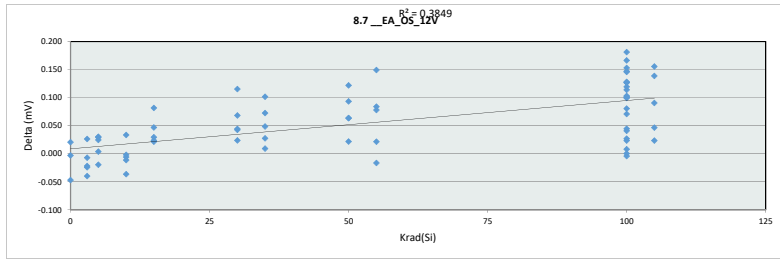


8.6_EA_OS_5V											
Test Site											
Tester											
Test Number											
Max Limit	2	mV									
Min Limit	-2	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.092	-0.243	0.244	-0.279	-0.090	0.103	-0.036	-0.010	0.439	-0.505	-0.222
Average	0.161	0.393	0.570	0.183	0.341	0.555	0.246	0.331	0.853	0.394	0.348
Max	0.592	0.627	1.012	0.858	0.870	0.981	0.677	0.703	1.454	1.215	0.571
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

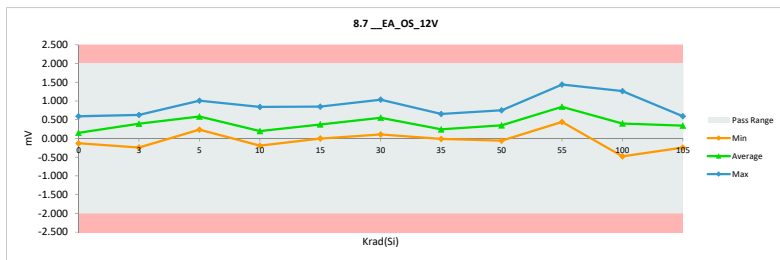


HDR TID Report
TPS7H5002-SP

8.7_EA_OS_12V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	2	2		
Min Limit	-2	-2		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	-0.033	-0.013	0.020
0	190	0.596	0.593	-0.004
0	191	-0.082	-0.129	-0.047
3	300	0.590	0.549	-0.040
3	305	0.499	0.525	0.026
3	309	0.527	0.502	-0.025
3	311	0.652	0.629	-0.022
3	313	-0.234	-0.242	-0.008
5	319	0.496	0.525	0.029
5	321	0.457	0.437	-0.020
5	324	1.005	1.008	0.003
5	327	0.695	0.725	0.030
5	331	0.210	0.234	0.024
10	234	-0.224	-0.191	0.033
10	235	0.020	-0.017	-0.037
10	236	-0.151	-0.158	-0.007
10	237	0.852	0.840	-0.012
10	238	0.516	0.514	-0.002
15	239	-0.083	-0.002	0.081
15	240	0.388	0.411	0.023
15	241	0.805	0.851	0.046
15	243	0.229	0.250	0.021
15	245	0.329	0.357	0.029
30	222	0.458	0.501	0.044
30	223	0.920	1.035	0.115
30	225	0.301	0.343	0.042
30	226	0.755	0.778	0.023
30	227	0.040	0.108	0.068
35	228	-0.084	-0.013	0.072
35	229	0.628	0.655	0.027
35	231	0.008	0.017	0.009
35	232	0.185	0.233	0.048
35	233	0.229	0.331	0.101
50	210	-0.082	-0.061	0.021
50	211	0.187	0.250	0.063
50	212	0.438	0.501	0.063
50	213	0.632	0.753	0.122
50	214	0.213	0.306	0.093
55	215	0.608	0.629	0.021
55	216	1.090	1.167	0.077
55	217	0.400	0.549	0.149
55	218	0.358	0.441	0.083
55	220	1.457	1.440	-0.017
100	247	0.874	0.976	0.102
100	249	0.090	0.170	0.080
100	250	0.331	0.512	0.181
100	251	0.082	0.126	0.044
100	253	0.590	0.598	0.008
100	255	0.076	0.221	0.146
100	256	0.203	0.356	0.153
100	257	0.492	0.638	0.147
100	258	0.503	0.630	0.127
100	259	-0.336	-0.313	0.023
100	260	-0.045	0.081	0.126
100	261	0.115	0.185	0.071
100	262	0.543	0.644	0.101
100	263	1.097	1.263	0.166
100	264	0.722	0.749	0.026
100	265	0.232	0.346	0.113
100	266	0.169	0.169	0.000
100	267	1.084	1.203	0.119
100	268	-0.135	-0.140	-0.005
100	269	0.598	0.726	0.128
100	270	-0.083	0.016	0.099
100	271	-0.578	-0.478	0.100
100	273	0.467	0.508	0.040
105	274	0.344	0.482	0.138
105	275	0.343	0.498	0.155
105	276	0.355	0.378	0.023
105	277	0.503	0.593	0.090
105	278	-0.290	-0.244	0.046
	Max	1.457	1.440	0.181
	Average	0.354	0.409	0.055
	Min	-0.578	-0.478	-0.047
	Std Dev	0.387	0.394	0.057

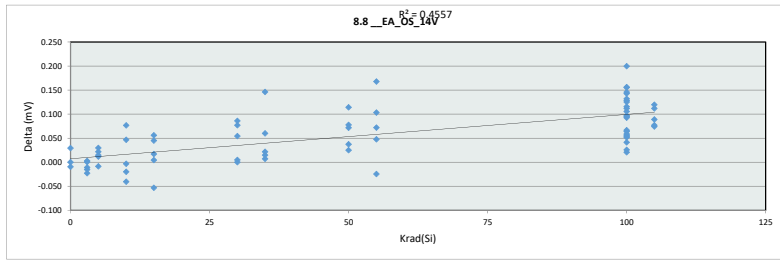


8.7_EA_OS_12V											
Test Site											
Tester											
Test Number											
Max Limit	2	mV									
Min Limit	-2	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.129	-0.242	0.234	-0.191	-0.002	0.108	-0.013	-0.061	0.441	-0.478	-0.244
Average	0.150	0.393	0.586	0.198	0.373	0.553	0.245	0.350	0.845	0.399	0.342
Max	0.593	0.629	1.008	0.840	0.851	1.035	0.655	0.753	1.440	1.263	0.593
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

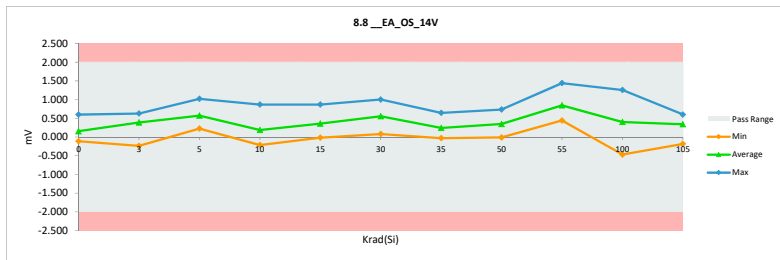


HDR TID Report
TPS7H5002-SP

8.8_EA_OS_14V				
Test Site				
Tester				
Test Number				
Unit		mV	mV	
Max Limit		2	2	
Min Limit		-2	-2	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	-0.017	-0.017	0.000
0	190	0.570	0.599	0.030
0	191	-0.101	-0.111	-0.009
3	300	0.551	0.528	-0.023
3	305	0.502	0.505	0.003
3	309	0.534	0.519	-0.015
3	311	0.629	0.629	0.000
3	313	-0.225	-0.235	-0.011
5	319	0.471	0.483	0.012
5	321	0.448	0.440	-0.009
5	324	1.001	1.022	0.022
5	327	0.665	0.694	0.030
5	331	0.213	0.227	0.015
10	234	-0.261	-0.214	0.047
10	235	-0.004	-0.007	-0.003
10	236	-0.156	-0.176	-0.020
10	237	0.791	0.868	0.076
10	238	0.523	0.483	-0.040
15	239	-0.072	-0.016	0.056
15	240	0.380	0.425	0.045
15	241	0.852	0.869	0.017
15	243	0.246	0.193	-0.053
15	245	0.320	0.324	0.005
30	222	0.458	0.512	0.054
30	223	0.918	1.004	0.086
30	225	0.404	0.404	0.000
30	226	0.770	0.774	0.005
30	227	0.006	0.083	0.077
35	228	-0.035	-0.028	0.007
35	229	0.630	0.645	0.015
35	231	0.007	0.029	0.022
35	232	0.162	0.222	0.060
35	233	0.208	0.354	0.146
50	210	-0.082	-0.010	0.072
50	211	0.206	0.231	0.025
50	212	0.463	0.500	0.037
50	213	0.622	0.736	0.114
50	214	0.209	0.287	0.078
55	215	0.603	0.651	0.048
55	216	1.093	1.165	0.072
55	217	0.434	0.537	0.103
55	218	0.275	0.442	0.168
55	220	1.464	1.440	-0.024
100	247	0.851	0.918	0.067
100	249	0.033	0.159	0.125
100	250	0.348	0.491	0.143
100	251	0.072	0.136	0.064
100	253	0.582	0.634	0.052
100	255	0.024	0.181	0.156
100	256	0.178	0.324	0.146
100	257	0.455	0.655	0.200
100	258	0.576	0.673	0.097
100	259	-0.339	-0.283	0.057
100	260	-0.004	0.091	0.095
100	261	0.131	0.185	0.054
100	262	0.559	0.618	0.059
100	263	1.102	1.258	0.156
100	264	0.717	0.743	0.026
100	265	0.234	0.362	0.128
100	266	0.149	0.169	0.021
100	267	1.066	1.159	0.092
100	268	-0.136	-0.095	0.041
100	269	0.613	0.719	0.106
100	270	-0.070	0.042	0.112
100	271	-0.599	-0.468	0.132
100	273	0.457	0.573	0.116
105	274	0.339	0.416	0.077
105	275	0.314	0.426	0.112
105	276	0.343	0.463	0.120
105	277	0.527	0.602	0.074
105	278	-0.273	-0.184	0.089
	Max	1.464	1.440	0.200
	Average	0.351	0.408	0.057
	Min	-0.599	-0.468	-0.053
	Std Dev	0.388	0.389	0.056

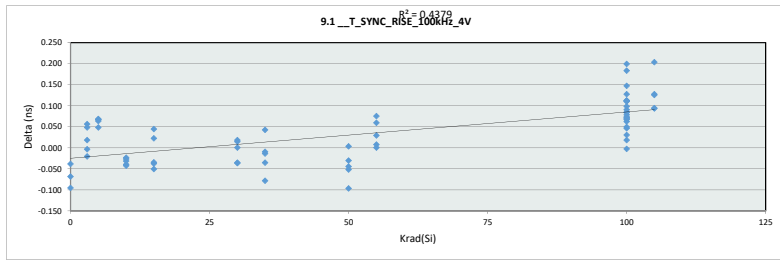


8.8_EA_OS_14V											
Test Site											
Tester											
Test Number											
Max Limit	2	mV									
Min Limit	-2	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.111	-0.236	0.227	-0.214	-0.016	0.083	-0.028	-0.010	0.442	-0.468	-0.184
Average	0.157	0.389	0.573	0.191	0.359	0.556	0.244	0.349	0.847	0.402	0.344
Max	0.599	0.629	1.022	0.868	0.869	1.004	0.645	0.736	1.440	1.258	0.602
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

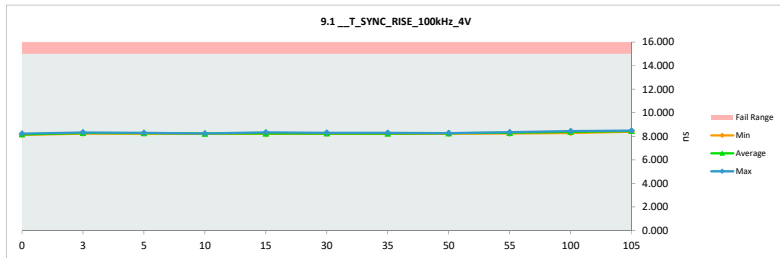


HDR TID Report TPS7H5002-SP

9.1_T_SYNC_RISE_100kHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.268	8.229	-0.039
0	190	8.248	8.152	-0.096
0	191	8.191	8.122	-0.069
3	300	8.254	8.233	-0.021
3	305	8.215	8.263	0.048
3	309	8.211	8.267	0.056
3	311	8.309	8.327	0.018
3	313	8.290	8.286	-0.004
5	319	8.187	8.255	0.068
5	321	8.250	8.298	0.048
5	324	8.181	8.244	0.063
5	327	8.197	8.264	0.067
5	331	8.207	8.273	0.066
10	234	8.283	8.243	-0.040
10	235	8.276	8.244	-0.032
10	236	8.244	8.201	-0.043
10	237	8.252	8.225	-0.027
10	238	8.246	8.222	-0.024
15	239	8.293	8.337	0.044
15	240	8.284	8.246	-0.038
15	241	8.159	8.181	0.022
15	243	8.269	8.234	-0.035
15	245	8.239	8.188	-0.051
30	222	8.283	8.301	0.018
30	223	8.239	8.253	0.014
30	225	8.278	8.241	-0.037
30	226	8.259	8.223	-0.036
30	227	8.248	8.248	0.000
35	228	8.272	8.193	-0.079
35	229	8.218	8.260	0.042
35	231	8.300	8.290	-0.010
35	232	8.268	8.254	-0.014
35	233	8.229	8.193	-0.036
50	210	8.320	8.223	-0.097
50	211	8.287	8.235	-0.052
50	212	8.304	8.273	-0.031
50	213	8.305	8.260	-0.045
50	214	8.271	8.274	0.003
55	215	8.221	8.221	0.000
55	216	8.201	8.230	0.029
55	217	8.196	8.271	0.075
55	218	8.296	8.303	0.007
55	220	8.292	8.351	0.059
100	247	8.298	8.369	0.071
100	249	8.297	8.327	0.030
100	250	8.305	8.372	0.067
100	251	8.329	8.347	0.018
100	253	8.336	8.385	0.049
100	255	8.278	8.356	0.078
100	256	8.312	8.423	0.111
100	257	8.202	8.275	0.073
100	258	8.221	8.404	0.183
100	259	8.365	8.411	0.046
100	260	8.243	8.442	0.199
100	261	8.306	8.416	0.110
100	262	8.247	8.331	0.084
100	263	8.253	8.351	0.098
100	264	8.304	8.415	0.111
100	265	8.323	8.407	0.084
100	266	8.398	8.395	-0.003
100	267	8.208	8.355	0.147
100	268	8.266	8.393	0.127
100	269	8.296	8.408	0.112
100	270	8.341	8.403	0.062
100	271	8.332	8.401	0.069
100	273	8.316	8.406	0.090
105	274	8.243	8.446	0.203
105	275	8.377	8.471	0.094
105	276	8.363	8.488	0.125
105	277	8.334	8.461	0.127
105	278	8.297	8.390	0.093
Max		8.398	8.488	0.203
Average		8.271	8.305	0.034
Min		8.159	8.122	-0.097
Std Dev		0.050	0.086	0.068

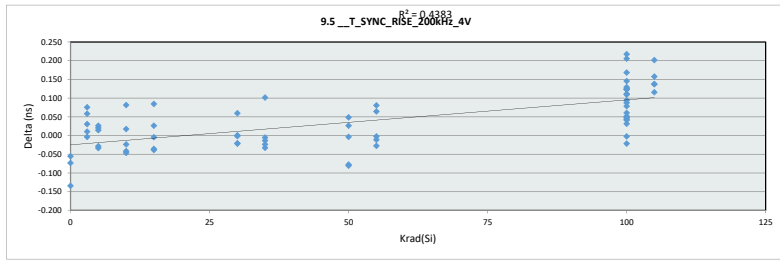


9.1_T_SYNC_RISE											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.122	8.233	8.244	8.201	8.181	8.223	8.193	8.223	8.221	8.275	8.390
Average	8.168	8.275	8.267	8.227	8.237	8.253	8.238	8.253	8.275	8.382	8.451
Max	8.229	8.327	8.298	8.244	8.337	8.301	8.290	8.274	8.351	8.442	8.488
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

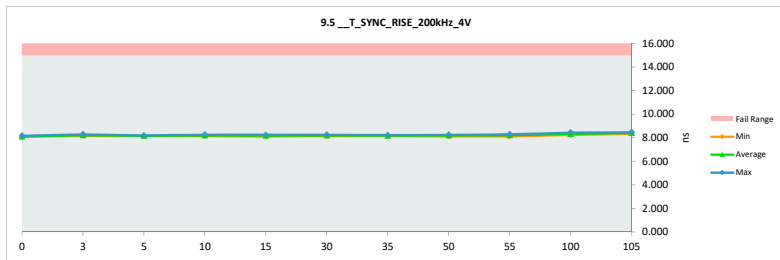


HDR TID Report
TPS7H5002-SP

9.5_T_SYNC_RISE_200kHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.239	8.165	-0.074
0	190	8.202	8.067	-0.135
0	191	8.144	8.088	-0.056
3	300	8.188	8.184	-0.004
3	305	8.152	8.210	0.058
3	309	8.164	8.174	0.010
3	311	8.218	8.293	0.075
3	313	8.160	8.190	0.030
5	319	8.161	8.187	0.026
5	321	8.213	8.184	-0.029
5	324	8.161	8.127	-0.034
5	327	8.134	8.148	0.014
5	331	8.143	8.163	0.020
10	234	8.183	8.264	0.081
10	235	8.235	8.193	-0.042
10	236	8.196	8.172	-0.024
10	237	8.165	8.182	0.017
10	238	8.191	8.144	-0.047
15	239	8.216	8.242	0.026
15	240	8.183	8.178	-0.005
15	241	8.138	8.102	-0.036
15	243	8.144	8.228	0.084
15	245	8.174	8.135	-0.039
30	222	8.195	8.254	0.059
30	223	8.208	8.187	-0.021
30	225	8.206	8.204	-0.002
30	226	8.145	8.145	0.000
30	227	8.193	8.171	-0.022
35	228	8.059	8.160	0.101
35	229	8.238	8.214	-0.024
35	231	8.190	8.176	-0.014
35	232	8.235	8.202	-0.033
35	233	8.140	8.134	-0.006
50	210	8.196	8.115	-0.081
50	211	8.230	8.152	-0.078
50	212	8.200	8.248	0.048
50	213	8.227	8.223	-0.004
50	214	8.199	8.225	0.026
55	215	8.117	8.114	-0.003
55	216	8.237	8.209	-0.028
55	217	8.120	8.200	0.080
55	218	8.242	8.230	-0.012
55	220	8.228	8.292	0.064
100	247	8.204	8.235	0.031
100	249	8.121	8.338	0.217
100	250	8.267	8.264	-0.003
100	251	8.228	8.279	0.051
100	253	8.243	8.411	0.168
100	255	8.208	8.268	0.060
100	256	8.263	8.372	0.109
100	257	8.138	8.261	0.123
100	258	8.231	8.436	0.205
100	259	8.246	8.391	0.145
100	260	8.219	8.266	0.047
100	261	8.262	8.303	0.041
100	262	8.192	8.315	0.123
100	263	8.287	8.265	-0.022
100	264	8.280	8.328	0.048
100	265	8.271	8.349	0.078
100	266	8.289	8.376	0.087
100	267	8.169	8.263	0.094
100	268	8.210	8.339	0.129
100	269	8.241	8.284	0.043
100	270	8.230	8.353	0.123
100	271	8.231	8.342	0.111
100	273	8.242	8.366	0.124
105	274	8.173	8.374	0.201
105	275	8.307	8.464	0.157
105	276	8.285	8.400	0.115
105	277	8.271	8.408	0.137
105	278	8.196	8.333	0.137
Max		8.307	8.464	0.217
Average		8.203	8.243	0.040
Min		8.059	8.067	-0.135
Std Dev		0.049	0.092	0.074

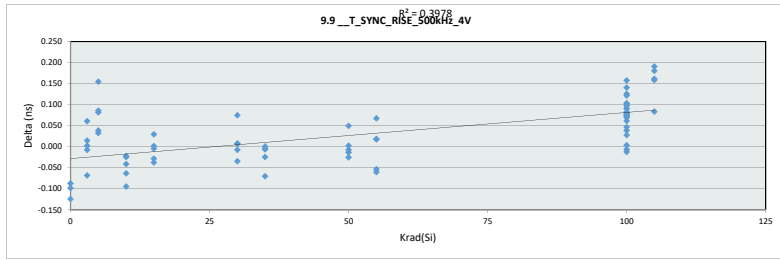


9.5_T_SYNC_RISE											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.067	8.174	8.127	8.144	8.102	8.145	8.134	8.115	8.114	8.235	8.333
Average	8.107	8.210	8.162	8.191	8.177	8.192	8.177	8.193	8.209	8.322	8.396
Max	8.165	8.293	8.187	8.264	8.242	8.254	8.214	8.248	8.292	8.436	8.464
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

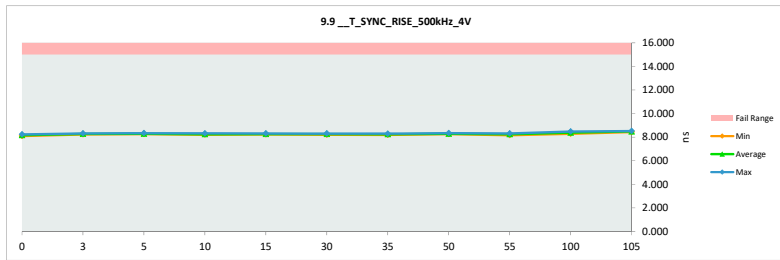


HDR TID Report TPS7H5002-SP

9.9_T_SYNC_RISE_500kHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.319	8.231	-0.088
0	190	8.263	8.164	-0.099
0	191	8.221	8.096	-0.125
3	300	8.298	8.229	-0.069
3	305	8.256	8.248	-0.008
3	309	8.250	8.252	0.002
3	311	8.300	8.314	0.014
3	313	8.247	8.307	0.060
5	319	8.224	8.262	0.038
5	321	8.276	8.308	0.032
5	324	8.176	8.261	0.085
5	327	8.190	8.271	0.081
5	331	8.189	8.343	0.154
10	234	8.290	8.248	-0.042
10	235	8.338	8.315	-0.023
10	236	8.278	8.214	-0.064
10	237	8.306	8.211	-0.095
10	238	8.268	8.243	-0.025
15	239	8.309	8.310	0.001
15	240	8.262	8.257	-0.005
15	241	8.204	8.233	0.029
15	243	8.294	8.256	-0.038
15	245	8.271	8.242	-0.029
30	222	8.279	8.286	0.007
30	223	8.218	8.210	-0.008
30	225	8.278	8.284	0.006
30	226	8.262	8.227	-0.035
30	227	8.236	8.310	0.074
35	228	8.246	8.239	-0.007
35	229	8.280	8.280	0.000
35	231	8.279	8.254	-0.025
35	232	8.359	8.288	-0.071
35	233	8.199	8.194	-0.005
50	210	8.278	8.252	-0.026
50	211	8.279	8.271	-0.008
50	212	8.285	8.334	0.049
50	213	8.311	8.313	0.002
50	214	8.298	8.284	-0.014
55	215	8.212	8.158	-0.054
55	216	8.190	8.257	0.067
55	217	8.293	8.232	-0.061
55	218	8.266	8.283	0.017
55	220	8.298	8.316	0.018
100	247	8.366	8.369	0.003
100	249	8.249	8.326	0.077
100	250	8.261	8.322	0.061
100	251	8.271	8.350	0.079
100	253	8.313	8.382	0.069
100	255	8.379	8.366	-0.013
100	256	8.351	8.452	0.101
100	257	8.239	8.285	0.046
100	258	8.280	8.383	0.103
100	259	8.341	8.334	-0.007
100	260	8.315	8.342	0.027
100	261	8.297	8.418	0.121
100	262	8.244	8.369	0.125
100	263	8.250	8.347	0.097
100	264	8.325	8.399	0.074
100	265	8.339	8.415	0.076
100	266	8.398	8.469	0.071
100	267	8.230	8.370	0.140
100	268	8.317	8.474	0.157
100	269	8.302	8.392	0.090
100	270	8.365	8.403	0.038
100	271	8.318	8.400	0.082
100	273	8.332	8.422	0.090
105	274	8.290	8.480	0.190
105	275	8.336	8.516	0.180
105	276	8.350	8.511	0.161
105	277	8.378	8.461	0.083
105	278	8.274	8.432	0.158
Max		8.398	8.516	0.190
Average		8.283	8.314	0.031
Min		8.176	8.096	-0.125
Std Dev		0.049	0.087	0.071

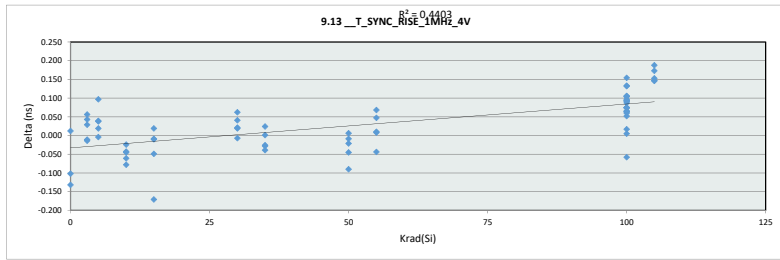


9.9_T_SYNC_RISE											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.096	8.229	8.261	8.211	8.233	8.210	8.194	8.252	8.158	8.285	8.432
Average	8.164	8.270	8.289	8.246	8.260	8.263	8.251	8.291	8.249	8.382	8.480
Max	8.231	8.314	8.343	8.315	8.310	8.310	8.288	8.334	8.316	8.474	8.516
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

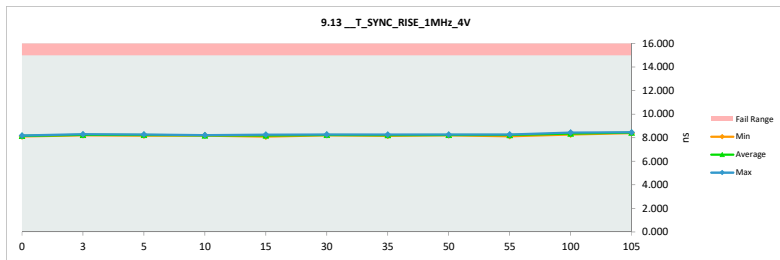


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		9.13 _T_SYNC_RISE_1MHz_4V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	15	15	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.317	8.185	-0.132
0	190	8.209	8.107	-0.102
0	191	8.157	8.169	0.012
3	300	8.221	8.210	-0.011
3	305	8.221	8.264	0.043
3	309	8.232	8.218	-0.014
3	311	8.238	8.294	0.056
3	313	8.207	8.236	0.029
5	319	8.197	8.236	0.039
5	321	8.238	8.276	0.038
5	324	8.184	8.180	-0.004
5	327	8.194	8.213	0.019
5	331	8.170	8.267	0.097
10	234	8.283	8.205	-0.078
10	235	8.282	8.221	-0.061
10	236	8.236	8.193	-0.043
10	237	8.216	8.192	-0.024
10	238	8.223	8.178	-0.045
15	239	8.274	8.264	-0.010
15	240	8.219	8.238	0.019
15	241	8.265	8.094	-0.171
15	243	8.211	8.202	-0.009
15	245	8.214	8.165	-0.049
30	222	8.257	8.276	0.019
30	223	8.148	8.210	0.062
30	225	8.217	8.238	0.021
30	226	8.215	8.208	-0.007
30	227	8.208	8.249	0.041
35	228	8.220	8.181	-0.039
35	229	8.264	8.265	0.001
35	231	8.244	8.268	0.024
35	232	8.253	8.225	-0.028
35	233	8.185	8.159	-0.026
50	210	8.289	8.199	-0.090
50	211	8.269	8.224	-0.045
50	212	8.280	8.271	-0.009
50	213	8.259	8.265	0.006
50	214	8.236	8.215	-0.021
55	215	8.168	8.124	-0.044
55	216	8.203	8.211	0.008
55	217	8.191	8.238	0.047
55	218	8.262	8.272	0.010
55	220	8.207	8.275	0.068
100	247	8.324	8.266	-0.058
100	249	8.277	8.294	0.017
100	250	8.299	8.304	0.005
100	251	8.284	8.358	0.074
100	253	8.291	8.382	0.091
100	255	8.224	8.289	0.065
100	256	8.330	8.405	0.075
100	257	8.206	8.271	0.065
100	258	8.267	8.319	0.052
100	259	8.313	8.373	0.060
100	260	8.231	8.318	0.087
100	261	8.290	8.387	0.097
100	262	8.182	8.314	0.132
100	263	8.247	8.353	0.106
100	264	8.283	8.437	0.154
100	265	8.315	8.409	0.094
100	266	8.327	8.420	0.093
100	267	8.248	8.311	0.063
100	268	8.305	8.379	0.074
100	269	8.268	8.373	0.105
100	270	8.293	8.397	0.104
100	271	8.326	8.400	0.074
100	273	8.261	8.394	0.133
105	274	8.253	8.441	0.188
105	275	8.286	8.459	0.173
105	276	8.294	8.440	0.146
105	277	8.294	8.447	0.153
105	278	8.246	8.393	0.147
Max		8.330	8.459	0.188
Average		8.247	8.278	0.031
Min		8.148	8.094	-0.171
Std Dev		0.045	0.090	0.072

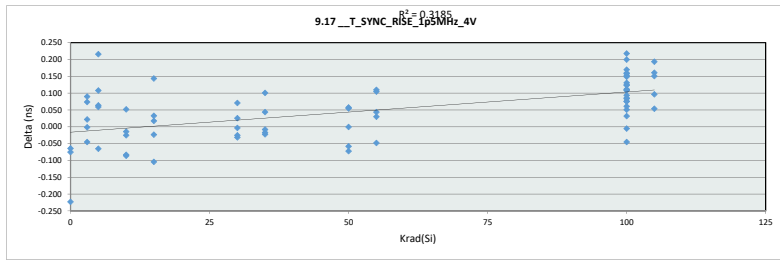


		9.13 _T_SYNC_RISE_1MHz_4V										
Test Site	Tester											
Test Number	Unit	15	ns									
Max Limit	Min Limit	15	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.107	8.210	8.180	8.178	8.094	8.208	8.159	8.199	8.124	8.266	8.393
Average		8.154	8.244	8.234	8.198	8.193	8.236	8.220	8.235	8.224	8.354	8.436
Max		8.185	8.294	8.276	8.221	8.264	8.276	8.268	8.271	8.275	8.437	8.459
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

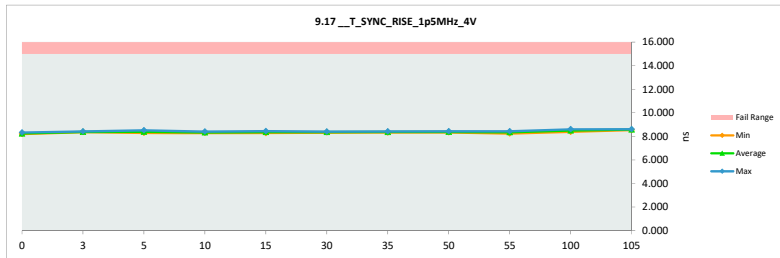


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9.17 __T_SYNC_RISE_1p5MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.388	8.323	-0.065
0	190	8.420	8.196	-0.224
0	191	8.278	8.202	-0.076
3	300	8.400	8.354	-0.046
3	305	8.337	8.358	0.021
3	309	8.303	8.376	0.073
3	311	8.390	8.388	-0.002
3	313	8.327	8.416	0.089
5	319	8.288	8.503	0.215
5	321	8.328	8.386	0.058
5	324	8.363	8.297	-0.066
5	327	8.258	8.365	0.107
5	331	8.290	8.353	0.063
10	234	8.348	8.399	0.051
10	235	8.383	8.357	-0.026
10	236	8.376	8.292	-0.084
10	237	8.331	8.316	-0.015
10	238	8.403	8.316	-0.087
15	239	8.378	8.410	0.032
15	240	8.355	8.331	-0.024
15	241	8.298	8.441	0.143
15	243	8.358	8.375	0.017
15	245	8.365	8.260	-0.105
30	222	8.429	8.403	-0.026
30	223	8.302	8.327	0.025
30	225	8.368	8.364	-0.004
30	226	8.269	8.339	0.070
30	227	8.392	8.360	-0.032
35	228	8.347	8.324	-0.023
35	229	8.352	8.395	0.043
35	231	8.316	8.416	0.100
35	232	8.397	8.379	-0.018
35	233	8.323	8.314	-0.009
50	210	8.399	8.326	-0.073
50	211	8.346	8.345	-0.001
50	212	8.402	8.343	-0.059
50	213	8.351	8.408	0.057
50	214	8.372	8.426	0.054
55	215	8.283	8.234	-0.049
55	216	8.269	8.298	0.029
55	217	8.320	8.363	0.043
55	218	8.321	8.425	0.104
55	220	8.317	8.426	0.109
100	247	8.381	8.412	0.031
100	249	8.437	8.431	-0.006
100	250	8.447	8.507	0.060
100	251	8.366	8.459	0.093
100	253	8.376	8.487	0.111
100	255	8.481	8.435	-0.046
100	256	8.423	8.532	0.109
100	257	8.295	8.380	0.085
100	258	8.441	8.491	0.050
100	259	8.414	8.537	0.123
100	260	8.384	8.508	0.124
100	261	8.399	8.558	0.159
100	262	8.321	8.490	0.169
100	263	8.352	8.459	0.107
100	264	8.383	8.600	0.217
100	265	8.383	8.458	0.075
100	266	8.429	8.580	0.151
100	267	8.312	8.469	0.157
100	268	8.353	8.552	0.199
100	269	8.399	8.482	0.083
100	270	8.400	8.548	0.148
100	271	8.424	8.499	0.075
100	273	8.426	8.556	0.130
105	274	8.375	8.568	0.193
105	275	8.537	8.590	0.053
105	276	8.464	8.560	0.096
105	277	8.438	8.598	0.160
105	278	8.388	8.538	0.150
	Max	8.537	8.600	0.217
	Average	8.367	8.416	0.049
	Min	8.258	8.196	-0.224
	Std Dev	0.055	0.097	0.087

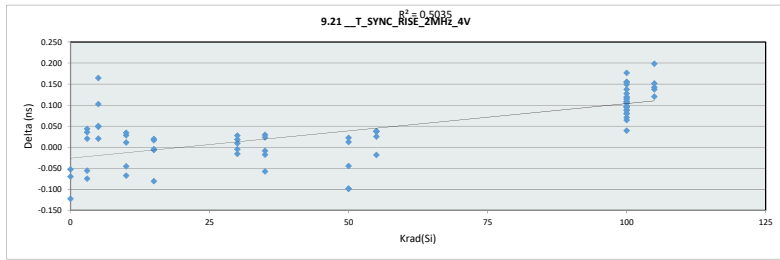


9.17 __T_SYNC_R											
Test Site											
Tester											
Test Number	15	ns									
Max Limit	15	ns									
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.196	8.354	8.297	8.292	8.260	8.327	8.314	8.326	8.234	8.380	8.538
Average	8.240	8.378	8.381	8.336	8.363	8.359	8.366	8.370	8.349	8.497	8.571
Max	8.323	8.416	8.503	8.399	8.441	8.403	8.416	8.426	8.426	8.600	8.598
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

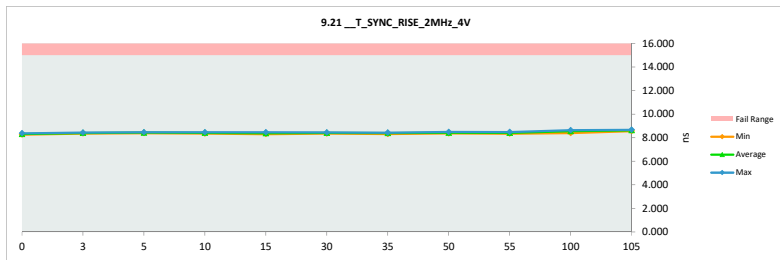


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9.21 __T_SYNC_RISE_2MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.493	8.370	-0.123
0	190	8.355	8.302	-0.053
0	191	8.331	8.261	-0.070
3	300	8.452	8.377	-0.075
3	305	8.384	8.404	0.020
3	309	8.413	8.357	-0.056
3	311	8.386	8.421	0.035
3	313	8.383	8.426	0.043
5	319	8.431	8.451	0.020
5	321	8.388	8.438	0.050
5	324	8.316	8.418	0.102
5	327	8.334	8.382	0.048
5	331	8.294	8.458	0.164
10	234	8.444	8.455	0.011
10	235	8.396	8.430	0.034
10	236	8.393	8.421	0.028
10	237	8.420	8.352	-0.068
10	238	8.431	8.385	-0.046
15	239	8.464	8.459	-0.005
15	240	8.340	8.333	-0.007
15	241	8.327	8.343	0.016
15	243	8.415	8.434	0.019
15	245	8.384	8.303	-0.081
30	222	8.405	8.432	0.027
30	223	8.364	8.348	-0.016
30	225	8.408	8.426	0.018
30	226	8.374	8.369	-0.005
30	227	8.432	8.441	0.009
35	228	8.365	8.307	-0.058
35	229	8.427	8.418	-0.009
35	231	8.402	8.425	0.023
35	232	8.443	8.425	-0.018
35	233	8.325	8.354	0.029
50	210	8.463	8.364	-0.099
50	211	8.458	8.413	-0.045
50	212	8.483	8.384	-0.099
50	213	8.459	8.481	0.022
50	214	8.432	8.444	0.012
55	215	8.314	8.351	0.037
55	216	8.359	8.340	-0.019
55	217	8.363	8.388	0.025
55	218	8.437	8.474	0.037
55	220	8.438	8.476	0.038
100	247	8.376	8.463	0.087
100	249	8.443	8.482	0.039
100	250	8.359	8.508	0.149
100	251	8.495	8.565	0.070
100	253	8.450	8.577	0.127
100	255	8.394	8.501	0.107
100	256	8.508	8.605	0.097
100	257	8.313	8.393	0.080
100	258	8.436	8.500	0.064
100	259	8.489	8.606	0.117
100	260	8.423	8.537	0.114
100	261	8.525	8.633	0.108
100	262	8.351	8.506	0.155
100	263	8.405	8.501	0.096
100	264	8.479	8.633	0.154
100	265	8.451	8.555	0.104
100	266	8.510	8.598	0.088
100	267	8.411	8.490	0.079
100	268	8.450	8.558	0.108
100	269	8.477	8.596	0.119
100	270	8.472	8.609	0.137
100	271	8.473	8.567	0.094
100	273	8.441	8.617	0.176
105	274	8.414	8.565	0.151
105	275	8.546	8.666	0.120
105	276	8.484	8.626	0.142
105	277	8.462	8.660	0.198
105	278	8.471	8.608	0.137
Max		8.546	8.666	0.198
Average		8.417	8.461	0.044
Min		8.294	8.261	-0.123
Std Dev		0.057	0.100	0.075

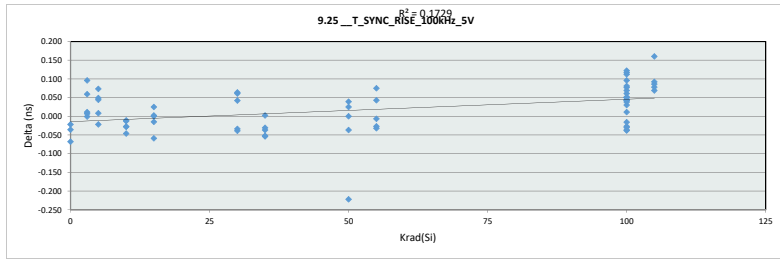


9.21 __T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.261	8.357	8.382	8.352	8.303	8.348	8.307	8.364	8.340	8.393	8.565
Average	8.311	8.397	8.429	8.409	8.374	8.403	8.386	8.417	8.406	8.548	8.625
Max	8.370	8.426	8.458	8.455	8.459	8.441	8.425	8.481	8.476	8.633	8.666
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

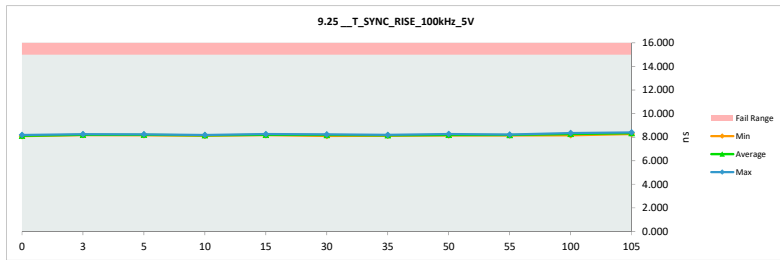


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		9.25 _T_SYNC_RISE_100kHz_5V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		15	15	
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.208	8.186	-0.022
0	190	8.164	8.096	-0.068
0	191	8.121	8.085	-0.036
3	300	8.159	8.170	0.011
3	305	8.201	8.208	0.007
3	309	8.181	8.180	-0.001
3	311	8.193	8.252	0.059
3	313	8.128	8.224	0.096
5	319	8.150	8.223	0.073
5	321	8.264	8.242	-0.022
5	324	8.114	8.163	0.049
5	327	8.153	8.161	0.008
5	331	8.156	8.200	0.044
10	234	8.232	8.186	-0.046
10	235	8.204	8.176	-0.028
10	236	8.195	8.182	-0.013
10	237	8.125	8.114	-0.011
10	238	8.188	8.160	-0.028
15	239	8.237	8.262	0.025
15	240	8.204	8.204	0.000
15	241	8.165	8.167	0.002
15	243	8.238	8.179	-0.059
15	245	8.186	8.171	-0.015
30	222	8.172	8.236	0.064
30	223	8.134	8.100	-0.034
30	225	8.222	8.183	-0.039
30	226	8.143	8.185	0.042
30	227	8.143	8.204	0.061
35	228	8.200	8.163	-0.037
35	229	8.193	8.195	0.002
35	231	8.217	8.186	-0.031
35	232	8.223	8.169	-0.054
35	233	8.168	8.116	-0.052
50	210	8.373	8.151	-0.222
50	211	8.167	8.192	0.025
50	212	8.228	8.191	-0.037
50	213	8.226	8.265	0.039
50	214	8.165	8.165	0.000
55	215	8.181	8.149	-0.032
55	216	8.098	8.173	0.075
55	217	8.122	8.165	0.043
55	218	8.230	8.223	-0.007
55	220	8.231	8.204	-0.027
100	247	8.235	8.198	-0.037
100	249	8.186	8.282	0.096
100	250	8.239	8.289	0.050
100	251	8.248	8.259	0.011
100	253	8.249	8.291	0.042
100	255	8.172	8.289	0.117
100	256	8.250	8.300	0.050
100	257	8.179	8.163	-0.016
100	258	8.237	8.210	-0.027
100	259	8.236	8.348	0.112
100	260	8.183	8.234	0.051
100	261	8.209	8.331	0.122
100	262	8.221	8.183	-0.038
100	263	8.161	8.212	0.051
100	264	8.274	8.244	-0.030
100	265	8.267	8.311	0.044
100	266	8.258	8.335	0.077
100	267	8.184	8.245	0.061
100	268	8.207	8.237	0.030
100	269	8.223	8.261	0.038
100	270	8.219	8.300	0.081
100	271	8.275	8.318	0.043
100	273	8.248	8.317	0.069
105	274	8.195	8.273	0.078
105	275	8.271	8.340	0.069
105	276	8.243	8.403	0.160
105	277	8.267	8.353	0.086
105	278	8.212	8.304	0.092
Max		8.373	8.403	0.160
Average		8.202	8.220	0.018
Min		8.098	8.085	-0.222
Std Dev		0.048	0.068	0.059

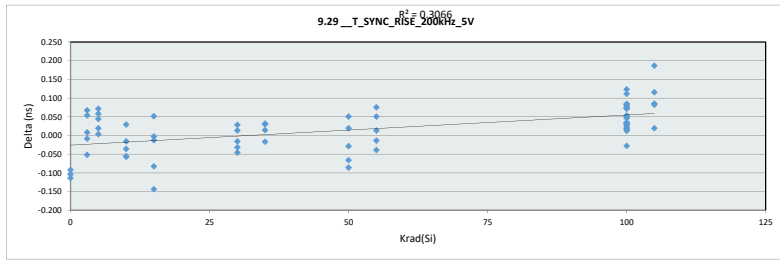


		9.25 _T_SYNC_R										
Test Site												
Tester												
Test Number												
Max Limit		15	ns									
Min Limit			ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.085	8.170	8.161	8.114	8.167	8.100	8.116	8.151	8.149	8.163	8.273
Average		8.122	8.207	8.198	8.164	8.197	8.182	8.166	8.193	8.183	8.268	8.335
Max		8.186	8.252	8.242	8.186	8.262	8.236	8.195	8.265	8.223	8.348	8.403
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

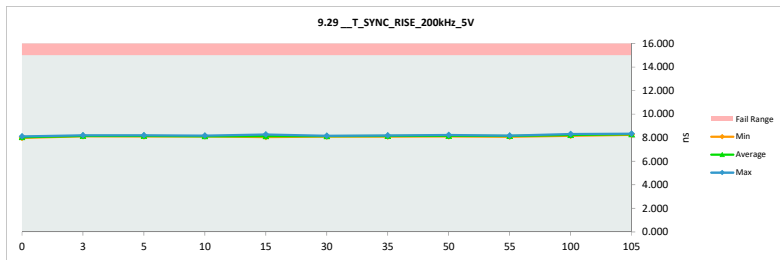


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		9.29 _T_SYNC_RISE_200kHz_5V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	15	15	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.200	8.108	-0.092
0	190	8.152	8.048	-0.104
0	191	8.100	7.986	-0.114
3	300	8.173	8.121	-0.052
3	305	8.149	8.140	-0.009
3	309	8.078	8.145	0.067
3	311	8.149	8.202	0.053
3	313	8.140	8.148	0.008
5	319	8.135	8.179	0.044
5	321	8.128	8.199	0.071
5	324	8.084	8.103	0.019
5	327	8.077	8.135	0.058
5	331	8.133	8.136	0.003
10	234	8.145	8.174	0.029
10	235	8.141	8.125	-0.016
10	236	8.159	8.103	-0.056
10	237	8.172	8.115	-0.057
10	238	8.137	8.101	-0.036
15	239	8.214	8.265	0.051
15	240	8.132	8.129	-0.003
15	241	8.191	8.047	-0.144
15	243	8.134	8.121	-0.013
15	245	8.167	8.084	-0.083
30	222	8.185	8.153	-0.032
30	223	8.064	8.092	0.028
30	225	8.193	8.147	-0.046
30	226	8.104	8.117	0.013
30	227	8.153	8.137	-0.016
35	228	8.067	8.097	0.030
35	229	8.148	8.179	0.031
35	231	8.133	8.164	0.031
35	232	8.188	8.171	-0.017
35	233	8.079	8.093	0.014
50	210	8.192	8.106	-0.086
50	211	8.152	8.171	0.019
50	212	8.151	8.122	-0.029
50	213	8.179	8.229	0.050
50	214	8.252	8.186	-0.066
55	215	8.120	8.081	-0.039
55	216	8.093	8.168	0.075
55	217	8.123	8.173	0.050
55	218	8.139	8.125	-0.014
55	220	8.163	8.176	0.013
100	247	8.138	8.163	0.025
100	249	8.144	8.159	0.015
100	250	8.199	8.232	0.033
100	251	8.189	8.161	-0.028
100	253	8.218	8.250	0.032
100	255	8.188	8.217	0.029
100	256	8.205	8.276	0.071
100	257	8.132	8.162	0.030
100	258	8.124	8.196	0.072
100	259	8.195	8.306	0.111
100	260	8.159	8.212	0.053
100	261	8.173	8.247	0.074
100	262	8.126	8.209	0.083
100	263	8.100	8.178	0.078
100	264	8.216	8.298	0.082
100	265	8.195	8.279	0.084
100	266	8.237	8.255	0.018
100	267	8.183	8.195	0.012
100	268	8.214	8.263	0.049
100	269	8.196	8.231	0.035
100	270	8.143	8.266	0.123
100	271	8.233	8.279	0.046
100	273	8.235	8.255	0.020
105	274	8.232	8.251	0.019
105	275	8.230	8.315	0.085
105	276	8.205	8.287	0.082
105	277	8.183	8.298	0.115
105	278	8.149	8.335	0.186
	Max	8.252	8.335	0.186
	Average	8.159	8.177	0.018
	Min	8.064	7.986	-0.144
	Std Dev	0.045	0.073	0.060

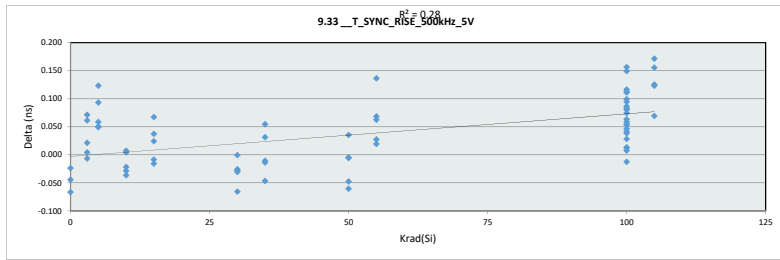


		9.29 _T_SYNC_R										
Test Site	Tester											
Test Number	Unit	15	ns									
Max Limit	Min Limit	15	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		7.986	8.121	8.103	8.101	8.047	8.092	8.093	8.106	8.081	8.159	8.251
Average		8.047	8.151	8.150	8.124	8.129	8.129	8.141	8.163	8.145	8.230	8.297
Max		8.108	8.202	8.199	8.174	8.265	8.153	8.179	8.229	8.176	8.306	8.335
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

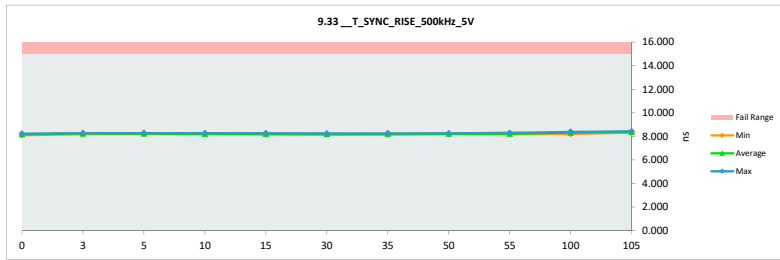


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		9.33 _T_SYNC_RISE_500kHz_5V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	15	15	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.251	8.227	-0.024
0	190	8.229	8.184	-0.045
0	191	8.159	8.092	-0.067
3	300	8.253	8.246	-0.007
3	305	8.216	8.237	0.021
3	309	8.175	8.179	0.004
3	311	8.216	8.277	0.061
3	313	8.187	8.258	0.071
5	319	8.197	8.290	0.093
5	321	8.168	8.218	0.050
5	324	8.107	8.230	0.123
5	327	8.166	8.224	0.058
5	331	8.167	8.216	0.049
10	234	8.261	8.265	0.004
10	235	8.212	8.183	-0.029
10	236	8.193	8.200	0.007
10	237	8.210	8.188	-0.022
10	238	8.239	8.202	-0.037
15	239	8.226	8.263	0.037
15	240	8.245	8.236	-0.009
15	241	8.133	8.157	0.024
15	243	8.228	8.212	-0.016
15	245	8.129	8.196	0.067
30	222	8.269	8.241	-0.028
30	223	8.169	8.143	-0.026
30	225	8.274	8.243	-0.031
30	226	8.178	8.177	-0.001
30	227	8.245	8.179	-0.066
35	228	8.167	8.198	0.031
35	229	8.213	8.267	0.054
35	231	8.274	8.260	-0.014
35	232	8.236	8.225	-0.011
35	233	8.186	8.139	-0.047
50	210	8.237	8.176	-0.061
50	211	8.239	8.191	-0.048
50	212	8.239	8.234	-0.005
50	213	8.262	8.256	-0.006
50	214	8.206	8.241	0.035
55	215	8.156	8.183	0.027
55	216	8.133	8.201	0.068
55	217	8.188	8.207	0.019
55	218	8.153	8.289	0.136
55	220	8.171	8.233	0.062
100	247	8.247	8.300	0.053
100	249	8.194	8.274	0.080
100	250	8.228	8.302	0.074
100	251	8.232	8.295	0.063
100	253	8.328	8.369	0.041
100	255	8.218	8.256	0.038
100	256	8.266	8.382	0.116
100	257	8.191	8.203	0.012
100	258	8.242	8.255	0.013
100	259	8.209	8.365	0.156
100	260	8.210	8.321	0.111
100	261	8.294	8.340	0.046
100	262	8.220	8.314	0.094
100	263	8.178	8.277	0.099
100	264	8.309	8.337	0.028
100	265	8.269	8.256	-0.013
100	266	8.301	8.383	0.082
100	267	8.232	8.239	0.007
100	268	8.260	8.372	0.112
100	269	8.210	8.296	0.086
100	270	8.264	8.318	0.054
100	271	8.208	8.357	0.149
100	273	8.313	8.371	0.058
105	274	8.209	8.380	0.171
105	275	8.273	8.428	0.155
105	276	8.264	8.387	0.123
105	277	8.301	8.370	0.069
105	278	8.187	8.312	0.125
	Max	8.328	8.428	0.171
	Average	8.220	8.258	0.038
	Min	8.107	8.092	-0.067
	Std Dev	0.047	0.071	0.059

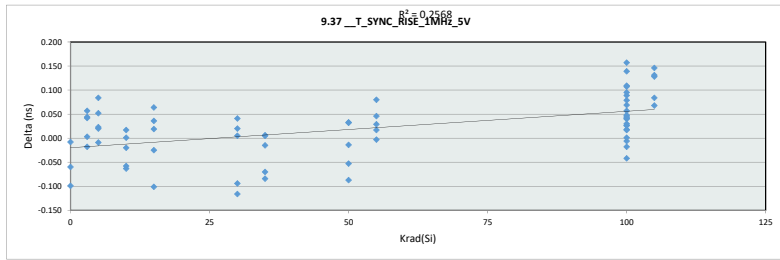


		9.33 _T_SYNC_RISE_500kHz_5V										
Test Site	Tester											
Test Number	Unit	15	ns									
Max Limit	Min Limit	15	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.092	8.179	8.216	8.183	8.157	8.143	8.139	8.176	8.183	8.203	8.312
Average		8.168	8.239	8.236	8.208	8.213	8.197	8.218	8.220	8.223	8.312	8.375
Max		8.227	8.277	8.290	8.265	8.263	8.243	8.267	8.256	8.289	8.383	8.428
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

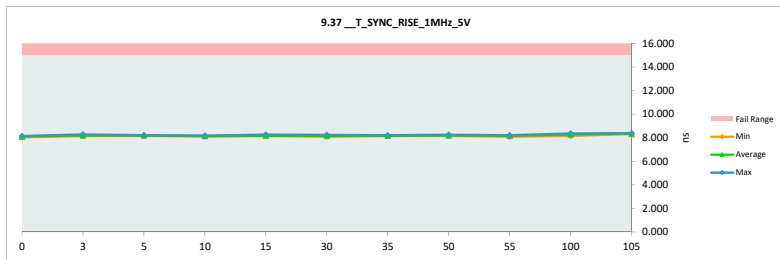


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		9.37 __T_SYNC_RISE_1MHz_5V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	15	15	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.203	8.143	-0.060
0	190	8.150	8.142	-0.008
0	191	8.132	8.033	-0.099
3	300	8.161	8.143	-0.018
3	305	8.153	8.210	0.057
3	309	8.139	8.183	0.044
3	311	8.231	8.273	0.042
3	313	8.197	8.200	0.003
5	319	8.215	8.206	-0.009
5	321	8.188	8.208	0.020
5	324	8.130	8.153	0.023
5	327	8.120	8.204	0.084
5	331	8.153	8.205	0.052
10	234	8.172	8.173	0.001
10	235	8.157	8.174	0.017
10	236	8.210	8.147	-0.063
10	237	8.122	8.102	-0.020
10	238	8.157	8.099	-0.058
15	239	8.206	8.270	0.064
15	240	8.186	8.161	-0.025
15	241	8.108	8.144	0.036
15	243	8.263	8.162	-0.101
15	245	8.128	8.147	0.019
30	222	8.259	8.143	-0.116
30	223	8.180	8.086	-0.094
30	225	8.229	8.234	0.005
30	226	8.093	8.134	0.041
30	227	8.161	8.181	0.020
35	228	8.169	8.154	-0.015
35	229	8.183	8.188	0.005
35	231	8.257	8.187	-0.070
35	232	8.205	8.211	0.006
35	233	8.221	8.137	-0.084
50	210	8.204	8.151	-0.053
50	211	8.188	8.174	-0.014
50	212	8.256	8.169	-0.087
50	213	8.217	8.249	0.032
50	214	8.217	8.250	0.033
55	215	8.115	8.144	0.029
55	216	8.095	8.092	-0.003
55	217	8.120	8.200	0.080
55	218	8.196	8.213	0.017
55	220	8.149	8.195	0.046
100	247	8.225	8.219	-0.006
100	249	8.205	8.232	0.027
100	250	8.225	8.265	0.040
100	251	8.247	8.304	0.057
100	253	8.263	8.245	-0.018
100	255	8.183	8.229	0.046
100	256	8.246	8.335	0.089
100	257	8.204	8.162	-0.042
100	258	8.193	8.235	0.042
100	259	8.267	8.293	0.026
100	260	8.192	8.299	0.107
100	261	8.185	8.342	0.157
100	262	8.153	8.248	0.095
100	263	8.165	8.234	0.069
100	264	8.259	8.260	0.001
100	265	8.211	8.320	0.109
100	266	8.293	8.335	0.042
100	267	8.226	8.244	0.018
100	268	8.269	8.317	0.048
100	269	8.189	8.206	0.017
100	270	8.254	8.284	0.030
100	271	8.261	8.340	0.079
100	273	8.221	8.360	0.139
105	274	8.187	8.318	0.131
105	275	8.284	8.352	0.068
105	276	8.245	8.329	0.084
105	277	8.246	8.392	0.146
105	278	8.180	8.308	0.128
	Max	8.293	8.392	0.157
	Average	8.195	8.217	0.021
	Min	8.093	8.033	-0.116
	Std Dev	0.049	0.076	0.061

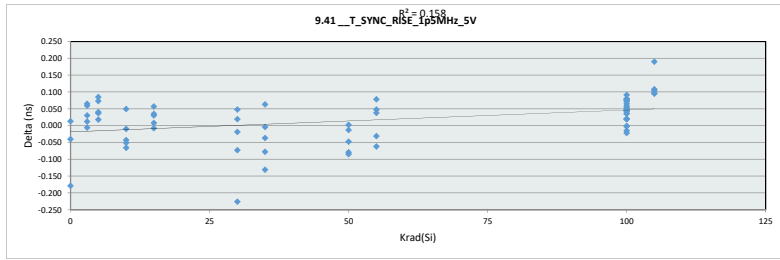


		9.37 __T_SYNC_R										
Test Site	Tester											
Test Number	Unit	15	ns									
Max Limit	Min Limit	15	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.033	8.143	8.153	8.099	8.144	8.086	8.137	8.151	8.092	8.162	8.308
Average		8.106	8.202	8.195	8.139	8.177	8.156	8.175	8.199	8.169	8.274	8.340
Max		8.143	8.273	8.208	8.174	8.270	8.234	8.211	8.250	8.213	8.360	8.392
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

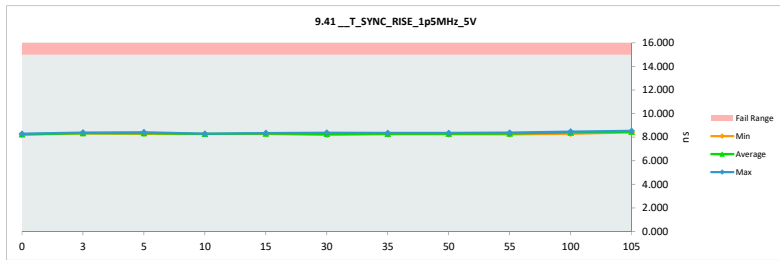


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9.41_T_SYNC_RISE_1p5MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.462	8.283	-0.179
0	190	8.232	8.245	0.013
0	191	8.249	8.209	-0.040
3	300	8.326	8.320	-0.006
3	305	8.281	8.340	0.059
3	309	8.325	8.390	0.065
3	311	8.306	8.318	0.012
3	313	8.290	8.320	0.030
5	319	8.308	8.345	0.037
5	321	8.344	8.417	0.073
5	324	8.265	8.283	0.018
5	327	8.279	8.319	0.040
5	331	8.234	8.319	0.085
10	234	8.310	8.267	-0.043
10	235	8.342	8.276	-0.066
10	236	8.240	8.289	0.049
10	237	8.273	8.263	-0.010
10	238	8.335	8.284	-0.051
15	239	8.339	8.347	0.008
15	240	8.291	8.283	-0.008
15	241	8.234	8.291	0.057
15	243	8.308	8.338	0.030
15	245	8.251	8.286	0.035
30	222	8.310	8.329	0.019
30	223	8.206	8.187	-0.019
30	225	8.333	8.260	-0.073
30	226	8.468	8.242	-0.226
30	227	8.329	8.377	0.048
35	228	8.294	8.257	-0.037
35	229	8.339	8.261	-0.078
35	231	8.292	8.355	0.063
35	232	8.318	8.314	-0.004
35	233	8.387	8.256	-0.131
50	210	8.326	8.247	-0.079
50	211	8.313	8.300	-0.013
50	212	8.361	8.313	-0.048
50	213	8.351	8.353	0.002
50	214	8.352	8.267	-0.085
55	215	8.303	8.272	-0.031
55	216	8.274	8.322	0.048
55	217	8.283	8.321	0.038
55	218	8.304	8.382	0.078
55	220	8.294	8.232	-0.062
100	247	8.290	8.355	0.065
100	249	8.292	8.335	0.043
100	250	8.363	8.361	-0.002
100	251	8.335	8.382	0.047
100	253	8.324	8.402	0.078
100	255	8.320	8.411	0.091
100	256	8.343	8.389	0.046
100	257	8.315	8.293	-0.022
100	258	8.305	8.324	0.019
100	259	8.406	8.464	0.058
100	260	8.316	8.396	0.080
100	261	8.397	8.469	0.072
100	262	8.359	8.358	-0.001
100	263	8.288	8.362	0.074
100	264	8.420	8.455	0.035
100	265	8.393	8.412	0.019
100	266	8.409	8.457	0.048
100	267	8.342	8.362	0.020
100	268	8.399	8.384	-0.015
100	269	8.330	8.374	0.044
100	270	8.379	8.456	0.077
100	271	8.387	8.441	0.054
100	273	8.375	8.412	0.037
105	274	8.347	8.442	0.095
105	275	8.338	8.528	0.190
105	276	8.377	8.481	0.104
105	277	8.302	8.399	0.097
105	278	8.330	8.438	0.108
Max		8.468	8.528	0.190
Average		8.325	8.341	0.017
Min		8.206	8.187	-0.226
Std Dev		0.052	0.072	0.067

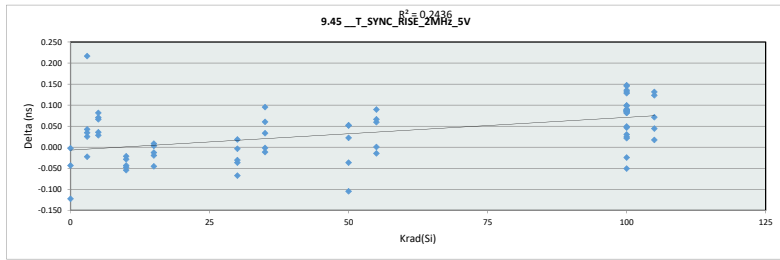


9.41_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.209	8.318	8.283	8.263	8.283	8.187	8.256	8.247	8.232	8.293	8.399
Average	8.246	8.338	8.337	8.276	8.309	8.279	8.289	8.296	8.306	8.394	8.458
Max	8.283	8.390	8.417	8.289	8.347	8.377	8.355	8.353	8.382	8.469	8.528
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

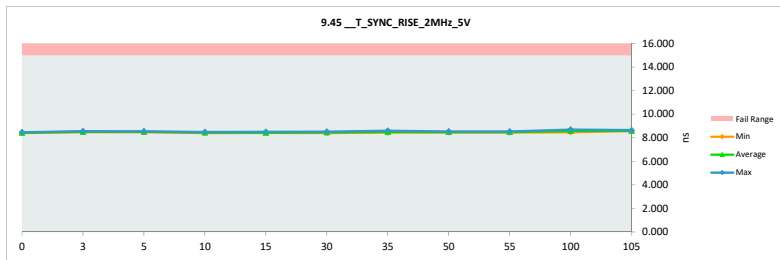


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		9.45 _T_SYNC_RISE_2MHz_5V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		15	15	
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.463	8.460	-0.003
0	190	8.541	8.418	-0.123
0	191	8.435	8.391	-0.044
3	300	8.495	8.472	-0.023
3	305	8.337	8.553	0.216
3	309	8.448	8.473	0.025
3	311	8.497	8.531	0.034
3	313	8.438	8.480	0.042
5	319	8.505	8.540	0.035
5	321	8.462	8.490	0.028
5	324	8.401	8.471	0.070
5	327	8.438	8.504	0.066
5	331	8.449	8.530	0.081
10	234	8.538	8.483	-0.055
10	235	8.466	8.444	-0.022
10	236	8.469	8.421	-0.048
10	237	8.479	8.450	-0.029
10	238	8.449	8.405	-0.044
15	239	8.488	8.491	0.003
15	240	8.404	8.412	0.008
15	241	8.421	8.408	-0.013
15	243	8.455	8.409	-0.046
15	245	8.447	8.427	-0.020
30	222	8.489	8.507	0.018
30	223	8.438	8.401	-0.037
30	225	8.492	8.424	-0.068
30	226	8.469	8.465	-0.004
30	227	8.480	8.449	-0.031
35	228	8.424	8.484	0.060
35	229	8.464	8.497	0.033
35	231	8.493	8.491	-0.002
35	232	8.492	8.587	0.095
35	233	8.446	8.434	-0.012
50	210	8.474	8.525	0.051
50	211	8.424	8.476	0.052
50	212	8.484	8.506	0.022
50	213	8.545	8.440	-0.105
50	214	8.487	8.450	-0.037
55	215	8.450	8.450	0.000
55	216	8.353	8.442	0.089
55	217	8.469	8.454	-0.015
55	218	8.454	8.520	0.066
55	220	8.462	8.521	0.059
100	247	8.523	8.547	0.024
100	249	8.536	8.485	-0.051
100	250	8.495	8.470	-0.025
100	251	8.504	8.550	0.046
100	253	8.512	8.542	0.030
100	255	8.505	8.526	0.021
100	256	8.543	8.641	0.098
100	257	8.430	8.515	0.085
100	258	8.536	8.635	0.099
100	259	8.508	8.589	0.081
100	260	8.450	8.581	0.131
100	261	8.531	8.617	0.086
100	262	8.455	8.545	0.090
100	263	8.455	8.504	0.049
100	264	8.522	8.650	0.128
100	265	8.487	8.574	0.087
100	266	8.541	8.688	0.147
100	267	8.457	8.538	0.081
100	268	8.538	8.683	0.145
100	269	8.467	8.602	0.135
100	270	8.568	8.657	0.089
100	271	8.538	8.623	0.085
100	273	8.498	8.584	0.086
105	274	8.507	8.638	0.131
105	275	8.578	8.595	0.017
105	276	8.512	8.556	0.044
105	277	8.513	8.584	0.071
105	278	8.436	8.559	0.123
	Max	8.578	8.688	0.216
	Average	8.478	8.514	0.035
	Min	8.337	8.391	-0.123
	Std Dev	0.046	0.075	0.065



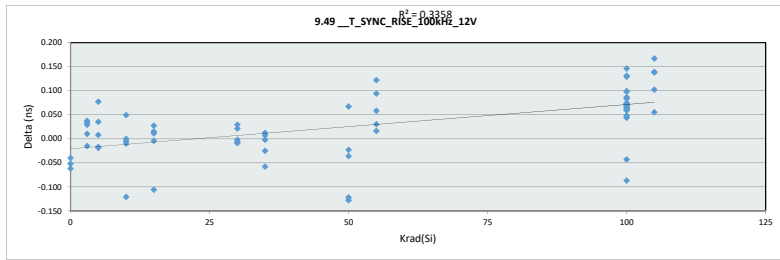
		9.45 _T_SYNC_R										
Test Site												
Tester												
Test Number												
Max Limit		15	ns									
Min Limit			ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.391	8.472	8.471	8.405	8.408	8.401	8.434	8.440	8.442	8.470	8.556
Average		8.423	8.502	8.507	8.441	8.429	8.449	8.499	8.479	8.477	8.580	8.586
Max		8.460	8.553	8.540	8.483	8.491	8.507	8.587	8.525	8.521	8.688	8.638
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



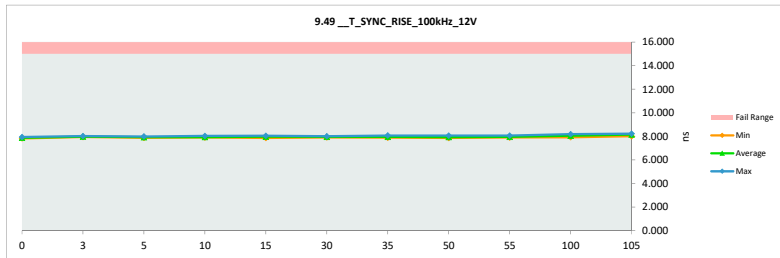
HDR TID Report TPS7H5002-SP

9.49 _T_SYNC_RISE_100kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.004	7.942	-0.062
0	190	7.880	7.828	-0.052
0	191	7.902	7.862	-0.040
3	300	7.954	7.939	-0.015
3	305	7.925	7.954	0.029
3	309	7.960	7.970	0.010
3	311	7.991	8.024	0.033
3	313	7.950	7.987	0.037
5	319	7.952	7.987	0.035
5	321	7.861	7.869	0.008
5	324	7.945	7.926	-0.019
5	327	7.954	7.937	-0.017
5	331	7.837	7.914	0.077
10	234	7.919	7.914	-0.005
10	235	7.910	7.910	0.000
10	236	7.985	8.034	0.049
10	237	8.017	7.896	-0.121
10	238	7.930	7.920	-0.010
15	239	8.054	8.050	-0.004
15	240	7.949	7.976	0.027
15	241	7.983	7.994	0.011
15	243	7.927	7.942	0.015
15	245	7.968	7.862	-0.106
30	222	7.992	8.013	0.021
30	223	7.922	7.951	0.029
30	225	7.958	7.956	-0.002
30	226	7.915	7.906	-0.009
30	227	7.973	7.967	-0.006
35	228	8.056	8.068	0.012
35	229	7.904	7.911	0.007
35	231	7.976	7.974	-0.002
35	232	7.942	7.884	-0.058
35	233	7.959	7.934	-0.025
50	210	8.063	7.941	-0.122
50	211	8.060	7.932	-0.128
50	212	7.889	7.866	-0.023
50	213	7.995	8.062	0.067
50	214	7.897	7.861	-0.036
55	215	7.884	7.900	0.016
55	216	7.816	7.910	0.094
55	217	7.878	7.936	0.058
55	218	7.836	7.958	0.122
55	220	8.035	8.065	0.030
100	247	8.009	7.922	-0.087
100	249	7.906	8.035	0.129
100	250	7.986	8.117	0.131
100	251	8.062	8.125	0.063
100	253	8.008	8.073	0.065
100	255	7.971	8.070	0.099
100	256	7.945	8.004	0.059
100	257	7.934	7.978	0.044
100	258	8.089	8.046	-0.043
100	259	8.072	8.143	0.071
100	260	7.907	8.053	0.146
100	261	7.937	8.023	0.086
100	262	7.908	7.982	0.074
100	263	7.986	8.060	0.074
100	264	7.855	7.938	0.083
100	265	7.998	8.067	0.069
100	266	8.025	8.092	0.067
100	267	7.931	8.001	0.070
100	268	8.133	8.181	0.048
100	269	7.976	8.020	0.044
100	270	8.047	8.145	0.098
100	271	7.998	8.058	0.060
100	273	7.966	8.052	0.086
105	274	8.027	8.194	0.167
105	275	8.086	8.225	0.139
105	276	8.048	8.186	0.138
105	277	8.102	8.157	0.055
105	278	7.910	8.012	0.102
Max		8.133	8.225	0.167
Average		7.965	7.994	0.029
Min		7.816	7.828	-0.128
Std Dev		0.068	0.091	0.065

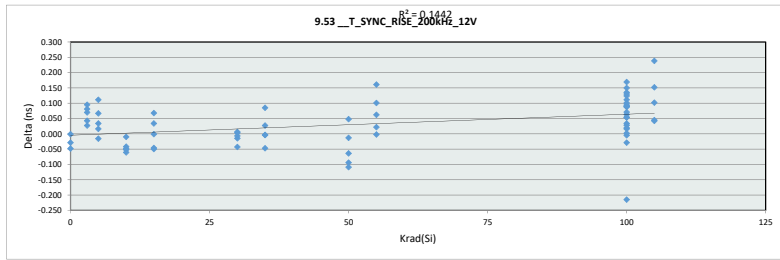


9.49 _T_SYNC_R		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		7.828	7.939	7.869	7.896	7.862	7.906	7.884	7.861	7.900	7.922	8.012
Average		7.877	7.975	7.927	7.935	7.965	7.959	7.954	7.932	7.954	8.052	8.155
Max		7.942	8.024	7.987	8.034	8.050	8.013	8.068	8.062	8.065	8.181	8.225
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

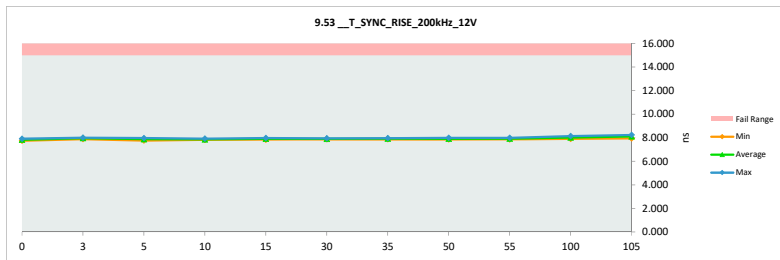


HDR TID Report TPS7H5002-SP

9.53 _T_SYNC_RISE_200kHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.912	7.911	-0.001
0	190	7.796	7.748	-0.048
0	191	7.858	7.829	-0.029
3	300	7.885	7.955	0.070
3	305	7.839	7.882	0.043
3	309	7.980	8.007	0.027
3	311	7.913	8.008	0.095
3	313	7.903	7.984	0.081
5	319	7.859	7.970	0.111
5	321	7.807	7.841	0.034
5	324	7.860	7.876	0.016
5	327	7.855	7.922	0.067
5	331	7.779	7.763	-0.016
10	234	7.881	7.820	-0.061
10	235	7.900	7.848	-0.052
10	236	7.967	7.919	-0.048
10	237	7.947	7.905	-0.042
10	238	7.834	7.824	-0.010
15	239	8.022	7.976	-0.046
15	240	7.893	7.927	0.034
15	241	7.875	7.943	0.068
15	243	7.892	7.891	-0.001
15	245	7.883	7.833	-0.050
30	222	7.990	7.947	-0.043
30	223	7.889	7.892	0.003
30	225	7.880	7.865	-0.015
30	226	7.877	7.883	0.006
30	227	7.929	7.922	-0.007
35	228	7.996	7.949	-0.047
35	229	7.873	7.869	-0.004
35	231	7.934	7.930	-0.004
35	232	7.875	7.960	0.085
35	233	7.882	7.909	0.027
50	210	8.007	7.898	-0.109
50	211	7.960	7.947	-0.013
50	212	7.930	7.866	-0.064
50	213	7.943	7.991	0.048
50	214	7.950	7.856	-0.094
55	215	7.794	7.856	0.062
55	216	7.823	7.984	0.161
55	217	7.798	7.899	0.101
55	218	7.908	7.906	-0.002
55	220	7.970	7.992	0.022
100	247	7.884	7.903	0.019
100	249	7.935	8.036	0.101
100	250	7.961	8.014	0.053
100	251	8.031	8.002	-0.029
100	253	7.976	8.047	0.071
100	255	7.995	8.023	0.028
100	256	7.919	7.973	0.054
100	257	7.930	7.932	0.002
100	258	7.942	7.976	0.034
100	259	8.001	8.089	0.088
100	260	7.879	8.003	0.124
100	261	7.903	8.072	0.169
100	262	7.884	7.947	0.063
100	263	7.945	8.095	0.150
100	264	7.831	7.919	0.088
100	265	7.998	8.128	0.130
100	266	8.026	8.042	0.016
100	267	7.899	8.010	0.111
100	268	8.044	8.138	0.094
100	269	7.928	7.923	-0.005
100	270	8.247	8.032	-0.215
100	271	7.932	8.023	0.091
100	273	7.945	8.080	0.135
105	274	8.067	8.109	0.042
105	275	7.995	8.233	0.238
105	276	7.971	8.123	0.152
105	277	8.050	8.152	0.102
105	278	7.900	7.945	0.045
	Max	8.247	8.233	0.238
	Average	7.923	7.956	0.032
	Min	7.779	7.748	-0.215
	Std Dev	0.076	0.094	0.074



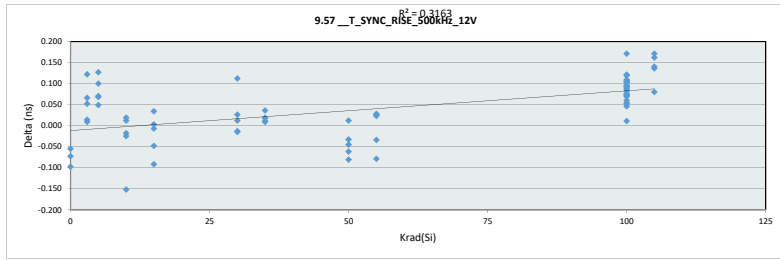
9.53 _T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.748	7.882	7.763	7.820	7.833	7.865	7.869	7.856	7.856	7.903	7.945
Average	7.829	7.967	7.874	7.863	7.914	7.902	7.923	7.912	7.927	8.018	8.112
Max	7.911	8.008	7.970	7.919	7.976	7.947	7.960	7.991	7.992	8.138	8.233
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



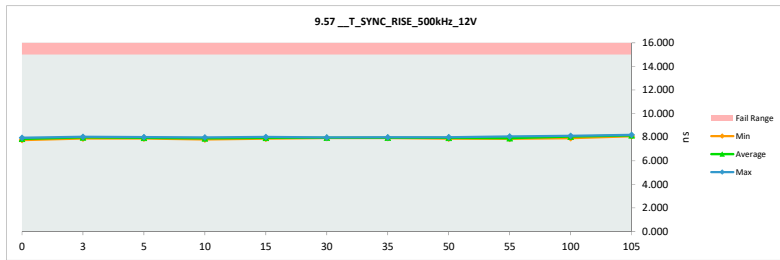
HDR TID Report TPS7H5002-SP

		9.57_T_SYNC_RISE_500kHz_12V	
Test Site	Tester		
Test Number	Unit	ns	ns
Max Limit	Min Limit	15	15

Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.999	7.944	-0.055
0	190	7.822	7.749	-0.073
0	191	7.948	7.850	-0.098
3	300	7.931	7.945	0.014
3	305	7.872	7.881	0.009
3	309	7.896	8.018	0.122
3	311	7.958	8.024	0.066
3	313	7.935	7.987	0.052
5	319	7.904	8.004	0.100
5	321	7.809	7.879	0.070
5	324	7.818	7.945	0.127
5	327	7.873	7.941	0.068
5	331	7.825	7.874	0.049
10	234	7.910	7.892	-0.018
10	235	7.901	7.876	-0.025
10	236	7.957	7.969	0.012
10	237	7.905	7.924	0.019
10	238	7.947	7.795	-0.152
15	239	8.009	8.012	0.003
15	240	7.893	7.927	0.034
15	241	7.979	7.972	-0.007
15	243	7.961	7.869	-0.092
15	245	7.922	7.874	-0.048
30	222	7.985	7.970	-0.015
30	223	7.915	7.941	0.026
30	225	7.911	7.923	0.012
30	226	7.829	7.941	0.112
30	227	7.946	7.933	-0.013
35	228	7.972	7.990	0.018
35	229	7.907	7.918	0.011
35	231	7.910	7.928	0.018
35	232	7.928	7.964	0.036
35	233	7.910	7.919	0.009
50	210	8.039	7.994	-0.045
50	211	8.037	7.956	-0.081
50	212	7.920	7.887	-0.033
50	213	7.968	7.980	0.012
50	214	7.934	7.872	-0.062
55	215	7.927	7.848	-0.079
55	216	7.883	7.849	-0.034
55	217	7.887	7.910	0.023
55	218	7.845	7.873	0.028
55	220	8.033	8.059	0.026
100	247	7.891	7.902	0.011
100	249	7.954	8.008	0.054
100	250	7.955	8.024	0.069
100	251	8.012	8.106	0.094
100	253	7.960	8.067	0.107
100	255	7.947	8.018	0.071
100	256	7.897	8.016	0.119
100	257	7.879	7.988	0.109
100	258	7.940	8.027	0.087
100	259	7.991	8.112	0.121
100	260	7.926	7.999	0.073
100	261	7.924	8.017	0.093
100	262	7.880	8.051	0.171
100	263	7.962	8.064	0.102
100	264	7.905	8.001	0.096
100	265	7.977	8.052	0.075
100	266	8.056	8.108	0.052
100	267	7.972	8.054	0.082
100	268	8.019	8.079	0.060
100	269	7.949	7.995	0.046
100	270	8.014	8.104	0.090
100	271	7.928	8.033	0.105
100	273	7.960	8.035	0.075
105	274	8.025	8.187	0.162
105	275	8.049	8.189	0.140
105	276	8.001	8.137	0.136
105	277	8.073	8.153	0.080
105	278	7.910	8.081	0.171
	Max	8.073	8.189	0.171
	Average	7.938	7.978	0.039
	Min	7.809	7.749	-0.152
	Std Dev	0.059	0.090	0.069

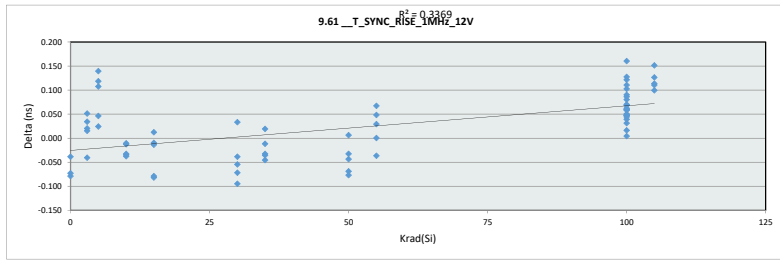


		9.57_T_SYNC_R										
Test Site	Tester											
Test Number	Unit	15	ns									
Max Limit	Min Limit	15	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		7.749	7.881	7.874	7.795	7.869	7.923	7.918	7.872	7.848	7.902	8.081
Average		7.848	7.971	7.929	7.891	7.931	7.942	7.944	7.938	7.908	8.037	8.149
Max		7.944	8.024	8.004	7.969	8.012	7.970	7.990	7.994	8.059	8.112	8.189
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

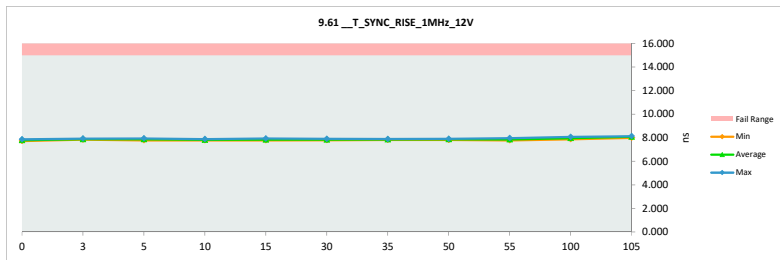


HDR TID Report TPS7H5002-SP

9.61_T_SYNC_RISE_1MHZ_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.928	7.855	-0.073
0	190	7.795	7.716	-0.079
0	191	7.819	7.780	-0.039
3	300	7.878	7.837	-0.041
3	305	7.785	7.836	0.051
3	309	7.873	7.888	0.015
3	311	7.903	7.923	0.020
3	313	7.858	7.892	0.034
5	319	7.863	7.887	0.024
5	321	7.730	7.848	0.118
5	324	7.774	7.881	0.107
5	327	7.800	7.939	0.139
5	331	7.724	7.770	0.046
10	234	7.810	7.775	-0.035
10	235	7.823	7.810	-0.013
10	236	7.915	7.882	-0.033
10	237	7.865	7.827	-0.038
10	238	7.806	7.795	-0.011
15	239	7.940	7.930	-0.010
15	240	7.852	7.770	-0.082
15	241	7.846	7.832	-0.014
15	243	7.870	7.882	0.012
15	245	7.883	7.804	-0.079
30	222	7.974	7.902	-0.072
30	223	7.885	7.790	-0.095
30	225	7.866	7.811	-0.055
30	226	7.806	7.839	0.033
30	227	7.884	7.845	-0.039
35	228	7.921	7.888	-0.033
35	229	7.829	7.817	-0.012
35	231	7.879	7.833	-0.046
35	232	7.854	7.873	0.019
35	233	7.905	7.869	-0.036
50	210	7.946	7.902	-0.044
50	211	7.970	7.901	-0.069
50	212	7.839	7.845	0.006
50	213	7.948	7.871	-0.077
50	214	7.840	7.807	-0.033
55	215	7.769	7.836	0.067
55	216	7.824	7.824	0.000
55	217	7.807	7.770	-0.037
55	218	7.843	7.872	0.029
55	220	7.918	7.966	0.048
100	247	7.843	7.891	0.048
100	249	7.868	7.958	0.090
100	250	7.879	7.959	0.080
100	251	7.978	8.017	0.039
100	253	7.895	7.981	0.086
100	255	7.919	7.981	0.062
100	256	7.861	7.865	0.004
100	257	7.786	7.946	0.160
100	258	7.885	7.953	0.068
100	259	7.941	8.043	0.102
100	260	7.846	7.973	0.127
100	261	7.869	7.919	0.050
100	262	7.861	7.920	0.059
100	263	7.845	7.955	0.110
100	264	7.828	7.872	0.044
100	265	7.938	7.998	0.060
100	266	7.964	8.011	0.047
100	267	7.857	7.978	0.121
100	268	8.017	8.064	0.047
100	269	7.889	7.920	0.031
100	270	8.025	8.041	0.016
100	271	7.921	7.979	0.058
100	273	7.914	7.984	0.070
105	274	7.963	8.073	0.110
105	275	8.002	8.115	0.113
105	276	7.974	8.125	0.151
105	277	8.008	8.107	0.099
105	278	7.862	7.988	0.126
Max		8.025	8.125	0.160
Average		7.877	7.902	0.025
Min		7.724	7.716	-0.095
Std Dev		0.066	0.091	0.065



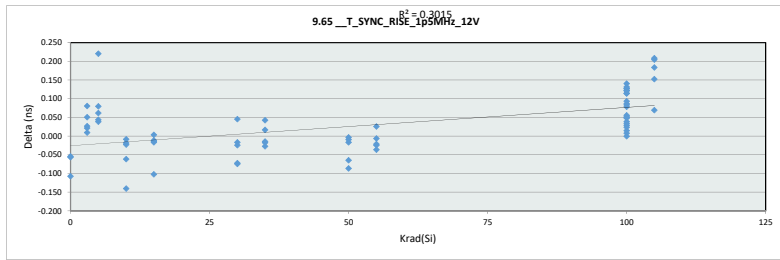
9.61_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.716	7.836	7.770	7.775	7.770	7.790	7.817	7.807	7.770	7.865	7.988
Average	7.784	7.875	7.865	7.818	7.844	7.837	7.856	7.865	7.854	7.966	8.082
Max	7.855	7.923	7.939	7.882	7.930	7.902	7.888	7.902	7.966	8.064	8.125
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



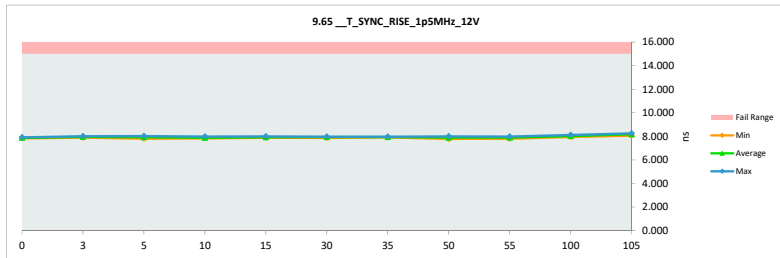
HDR TID Report TPS7H5002-SP

9.65 __T_SYNC_RISE_1p5MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.018	7.910	-0.108
0	190	7.883	7.826	-0.057
0	191	7.922	7.867	-0.055
3	300	7.891	7.900	0.009
3	305	7.908	7.929	0.021
3	309	7.914	7.964	0.050
3	311	7.922	8.002	0.080
3	313	7.931	7.957	0.026
5	319	7.967	8.010	0.043
5	321	7.814	7.852	0.038
5	324	7.797	8.017	0.220
5	327	7.842	7.921	0.079
5	331	7.741	7.802	0.061
10	234	7.926	7.864	-0.062
10	235	7.897	7.874	-0.023
10	236	7.990	7.981	-0.009
10	237	8.033	7.892	-0.141
10	238	7.844	7.825	-0.019
15	239	8.005	7.988	-0.017
15	240	7.901	7.904	0.003
15	241	8.021	7.918	-0.103
15	243	7.926	7.912	-0.014
15	245	7.893	7.881	-0.012
30	222	7.984	7.967	-0.017
30	223	7.921	7.896	-0.025
30	225	7.952	7.877	-0.075
30	226	7.915	7.960	0.045
30	227	8.043	7.970	-0.073
35	228	7.935	7.917	-0.018
35	229	7.892	7.908	0.016
35	231	7.905	7.947	0.042
35	232	7.945	7.930	-0.015
35	233	7.999	7.971	-0.028
50	210	7.976	7.959	-0.017
50	211	8.010	8.000	-0.010
50	212	7.867	7.802	-0.065
50	213	7.975	7.971	-0.004
50	214	7.868	7.781	-0.087
55	215	7.923	7.898	-0.025
55	216	7.809	7.802	-0.007
55	217	7.919	7.897	-0.022
55	218	7.905	7.930	0.025
55	220	8.013	7.976	-0.037
100	247	7.934	7.962	0.028
100	249	7.919	7.967	0.048
100	250	7.962	8.040	0.078
100	251	7.983	8.097	0.114
100	253	7.939	8.025	0.086
100	255	8.019	8.026	0.007
100	256	7.939	7.962	0.023
100	257	7.832	7.953	0.121
100	258	7.932	8.053	0.121
100	259	8.043	8.094	0.051
100	260	7.872	8.012	0.140
100	261	7.968	7.967	-0.001
100	262	7.940	7.989	0.049
100	263	7.914	8.045	0.131
100	264	7.893	7.948	0.055
100	265	8.003	8.041	0.038
100	266	8.065	8.098	0.033
100	267	7.910	8.003	0.093
100	268	8.037	8.051	0.014
100	269	7.899	8.013	0.114
100	270	7.983	8.110	0.127
100	271	7.974	8.057	0.083
100	273	7.911	8.037	0.126
105	274	8.045	8.253	0.208
105	275	8.054	8.206	0.152
105	276	8.002	8.185	0.183
105	277	8.094	8.163	0.069
105	278	7.852	8.056	0.204
Max		8.094	8.253	0.220
Average		7.939	7.969	0.030
Min		7.741	7.781	-0.141
Std Dev		0.069	0.095	0.076

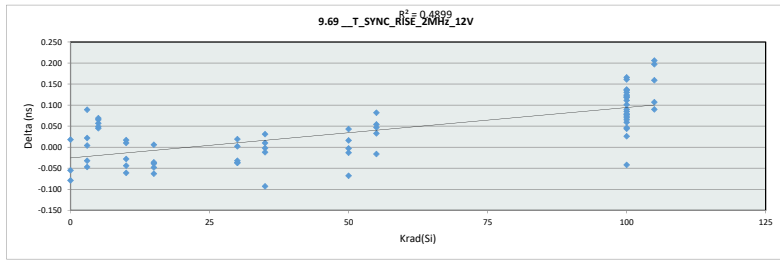


9.65 __T_SYNC_R		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		7.826	7.900	7.802	7.825	7.881	7.877	7.908	7.781	7.802	7.948	8.056
Average		7.868	7.950	7.920	7.887	7.921	7.934	7.935	7.903	7.901	8.024	8.173
Max		7.910	8.002	8.017	7.981	7.988	7.970	7.971	8.000	7.976	8.110	8.253
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

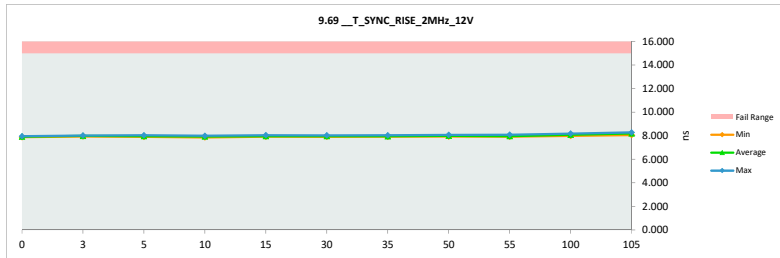


HDR TID Report
TPS7H5002-SP

9.69 _T_SYNC_RISE_2MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.011	7.932	-0.079
0	190	7.926	7.871	-0.055
0	191	7.938	7.956	0.018
3	300	7.990	7.958	-0.032
3	305	7.911	7.933	0.022
3	309	8.066	8.019	-0.047
3	311	7.984	7.988	0.004
3	313	7.908	7.997	0.089
5	319	7.972	8.037	0.065
5	321	7.889	7.934	0.045
5	324	7.888	7.957	0.069
5	327	7.940	7.989	0.049
5	331	7.845	7.902	0.057
10	234	7.941	7.958	0.017
10	235	7.912	7.851	-0.061
10	236	7.983	7.993	0.010
10	237	8.026	7.982	-0.044
10	238	7.912	7.884	-0.028
15	239	8.093	8.045	-0.048
15	240	7.917	7.923	0.006
15	241	8.063	8.024	-0.039
15	243	7.951	7.915	-0.036
15	245	7.980	7.917	-0.063
30	222	8.062	8.030	-0.032
30	223	7.963	7.965	0.002
30	225	7.983	7.946	-0.037
30	226	7.945	7.908	-0.037
30	227	7.950	7.969	0.019
35	228	8.035	8.033	-0.002
35	229	7.902	7.912	0.010
35	231	7.932	7.963	0.031
35	232	7.953	7.941	-0.012
35	233	8.004	7.911	-0.093
50	210	8.021	7.953	-0.068
50	211	8.071	8.068	-0.003
50	212	7.967	7.954	-0.013
50	213	8.023	8.039	0.016
50	214	7.889	7.932	0.043
55	215	7.934	7.918	-0.016
55	216	7.887	7.920	0.033
55	217	7.863	7.917	0.054
55	218	7.904	7.986	0.082
55	220	8.035	8.082	0.047
100	247	7.975	8.019	0.044
100	249	8.018	8.044	0.026
100	250	8.016	8.117	0.101
100	251	8.035	8.081	0.046
100	253	8.006	8.117	0.111
100	255	8.024	7.982	-0.042
100	256	7.906	8.028	0.122
100	257	7.968	8.033	0.065
100	258	7.995	8.069	0.074
100	259	8.029	8.159	0.130
100	260	7.938	8.017	0.079
100	261	7.935	8.101	0.166
100	262	7.965	8.102	0.137
100	263	8.019	8.089	0.070
100	264	7.897	8.016	0.119
100	265	8.043	8.178	0.135
100	266	7.978	8.139	0.161
100	267	7.951	8.069	0.118
100	268	8.087	8.177	0.090
100	269	7.934	8.012	0.078
100	270	8.105	8.164	0.059
100	271	8.008	8.132	0.124
100	273	7.934	8.019	0.085
105	274	8.038	8.235	0.197
105	275	8.117	8.276	0.159
105	276	8.090	8.197	0.107
105	277	8.127	8.217	0.090
105	278	7.856	8.062	0.206
	Max	8.127	8.276	0.206
	Average	7.977	8.016	0.039
	Min	7.845	7.851	-0.093
	Std Dev	0.066	0.094	0.070



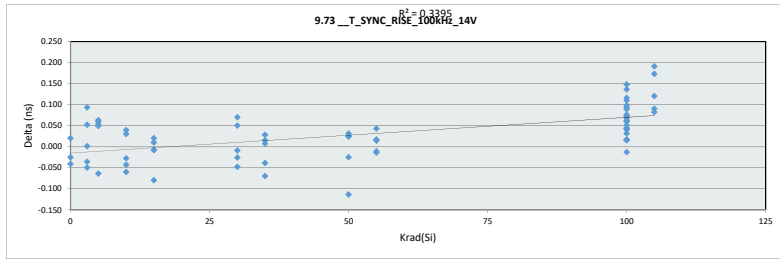
9.69 _T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.871	7.933	7.902	7.851	7.915	7.908	7.911	7.932	7.917	7.982	8.062
Average	7.920	7.979	7.964	7.934	7.965	7.964	7.952	7.989	7.965	8.081	8.197
Max	7.956	8.019	8.037	7.993	8.045	8.030	8.033	8.068	8.082	8.178	8.276
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



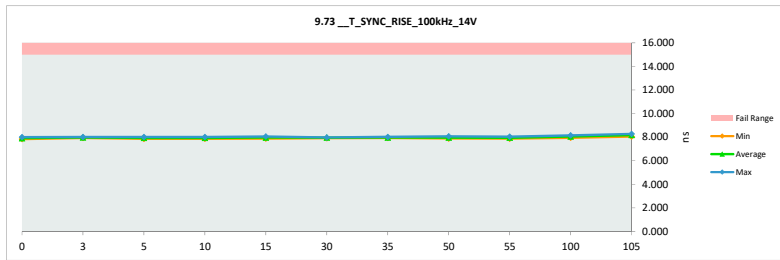
HDR TID Report TPS7H5002-SP

9.73_T_SYNC_RISE_100kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.037	7.996	-0.041
0	190	7.862	7.837	-0.025
0	191	7.880	7.900	0.020
3	300	7.985	7.935	-0.050
3	305	7.878	7.971	0.093
3	309	8.012	8.013	0.001
3	311	8.009	7.973	-0.036
3	313	7.939	7.991	0.052
5	319	7.927	7.976	0.049
5	321	7.921	7.857	-0.064
5	324	7.879	7.942	0.063
5	327	7.963	8.017	0.054
5	331	7.801	7.861	0.060
10	234	7.929	7.869	-0.060
10	235	7.930	7.887	-0.043
10	236	7.986	8.016	0.030
10	237	7.970	7.942	-0.028
10	238	7.881	7.920	0.039
15	239	8.066	8.058	-0.008
15	240	7.937	7.947	0.010
15	241	7.978	7.970	-0.008
15	243	7.956	7.976	0.020
15	245	7.954	7.874	-0.080
30	222	8.016	7.968	-0.048
30	223	7.950	7.941	-0.009
30	225	7.936	7.910	-0.026
30	226	7.928	7.978	0.050
30	227	7.909	7.979	0.070
35	228	7.992	8.020	0.028
35	229	7.932	7.947	0.015
35	231	7.980	7.987	0.007
35	232	8.012	7.942	-0.070
35	233	7.969	7.930	-0.039
50	210	8.009	7.984	-0.025
50	211	8.044	7.930	-0.114
50	212	7.906	7.931	0.025
50	213	8.039	8.070	0.031
50	214	7.860	7.884	0.024
55	215	7.891	7.877	-0.014
55	216	7.882	7.871	-0.011
55	217	7.912	7.955	0.043
55	218	7.953	7.967	0.014
55	220	8.027	8.044	0.017
100	247	7.929	7.945	0.016
100	249	7.951	8.024	0.073
100	250	8.014	8.075	0.061
100	251	8.068	8.112	0.044
100	253	7.999	8.135	0.136
100	255	7.952	7.995	0.043
100	256	7.973	7.990	0.017
100	257	7.913	7.984	0.071
100	258	8.014	8.045	0.031
100	259	8.058	8.109	0.051
100	260	7.933	8.043	0.110
100	261	7.954	8.043	0.089
100	262	7.945	8.007	0.062
100	263	8.008	8.068	0.060
100	264	7.872	8.020	0.148
100	265	8.037	8.052	0.015
100	266	8.054	8.094	0.040
100	267	7.932	8.030	0.098
100	268	8.055	8.148	0.093
100	269	7.951	8.067	0.116
100	270	8.050	8.118	0.068
100	271	7.984	8.060	0.076
100	273	8.037	8.024	-0.013
105	274	8.009	8.182	0.173
105	275	8.085	8.276	0.191
105	276	8.044	8.164	0.120
105	277	8.123	8.205	0.082
105	278	7.964	8.054	0.090
Max		8.123	8.276	0.191
Average		7.968	7.999	0.031
Min		7.801	7.837	-0.114
Std Dev		0.063	0.088	0.060

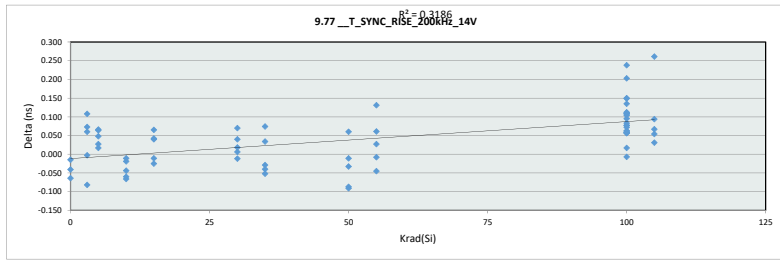


9.73_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.837	7.935	7.857	7.869	7.874	7.910	7.930	7.884	7.871	7.945	8.054
Average	7.911	7.977	7.931	7.927	7.965	7.955	7.965	7.960	7.943	8.052	8.176
Max	7.996	8.013	8.017	8.016	8.058	7.979	8.020	8.070	8.044	8.148	8.276
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

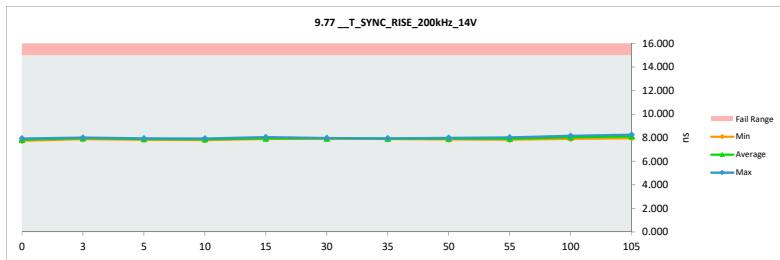


HDR TID Report TPS7H5002-SP

9.77_T_SYNC_RISE_200kHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.949	7.934	-0.015
0	190	7.802	7.738	-0.064
0	191	7.879	7.838	-0.041
3	300	7.904	7.977	0.073
3	305	7.828	7.936	0.108
3	309	7.956	7.953	-0.003
3	311	7.951	8.011	0.060
3	313	7.956	7.874	-0.082
5	319	7.823	7.889	0.066
5	321	7.815	7.842	0.027
5	324	7.877	7.941	0.064
5	327	7.898	7.915	0.017
5	331	7.777	7.825	0.048
10	234	7.909	7.890	-0.019
10	235	7.875	7.815	-0.060
10	236	7.977	7.933	-0.044
10	237	7.907	7.896	-0.011
10	238	7.861	7.795	-0.066
15	239	7.982	8.047	0.065
15	240	7.861	7.901	0.040
15	241	7.901	7.943	0.042
15	243	7.929	7.904	-0.025
15	245	7.899	7.888	-0.011
30	222	7.956	7.974	0.018
30	223	7.908	7.948	0.040
30	225	7.916	7.922	0.006
30	226	7.846	7.916	0.070
30	227	7.938	7.926	-0.012
35	228	7.913	7.947	0.034
35	229	7.857	7.931	0.074
35	231	7.956	7.927	-0.029
35	232	7.959	7.907	-0.052
35	233	7.930	7.890	-0.040
50	210	7.973	7.882	-0.091
50	211	7.999	7.988	-0.011
50	212	7.906	7.873	-0.033
50	213	7.927	7.987	0.060
50	214	7.933	7.846	-0.087
55	215	7.871	7.863	-0.008
55	216	7.818	7.949	0.131
55	217	7.824	7.885	0.061
55	218	7.878	7.833	-0.045
55	220	8.003	8.030	0.027
100	247	7.840	7.898	0.058
100	249	7.913	8.048	0.135
100	250	7.947	8.030	0.083
100	251	8.023	8.016	-0.007
100	253	7.952	8.056	0.104
100	255	8.003	8.080	0.077
100	256	7.870	7.978	0.108
100	257	7.885	7.947	0.062
100	258	7.901	8.011	0.110
100	259	7.993	8.105	0.112
100	260	7.912	8.061	0.149
100	261	7.896	8.004	0.108
100	262	7.918	7.974	0.056
100	263	7.987	8.059	0.072
100	264	7.900	7.958	0.058
100	265	7.952	8.155	0.203
100	266	7.964	8.059	0.095
100	267	7.982	7.999	0.017
100	268	8.041	8.103	0.062
100	269	7.938	8.015	0.077
100	270	7.997	8.147	0.150
100	271	7.955	8.012	0.057
100	273	7.924	8.162	0.238
105	274	8.038	8.105	0.067
105	275	7.994	8.255	0.261
105	276	8.081	8.112	0.031
105	277	8.059	8.113	0.054
105	278	7.868	7.962	0.094
Max		8.081	8.255	0.261
Average		7.923	7.965	0.042
Min		7.777	7.738	-0.091
Std Dev		0.063	0.097	0.072

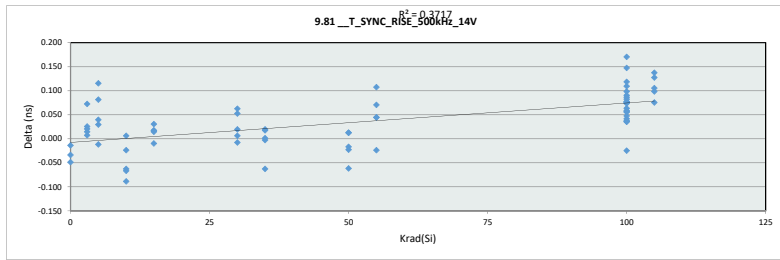


9.77_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.738	7.874	7.825	7.795	7.888	7.916	7.890	7.846	7.833	7.898	7.962
Average	7.837	7.950	7.882	7.866	7.937	7.937	7.920	7.915	7.912	8.038	8.109
Max	7.934	8.011	7.941	7.933	8.047	7.974	7.947	7.988	8.030	8.162	8.255
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

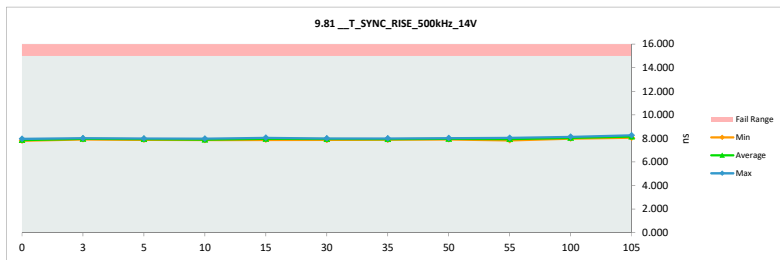


HDR TID Report TPS7H5002-SP

		9.81 _T_SYNC_RISE_500kHz_14V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	15	15	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.999	7.950	-0.049
0	190	7.833	7.799	-0.034
0	191	7.836	7.822	-0.014
3	300	7.939	7.959	0.020
3	305	7.882	7.907	0.025
3	309	7.940	8.012	0.072
3	311	7.963	7.970	0.007
3	313	7.969	7.983	0.014
5	319	7.946	7.985	0.039
5	321	7.858	7.887	0.029
5	324	7.927	7.915	-0.012
5	327	7.900	7.981	0.081
5	331	7.789	7.904	0.115
10	234	7.905	7.911	0.006
10	235	7.913	7.846	-0.067
10	236	7.993	7.969	-0.024
10	237	7.952	7.889	-0.063
10	238	7.932	7.843	-0.089
15	239	8.028	8.045	0.017
15	240	7.913	7.943	0.030
15	241	7.979	7.969	-0.010
15	243	7.950	7.966	0.016
15	245	7.871	7.885	0.014
30	222	7.983	7.989	0.006
30	223	7.897	7.959	0.062
30	225	7.932	7.924	-0.008
30	226	7.844	7.863	0.019
30	227	7.936	7.988	0.052
35	228	7.976	7.995	0.019
35	229	7.895	7.912	0.017
35	231	7.950	7.947	-0.003
35	232	7.937	7.938	0.001
35	233	7.939	7.876	-0.063
50	210	8.041	7.979	-0.062
50	211	8.023	8.000	-0.023
50	212	7.922	7.905	-0.017
50	213	8.003	8.015	0.012
50	214	7.902	7.914	0.012
55	215	7.851	7.827	-0.024
55	216	7.855	7.962	0.107
55	217	7.842	7.912	0.070
55	218	7.900	7.944	0.044
55	220	7.995	8.039	0.044
100	247	7.897	7.973	0.076
100	249	7.974	8.011	0.037
100	250	7.973	8.063	0.090
100	251	8.032	8.109	0.077
100	253	7.991	8.073	0.082
100	255	7.995	8.050	0.055
100	256	7.894	8.012	0.118
100	257	7.901	7.987	0.086
100	258	8.003	8.038	0.035
100	259	8.013	8.086	0.073
100	260	7.945	8.043	0.098
100	261	7.948	7.989	0.041
100	262	7.906	7.981	0.075
100	263	7.975	8.084	0.109
100	264	7.933	7.989	0.056
100	265	8.041	8.076	0.035
100	266	8.008	8.071	0.063
100	267	7.874	8.021	0.147
100	268	8.112	8.087	-0.025
100	269	7.969	8.016	0.047
100	270	8.054	8.130	0.076
100	271	7.997	8.055	0.058
100	273	7.943	8.113	0.170
105	274	8.062	8.137	0.075
105	275	8.120	8.257	0.137
105	276	8.061	8.159	0.098
105	277	8.051	8.178	0.127
105	278	7.949	8.054	0.105
	Max	8.120	8.257	0.170
	Average	7.950	7.987	0.037
	Min	7.789	7.799	-0.089
	Std Dev	0.067	0.089	0.055

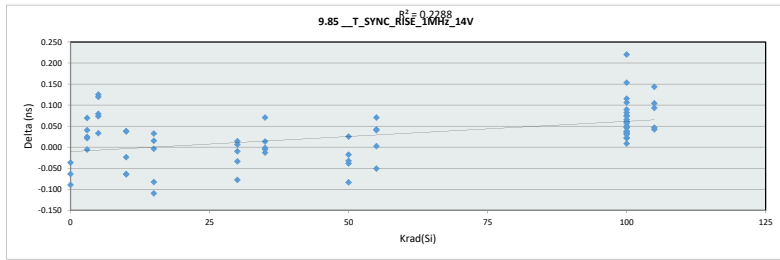


		9.81 _T_SYNC_R										
Test Site	Tester											
Test Number	Unit	15	ns									
Max Limit	Min Limit	15	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		7.799	7.907	7.887	7.843	7.885	7.863	7.876	7.905	7.827	7.973	8.054
Average		7.857	7.966	7.934	7.892	7.962	7.945	7.934	7.963	7.937	8.046	8.157
Max		7.950	8.012	7.985	7.969	8.045	7.989	7.995	8.015	8.039	8.130	8.257
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

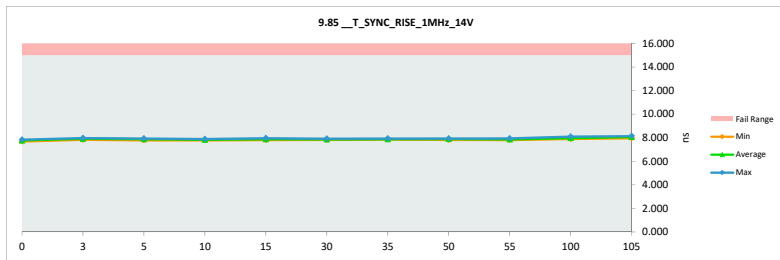


HDR TID Report TPS7H5002-SP

9.85 _T_SYNC_RISE_1MHZ_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.901	7.837	-0.064
0	190	7.718	7.681	-0.037
0	191	7.870	7.780	-0.090
3	300	7.891	7.915	0.024
3	305	7.794	7.834	0.040
3	309	7.902	7.971	0.069
3	311	7.917	7.911	-0.006
3	313	7.888	7.909	0.021
5	319	7.851	7.924	0.073
5	321	7.728	7.847	0.119
5	324	7.801	7.926	0.125
5	327	7.777	7.856	0.079
5	331	7.745	7.778	0.033
10	234	7.848	7.784	-0.064
10	235	7.793	7.831	0.038
10	236	7.931	7.866	-0.065
10	237	7.843	7.880	0.037
10	238	7.821	7.797	-0.024
15	239	7.945	7.960	0.015
15	240	7.857	7.853	-0.004
15	241	7.984	7.874	-0.110
15	243	7.888	7.805	-0.083
15	245	7.823	7.855	0.032
30	222	7.901	7.907	0.006
30	223	7.861	7.827	-0.034
30	225	7.894	7.816	-0.078
30	226	7.818	7.832	0.014
30	227	7.890	7.880	-0.010
35	228	7.892	7.887	-0.005
35	229	7.835	7.848	0.013
35	231	7.852	7.922	0.070
35	232	7.862	7.849	-0.013
35	233	7.853	7.851	-0.002
50	210	7.935	7.902	-0.033
50	211	7.996	7.912	-0.084
50	212	7.853	7.814	-0.039
50	213	7.945	7.927	-0.018
50	214	7.825	7.850	0.025
55	215	7.852	7.801	-0.051
55	216	7.804	7.806	0.002
55	217	7.774	7.844	0.070
55	218	7.840	7.880	0.040
55	220	7.900	7.942	0.042
100	247	7.858	7.896	0.038
100	249	7.897	7.918	0.021
100	250	7.907	7.929	0.022
100	251	7.989	8.024	0.035
100	253	7.966	8.012	0.046
100	255	7.903	7.940	0.037
100	256	7.805	8.025	0.220
100	257	7.897	7.970	0.073
100	258	7.896	7.928	0.032
100	259	7.956	8.016	0.060
100	260	7.818	7.933	0.115
100	261	7.914	7.964	0.050
100	262	7.863	7.928	0.065
100	263	7.821	7.974	0.153
100	264	7.863	7.945	0.082
100	265	7.903	7.992	0.089
100	266	7.959	7.967	0.008
100	267	7.864	7.970	0.106
100	268	8.026	8.087	0.061
100	269	7.920	7.950	0.030
100	270	7.956	8.002	0.046
100	271	7.928	7.985	0.057
100	273	7.912	7.987	0.075
105	274	7.963	8.056	0.093
105	275	7.996	8.139	0.143
105	276	8.025	8.067	0.042
105	277	8.023	8.069	0.046
105	278	7.848	7.952	0.104
Max		8.026	8.139	0.220
Average		7.881	7.910	0.028
Min		7.718	7.681	-0.110
Std Dev		0.068	0.084	0.062



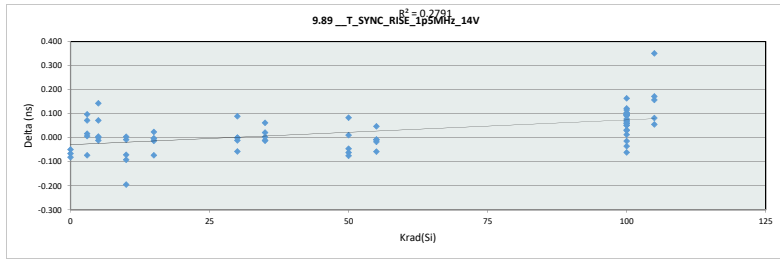
9.85 _T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.681	7.834	7.778	7.784	7.805	7.816	7.848	7.814	7.801	7.896	7.952
Average	7.766	7.908	7.866	7.832	7.869	7.852	7.871	7.881	7.855	7.971	8.057
Max	7.837	7.971	7.926	7.880	7.960	7.907	7.922	7.927	7.942	8.087	8.139
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



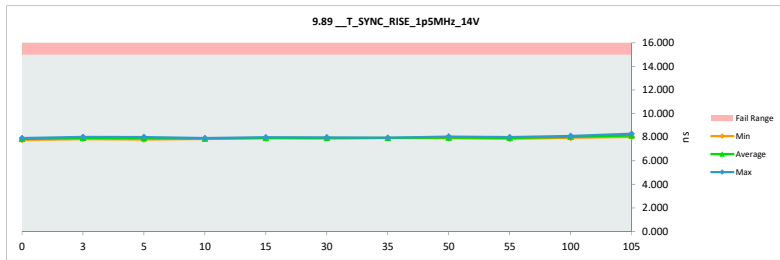
HDR TID Report TPS7H5002-SP

9.89_T_SYNC_RISE_1p5MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.987	7.920	-0.067
0	190	7.852	7.770	-0.082
0	191	7.917	7.867	-0.050
3	300	7.908	7.834	-0.074
3	305	7.869	7.885	0.016
3	309	7.948	8.019	0.071
3	311	7.962	7.968	0.006
3	313	7.909	8.005	0.096
5	319	7.916	7.987	0.071
5	321	7.860	7.848	-0.012
5	324	7.866	8.008	0.142
5	327	7.903	7.902	-0.001
5	331	7.796	7.800	0.004
10	234	8.044	7.848	-0.196
10	235	7.863	7.866	0.003
10	236	7.964	7.922	-0.042
10	237	7.933	7.924	-0.009
10	238	7.941	7.848	-0.093
15	239	8.003	7.989	-0.014
15	240	7.875	7.898	0.023
15	241	8.009	7.935	-0.074
15	243	7.949	7.945	-0.004
15	245	7.916	7.912	-0.004
30	222	7.954	7.953	-0.001
30	223	7.946	7.888	-0.058
30	225	7.912	7.900	-0.012
30	226	7.892	7.980	0.088
30	227	7.942	7.941	-0.001
35	228	7.964	7.951	-0.013
35	229	7.934	7.922	-0.012
35	231	7.925	7.927	0.002
35	232	7.875	7.936	0.061
35	233	7.925	7.945	0.020
50	210	8.018	7.971	-0.047
50	211	8.031	7.968	-0.063
50	212	7.986	7.910	-0.076
50	213	7.970	8.052	0.082
50	214	7.911	7.921	0.010
55	215	7.874	7.866	-0.008
55	216	8.014	8.004	-0.010
55	217	7.919	7.860	-0.059
55	218	7.922	7.968	0.046
55	220	8.012	7.994	-0.018
100	247	7.880	7.975	0.095
100	249	7.897	7.997	0.100
100	250	7.919	8.014	0.095
100	251	8.047	8.097	0.050
100	253	7.970	8.084	0.114
100	255	7.939	8.029	0.090
100	256	7.978	7.990	0.012
100	257	7.999	8.030	0.031
100	258	7.935	8.038	0.103
100	259	8.054	8.105	0.051
100	260	7.885	8.006	0.121
100	261	7.958	7.987	0.029
100	262	7.964	7.949	-0.015
100	263	7.910	7.969	0.059
100	264	7.900	7.995	0.095
100	265	8.048	8.012	-0.036
100	266	8.002	8.079	0.077
100	267	7.918	7.988	0.070
100	268	8.145	8.083	-0.062
100	269	7.956	8.015	0.059
100	270	8.034	8.099	0.065
100	271	7.920	8.012	0.092
100	273	7.890	8.053	0.163
105	274	7.933	8.283	0.350
105	275	8.006	8.177	0.171
105	276	8.038	8.119	0.081
105	277	8.062	8.116	0.054
105	278	7.869	8.025	0.156
Max		8.145	8.283	0.350
Average		7.947	7.973	0.026
Min		7.796	7.770	-0.196
Std Dev		0.062	0.089	0.080

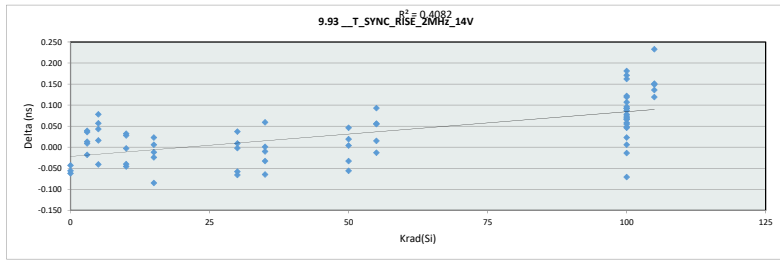


9.89_T_SYNC_R		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		7.770	7.834	7.800	7.848	7.898	7.888	7.922	7.910	7.860	7.949	8.025
Average		7.852	7.942	7.909	7.882	7.936	7.932	7.936	7.964	7.938	8.026	8.144
Max		7.920	8.019	8.008	7.924	7.989	7.980	7.951	8.052	8.004	8.105	8.283
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

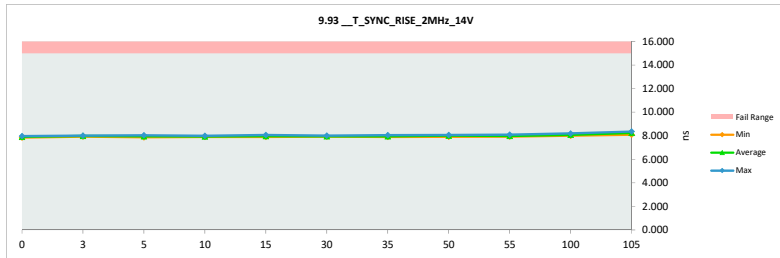


HDR TID Report TPS7H5002-SP

9.93_T_SYNC_RISE_2MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.020	7.964	-0.056
0	190	7.886	7.843	-0.043
0	191	7.960	7.898	-0.062
3	300	7.976	7.958	-0.018
3	305	7.922	7.931	0.009
3	309	7.998	8.011	0.013
3	311	7.976	8.012	0.036
3	313	7.941	7.980	0.039
5	319	8.016	8.032	0.016
5	321	7.905	7.864	-0.041
5	324	7.902	7.980	0.078
5	327	7.919	7.962	0.043
5	331	7.843	7.900	0.057
10	234	7.971	7.925	-0.046
10	235	7.871	7.899	0.028
10	236	7.966	7.993	-0.003
10	237	7.957	7.989	0.032
10	238	7.960	7.920	-0.040
15	239	8.054	8.060	0.006
15	240	7.931	7.954	0.023
15	241	8.039	8.015	-0.024
15	243	7.933	7.921	-0.012
15	245	7.981	7.896	-0.085
30	222	8.077	8.011	-0.066
30	223	7.959	7.968	0.009
30	225	7.903	7.940	0.037
30	226	7.925	7.923	-0.002
30	227	7.984	7.926	-0.058
35	228	7.984	8.043	0.059
35	229	7.938	7.928	-0.010
35	231	7.965	7.966	0.001
35	232	7.961	7.928	-0.033
35	233	7.963	7.898	-0.065
50	210	8.053	8.057	0.004
50	211	8.019	8.038	0.019
50	212	7.991	7.958	-0.033
50	213	8.016	8.062	0.046
50	214	7.962	7.906	-0.056
55	215	7.933	7.920	-0.013
55	216	7.913	7.928	0.015
55	217	7.897	7.952	0.055
55	218	7.936	7.992	0.056
55	220	7.988	8.091	0.093
100	247	7.951	8.047	0.096
100	249	8.019	8.085	0.066
100	250	8.030	8.122	0.092
100	251	8.039	8.085	0.046
100	253	8.150	8.079	-0.071
100	255	8.039	8.025	-0.014
100	256	7.968	8.055	0.087
100	257	8.006	8.012	0.006
100	258	8.007	8.077	0.070
100	259	8.008	8.170	0.162
100	260	7.888	8.059	0.171
100	261	7.967	8.059	0.092
100	262	7.985	8.033	0.048
100	263	7.982	8.060	0.078
100	264	7.878	8.000	0.122
100	265	8.050	8.108	0.058
100	266	7.995	8.176	0.181
100	267	7.957	8.012	0.055
100	268	8.077	8.151	0.074
100	269	7.992	8.015	0.023
100	270	8.123	8.190	0.067
100	271	8.004	8.111	0.107
100	273	7.900	8.019	0.119
105	274	8.085	8.234	0.149
105	275	8.124	8.357	0.233
105	276	8.129	8.265	0.136
105	277	8.087	8.238	0.151
105	278	7.965	8.084	0.119
Max		8.150	8.357	0.233
Average		7.982	8.018	0.036
Min		7.843	7.843	-0.085
Std Dev		0.065	0.100	0.068

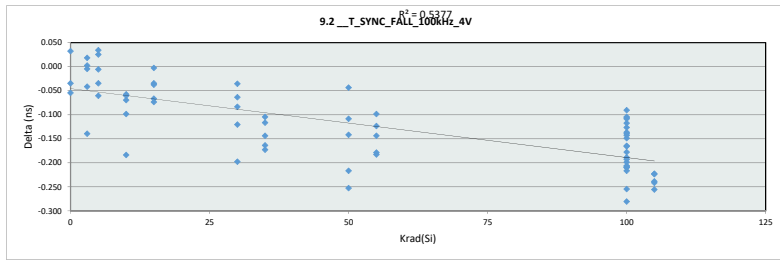


9.93_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.843	7.931	7.864	7.899	7.896	7.923	7.898	7.906	7.920	8.000	8.084
Average	7.902	7.978	7.948	7.945	7.969	7.954	7.953	8.004	7.977	8.076	8.236
Max	7.964	8.012	8.032	7.993	8.060	8.011	8.043	8.062	8.091	8.190	8.357
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

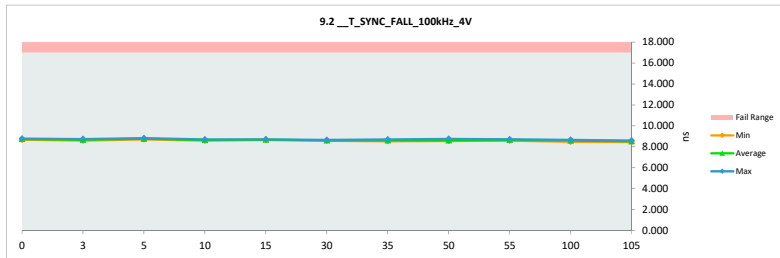


HDR TID Report
TPS7H5002-SP

		9.2_T_SYNC_FALL_100kHz_4V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	17	17	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.723	8.755	0.032
0	190	8.815	8.780	-0.035
0	191	8.732	8.677	-0.055
3	300	8.722	8.582	-0.140
3	305	8.722	8.724	0.002
3	309	8.665	8.660	-0.005
3	311	8.727	8.745	0.018
3	313	8.699	8.657	-0.042
5	319	8.751	8.785	0.034
5	321	8.746	8.711	-0.035
5	324	8.768	8.707	-0.061
5	327	8.832	8.826	-0.006
5	331	8.781	8.806	0.025
10	234	8.744	8.686	-0.058
10	235	8.656	8.586	-0.070
10	236	8.678	8.579	-0.099
10	237	8.846	8.662	-0.184
10	238	8.769	8.708	-0.061
15	239	8.713	8.710	-0.003
15	240	8.752	8.714	-0.038
15	241	8.767	8.700	-0.067
15	243	8.721	8.686	-0.035
15	245	8.724	8.650	-0.074
30	222	8.766	8.568	-0.198
30	223	8.698	8.662	-0.036
30	225	8.712	8.591	-0.121
30	226	8.685	8.601	-0.084
30	227	8.677	8.613	-0.064
35	228	8.795	8.651	-0.144
35	229	8.706	8.589	-0.117
35	231	8.701	8.528	-0.173
35	232	8.743	8.579	-0.164
35	233	8.807	8.702	-0.105
50	210	8.807	8.554	-0.253
50	211	8.772	8.555	-0.217
50	212	8.805	8.761	-0.044
50	213	8.690	8.581	-0.109
50	214	8.736	8.594	-0.142
55	215	8.868	8.724	-0.144
55	216	8.809	8.626	-0.183
55	217	8.798	8.674	-0.124
55	218	8.747	8.648	-0.099
55	220	8.762	8.583	-0.179
100	247	8.902	8.621	-0.281
100	249	8.804	8.667	-0.137
100	250	8.754	8.555	-0.199
100	251	8.680	8.473	-0.207
100	253	8.756	8.649	-0.107
100	255	8.725	8.559	-0.166
100	256	8.737	8.646	-0.091
100	257	8.897	8.642	-0.255
100	258	8.775	8.626	-0.149
100	259	8.622	8.513	-0.109
100	260	8.760	8.642	-0.118
100	261	8.778	8.600	-0.178
100	262	8.780	8.653	-0.127
100	263	8.780	8.591	-0.189
100	264	8.773	8.608	-0.165
100	265	8.825	8.618	-0.207
100	266	8.745	8.605	-0.140
100	267	8.834	8.624	-0.210
100	268	8.742	8.532	-0.210
100	269	8.786	8.593	-0.193
100	270	8.782	8.565	-0.217
100	271	8.720	8.577	-0.143
100	273	8.739	8.634	-0.105
105	274	8.843	8.604	-0.239
105	275	8.765	8.541	-0.224
105	276	8.704	8.481	-0.223
105	277	8.698	8.456	-0.242
105	278	8.794	8.538	-0.256
Max		8.902	8.826	0.034
Average		8.755	8.632	-0.123
Min		8.622	8.456	-0.281
Std Dev		0.055	0.078	0.080

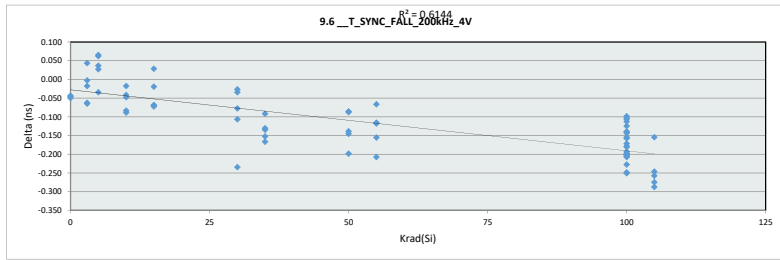


		9.2_T_SYNC_FALL										
Test Site	Tester											
Test Number	Unit	17	ns									
Max Limit	Min Limit	17	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.677	8.582	8.707	8.579	8.650	8.568	8.528	8.554	8.583	8.473	8.456
Average		8.737	8.674	8.767	8.644	8.692	8.607	8.610	8.609	8.651	8.600	8.524
Max		8.780	8.745	8.826	8.708	8.714	8.662	8.702	8.761	8.724	8.667	8.604
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

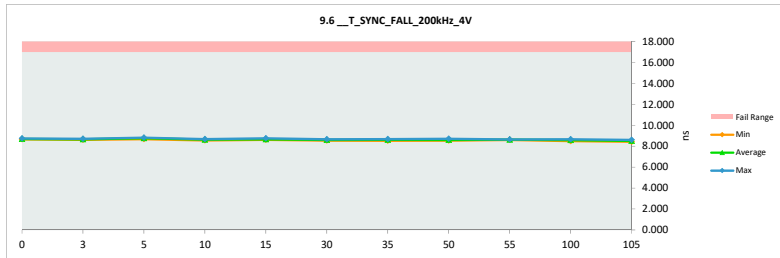


HDR TID Report TPS7H5002-SP

		9.6 _T_SYNC_FALL_200kHz_4V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	17	17	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.726	8.676	-0.050
0	190	8.775	8.731	-0.044
0	191	8.702	8.656	-0.046
3	300	8.676	8.613	-0.063
3	305	8.719	8.701	-0.018
3	309	8.705	8.640	-0.065
3	311	8.681	8.678	-0.003
3	313	8.587	8.630	0.043
5	319	8.742	8.778	0.036
5	321	8.706	8.671	-0.035
5	324	8.736	8.763	0.027
5	327	8.748	8.813	0.065
5	331	8.715	8.777	0.062
10	234	8.707	8.665	-0.042
10	235	8.609	8.591	-0.018
10	236	8.631	8.542	-0.089
10	237	8.705	8.657	-0.048
10	238	8.750	8.666	-0.084
15	239	8.694	8.674	-0.020
15	240	8.714	8.644	-0.070
15	241	8.717	8.745	0.028
15	243	8.684	8.615	-0.069
15	245	8.680	8.607	-0.073
30	222	8.699	8.621	-0.078
30	223	8.687	8.652	-0.035
30	225	8.771	8.536	-0.235
30	226	8.617	8.590	-0.027
30	227	8.667	8.560	-0.107
35	228	8.758	8.605	-0.153
35	229	8.723	8.588	-0.135
35	231	8.687	8.520	-0.167
35	232	8.726	8.634	-0.092
35	233	8.802	8.671	-0.131
50	210	8.733	8.534	-0.199
50	211	8.695	8.549	-0.146
50	212	8.795	8.707	-0.088
50	213	8.654	8.568	-0.086
50	214	8.738	8.599	-0.139
55	215	8.847	8.639	-0.208
55	216	8.793	8.637	-0.156
55	217	8.750	8.632	-0.118
55	218	8.668	8.601	-0.067
55	220	8.706	8.590	-0.116
100	247	8.891	8.642	-0.249
100	249	8.847	8.596	-0.251
100	250	8.745	8.517	-0.228
100	251	8.661	8.480	-0.181
100	253	8.729	8.557	-0.172
100	255	8.712	8.509	-0.203
100	256	8.713	8.573	-0.140
100	257	8.807	8.652	-0.155
100	258	8.771	8.563	-0.208
100	259	8.644	8.503	-0.141
100	260	8.761	8.582	-0.179
100	261	8.737	8.631	-0.106
100	262	8.722	8.609	-0.113
100	263	8.733	8.575	-0.158
100	264	8.726	8.582	-0.144
100	265	8.797	8.599	-0.198
100	266	8.747	8.643	-0.104
100	267	8.799	8.605	-0.194
100	268	8.744	8.543	-0.201
100	269	8.787	8.587	-0.200
100	270	8.773	8.567	-0.206
100	271	8.675	8.550	-0.125
100	273	8.682	8.583	-0.099
105	274	8.750	8.595	-0.155
105	275	8.813	8.538	-0.275
105	276	8.726	8.438	-0.288
105	277	8.672	8.414	-0.258
105	278	8.765	8.518	-0.247
	Max	8.891	8.813	0.065
	Average	8.726	8.610	-0.116
	Min	8.587	8.414	-0.288
	Std Dev	0.056	0.075	0.085

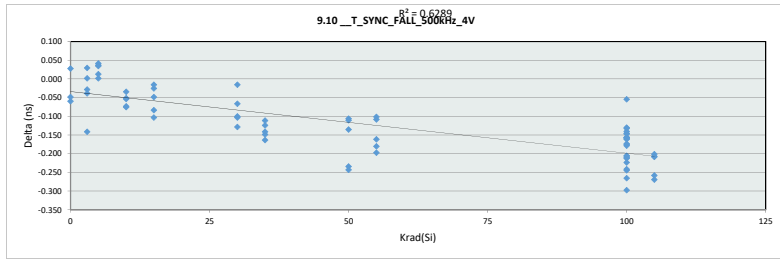


		9.6 _T_SYNC_FALL										
Test Site	Tester											
Test Number	Unit	17	ns									
Max Limit	Min Limit	17	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.656	8.613	8.671	8.542	8.607	8.536	8.520	8.534	8.590	8.480	8.414
Average		8.688	8.652	8.760	8.624	8.657	8.592	8.604	8.591	8.620	8.576	8.501
Max		8.731	8.701	8.813	8.666	8.745	8.652	8.671	8.707	8.639	8.652	8.595
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

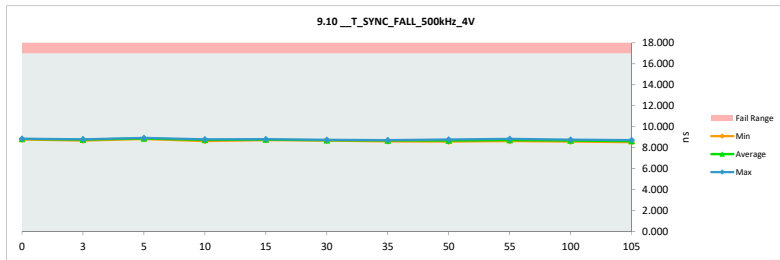


HDR TID Report TPS7H5002-SP

9.10_T_SYNC_FALL_500kHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.818	8.769	-0.049
0	190	8.910	8.850	-0.060
0	191	8.812	8.839	0.027
3	300	8.815	8.673	-0.142
3	305	8.812	8.783	-0.029
3	309	8.808	8.769	-0.039
3	311	8.796	8.797	0.001
3	313	8.711	8.740	0.029
5	319	8.864	8.905	0.041
5	321	8.810	8.822	0.012
5	324	8.878	8.879	0.001
5	327	8.892	8.926	0.034
5	331	8.822	8.858	0.036
10	234	8.800	8.765	-0.035
10	235	8.748	8.696	-0.052
10	236	8.683	8.629	-0.054
10	237	8.860	8.784	-0.076
10	238	8.826	8.752	-0.074
15	239	8.820	8.804	-0.016
15	240	8.831	8.747	-0.084
15	241	8.835	8.809	-0.026
15	243	8.795	8.746	-0.049
15	245	8.817	8.713	-0.104
30	222	8.785	8.684	-0.101
30	223	8.807	8.740	-0.067
30	225	8.808	8.679	-0.129
30	226	8.767	8.663	-0.104
30	227	8.719	8.703	-0.016
35	228	8.829	8.704	-0.125
35	229	8.808	8.696	-0.112
35	231	8.746	8.597	-0.149
35	232	8.821	8.679	-0.142
35	233	8.877	8.713	-0.164
50	210	8.861	8.626	-0.235
50	211	8.822	8.578	-0.244
50	212	8.914	8.778	-0.136
50	213	8.783	8.672	-0.111
50	214	8.787	8.680	-0.107
55	215	8.933	8.831	-0.102
55	216	8.885	8.704	-0.181
55	217	8.911	8.713	-0.198
55	218	8.815	8.706	-0.109
55	220	8.786	8.624	-0.162
100	247	9.037	8.739	-0.298
100	249	8.890	8.711	-0.179
100	250	8.847	8.581	-0.266
100	251	8.780	8.574	-0.206
100	253	8.811	8.665	-0.146
100	255	8.701	8.646	-0.055
100	256	8.822	8.691	-0.131
100	257	8.912	8.754	-0.158
100	258	8.892	8.679	-0.213
100	259	8.787	8.646	-0.141
100	260	8.877	8.715	-0.162
100	261	8.873	8.628	-0.245
100	262	8.845	8.697	-0.148
100	263	8.808	8.634	-0.174
100	264	8.816	8.659	-0.157
100	265	8.936	8.694	-0.242
100	266	8.874	8.698	-0.176
100	267	8.930	8.723	-0.207
100	268	8.803	8.670	-0.133
100	269	8.888	8.664	-0.224
100	270	8.823	8.666	-0.157
100	271	8.813	8.653	-0.160
100	273	8.884	8.673	-0.211
105	274	8.929	8.721	-0.208
105	275	8.885	8.676	-0.209
105	276	8.819	8.549	-0.270
105	277	8.728	8.526	-0.202
105	278	8.869	8.610	-0.259
	Max	9.037	8.926	0.041
	Average	8.832	8.709	-0.123
	Min	8.683	8.526	-0.298
	Std Dev	0.062	0.081	0.085

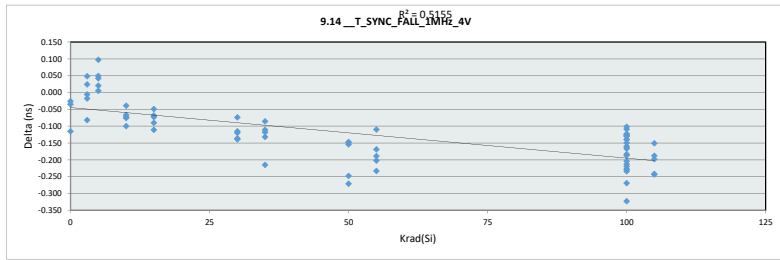


9.10_T_SYNC_FALL_500kHz_4V											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.769	8.673	8.822	8.629	8.713	8.663	8.597	8.578	8.624	8.574	8.526
Average	8.819	8.752	8.878	8.725	8.764	8.694	8.678	8.667	8.716	8.672	8.616
Max	8.850	8.797	8.926	8.784	8.809	8.740	8.713	8.778	8.831	8.754	8.721
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

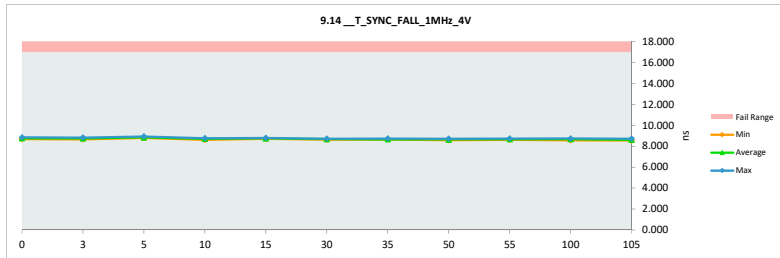


HDR TID Report
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9.14 _T_SYNC_FALL_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.783	8.748	-0.035
0	190	8.872	8.846	-0.026
0	191	8.796	8.681	-0.115
3	300	8.735	8.653	-0.082
3	305	8.792	8.786	-0.006
3	309	8.760	8.742	-0.018
3	311	8.777	8.825	0.048
3	313	8.699	8.723	0.024
5	319	8.764	8.861	0.097
5	321	8.764	8.784	0.020
5	324	8.831	8.836	0.005
5	327	8.886	8.928	0.042
5	331	8.774	8.823	0.049
10	234	8.785	8.712	-0.073
10	235	8.743	8.667	-0.076
10	236	8.712	8.612	-0.100
10	237	8.824	8.757	-0.067
10	238	8.784	8.745	-0.039
15	239	8.828	8.779	-0.049
15	240	8.839	8.749	-0.090
15	241	8.868	8.795	-0.073
15	243	8.801	8.733	-0.068
15	245	8.820	8.709	-0.111
30	222	8.843	8.707	-0.136
30	223	8.792	8.718	-0.074
30	225	8.798	8.659	-0.139
30	226	8.741	8.621	-0.120
30	227	8.758	8.643	-0.115
35	228	8.853	8.638	-0.215
35	229	8.781	8.670	-0.111
35	231	8.728	8.642	-0.086
35	232	8.804	8.672	-0.132
35	233	8.848	8.730	-0.118
50	210	8.842	8.571	-0.271
50	211	8.821	8.573	-0.248
50	212	8.874	8.720	-0.154
50	213	8.753	8.606	-0.147
50	214	8.839	8.690	-0.149
55	215	8.915	8.726	-0.189
55	216	8.879	8.677	-0.202
55	217	8.843	8.610	-0.233
55	218	8.825	8.715	-0.110
55	220	8.797	8.628	-0.169
100	247	9.050	8.727	-0.323
100	249	8.933	8.705	-0.228
100	250	8.840	8.571	-0.269
100	251	8.791	8.562	-0.229
100	253	8.760	8.658	-0.102
100	255	8.810	8.681	-0.129
100	256	8.814	8.653	-0.161
100	257	8.880	8.697	-0.183
100	258	8.922	8.688	-0.234
100	259	8.779	8.575	-0.204
100	260	8.851	8.722	-0.129
100	261	8.837	8.698	-0.139
100	262	8.825	8.700	-0.125
100	263	8.820	8.680	-0.140
100	264	8.788	8.659	-0.129
100	265	8.906	8.693	-0.213
100	266	8.834	8.711	-0.123
100	267	8.906	8.739	-0.167
100	268	8.783	8.622	-0.161
100	269	8.832	8.723	-0.109
100	270	8.850	8.630	-0.220
100	271	8.827	8.640	-0.187
100	273	8.845	8.695	-0.150
105	274	8.865	8.714	-0.151
105	275	8.846	8.603	-0.243
105	276	8.784	8.542	-0.242
105	277	8.730	8.542	-0.188
105	278	8.829	8.631	-0.198
Max		9.050	8.928	0.097
Average		8.818	8.693	-0.126
Min		8.699	8.542	-0.323
Std Dev		0.058	0.077	0.086

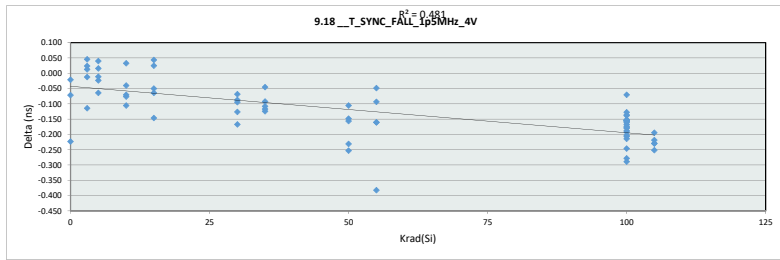


9.14 _T_SYNC_FALL_1MHz_4V											
Test Site											
Tester											
Test Number	17	ns									
Max Limit		ns									
Min Limit											
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.681	8.653	8.784	8.612	8.709	8.621	8.638	8.571	8.610	8.562	8.542
Average	8.758	8.746	8.846	8.699	8.753	8.670	8.670	8.632	8.671	8.671	8.606
Max	8.846	8.825	8.928	8.757	8.795	8.718	8.730	8.720	8.726	8.739	8.714
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

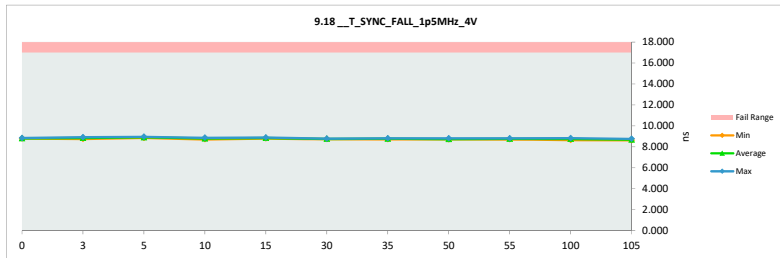


HDR TID Report
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		9.18_T_SYNC_FALL_1p5MHz_4V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	17	17	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.910	8.838	-0.072
0	190	9.029	8.805	-0.224
0	191	8.815	8.793	-0.022
3	300	8.868	8.753	-0.115
3	305	8.900	8.912	0.012
3	309	8.853	8.840	-0.013
3	311	8.853	8.876	0.023
3	313	8.788	8.833	0.045
5	319	8.916	8.955	0.039
5	321	8.871	8.847	-0.024
5	324	8.920	8.855	-0.065
5	327	8.933	8.948	0.015
5	331	8.929	8.917	-0.012
10	234	8.831	8.863	0.032
10	235	8.795	8.754	-0.041
10	236	8.792	8.711	-0.071
10	237	8.948	8.842	-0.106
10	238	8.867	8.790	-0.077
15	239	8.861	8.885	0.024
15	240	8.904	8.839	-0.065
15	241	8.838	8.881	0.043
15	243	8.864	8.813	-0.051
15	245	8.942	8.795	-0.147
30	222	8.938	8.770	-0.168
30	223	8.867	8.779	-0.088
30	225	8.847	8.752	-0.095
30	226	8.811	8.742	-0.069
30	227	8.867	8.740	-0.127
35	228	8.929	8.811	-0.118
35	229	8.840	8.794	-0.046
35	231	8.823	8.714	-0.109
35	232	8.886	8.793	-0.093
35	233	8.919	8.794	-0.125
50	210	8.942	8.710	-0.232
50	211	8.957	8.703	-0.254
50	212	8.971	8.814	-0.157
50	213	8.830	8.724	-0.106
50	214	8.898	8.749	-0.149
55	215	9.128	8.745	-0.383
55	216	8.938	8.777	-0.161
55	217	8.876	8.782	-0.094
55	218	8.852	8.803	-0.049
55	220	8.879	8.718	-0.161
100	247	9.038	8.791	-0.247
100	249	9.011	8.732	-0.279
100	250	8.943	8.653	-0.290
100	251	8.853	8.646	-0.207
100	253	8.956	8.767	-0.189
100	255	8.836	8.680	-0.156
100	256	8.896	8.768	-0.128
100	257	9.001	8.821	-0.180
100	258	8.972	8.818	-0.154
100	259	8.881	8.666	-0.215
100	260	8.948	8.792	-0.156
100	261	8.969	8.766	-0.203
100	262	8.938	8.777	-0.161
100	263	8.900	8.761	-0.139
100	264	8.911	8.758	-0.153
100	265	8.935	8.742	-0.193
100	266	8.930	8.756	-0.174
100	267	8.988	8.820	-0.168
100	268	8.855	8.718	-0.137
100	269	8.918	8.760	-0.158
100	270	8.906	8.751	-0.155
100	271	8.837	8.766	-0.071
100	273	8.946	8.767	-0.179
105	274	8.989	8.759	-0.230
105	275	8.906	8.654	-0.252
105	276	8.859	8.640	-0.219
105	277	8.853	8.623	-0.230
105	278	8.924	8.729	-0.195
Max		9.128	8.955	0.045
Average		8.903	8.778	-0.125
Min		8.782	8.623	-0.383
Std Dev		0.063	0.069	0.090

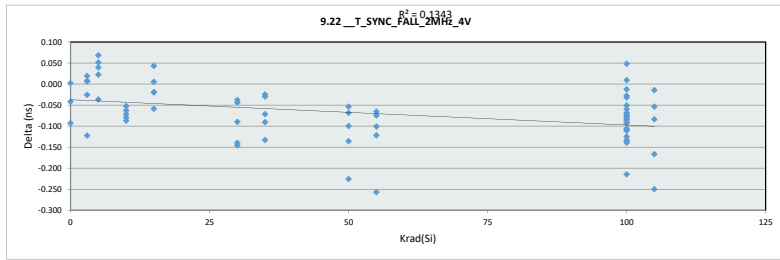


		9.18_T_SYNC_FALL_1p5MHz_4V										
Test Site	Tester											
Test Number	Unit	17	ns									
Max Limit	Min Limit	17	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.793	8.753	8.847	8.711	8.795	8.740	8.714	8.703	8.718	8.646	8.623
Average		8.812	8.843	8.904	8.792	8.843	8.757	8.781	8.740	8.765	8.751	8.681
Max		8.838	8.912	8.955	8.863	8.885	8.779	8.811	8.814	8.803	8.821	8.759
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

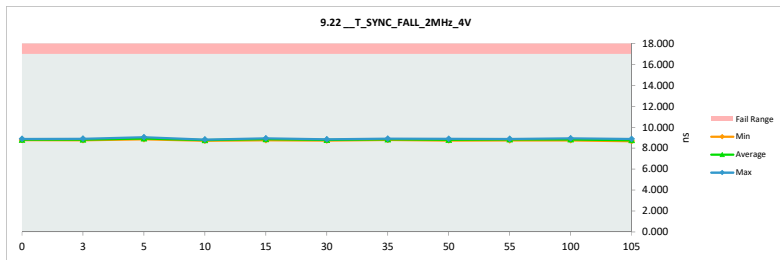


HDR TID Report TPS7H5002-SP

9.22 _T_SYNC_FALL_2MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.796	8.798	0.002
0	190	8.973	8.880	-0.093
0	191	8.808	8.766	-0.042
3	300	8.879	8.756	-0.123
3	305	8.925	8.899	-0.026
3	309	8.783	8.789	0.006
3	311	8.871	8.890	0.019
3	313	8.745	8.752	0.007
5	319	8.927	8.966	0.039
5	321	8.825	8.847	0.022
5	324	8.919	8.882	-0.037
5	327	8.973	9.041	0.068
5	331	8.855	8.906	0.051
10	234	8.859	8.796	-0.063
10	235	8.783	8.730	-0.053
10	236	8.792	8.705	-0.087
10	237	8.867	8.787	-0.080
10	238	8.892	8.820	-0.072
15	239	8.892	8.935	0.043
15	240	8.890	8.870	-0.020
15	241	8.897	8.902	0.005
15	243	8.808	8.749	-0.059
15	245	8.867	8.847	-0.020
30	222	8.984	8.838	-0.146
30	223	8.889	8.845	-0.044
30	225	8.899	8.759	-0.140
30	226	8.808	8.718	-0.090
30	227	8.808	8.770	-0.038
35	228	8.919	8.786	-0.133
35	229	8.882	8.852	-0.030
35	231	8.903	8.831	-0.072
35	232	8.918	8.827	-0.091
35	233	8.933	8.908	-0.025
50	210	9.018	8.792	-0.226
50	211	8.861	8.725	-0.136
50	212	8.993	8.893	-0.100
50	213	8.858	8.789	-0.069
50	214	8.859	8.805	-0.054
55	215	9.138	8.881	-0.257
55	216	8.979	8.878	-0.101
55	217	8.878	8.803	-0.075
55	218	8.934	8.868	-0.066
55	220	8.857	8.735	-0.122
100	247	9.091	8.876	-0.215
100	249	8.960	8.851	-0.109
100	250	8.811	8.779	-0.032
100	251	8.814	8.754	-0.060
100	253	8.909	8.826	-0.083
100	255	8.884	8.748	-0.136
100	256	8.891	8.840	-0.051
100	257	9.012	8.872	-0.140
100	258	8.951	8.826	-0.125
100	259	8.869	8.794	-0.075
100	260	8.926	8.935	0.009
100	261	8.842	8.814	-0.028
100	262	8.919	8.826	-0.093
100	263	8.957	8.824	-0.133
100	264	8.915	8.837	-0.078
100	265	8.927	8.858	-0.069
100	266	8.865	8.913	0.048
100	267	8.981	8.911	-0.070
100	268	8.890	8.798	-0.092
100	269	8.937	8.851	-0.086
100	270	8.953	8.842	-0.111
100	271	8.924	8.818	-0.106
100	273	8.861	8.848	-0.013
105	274	8.959	8.875	-0.084
105	275	8.819	8.804	-0.015
105	276	8.848	8.681	-0.167
105	277	8.923	8.673	-0.250
105	278	8.834	8.780	-0.054
Max		9.138	9.041	0.068
Average		8.896	8.826	-0.070
Min		8.745	8.673	-0.257
Std Dev		0.070	0.067	0.067

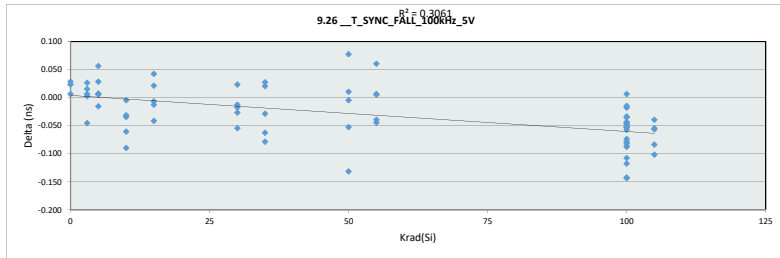


9.22 _T_SYNC_FALL_2MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.766	8.752	8.847	8.705	8.749	8.718	8.786	8.725	8.735	8.748	8.673
Average	8.815	8.817	8.928	8.768	8.861	8.786	8.841	8.801	8.833	8.837	8.763
Max	8.880	8.899	9.041	8.820	8.935	8.845	8.908	8.893	8.881	8.935	8.875
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

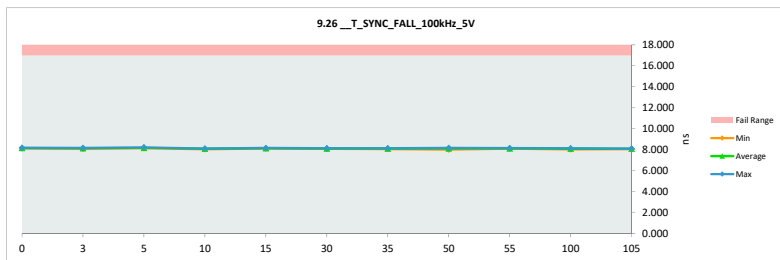


HDR TID Report
TPS7H5002-SP

9.26_T_SYNC_FALL_100kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.141	8.147	0.006
0	190	8.150	8.173	0.023
0	191	8.099	8.127	0.028
3	300	8.118	8.124	0.006
3	305	8.110	8.112	0.002
3	309	8.127	8.081	-0.046
3	311	8.147	8.162	0.015
3	313	8.110	8.136	0.026
5	319	8.153	8.137	-0.016
5	321	8.138	8.166	0.028
5	324	8.157	8.162	0.005
5	327	8.160	8.216	0.056
5	331	8.138	8.145	0.007
10	234	8.165	8.104	-0.061
10	235	8.103	8.071	-0.032
10	236	8.090	8.085	-0.005
10	237	8.126	8.091	-0.035
10	238	8.128	8.038	-0.090
15	239	8.110	8.152	0.042
15	240	8.176	8.134	-0.042
15	241	8.150	8.137	-0.013
15	243	8.123	8.144	0.021
15	245	8.116	8.109	-0.007
30	222	8.150	8.095	-0.055
30	223	8.128	8.115	-0.013
30	225	8.105	8.078	-0.027
30	226	8.100	8.082	-0.018
30	227	8.104	8.127	0.023
35	228	8.154	8.075	-0.079
35	229	8.108	8.128	0.020
35	231	8.128	8.099	-0.029
35	232	8.116	8.053	-0.063
35	233	8.095	8.122	0.027
50	210	8.120	8.067	-0.053
50	211	8.137	8.005	-0.132
50	212	8.085	8.162	0.077
50	213	8.141	8.151	0.010
50	214	8.161	8.156	-0.005
55	215	8.151	8.111	-0.040
55	216	8.139	8.094	-0.045
55	217	8.116	8.121	0.005
55	218	8.133	8.139	0.006
55	220	8.064	8.124	0.060
100	247	8.227	8.109	-0.118
100	249	8.179	8.100	-0.079
100	250	8.159	8.105	-0.054
100	251	8.159	8.016	-0.143
100	253	8.115	8.071	-0.044
100	255	8.109	8.091	-0.018
100	256	8.130	8.085	-0.045
100	257	8.206	8.098	-0.108
100	258	8.148	8.060	-0.088
100	259	8.131	8.078	-0.053
100	260	8.160	8.124	-0.036
100	261	8.162	8.104	-0.058
100	262	8.162	8.088	-0.074
100	263	8.102	8.108	0.006
100	264	8.140	8.088	-0.052
100	265	8.131	8.049	-0.082
100	266	8.085	8.067	-0.018
100	267	8.171	8.084	-0.087
100	268	8.125	8.079	-0.046
100	269	8.150	8.116	-0.034
100	270	8.198	8.054	-0.144
100	271	8.120	8.071	-0.049
100	273	8.112	8.097	-0.015
105	274	8.187	8.085	-0.102
105	275	8.137	8.097	-0.040
105	276	8.122	8.065	-0.057
105	277	8.136	8.052	-0.084
105	278	8.138	8.083	-0.055
	Max	8.227	8.216	0.077
	Average	8.135	8.104	-0.031
	Min	8.064	8.005	-0.144
	Std Dev	0.029	0.038	0.048

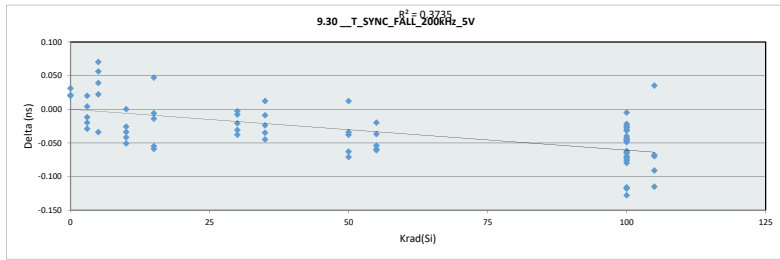


9.26_T_SYNC_FALL_100kHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.127	8.081	8.137	8.038	8.109	8.078	8.053	8.005	8.094	8.016	8.052
Average	8.149	8.123	8.165	8.078	8.135	8.099	8.095	8.108	8.118	8.084	8.076
Max	8.173	8.162	8.216	8.104	8.152	8.127	8.128	8.162	8.139	8.124	8.097
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

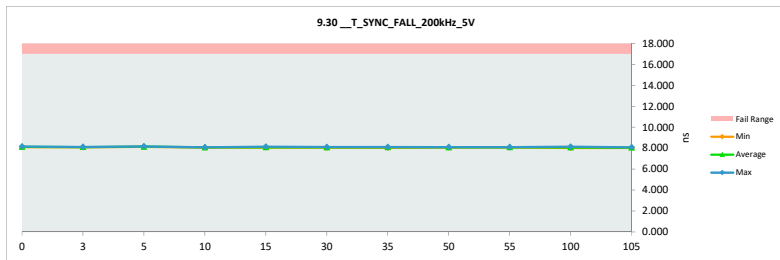


HDR TID Report
TPS7H5002-SP

9.30 _T_SYNC_FALL_200kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.078	8.098	0.020
0	190	8.127	8.158	0.031
0	191	8.082	8.103	0.021
3	300	8.121	8.092	-0.029
3	305	8.120	8.108	-0.012
3	309	8.111	8.115	0.004
3	311	8.095	8.115	0.020
3	313	8.096	8.076	-0.020
5	319	8.093	8.163	0.070
5	321	8.094	8.133	0.039
5	324	8.110	8.166	0.056
5	327	8.160	8.126	-0.034
5	331	8.111	8.133	0.022
10	234	8.124	8.082	-0.042
10	235	8.113	8.079	-0.034
10	236	8.055	8.055	0.000
10	237	8.097	8.071	-0.026
10	238	8.133	8.082	-0.051
15	239	8.115	8.056	-0.059
15	240	8.092	8.139	0.047
15	241	8.086	8.080	-0.006
15	243	8.129	8.074	-0.055
15	245	8.104	8.090	-0.014
30	222	8.084	8.046	-0.038
30	223	8.093	8.085	-0.008
30	225	8.083	8.052	-0.031
30	226	8.085	8.082	-0.003
30	227	8.129	8.108	-0.021
35	228	8.100	8.076	-0.024
35	229	8.097	8.109	0.012
35	231	8.045	8.036	-0.009
35	232	8.132	8.087	-0.045
35	233	8.144	8.109	-0.035
50	210	8.136	8.065	-0.071
50	211	8.092	8.054	-0.038
50	212	8.151	8.088	-0.063
50	213	8.068	8.080	0.012
50	214	8.134	8.100	-0.034
55	215	8.157	8.098	-0.059
55	216	8.148	8.087	-0.061
55	217	8.118	8.081	-0.037
55	218	8.119	8.099	-0.020
55	220	8.113	8.059	-0.054
100	247	8.185	8.069	-0.116
100	249	8.203	8.075	-0.128
100	250	8.123	8.061	-0.062
100	251	8.118	8.053	-0.065
100	253	8.109	8.077	-0.032
100	255	8.150	8.074	-0.076
100	256	8.135	8.017	-0.118
100	257	8.175	8.135	-0.040
100	258	8.114	8.069	-0.045
100	259	8.099	8.050	-0.049
100	260	8.120	8.049	-0.071
100	261	8.122	8.048	-0.074
100	262	8.122	8.117	-0.005
100	263	8.125	8.080	-0.045
100	264	8.104	8.077	-0.027
100	265	8.136	8.114	-0.022
100	266	8.115	8.072	-0.043
100	267	8.108	8.083	-0.025
100	268	8.125	8.060	-0.065
100	269	8.180	8.110	-0.070
100	270	8.117	8.037	-0.080
100	271	8.112	8.065	-0.047
100	273	8.119	8.089	-0.030
105	274	8.176	8.061	-0.115
105	275	8.155	8.087	-0.068
105	276	8.043	8.078	0.035
105	277	8.091	8.021	-0.070
105	278	8.122	8.031	-0.091
	Max	8.203	8.166	0.070
	Average	8.117	8.084	-0.033
	Min	8.043	8.017	-0.128
	Std Dev	0.030	0.031	0.041

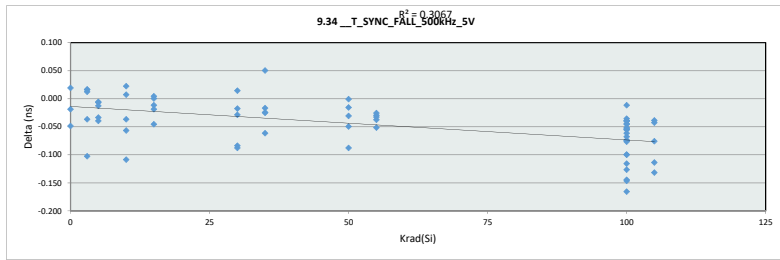


9.30 _T_SYNC_FALL_200kHz_5V											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.098	8.076	8.126	8.055	8.056	8.046	8.036	8.054	8.059	8.017	8.021
Average	8.120	8.101	8.144	8.074	8.088	8.075	8.083	8.077	8.085	8.073	8.056
Max	8.158	8.115	8.166	8.082	8.139	8.108	8.109	8.100	8.099	8.135	8.087
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

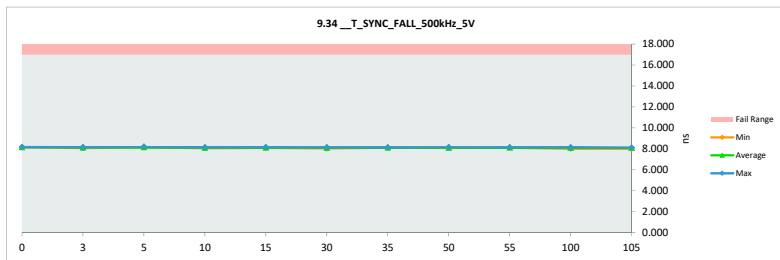


HDR TID Report
TPS7H5002-SP

9.34_T_SYNC_FALL_500kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.168	8.149	-0.019
0	190	8.207	8.158	-0.049
0	191	8.140	8.159	0.019
3	300	8.178	8.075	-0.103
3	305	8.136	8.152	0.016
3	309	8.126	8.138	0.012
3	311	8.170	8.133	-0.037
3	313	8.119	8.135	0.016
5	319	8.171	8.131	-0.040
5	321	8.174	8.140	-0.034
5	324	8.164	8.157	-0.007
5	327	8.197	8.191	-0.006
5	331	8.154	8.141	-0.013
10	234	8.106	8.128	0.022
10	235	8.114	8.057	-0.057
10	236	8.210	8.101	-0.109
10	237	8.131	8.138	0.007
10	238	8.139	8.102	-0.037
15	239	8.123	8.111	-0.012
15	240	8.114	8.118	0.004
15	241	8.152	8.152	0.000
15	243	8.111	8.092	-0.019
15	245	8.144	8.098	-0.046
30	222	8.133	8.104	-0.029
30	223	8.118	8.100	-0.018
30	225	8.199	8.111	-0.088
30	226	8.120	8.036	-0.084
30	227	8.116	8.130	0.014
35	228	8.138	8.121	-0.017
35	229	8.074	8.124	0.050
35	231	8.143	8.118	-0.025
35	232	8.166	8.104	-0.062
35	233	8.129	8.104	-0.025
50	210	8.135	8.085	-0.050
50	211	8.127	8.096	-0.031
50	212	8.155	8.154	-0.001
50	213	8.102	8.086	-0.016
50	214	8.172	8.084	-0.088
55	215	8.187	8.135	-0.052
55	216	8.163	8.137	-0.026
55	217	8.167	8.134	-0.033
55	218	8.150	8.120	-0.030
55	220	8.137	8.099	-0.038
100	247	8.215	8.099	-0.116
100	249	8.182	8.082	-0.100
100	250	8.147	8.047	-0.100
100	251	8.107	8.071	-0.036
100	253	8.129	8.089	-0.040
100	255	8.150	8.075	-0.075
100	256	8.145	8.099	-0.046
100	257	8.204	8.038	-0.166
100	258	8.212	8.144	-0.068
100	259	8.118	8.044	-0.074
100	260	8.164	8.087	-0.077
100	261	8.156	8.100	-0.056
100	262	8.170	8.130	-0.040
100	263	8.143	8.091	-0.052
100	264	8.128	8.116	-0.012
100	265	8.169	8.107	-0.062
100	266	8.187	8.040	-0.147
100	267	8.242	8.115	-0.127
100	268	8.155	8.010	-0.145
100	269	8.175	8.130	-0.045
100	270	8.145	8.091	-0.054
100	271	8.152	8.112	-0.040
100	273	8.109	8.054	-0.055
105	274	8.155	8.112	-0.043
105	275	8.165	8.089	-0.076
105	276	8.216	8.084	-0.132
105	277	8.125	8.011	-0.114
105	278	8.134	8.095	-0.039
Max		8.242	8.191	0.050
Average		8.152	8.106	-0.046
Min		8.074	8.010	-0.166
Std Dev		0.032	0.036	0.044

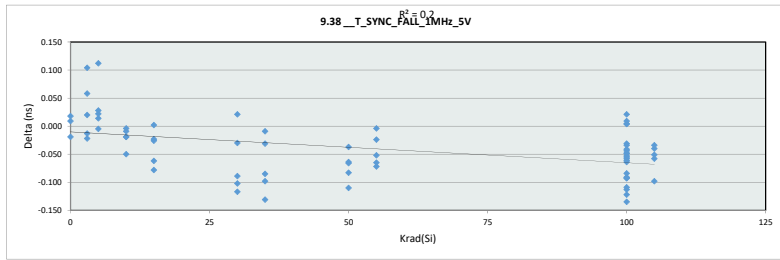


9.34_T_SYNC_FALL_500kHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.149	8.075	8.131	8.057	8.092	8.036	8.104	8.084	8.099	8.010	8.011
Average	8.155	8.127	8.152	8.105	8.114	8.096	8.114	8.101	8.125	8.086	8.078
Max	8.159	8.152	8.191	8.138	8.152	8.130	8.124	8.154	8.137	8.144	8.112
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

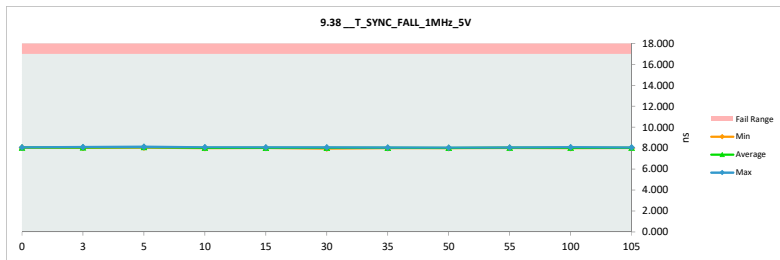


HDR TID Report
TPS7H5002-SP

9.38 _T_SYNC_FALL_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.045	8.063	0.018
0	190	8.066	8.047	-0.019
0	191	8.066	8.075	0.009
3	300	8.010	8.030	0.020
3	305	8.104	8.091	-0.013
3	309	8.049	8.027	-0.022
3	311	8.032	8.090	0.058
3	313	8.006	8.110	0.104
5	319	8.058	8.053	-0.005
5	321	8.018	8.130	0.112
5	324	8.085	8.107	0.022
5	327	8.122	8.136	0.014
5	331	8.061	8.089	0.028
10	234	8.045	8.036	-0.009
10	235	8.089	8.039	-0.050
10	236	8.023	8.004	-0.019
10	237	8.036	8.032	-0.004
10	238	8.104	8.085	-0.019
15	239	8.047	8.049	0.002
15	240	8.097	8.074	-0.023
15	241	8.098	8.020	-0.078
15	243	8.083	8.021	-0.062
15	245	8.038	8.012	-0.026
30	222	8.012	8.033	0.021
30	223	8.168	8.079	-0.089
30	225	8.072	7.970	-0.102
30	226	8.141	8.024	-0.117
30	227	8.060	8.030	-0.030
35	228	8.118	8.033	-0.085
35	229	8.055	8.024	-0.031
35	231	8.094	7.996	-0.098
35	232	8.047	8.038	-0.009
35	233	8.192	8.061	-0.131
50	210	8.091	8.008	-0.083
50	211	8.072	8.035	-0.037
50	212	8.102	8.038	-0.064
50	213	8.115	8.005	-0.110
50	214	8.113	8.047	-0.066
55	215	8.070	8.066	-0.004
55	216	8.118	8.053	-0.065
55	217	8.074	8.022	-0.052
55	218	8.075	8.051	-0.024
55	220	8.096	8.024	-0.072
100	247	8.175	8.082	-0.093
100	249	8.136	8.023	-0.113
100	250	8.087	8.029	-0.058
100	251	8.124	8.002	-0.122
100	253	8.022	8.026	0.004
100	255	8.096	8.035	-0.061
100	256	8.063	8.015	-0.048
100	257	8.040	8.061	0.021
100	258	8.091	8.050	-0.041
100	259	8.123	8.032	-0.091
100	260	8.093	8.040	-0.053
100	261	8.118	8.009	-0.109
100	262	8.143	8.094	-0.049
100	263	8.083	8.028	-0.055
100	264	8.080	8.089	0.009
100	265	8.084	8.020	-0.064
100	266	8.084	8.050	-0.034
100	267	8.104	8.020	-0.084
100	268	8.052	8.021	-0.031
100	269	8.062	8.066	0.004
100	270	8.132	7.997	-0.135
100	271	8.109	8.065	-0.044
100	273	8.102	8.010	-0.092
105	274	8.105	8.071	-0.034
105	275	8.080	8.022	-0.058
105	276	8.077	8.037	-0.040
105	277	8.081	8.030	-0.051
105	278	8.134	8.036	-0.098
	Max	8.192	8.136	0.112
	Average	8.084	8.044	-0.040
	Min	8.006	7.970	-0.135
	Std Dev	0.039	0.032	0.050

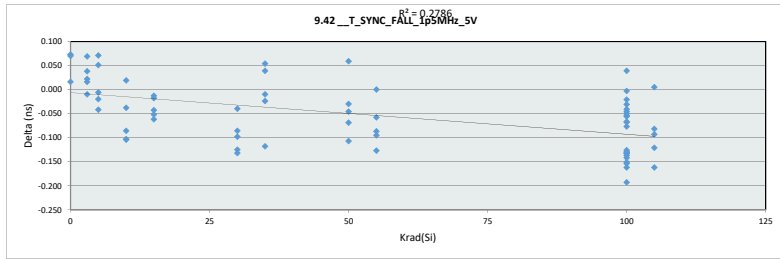


9.38 _T_SYNC_FALL_1MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.047	8.027	8.053	8.004	8.012	7.970	7.996	8.005	8.022	7.997	8.022
Average	8.062	8.070	8.103	8.039	8.035	8.027	8.030	8.027	8.043	8.038	8.039
Max	8.075	8.110	8.136	8.085	8.074	8.079	8.061	8.047	8.066	8.094	8.071
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

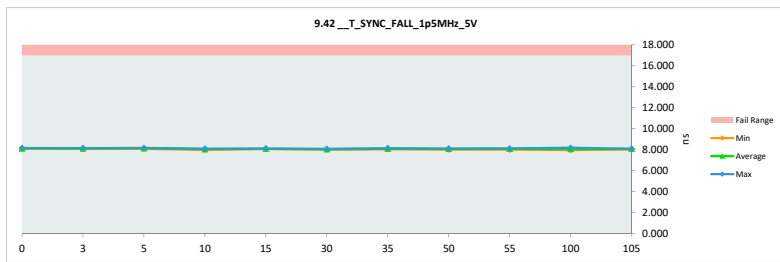


HDR TID Report
TPS7H5002-SP

		9.42_T_SYNC_FALL_1p5MHz_5V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	17	17	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.071	8.087	0.016
0	190	8.085	8.158	0.073
0	191	8.044	8.114	0.070
3	300	8.083	8.099	0.016
3	305	8.110	8.132	0.022
3	309	8.149	8.139	-0.010
3	311	8.045	8.114	0.069
3	313	8.062	8.100	0.038
5	319	8.131	8.111	-0.020
5	321	8.085	8.079	-0.006
5	324	8.069	8.140	0.071
5	327	8.199	8.157	-0.042
5	331	8.112	8.163	0.051
10	234	8.061	8.023	-0.038
10	235	8.073	8.092	0.019
10	236	8.083	7.979	-0.104
10	237	8.150	8.064	-0.086
10	238	8.168	8.065	-0.103
15	239	8.109	8.066	-0.043
15	240	8.123	8.061	-0.062
15	241	8.129	8.111	-0.018
15	243	8.128	8.076	-0.052
15	245	8.127	8.114	-0.013
30	222	8.152	8.066	-0.086
30	223	8.097	8.057	-0.040
30	225	8.127	7.995	-0.132
30	226	8.172	8.047	-0.125
30	227	8.133	8.035	-0.098
35	228	8.096	8.086	-0.010
35	229	8.153	8.035	-0.118
35	231	8.075	8.129	0.054
35	232	8.058	8.034	-0.024
35	233	8.106	8.145	0.039
50	210	8.077	8.008	-0.069
50	211	8.106	8.060	-0.046
50	212	8.182	8.075	-0.107
50	213	8.116	8.086	-0.030
50	214	8.049	8.108	0.059
55	215	8.212	8.117	-0.095
55	216	8.154	8.067	-0.087
55	217	8.147	8.089	-0.058
55	218	8.094	8.094	0.000
55	220	8.126	7.999	-0.127
100	247	8.212	8.075	-0.137
100	249	8.095	8.064	-0.031
100	250	8.111	8.070	-0.041
100	251	8.146	8.013	-0.133
100	253	8.126	8.049	-0.077
100	255	8.107	8.061	-0.046
100	256	8.139	8.009	-0.130
100	257	8.219	8.026	-0.193
100	258	8.148	8.127	-0.021
100	259	8.108	7.982	-0.126
100	260	8.122	8.054	-0.068
100	261	8.114	7.952	-0.162
100	262	8.240	8.173	-0.067
100	263	8.172	8.042	-0.130
100	264	8.181	8.039	-0.142
100	265	8.118	8.062	-0.056
100	266	8.151	8.101	-0.050
100	267	8.108	8.105	-0.003
100	268	8.148	7.994	-0.154
100	269	8.074	8.113	0.039
100	270	8.142	8.009	-0.133
100	271	8.182	8.031	-0.151
100	273	8.104	8.050	-0.054
105	274	8.157	8.075	-0.082
105	275	8.178	8.057	-0.121
105	276	8.082	8.087	0.005
105	277	8.228	8.066	-0.162
105	278	8.114	8.021	-0.093
	Max	8.240	8.173	0.073
	Average	8.125	8.072	-0.053
	Min	8.044	7.952	-0.193
	Std Dev	0.045	0.048	0.067

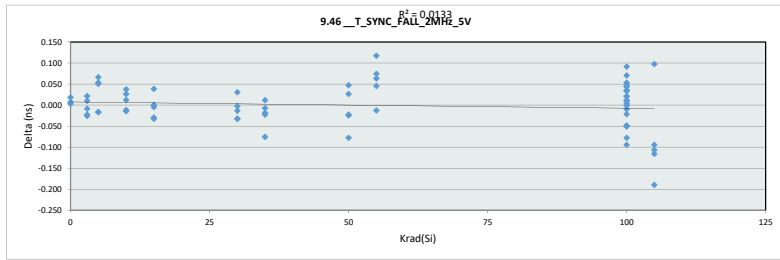


		9.42_T_SYNC_FALL_1p5MHz_5V										
Test Site	Tester											
Test Number	Unit	17	ns									
Max Limit	Min Limit	17	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.087	8.099	8.079	7.979	8.061	7.995	8.034	8.008	7.999	7.952	8.021
Average		8.120	8.117	8.130	8.045	8.086	8.040	8.086	8.067	8.073	8.052	8.061
Max		8.158	8.139	8.163	8.092	8.114	8.066	8.145	8.108	8.117	8.173	8.087
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

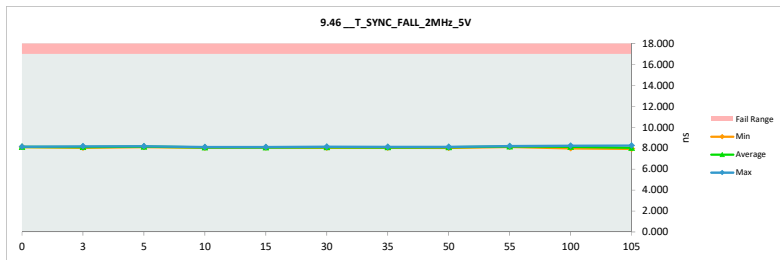


HDR TID Report
TPS7H5002-SP

9.46 __T_SYNC_FALL_2MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.102	8.120	0.018
0	190	8.136	8.139	0.003
0	191	8.094	8.101	0.007
3	300	8.095	8.069	-0.026
3	305	8.154	8.175	0.021
3	309	8.135	8.145	0.010
3	311	8.083	8.074	-0.009
3	313	8.073	8.050	-0.023
5	319	8.138	8.121	-0.017
5	321	8.128	8.111	-0.017
5	324	8.105	8.158	0.053
5	327	8.132	8.182	0.050
5	331	8.112	8.178	0.066
10	234	8.018	8.044	0.026
10	235	8.078	8.066	-0.012
10	236	8.080	8.092	0.012
10	237	8.104	8.089	-0.015
10	238	8.070	8.107	0.037
15	239	8.066	8.104	0.038
15	240	8.073	8.073	0.000
15	241	8.108	8.078	-0.030
15	243	8.085	8.080	-0.005
15	245	8.088	8.055	-0.033
30	222	8.111	8.078	-0.033
30	223	8.143	8.140	-0.003
30	225	8.081	8.111	0.030
30	226	8.105	8.091	-0.014
30	227	8.077	8.044	-0.033
35	228	8.112	8.123	0.011
35	229	8.107	8.088	-0.019
35	231	8.108	8.085	-0.023
35	232	8.122	8.046	-0.076
35	233	8.084	8.076	-0.008
50	210	8.113	8.035	-0.078
50	211	8.082	8.108	0.026
50	212	8.128	8.105	-0.023
50	213	8.071	8.118	0.047
50	214	8.099	8.074	-0.025
55	215	8.130	8.175	0.045
55	216	8.088	8.162	0.074
55	217	8.130	8.117	-0.013
55	218	8.102	8.165	0.063
55	220	8.080	8.197	0.117
100	247	8.208	8.198	-0.010
100	249	8.180	8.200	0.020
100	250	8.115	8.093	-0.022
100	251	8.109	8.118	0.009
100	253	8.158	8.161	0.003
100	255	8.123	8.167	0.044
100	256	8.110	8.121	0.011
100	257	8.153	8.186	0.033
100	258	8.143	8.234	0.091
100	259	8.085	7.990	-0.095
100	260	8.114	8.113	-0.001
100	261	8.174	8.125	-0.049
100	262	8.130	8.151	0.021
100	263	8.209	8.158	-0.051
100	264	8.093	8.127	0.034
100	265	8.217	8.139	-0.078
100	266	8.127	8.162	0.035
100	267	8.146	8.190	0.044
100	268	8.086	8.139	0.053
100	269	8.132	8.202	0.070
100	270	8.121	8.120	-0.001
100	271	8.116	8.164	0.048
100	273	8.109	8.059	-0.050
105	274	8.151	8.248	0.097
105	275	8.098	7.982	-0.116
105	276	8.130	7.940	-0.190
105	277	8.122	8.027	-0.095
105	278	8.076	7.969	-0.107
	Max	8.217	8.248	0.117
	Average	8.114	8.113	0.000
	Min	8.018	7.940	-0.190
	Std Dev	0.035	0.060	0.053

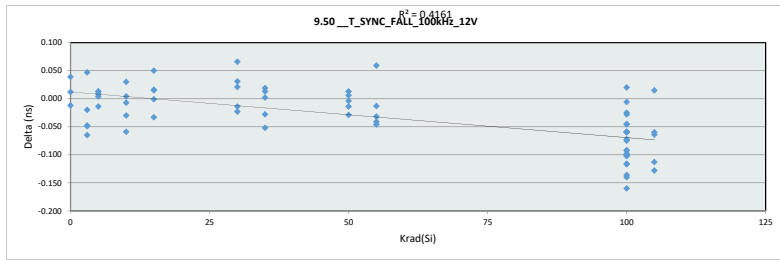


9.46 __T_SYNC_FALL_2MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.101	8.050	8.111	8.044	8.055	8.044	8.046	8.035	8.117	7.990	7.940
Average	8.120	8.103	8.150	8.080	8.078	8.093	8.084	8.088	8.163	8.144	8.033
Max	8.139	8.175	8.182	8.107	8.104	8.140	8.123	8.118	8.197	8.234	8.248
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

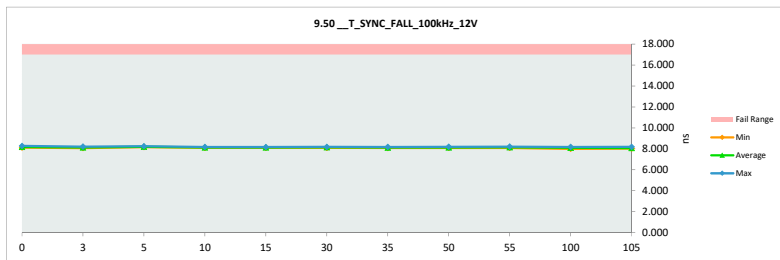


HDR TID Report TPS7H5002-SP

		9.50_T_SYNC_FALL_100kHz_12V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	17	17	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.152	8.140	-0.012
0	190	8.238	8.277	0.039
0	191	8.109	8.121	0.012
3	300	8.182	8.117	-0.065
3	305	8.256	8.207	-0.049
3	309	8.095	8.075	-0.020
3	311	8.147	8.099	-0.048
3	313	8.104	8.151	0.047
5	319	8.176	8.162	-0.014
5	321	8.245	8.249	0.004
5	324	8.159	8.168	0.009
5	327	8.190	8.203	0.013
5	331	8.199	8.212	0.013
10	234	8.149	8.179	0.030
10	235	8.147	8.151	0.004
10	236	8.106	8.099	-0.007
10	237	8.155	8.096	-0.059
10	238	8.183	8.153	-0.030
15	239	8.093	8.092	-0.001
15	240	8.163	8.130	-0.033
15	241	8.101	8.116	0.015
15	243	8.147	8.163	0.016
15	245	8.124	8.174	0.050
30	222	8.154	8.185	0.031
30	223	8.120	8.097	-0.023
30	225	8.166	8.152	-0.014
30	226	8.102	8.168	0.066
30	227	8.105	8.126	0.021
35	228	8.106	8.078	-0.028
35	229	8.156	8.175	0.019
35	231	8.156	8.158	0.002
35	232	8.157	8.105	-0.052
35	233	8.104	8.117	0.013
50	210	8.114	8.085	-0.029
50	211	8.106	8.112	0.006
50	212	8.161	8.147	-0.014
50	213	8.112	8.125	0.013
50	214	8.196	8.192	-0.004
55	215	8.173	8.160	-0.013
55	216	8.169	8.128	-0.041
55	217	8.208	8.162	-0.046
55	218	8.150	8.209	0.059
55	220	8.119	8.087	-0.032
100	247	8.263	8.147	-0.116
100	249	8.168	8.093	-0.075
100	250	8.225	8.065	-0.160
100	251	8.120	8.021	-0.099
100	253	8.137	8.109	-0.028
100	255	8.174	8.057	-0.117
100	256	8.197	8.095	-0.102
100	257	8.163	8.071	-0.092
100	258	8.183	8.111	-0.072
100	259	8.153	8.094	-0.059
100	260	8.207	8.147	-0.060
100	261	8.155	8.175	0.020
100	262	8.180	8.155	-0.025
100	263	8.117	8.071	-0.046
100	264	8.203	8.158	-0.045
100	265	8.163	8.071	-0.092
100	266	8.150	8.090	-0.060
100	267	8.182	8.046	-0.136
100	268	8.142	8.040	-0.102
100	269	8.170	8.111	-0.059
100	270	8.138	7.998	-0.140
100	271	8.152	8.146	-0.006
100	273	8.220	8.122	-0.098
105	274	8.158	8.045	-0.113
105	275	8.169	8.041	-0.128
105	276	8.122	8.062	-0.060
105	277	8.092	8.028	-0.064
105	278	8.171	8.186	0.015
	Max	8.263	8.277	0.066
	Average	8.157	8.125	-0.032
	Min	8.092	7.998	-0.160
	Std Dev	0.040	0.054	0.052



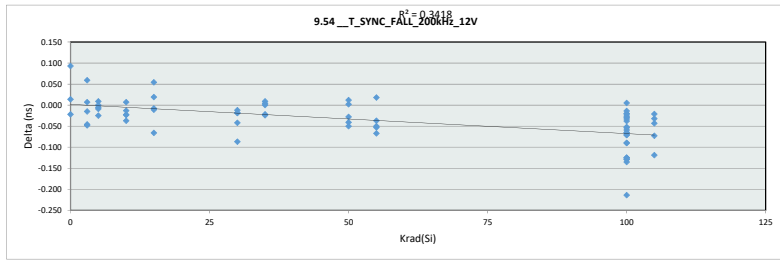
		9.50_T_SYNC_FALL_100kHz_12V										
Test Site	Tester											
Test Number	Unit	17	ns									
Max Limit	Min Limit	17	ns									
LL	UL	0	3	5	10	15	30	35	50	55	100	105
Min	8.121	8.075	8.162	8.096	8.092	8.097	8.078	8.085	8.087	7.998	8.028	
Average	8.179	8.130	8.199	8.136	8.135	8.146	8.127	8.132	8.149	8.095	8.072	
Max	8.277	8.207	8.249	8.179	8.174	8.185	8.175	8.192	8.209	8.175	8.186	
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	



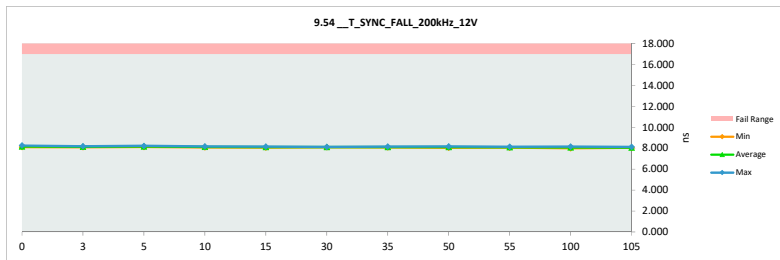
HDR TID Report
TPS7H5002-SP

9.54_T_SYNC_FALL_200kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.045	8.138	0.093
0	190	8.239	8.253	0.014
0	191	8.121	8.099	-0.022
3	300	8.160	8.112	-0.048
3	305	8.224	8.178	-0.046
3	309	8.082	8.089	0.007
3	311	8.098	8.157	0.059
3	313	8.142	8.127	-0.015
5	319	8.134	8.109	-0.025
5	321	8.229	8.220	-0.009
5	324	8.138	8.147	0.009
5	327	8.152	8.152	0.000
5	331	8.204	8.199	-0.005
10	234	8.166	8.153	-0.013
10	235	8.156	8.163	0.007
10	236	8.105	8.082	-0.023
10	237	8.130	8.107	-0.023
10	238	8.164	8.127	-0.037
15	239	8.115	8.107	-0.008
15	240	8.097	8.151	0.054
15	241	8.038	8.057	0.019
15	243	8.149	8.083	-0.066
15	245	8.147	8.136	-0.011
30	222	8.138	8.096	-0.042
30	223	8.121	8.102	-0.019
30	225	8.182	8.095	-0.087
30	226	8.137	8.125	-0.012
30	227	8.109	8.091	-0.018
35	228	8.062	8.071	0.009
35	229	8.155	8.155	0.000
35	231	8.120	8.096	-0.024
35	232	8.094	8.098	0.004
35	233	8.128	8.106	-0.022
50	210	8.116	8.075	-0.041
50	211	8.099	8.049	-0.050
50	212	8.149	8.151	0.002
50	213	8.079	8.091	0.012
50	214	8.192	8.164	-0.028
55	215	8.146	8.096	-0.050
55	216	8.151	8.084	-0.067
55	217	8.145	8.108	-0.037
55	218	8.115	8.133	0.018
55	220	8.105	8.052	-0.053
100	247	8.234	8.109	-0.125
100	249	8.105	8.085	-0.020
100	250	8.158	8.068	-0.090
100	251	8.106	8.035	-0.071
100	253	8.133	8.067	-0.066
100	255	8.123	8.097	-0.026
100	256	8.171	8.150	-0.021
100	257	8.104	8.109	0.005
100	258	8.131	8.117	-0.014
100	259	8.136	8.046	-0.090
100	260	8.201	7.987	-0.214
100	261	8.171	8.112	-0.059
100	262	8.126	8.088	-0.038
100	263	8.146	8.081	-0.065
100	264	8.252	8.117	-0.135
100	265	8.170	8.045	-0.125
100	266	8.159	8.030	-0.129
100	267	8.176	8.105	-0.071
100	268	8.109	8.055	-0.054
100	269	8.157	8.128	-0.029
100	270	8.129	8.001	-0.128
100	271	8.136	8.103	-0.033
100	273	8.158	8.106	-0.052
105	274	8.069	8.037	-0.032
105	275	8.156	8.037	-0.119
105	276	8.125	8.052	-0.073
105	277	8.076	8.055	-0.021
105	278	8.165	8.122	-0.043
	Max	8.252	8.253	0.093
	Average	8.139	8.103	-0.036
	Min	8.038	7.987	-0.214
	Std Dev	0.043	0.047	0.049



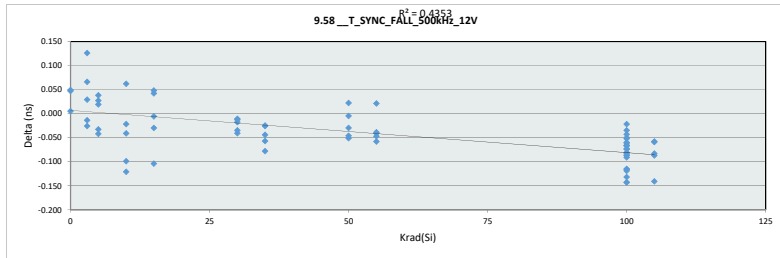
9.54_T_SYNC_FALL		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.099	8.089	8.109	8.082	8.057	8.091	8.071	8.049	8.052	7.987	8.037
Average		8.163	8.133	8.165	8.126	8.107	8.102	8.105	8.106	8.095	8.080	8.061
Max		8.253	8.178	8.220	8.163	8.151	8.125	8.155	8.164	8.133	8.150	8.122
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



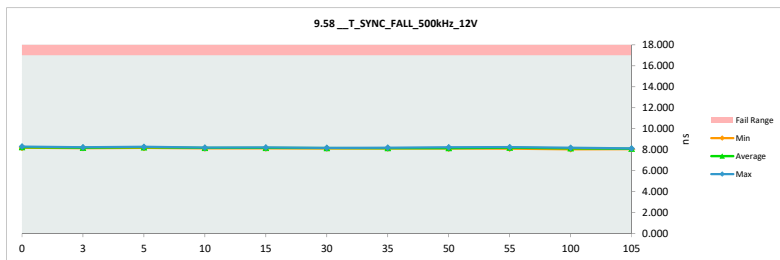
HDR TID Report TPS7H5002-SP

9.58_T_SYNC_FALL_500kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(S)	Serial #	PRE	POST	Delta
0	189	8.164	8.213	0.049
0	190	8.247	8.294	0.047
0	191	8.177	8.182	0.005
3	300	8.187	8.161	-0.026
3	305	8.235	8.221	-0.014
3	309	8.016	8.142	0.126
3	311	8.147	8.213	0.066
3	313	8.139	8.168	0.029
5	319	8.134	8.161	0.027
5	321	8.268	8.226	-0.042
5	324	8.263	8.230	-0.033
5	327	8.203	8.222	0.019
5	331	8.231	8.269	0.038
10	234	8.294	8.173	-0.121
10	235	8.215	8.193	-0.022
10	236	8.112	8.174	0.062
10	237	8.213	8.114	-0.099
10	238	8.199	8.158	-0.041
15	239	8.143	8.113	-0.030
15	240	8.175	8.169	-0.006
15	241	8.123	8.171	0.048
15	243	8.166	8.208	0.042
15	245	8.250	8.146	-0.104
30	222	8.148	8.130	-0.018
30	223	8.153	8.112	-0.041
30	225	8.204	8.169	-0.035
30	226	8.161	8.148	-0.013
30	227	8.170	8.159	-0.011
35	228	8.187	8.109	-0.078
35	229	8.210	8.185	-0.025
35	231	8.158	8.132	-0.026
35	232	8.207	8.150	-0.057
35	233	8.181	8.137	-0.044
50	210	8.156	8.110	-0.046
50	211	8.128	8.098	-0.030
50	212	8.173	8.168	-0.005
50	213	8.166	8.115	-0.051
50	214	8.199	8.221	0.022
55	215	8.219	8.240	0.021
55	216	8.237	8.178	-0.039
55	217	8.233	8.186	-0.047
55	218	8.281	8.240	-0.041
55	220	8.154	8.096	-0.058
100	247	8.253	8.134	-0.119
100	249	8.209	8.128	-0.081
100	250	8.198	8.138	-0.060
100	251	8.176	8.033	-0.143
100	253	8.169	8.102	-0.067
100	255	8.177	8.086	-0.091
100	256	8.229	8.177	-0.052
100	257	8.214	8.098	-0.116
100	258	8.248	8.162	-0.086
100	259	8.172	8.090	-0.082
100	260	8.219	8.087	-0.132
100	261	8.239	8.096	-0.143
100	262	8.205	8.170	-0.035
100	263	8.176	8.103	-0.073
100	264	8.266	8.179	-0.087
100	265	8.202	8.136	-0.066
100	266	8.179	8.157	-0.022
100	267	8.233	8.118	-0.115
100	268	8.170	8.127	-0.043
100	269	8.197	8.147	-0.050
100	270	8.167	8.101	-0.066
100	271	8.187	8.113	-0.074
100	273	8.214	8.153	-0.061
105	274	8.228	8.087	-0.141
105	275	8.136	8.049	-0.087
105	276	8.166	8.108	-0.058
105	277	8.121	8.062	-0.059
105	278	8.196	8.113	-0.083
	Max	8.294	8.294	0.126
	Average	8.191	8.150	-0.041
	Min	8.016	8.033	-0.143
	Std Dev	0.046	0.051	0.055

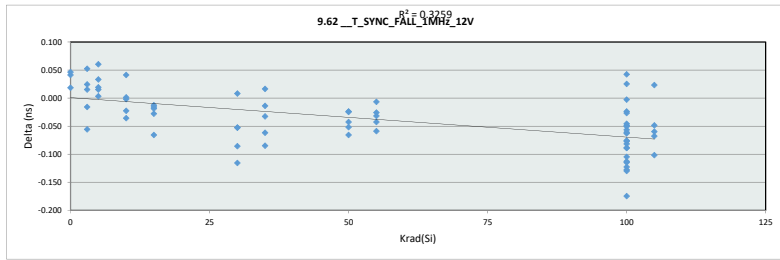


9.58_T_SYNC_FALL_500kHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.182	8.142	8.161	8.114	8.113	8.112	8.109	8.098	8.096	8.033	8.049
Average	8.230	8.181	8.222	8.162	8.161	8.144	8.143	8.142	8.188	8.123	8.084
Max	8.294	8.221	8.269	8.193	8.208	8.169	8.185	8.221	8.240	8.179	8.113
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

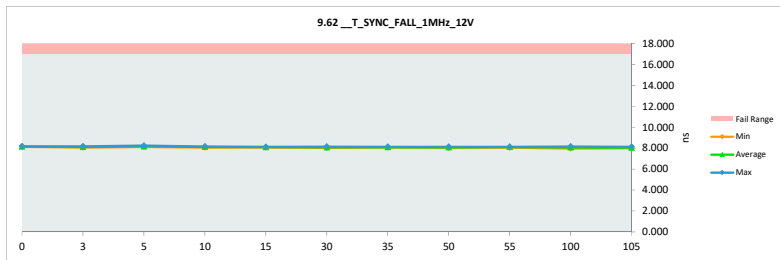


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		9.62 _T_SYNC_FALL_1MHz_12V		
Test Site				
Tester				
Test Number				
Unit		ns	ns	
Max Limit		17	17	
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.120	8.138	0.018
0	190	8.140	8.186	0.046
0	191	8.095	8.136	0.041
3	300	8.118	8.062	-0.056
3	305	8.152	8.176	0.024
3	309	8.086	8.070	-0.016
3	311	8.090	8.142	0.052
3	313	8.093	8.108	0.015
5	319	8.106	8.121	0.015
5	321	8.187	8.247	0.060
5	324	8.129	8.132	0.003
5	327	8.161	8.180	0.019
5	331	8.161	8.194	0.033
10	234	8.125	8.166	0.041
10	235	8.147	8.124	-0.023
10	236	8.062	8.063	0.001
10	237	8.110	8.108	-0.002
10	238	8.151	8.115	-0.036
15	239	8.082	8.069	-0.013
15	240	8.135	8.119	-0.016
15	241	8.110	8.091	-0.019
15	243	8.161	8.095	-0.066
15	245	8.145	8.117	-0.028
30	222	8.162	8.046	-0.116
30	223	8.119	8.033	-0.086
30	225	8.149	8.096	-0.053
30	226	8.142	8.150	0.008
30	227	8.131	8.078	-0.053
35	228	8.059	8.075	0.016
35	229	8.143	8.129	-0.014
35	231	8.166	8.081	-0.085
35	232	8.126	8.093	-0.033
35	233	8.151	8.089	-0.062
50	210	8.103	8.037	-0.066
50	211	8.088	8.063	-0.025
50	212	8.161	8.118	-0.043
50	213	8.098	8.046	-0.052
50	214	8.156	8.132	-0.024
55	215	8.123	8.097	-0.026
55	216	8.131	8.124	-0.007
55	217	8.171	8.128	-0.043
55	218	8.166	8.134	-0.032
55	220	8.109	8.050	-0.059
100	247	8.213	8.090	-0.123
100	249	8.180	8.075	-0.105
100	250	8.146	8.057	-0.089
100	251	8.125	7.997	-0.128
100	253	8.093	8.069	-0.024
100	255	8.123	8.074	-0.049
100	256	8.139	8.062	-0.077
100	257	8.123	8.120	-0.003
100	258	8.159	8.077	-0.082
100	259	8.110	8.047	-0.063
100	260	8.201	8.086	-0.115
100	261	8.209	8.096	-0.113
100	262	8.141	8.166	0.025
100	263	8.133	8.044	-0.089
100	264	8.197	8.140	-0.057
100	265	8.206	8.031	-0.175
100	266	8.097	8.139	0.042
100	267	8.119	8.068	-0.051
100	268	8.154	8.093	-0.061
100	269	8.152	8.106	-0.046
100	270	8.097	7.967	-0.130
100	271	8.177	8.101	-0.076
100	273	8.151	8.124	-0.027
105	274	8.094	8.026	-0.068
105	275	8.096	7.994	-0.102
105	276	8.041	8.064	0.023
105	277	8.079	8.019	-0.060
105	278	8.182	8.133	-0.049
	Max	8.213	8.247	0.060
	Average	8.133	8.096	-0.037
	Min	8.041	7.967	-0.175
	Std Dev	0.037	0.050	0.050



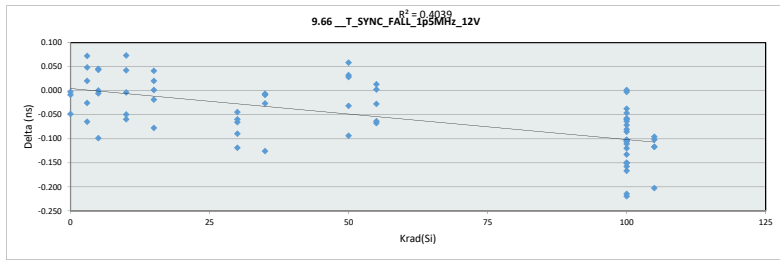
		9.62 _T_SYNC_FALL_1MHz_12V										
Test Site												
Tester												
Test Number		17	ns									
Max Limit		17	ns									
Min Limit												
LL	Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
Min		8.136	8.062	8.121	8.063	8.069	8.033	8.075	8.037	8.050	7.967	7.994
Average		8.153	8.112	8.175	8.115	8.098	8.081	8.093	8.079	8.107	8.080	8.047
Max		8.186	8.176	8.247	8.166	8.119	8.150	8.129	8.132	8.134	8.166	8.133
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



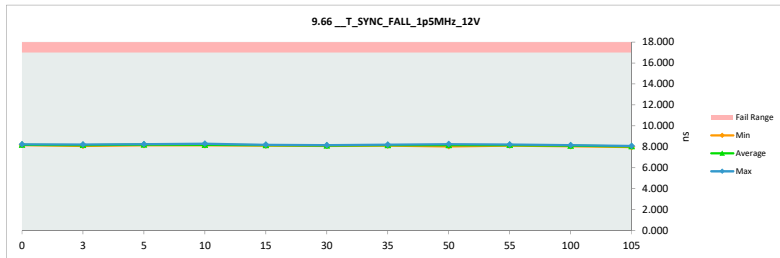
HDR TID Report TPS7H5002-SP

9.66 __T_SYNC_FALL_1p5MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.210	8.161	-0.049
0	190	8.244	8.235	-0.009
0	191	8.186	8.183	-0.003
3	300	8.162	8.136	-0.026
3	305	8.192	8.212	0.020
3	309	8.150	8.085	-0.065
3	311	8.146	8.218	0.072
3	313	8.108	8.156	0.048
5	319	8.189	8.234	0.045
5	321	8.244	8.244	0.000
5	324	8.218	8.212	-0.006
5	327	8.244	8.145	-0.099
5	331	8.197	8.240	0.043
10	234	8.217	8.290	0.073
10	235	8.190	8.140	-0.050
10	236	8.160	8.156	-0.004
10	237	8.119	8.161	0.042
10	238	8.235	8.175	-0.060
15	239	8.093	8.113	0.020
15	240	8.133	8.174	0.041
15	241	8.184	8.106	-0.078
15	243	8.176	8.157	-0.019
15	245	8.190	8.191	0.001
30	222	8.158	8.092	-0.066
30	223	8.164	8.074	-0.090
30	225	8.209	8.149	-0.060
30	226	8.176	8.131	-0.045
30	227	8.274	8.155	-0.119
35	228	8.124	8.115	-0.009
35	229	8.221	8.194	-0.027
35	231	8.237	8.111	-0.126
35	232	8.161	8.153	-0.008
35	233	8.196	8.189	-0.007
50	210	8.134	8.040	-0.094
50	211	8.137	8.105	-0.032
50	212	8.173	8.205	0.032
50	213	8.121	8.149	0.028
50	214	8.195	8.253	0.058
55	215	8.260	8.196	-0.064
55	216	8.151	8.164	0.013
55	217	8.143	8.115	-0.028
55	218	8.211	8.213	0.002
55	220	8.188	8.120	-0.068
100	247	8.265	8.154	-0.111
100	249	8.199	8.139	-0.060
100	250	8.255	8.122	-0.133
100	251	8.138	8.066	-0.072
100	253	8.245	8.139	-0.106
100	255	8.150	8.147	-0.003
100	256	8.263	8.113	-0.150
100	257	8.155	8.074	-0.081
100	258	8.264	8.044	-0.220
100	259	8.144	8.097	-0.047
100	260	8.205	8.119	-0.086
100	261	8.285	8.118	-0.167
100	262	8.243	8.092	-0.151
100	263	8.198	8.078	-0.120
100	264	8.190	8.152	-0.038
100	265	8.221	8.119	-0.102
100	266	8.221	8.116	-0.105
100	267	8.245	8.087	-0.158
100	268	8.271	8.056	-0.215
100	269	8.231	8.128	-0.103
100	270	8.141	8.077	-0.064
100	271	8.181	8.123	-0.058
100	273	8.138	8.139	0.001
105	274	8.175	7.972	-0.203
105	275	8.199	8.082	-0.117
105	276	8.150	8.054	-0.096
105	277	8.127	8.010	-0.117
105	278	8.180	8.078	-0.102
Max		8.285	8.290	0.073
Average		8.190	8.137	-0.053
Min		8.093	7.972	-0.220
Std Dev		0.046	0.059	0.068

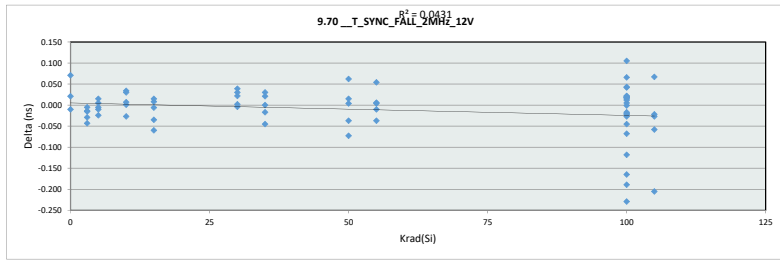


9.66 __T_SYNC_FALL_1p5MHz_12V		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.161	8.085	8.145	8.140	8.106	8.074	8.111	8.040	8.115	8.044	7.972
Average		8.193	8.161	8.215	8.184	8.148	8.120	8.152	8.150	8.162	8.109	8.039
Max		8.235	8.218	8.244	8.290	8.191	8.155	8.194	8.253	8.213	8.154	8.082
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

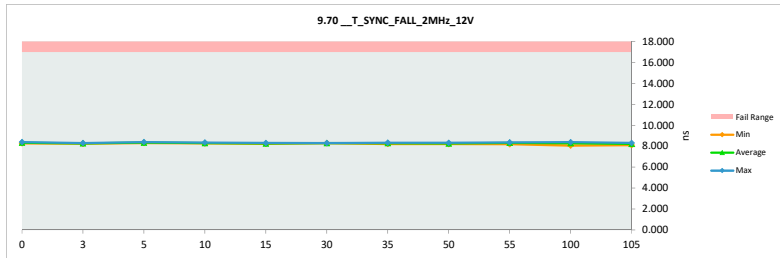


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9.70 _T_SYNC_FALL_2MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.256	8.327	0.071
0	190	8.370	8.391	0.021
0	191	8.284	8.274	-0.010
3	300	8.295	8.281	-0.014
3	305	8.344	8.301	-0.043
3	309	8.263	8.234	-0.029
3	311	8.302	8.297	-0.005
3	313	8.277	8.262	-0.015
5	319	8.300	8.315	0.015
5	321	8.345	8.335	-0.010
5	324	8.323	8.299	-0.024
5	327	8.353	8.348	-0.005
5	331	8.388	8.393	0.005
10	234	8.343	8.344	0.001
10	235	8.300	8.273	-0.027
10	236	8.232	8.266	0.034
10	237	8.244	8.274	0.030
10	238	8.311	8.318	0.007
15	239	8.214	8.222	0.008
15	240	8.293	8.308	0.015
15	241	8.303	8.297	-0.006
15	243	8.276	8.241	-0.035
15	245	8.314	8.254	-0.060
30	222	8.280	8.282	0.002
30	223	8.284	8.306	0.022
30	225	8.256	8.295	0.039
30	226	8.265	8.295	0.030
30	227	8.297	8.293	-0.004
35	228	8.224	8.207	-0.017
35	229	8.307	8.328	0.021
35	231	8.273	8.303	0.030
35	232	8.331	8.286	-0.045
35	233	8.305	8.305	0.000
50	210	8.274	8.201	-0.073
50	211	8.244	8.259	0.015
50	212	8.261	8.323	0.062
50	213	8.240	8.244	0.004
50	214	8.339	8.302	-0.037
55	215	8.377	8.367	-0.010
55	216	8.342	8.346	0.004
55	217	8.299	8.353	0.054
55	218	8.344	8.350	0.006
55	220	8.226	8.189	-0.037
100	247	8.413	8.386	-0.027
100	249	8.324	8.339	0.015
100	250	8.378	8.383	0.005
100	251	8.213	8.145	-0.068
100	253	8.286	8.304	0.018
100	255	8.288	8.059	-0.229
100	256	8.356	8.355	-0.001
100	257	8.300	8.321	0.021
100	258	8.341	8.321	-0.020
100	259	8.284	8.306	0.022
100	260	8.328	8.306	-0.022
100	261	8.271	8.376	0.105
100	262	8.303	8.345	0.042
100	263	8.297	8.132	-0.165
100	264	8.322	8.340	0.018
100	265	8.326	8.208	-0.118
100	266	8.269	8.335	0.066
100	267	8.308	8.351	0.043
100	268	8.294	8.249	-0.045
100	269	8.308	8.291	-0.017
100	270	8.295	8.314	0.019
100	271	8.315	8.327	0.012
100	273	8.326	8.137	-0.189
105	274	8.241	8.308	0.067
105	275	8.268	8.210	-0.058
105	276	8.266	8.239	-0.027
105	277	8.255	8.233	-0.022
105	278	8.313	8.108	-0.205
	Max	8.413	8.393	0.105
	Average	8.298	8.288	-0.011
	Min	8.213	8.059	-0.229
	Std Dev	0.042	0.067	0.059



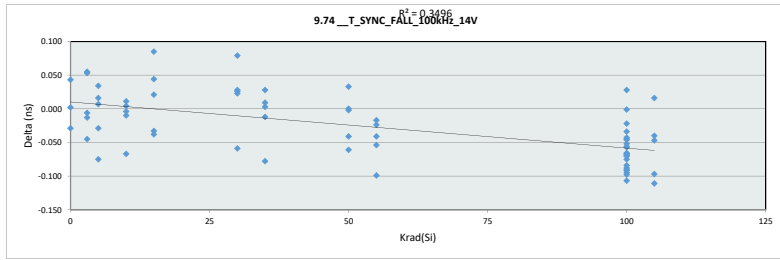
9.70 _T_SYNC_FALL_2MHz_12V											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.274	8.234	8.299	8.266	8.222	8.282	8.207	8.201	8.189	8.059	8.108
Average	8.331	8.275	8.338	8.295	8.264	8.294	8.286	8.266	8.321	8.288	8.220
Max	8.391	8.301	8.393	8.344	8.308	8.306	8.328	8.323	8.367	8.386	8.308
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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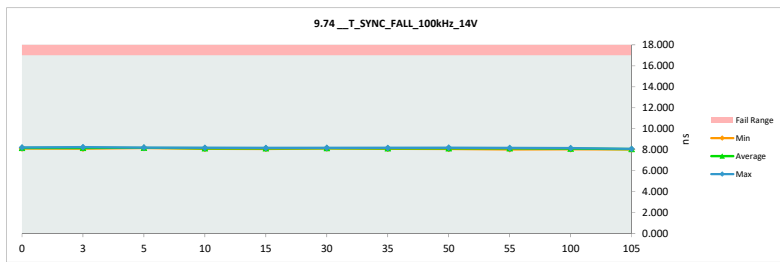
9.74_T_SYNC_FALL_100kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.117	8.160	0.043
0	190	8.238	8.209	-0.029
0	191	8.114	8.116	0.002
3	300	8.086	8.141	0.055
3	305	8.192	8.186	-0.006
3	309	8.158	8.113	-0.045
3	311	8.185	8.238	0.053
3	313	8.113	8.100	-0.013
5	319	8.123	8.157	0.034
5	321	8.209	8.180	-0.029
5	324	8.145	8.152	0.007
5	327	8.185	8.201	0.016
5	331	8.282	8.207	-0.075
10	234	8.169	8.180	0.011
10	235	8.165	8.169	0.004
10	236	8.090	8.086	-0.004
10	237	8.107	8.097	-0.010
10	238	8.166	8.099	-0.067
15	239	8.076	8.120	0.044
15	240	8.145	8.166	0.021
15	241	8.115	8.077	-0.038
15	243	8.168	8.135	-0.033
15	245	8.067	8.152	0.085
30	222	8.102	8.181	0.079
30	223	8.110	8.133	0.023
30	225	8.139	8.167	0.028
30	226	8.182	8.123	-0.059
30	227	8.085	8.111	0.026
35	228	8.144	8.132	-0.012
35	229	8.139	8.167	0.028
35	231	8.140	8.143	0.003
35	232	8.133	8.142	0.009
35	233	8.169	8.091	-0.078
50	210	8.159	8.098	-0.061
50	211	8.114	8.073	-0.041
50	212	8.170	8.168	-0.002
50	213	8.120	8.153	0.033
50	214	8.192	8.192	0.000
55	215	8.193	8.152	-0.041
55	216	8.179	8.125	-0.054
55	217	8.169	8.145	-0.024
55	218	8.179	8.162	-0.017
55	220	8.130	8.031	-0.099
100	247	8.232	8.137	-0.095
100	249	8.144	8.092	-0.052
100	250	8.162	8.064	-0.098
100	251	8.110	8.044	-0.066
100	253	8.120	8.098	-0.022
100	255	8.112	8.111	-0.001
100	256	8.202	8.136	-0.066
100	257	8.186	8.116	-0.070
100	258	8.192	8.104	-0.088
100	259	8.139	8.083	-0.056
100	260	8.189	8.121	-0.068
100	261	8.161	8.116	-0.045
100	262	8.183	8.149	-0.034
100	263	8.122	8.065	-0.057
100	264	8.175	8.085	-0.090
100	265	8.139	8.069	-0.070
100	266	8.173	8.089	-0.084
100	267	8.191	8.084	-0.107
100	268	8.099	8.053	-0.046
100	269	8.190	8.098	-0.092
100	270	8.109	8.034	-0.075
100	271	8.097	8.125	0.028
100	273	8.193	8.150	-0.043
105	274	8.085	8.038	-0.047
105	275	8.097	8.057	-0.040
105	276	8.131	8.020	-0.111
105	277	8.058	8.074	0.016
105	278	8.169	8.072	-0.097
	Max	8.282	8.238	0.085
	Average	8.148	8.121	-0.027
	Min	8.058	8.020	-0.111
	Std Dev	0.043	0.047	0.048



9.74_T_SYNC_FALL_100kHz_14V	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

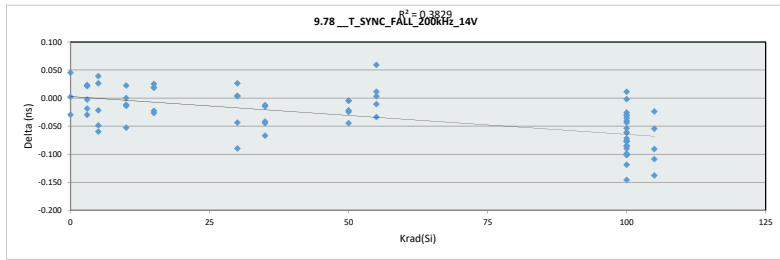
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.116	8.100	8.152	8.086	8.077	8.111	8.091	8.073	8.031	8.034	8.020
Average	8.162	8.156	8.179	8.126	8.130	8.143	8.135	8.137	8.123	8.097	8.052
Max	8.209	8.238	8.207	8.180	8.166	8.181	8.167	8.192	8.162	8.150	8.074
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



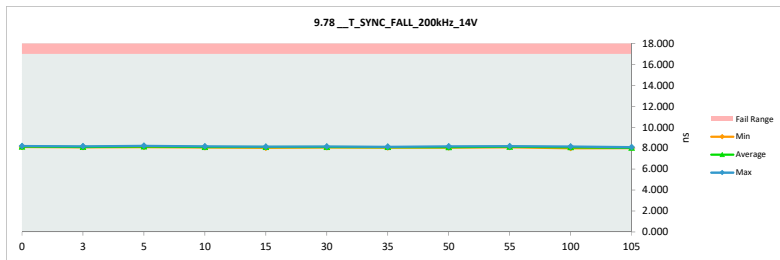
HDR TID Report
TPS7H5002-SP

9.78_T_SYNC_FALL_200kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.099	8.144	0.045
0	190	8.229	8.199	-0.030
0	191	8.111	8.113	0.002
3	300	8.116	8.086	-0.030
3	305	8.194	8.175	-0.019
3	309	8.102	8.099	-0.003
3	311	8.097	8.118	0.021
3	313	8.084	8.107	0.023
5	319	8.149	8.100	-0.049
5	321	8.186	8.225	0.039
5	324	8.179	8.157	-0.022
5	327	8.182	8.122	-0.060
5	331	8.191	8.217	0.026
10	234	8.138	8.138	0.000
10	235	8.146	8.168	0.022
10	236	8.082	8.070	-0.012
10	237	8.143	8.090	-0.053
10	238	8.113	8.099	-0.014
15	239	8.089	8.108	0.019
15	240	8.096	8.121	0.025
15	241	8.064	8.037	-0.027
15	243	8.150	8.127	-0.023
15	245	8.125	8.143	0.018
30	222	8.091	8.117	0.026
30	223	8.075	8.079	0.004
30	225	8.196	8.152	-0.044
30	226	8.159	8.069	-0.090
30	227	8.123	8.126	0.003
35	228	8.152	8.107	-0.045
35	229	8.144	8.129	-0.015
35	231	8.095	8.053	-0.042
35	232	8.179	8.112	-0.067
35	233	8.108	8.095	-0.013
50	210	8.073	8.068	-0.005
50	211	8.094	8.069	-0.025
50	212	8.130	8.125	-0.005
50	213	8.104	8.059	-0.045
50	214	8.193	8.171	-0.022
55	215	8.131	8.142	0.011
55	216	8.152	8.118	-0.034
55	217	8.179	8.168	-0.011
55	218	8.130	8.159	0.029
55	220	8.087	8.090	0.003
100	247	8.194	8.095	-0.099
100	249	8.181	8.079	-0.102
100	250	8.192	8.073	-0.119
100	251	8.096	8.066	-0.030
100	253	8.105	8.064	-0.041
100	255	8.141	8.040	-0.101
100	256	8.159	8.105	-0.054
100	257	8.131	8.095	-0.036
100	258	8.178	8.088	-0.090
100	259	8.115	8.113	-0.002
100	260	8.177	8.101	-0.076
100	261	8.179	8.135	-0.044
100	262	8.151	8.066	-0.085
100	263	8.090	8.058	-0.032
100	264	8.180	8.118	-0.062
100	265	8.160	8.074	-0.086
100	266	8.102	8.076	-0.026
100	267	8.161	8.084	-0.077
100	268	8.109	8.031	-0.078
100	269	8.217	8.156	-0.061
100	270	8.072	8.000	-0.072
100	271	8.127	8.138	0.011
100	273	8.214	8.068	-0.146
105	274	8.092	8.037	-0.055
105	275	8.102	8.011	-0.091
105	276	8.134	8.025	-0.109
105	277	8.058	8.034	-0.024
105	278	8.226	8.088	-0.138
	Max	8.229	8.225	0.059
	Average	8.137	8.103	-0.034
	Min	8.058	8.000	-0.146
	Std Dev	0.043	0.047	0.045



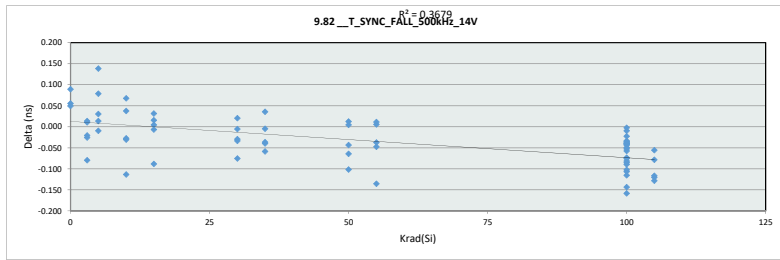
9.78_T_SYNC_FALL_200kHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.113	8.086	8.100	8.070	8.037	8.069	8.053	8.059	8.090	8.000	8.011
Average	8.152	8.117	8.164	8.113	8.107	8.109	8.099	8.098	8.141	8.084	8.039
Max	8.199	8.175	8.225	8.168	8.143	8.152	8.129	8.171	8.189	8.156	8.088
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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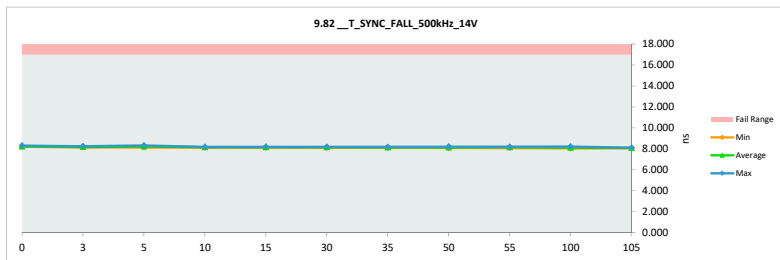
9.82_T_SYNC_FALL_500kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.145	8.200	0.055
0	190	8.250	8.299	0.049
0	191	8.102	8.191	0.089
3	300	8.219	8.139	-0.080
3	305	8.257	8.236	-0.021
3	309	8.134	8.147	0.013
3	311	8.177	8.151	-0.026
3	313	8.157	8.167	0.010
5	319	8.144	8.134	-0.010
5	321	8.222	8.252	0.030
5	324	8.138	8.216	0.078
5	327	8.205	8.218	0.013
5	331	8.184	8.322	0.138
10	234	8.292	8.178	-0.114
10	235	8.186	8.158	-0.028
10	236	8.079	8.107	0.027
10	237	8.106	8.173	0.067
10	238	8.167	8.136	-0.031
15	239	8.112	8.105	-0.007
15	240	8.190	8.205	0.015
15	241	8.127	8.131	0.004
15	243	8.228	8.139	-0.089
15	245	8.173	8.204	0.031
30	222	8.177	8.101	-0.076
30	223	8.133	8.153	0.020
30	225	8.192	8.186	-0.006
30	226	8.181	8.151	-0.030
30	227	8.187	8.153	-0.034
35	228	8.151	8.146	-0.005
35	229	8.206	8.166	-0.040
35	231	8.116	8.151	0.035
35	232	8.178	8.141	-0.037
35	233	8.164	8.105	-0.059
50	210	8.137	8.093	-0.044
50	211	8.162	8.060	-0.102
50	212	8.207	8.219	0.012
50	213	8.127	8.131	0.004
50	214	8.232	8.167	-0.065
55	215	8.252	8.116	-0.136
55	216	8.160	8.171	0.011
55	217	8.222	8.174	-0.048
55	218	8.185	8.190	0.005
55	220	8.136	8.088	-0.038
100	247	8.290	8.146	-0.144
100	249	8.174	8.133	-0.041
100	250	8.187	8.129	-0.058
100	251	8.126	8.036	-0.090
100	253	8.203	8.127	-0.076
100	255	8.128	8.094	-0.034
100	256	8.212	8.209	-0.003
100	257	8.173	8.137	-0.036
100	258	8.295	8.136	-0.159
100	259	8.125	8.102	-0.023
100	260	8.234	8.118	-0.116
100	261	8.194	8.154	-0.040
100	262	8.179	8.129	-0.050
100	263	8.173	8.087	-0.086
100	264	8.239	8.197	-0.042
100	265	8.151	8.098	-0.053
100	266	8.180	8.106	-0.074
100	267	8.152	8.142	-0.010
100	268	8.114	8.070	-0.044
100	269	8.225	8.122	-0.103
100	270	8.160	8.122	-0.038
100	271	8.243	8.136	-0.107
100	273	8.205	8.123	-0.082
105	274	8.220	8.099	-0.121
105	275	8.185	8.056	-0.129
105	276	8.148	8.092	-0.056
105	277	8.130	8.051	-0.079
105	278	8.223	8.106	-0.117
	Max	8.295	8.322	0.138
	Average	8.180	8.145	-0.034
	Min	8.070	8.036	-0.159
	Std Dev	0.047	0.053	0.058



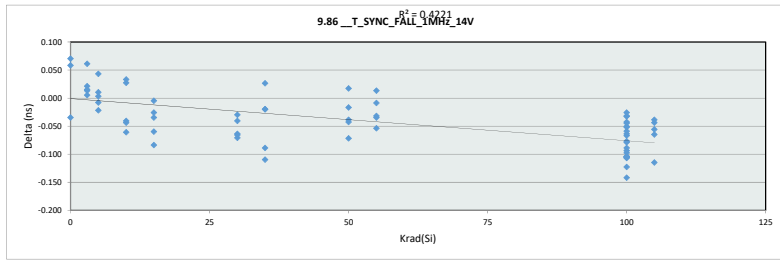
9.82_T_SYNC_FALL_500kHz_14V	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.191	8.139	8.134	8.107	8.105	8.101	8.105	8.060	8.088	8.036	8.051
Average	8.230	8.168	8.228	8.150	8.157	8.149	8.142	8.134	8.148	8.124	8.081
Max	8.299	8.236	8.322	8.178	8.205	8.186	8.166	8.219	8.190	8.209	8.106
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

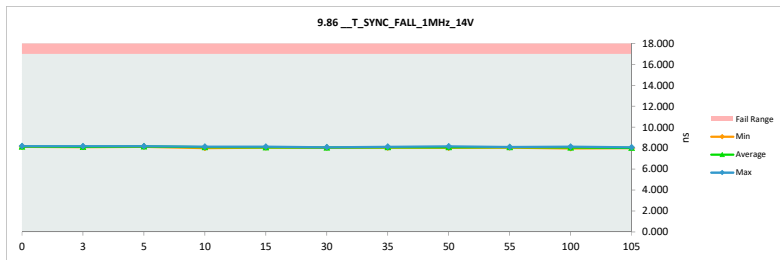


HDR TID Report
TPS7H5002-SP

9.86_T_SYNC_FALL_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.093	8.151	0.058
0	190	8.228	8.193	-0.035
0	191	8.062	8.132	0.070
3	300	8.060	8.121	0.061
3	305	8.166	8.187	0.021
3	309	8.086	8.099	0.013
3	311	8.085	8.100	0.015
3	313	8.119	8.124	0.005
5	319	8.090	8.133	0.043
5	321	8.212	8.190	-0.022
5	324	8.119	8.129	0.010
5	327	8.197	8.189	-0.008
5	331	8.186	8.189	0.003
10	234	8.105	8.138	0.033
10	235	8.157	8.113	-0.044
10	236	8.069	8.098	-0.061
10	237	8.070	8.097	0.027
10	238	8.156	8.115	-0.041
15	239	8.085	8.059	-0.026
15	240	8.129	8.094	-0.035
15	241	8.138	8.054	-0.084
15	243	8.117	8.057	-0.060
15	245	8.144	8.139	-0.005
30	222	8.097	8.067	-0.030
30	223	8.123	8.059	-0.064
30	225	8.143	8.077	-0.066
30	226	8.113	8.042	-0.071
30	227	8.126	8.085	-0.041
35	228	8.157	8.068	-0.089
35	229	8.153	8.133	-0.020
35	231	8.068	8.094	0.026
35	232	8.109	8.089	-0.020
35	233	8.158	8.048	-0.110
50	210	8.076	8.059	-0.017
50	211	8.084	8.045	-0.039
50	212	8.157	8.114	-0.043
50	213	8.100	8.028	-0.072
50	214	8.153	8.170	0.017
55	215	8.158	8.104	-0.054
55	216	8.117	8.082	-0.035
55	217	8.137	8.105	-0.032
55	218	8.095	8.108	0.013
55	220	8.062	8.053	-0.009
100	247	8.230	8.123	-0.107
100	249	8.165	8.060	-0.105
100	250	8.149	8.043	-0.106
100	251	8.069	8.005	-0.064
100	253	8.143	8.049	-0.094
100	255	8.117	8.058	-0.059
100	256	8.216	8.137	-0.079
100	257	8.183	8.060	-0.123
100	258	8.157	8.105	-0.052
100	259	8.139	8.041	-0.098
100	260	8.184	8.080	-0.104
100	261	8.127	8.095	-0.032
100	262	8.146	8.113	-0.033
100	263	8.143	8.038	-0.105
100	264	8.181	8.130	-0.051
100	265	8.087	8.061	-0.026
100	266	8.099	8.056	-0.043
100	267	8.150	8.008	-0.142
100	268	8.048	7.981	-0.067
100	269	8.153	8.108	-0.045
100	270	8.134	8.045	-0.089
100	271	8.165	8.060	-0.105
100	273	8.179	8.102	-0.077
105	274	8.061	8.022	-0.039
105	275	8.127	8.012	-0.115
105	276	8.055	7.999	-0.056
105	277	8.083	8.039	-0.044
105	278	8.147	8.082	-0.065
	Max	8.230	8.193	0.070
	Average	8.128	8.087	-0.041
	Min	8.048	7.981	-0.142
	Std Dev	0.044	0.049	0.047



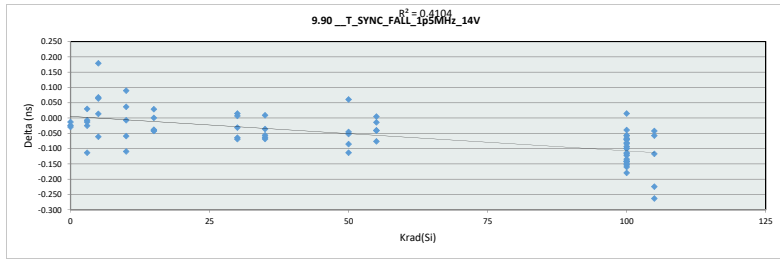
9.86_T_SYNC_FALL_1MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.132	8.099	8.129	8.008	8.054	8.042	8.048	8.028	8.053	7.981	7.999
Average	8.159	8.126	8.166	8.094	8.081	8.066	8.086	8.083	8.090	8.068	8.031
Max	8.193	8.187	8.190	8.138	8.139	8.085	8.133	8.170	8.108	8.137	8.082
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



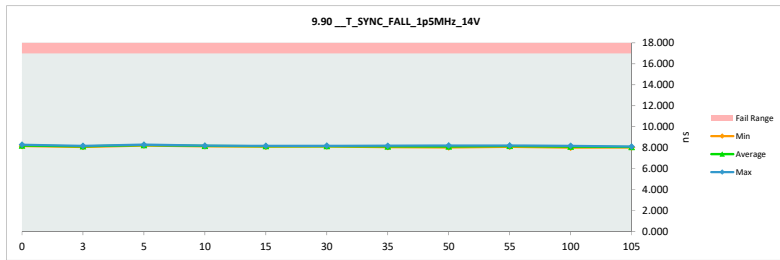
HDR TID Report
TPS7H5002-SP

9.90_T_SYNC_FALL_1p5MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.179	8.149	-0.030
0	190	8.303	8.278	-0.025
0	191	8.212	8.198	-0.014
3	300	8.181	8.067	-0.114
3	305	8.204	8.178	-0.026
3	309	8.083	8.112	0.029
3	311	8.148	8.140	-0.008
3	313	8.154	8.140	-0.014
5	319	8.214	8.227	0.013
5	321	8.216	8.283	0.067
5	324	8.101	8.279	0.178
5	327	8.136	8.199	0.063
5	331	8.286	8.224	-0.062
10	234	8.155	8.191	0.036
10	235	8.236	8.126	-0.110
10	236	8.140	8.132	-0.008
10	237	8.104	8.193	0.089
10	238	8.255	8.195	-0.060
15	239	8.128	8.089	-0.039
15	240	8.152	8.152	0.000
15	241	8.143	8.100	-0.043
15	243	8.214	8.172	-0.042
15	245	8.150	8.178	0.028
30	222	8.162	8.130	-0.032
30	223	8.178	8.113	-0.065
30	225	8.217	8.147	-0.070
30	226	8.175	8.189	0.014
30	227	8.135	8.141	0.006
35	228	8.109	8.052	-0.057
35	229	8.238	8.174	-0.064
35	231	8.152	8.160	0.008
35	232	8.173	8.136	-0.037
35	233	8.156	8.087	-0.069
50	210	8.102	8.016	-0.086
50	211	8.150	8.104	-0.046
50	212	8.151	8.211	0.060
50	213	8.172	8.058	-0.114
50	214	8.250	8.197	-0.053
55	215	8.223	8.182	-0.041
55	216	8.191	8.176	-0.015
55	217	8.141	8.145	0.004
55	218	8.250	8.208	-0.042
55	220	8.155	8.078	-0.077
100	247	8.266	8.148	-0.118
100	249	8.173	8.116	-0.057
100	250	8.242	8.062	-0.180
100	251	8.131	8.031	-0.100
100	253	8.125	8.085	-0.040
100	255	8.205	8.123	-0.082
100	256	8.239	8.168	-0.071
100	257	8.221	8.079	-0.142
100	258	8.199	8.076	-0.123
100	259	8.133	8.147	0.014
100	260	8.195	8.128	-0.067
100	261	8.265	8.165	-0.100
100	262	8.195	8.080	-0.115
100	263	8.156	7.995	-0.161
100	264	8.242	8.157	-0.085
100	265	8.249	8.114	-0.135
100	266	8.183	8.089	-0.094
100	267	8.192	8.048	-0.144
100	268	8.159	8.017	-0.142
100	269	8.186	8.114	-0.072
100	270	8.213	8.059	-0.154
100	271	8.207	8.056	-0.151
100	273	8.162	8.103	-0.059
105	274	8.290	8.026	-0.264
105	275	8.188	8.070	-0.118
105	276	8.259	8.034	-0.225
105	277	8.089	8.046	-0.043
105	278	8.160	8.102	-0.058
Max		8.303	8.283	0.178
Average		8.184	8.129	-0.056
Min		8.083	7.995	-0.264
Std Dev		0.051	0.065	0.072

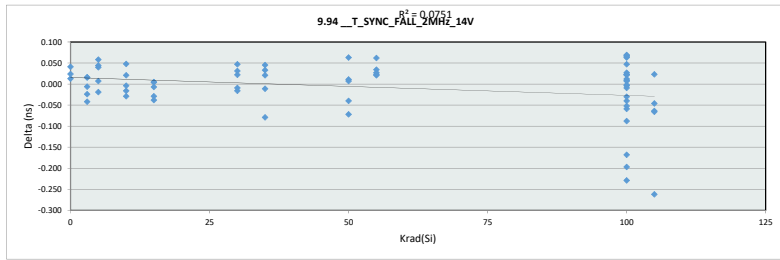


9.90_T_SYNC_FALL_1p5MHz_14V		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.149	8.067	8.199	8.126	8.089	8.113	8.052	8.016	8.078	7.995	8.026
Average		8.208	8.127	8.242	8.167	8.138	8.144	8.122	8.117	8.158	8.094	8.056
Max		8.278	8.178	8.283	8.195	8.178	8.189	8.174	8.211	8.208	8.168	8.102
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

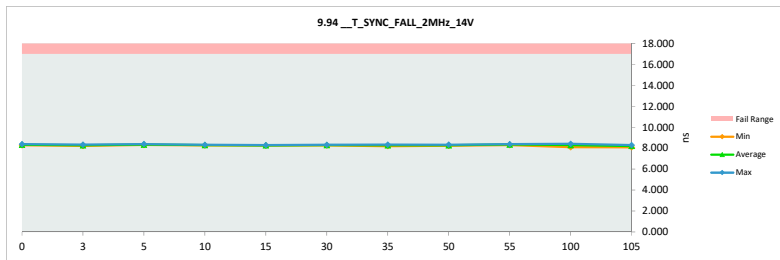


HDR TID Report
TPS7H5002-SP

9.94_T_SYNC_FALL_2MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.235	8.276	0.041
0	190	8.370	8.383	0.013
0	191	8.268	8.292	0.024
3	300	8.298	8.274	-0.024
3	305	8.320	8.336	0.016
3	309	8.257	8.215	-0.042
3	311	8.258	8.274	0.016
3	313	8.269	8.263	-0.006
5	319	8.278	8.318	0.040
5	321	8.343	8.350	0.007
5	324	8.267	8.311	0.044
5	327	8.331	8.312	-0.019
5	331	8.333	8.391	0.058
10	234	8.331	8.315	-0.016
10	235	8.284	8.255	-0.029
10	236	8.230	8.278	0.048
10	237	8.270	8.266	-0.004
10	238	8.305	8.326	0.021
15	239	8.255	8.260	0.005
15	240	8.303	8.296	-0.007
15	241	8.281	8.243	-0.038
15	243	8.276	8.280	0.004
15	245	8.296	8.267	-0.029
30	222	8.275	8.259	-0.016
30	223	8.262	8.309	0.047
30	225	8.286	8.317	0.031
30	226	8.267	8.289	0.022
30	227	8.294	8.285	-0.009
35	228	8.270	8.191	-0.079
35	229	8.299	8.332	0.033
35	231	8.291	8.312	0.021
35	232	8.296	8.285	-0.011
35	233	8.243	8.288	0.045
50	210	8.267	8.227	-0.040
50	211	8.256	8.267	0.011
50	212	8.291	8.298	0.007
50	213	8.265	8.328	0.063
50	214	8.336	8.264	-0.072
55	215	8.354	8.377	0.023
55	216	8.366	8.387	0.021
55	217	8.322	8.349	0.027
55	218	8.344	8.378	0.034
55	220	8.332	8.294	-0.038
100	247	8.389	8.398	0.009
100	249	8.321	8.390	0.069
100	250	8.364	8.276	-0.088
100	251	8.274	8.106	-0.168
100	253	8.290	8.312	0.022
100	255	8.324	8.346	0.022
100	256	8.352	8.415	0.063
100	257	8.321	8.312	-0.009
100	258	8.361	8.357	-0.004
100	259	8.296	8.308	0.012
100	260	8.280	8.348	0.068
100	261	8.277	8.342	0.065
100	262	8.304	8.351	0.047
100	263	8.287	8.090	-0.197
100	264	8.340	8.363	0.023
100	265	8.308	8.256	-0.052
100	266	8.280	8.287	0.007
100	267	8.328	8.288	-0.040
100	268	8.275	8.245	-0.030
100	269	8.327	8.268	-0.059
100	270	8.274	8.272	-0.002
100	271	8.324	8.351	0.027
100	273	8.326	8.097	-0.229
105	274	8.272	8.295	0.023
105	275	8.274	8.210	-0.064
105	276	8.303	8.257	-0.046
105	277	8.287	8.221	-0.066
105	278	8.350	8.088	-0.262
	Max	8.389	8.415	0.069
	Average	8.298	8.291	-0.007
	Min	8.230	8.088	-0.262
	Std Dev	0.036	0.068	0.064

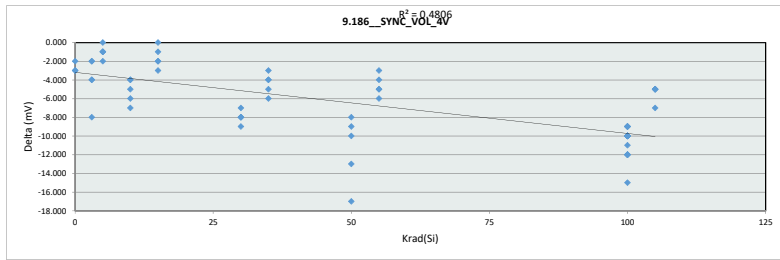


9.94_T_SYNC_FALL_2MHz_14V											
Test Site											
Tester											
Test Number	17	ns									
Max Limit		ns									
Min Limit											
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.276	8.215	8.311	8.255	8.243	8.259	8.191	8.227	8.294	8.090	8.088
Average	8.317	8.272	8.336	8.288	8.269	8.292	8.282	8.277	8.357	8.295	8.214
Max	8.383	8.336	8.391	8.326	8.296	8.317	8.332	8.328	8.387	8.415	8.295
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

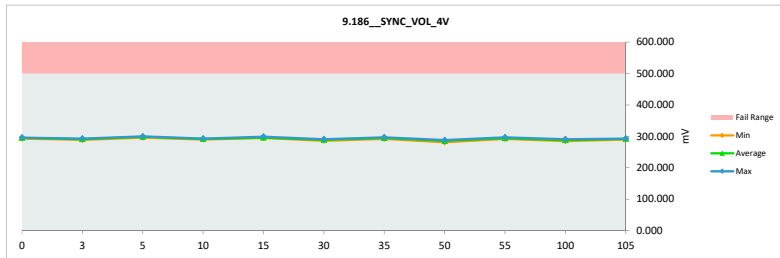


HDR TID Report
TPS7H5002-SP

9.186_SYNC_VOL_4V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	500	500		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	295.000	293.000	-2.000
0	190	299.000	296.000	-3.000
0	191	299.000	296.000	-3.000
3	300	297.000	289.000	-8.000
3	305	295.000	291.000	-4.000
3	309	294.000	290.000	-4.000
3	311	294.000	292.000	-2.000
3	313	295.000	293.000	-2.000
5	319	300.000	298.000	-2.000
5	321	297.000	296.000	-1.000
5	324	300.000	299.000	-1.000
5	327	301.000	300.000	-1.000
5	331	300.000	300.000	0.000
10	234	295.000	291.000	-4.000
10	235	296.000	292.000	-4.000
10	236	297.000	290.000	-7.000
10	237	298.000	293.000	-5.000
10	238	298.000	292.000	-6.000
15	239	295.000	295.000	0.000
15	240	297.000	295.000	-2.000
15	241	300.000	299.000	-1.000
15	243	297.000	295.000	-2.000
15	245	300.000	297.000	-3.000
30	222	295.000	286.000	-9.000
30	223	299.000	291.000	-8.000
30	225	296.000	288.000	-8.000
30	226	297.000	290.000	-7.000
30	227	297.000	289.000	-8.000
35	228	302.000	296.000	-6.000
35	229	298.000	295.000	-3.000
35	231	297.000	292.000	-5.000
35	232	296.000	292.000	-4.000
35	233	301.000	297.000	-4.000
50	210	298.000	281.000	-17.000
50	211	297.000	284.000	-13.000
50	212	298.000	288.000	-10.000
50	213	297.000	288.000	-9.000
50	214	295.000	287.000	-8.000
55	215	303.000	297.000	-6.000
55	216	299.000	294.000	-5.000
55	217	296.000	293.000	-3.000
55	218	298.000	294.000	-4.000
55	220	297.000	292.000	-5.000
100	247	302.000	287.000	-15.000
100	249	299.000	287.000	-12.000
100	250	299.000	287.000	-12.000
100	251	297.000	285.000	-12.000
100	253	298.000	287.000	-11.000
100	255	297.000	288.000	-9.000
100	256	297.000	288.000	-9.000
100	257	302.000	290.000	-12.000
100	258	300.000	290.000	-10.000
100	259	296.000	286.000	-10.000
100	260	297.000	287.000	-10.000
100	261	298.000	288.000	-10.000
100	262	299.000	290.000	-9.000
100	263	299.000	290.000	-9.000
100	264	297.000	287.000	-10.000
100	265	298.000	286.000	-12.000
100	266	298.000	288.000	-10.000
100	267	301.000	291.000	-10.000
100	268	297.000	287.000	-10.000
100	269	299.000	289.000	-10.000
100	270	298.000	288.000	-10.000
100	271	298.000	288.000	-10.000
100	273	295.000	286.000	-9.000
105	274	298.000	293.000	-5.000
105	275	297.000	292.000	-5.000
105	276	297.000	290.000	-7.000
105	277	297.000	292.000	-5.000
105	278	298.000	293.000	-5.000
Max		303.000	300.000	0.000
Average		297.789	291.070	-6.718
Min		294.000	281.000	-17.000
Std Dev		1.978	4.096	3.866

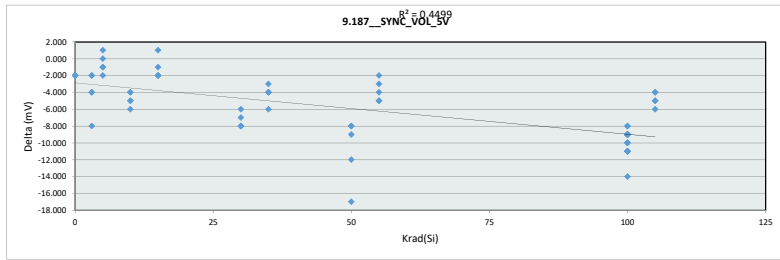


9.186_SYNC_VOL											
Test Site											
Tester											
Test Number											
Max Limit	500	mV									
Min Limit		mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	293.000	289.000	296.000	290.000	295.000	286.000	292.000	281.000	292.000	285.000	290.000
Average	295.000	291.000	298.600	291.600	296.200	288.800	294.400	285.600	294.000	287.826	292.000
Max	296.000	293.000	300.000	293.000	299.000	291.000	297.000	288.000	297.000	291.000	293.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

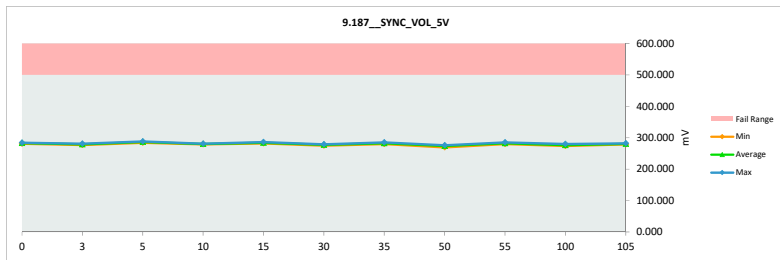


HDR TID Report
TPS7H5002-SP

9.187_SYNC_VOL_5V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	500	500		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	283.000	281.000	-2.000
0	190	286.000	284.000	-2.000
0	191	286.000	284.000	-2.000
3	300	285.000	277.000	-8.000
3	305	283.000	279.000	-4.000
3	309	282.000	278.000	-4.000
3	311	282.000	280.000	-2.000
3	313	283.000	281.000	-2.000
5	319	287.000	286.000	-1.000
5	321	285.000	284.000	-1.000
5	324	287.000	287.000	0.000
5	327	289.000	287.000	-2.000
5	331	287.000	288.000	1.000
10	234	283.000	279.000	-4.000
10	235	284.000	280.000	-4.000
10	236	284.000	279.000	-5.000
10	237	286.000	281.000	-5.000
10	238	286.000	280.000	-6.000
15	239	282.000	283.000	1.000
15	240	284.000	283.000	-1.000
15	241	288.000	286.000	-2.000
15	243	284.000	282.000	-2.000
15	245	287.000	285.000	-2.000
30	222	283.000	275.000	-8.000
30	223	287.000	279.000	-8.000
30	225	284.000	276.000	-8.000
30	226	285.000	279.000	-6.000
30	227	284.000	277.000	-7.000
35	228	290.000	284.000	-6.000
35	229	286.000	283.000	-3.000
35	231	285.000	281.000	-4.000
35	232	284.000	280.000	-4.000
35	233	289.000	285.000	-4.000
50	210	286.000	269.000	-17.000
50	211	284.000	272.000	-12.000
50	212	285.000	276.000	-9.000
50	213	284.000	276.000	-8.000
50	214	283.000	275.000	-8.000
55	215	290.000	285.000	-5.000
55	216	287.000	282.000	-5.000
55	217	284.000	281.000	-3.000
55	218	285.000	283.000	-2.000
55	220	284.000	280.000	-4.000
100	247	289.000	275.000	-14.000
100	249	287.000	276.000	-11.000
100	250	287.000	276.000	-11.000
100	251	285.000	274.000	-11.000
100	253	286.000	276.000	-10.000
100	255	285.000	276.000	-9.000
100	256	285.000	277.000	-8.000
100	257	289.000	278.000	-11.000
100	258	288.000	278.000	-10.000
100	259	284.000	274.000	-10.000
100	260	285.000	276.000	-9.000
100	261	286.000	277.000	-9.000
100	262	286.000	278.000	-8.000
100	263	287.000	278.000	-9.000
100	264	285.000	276.000	-9.000
100	265	286.000	275.000	-11.000
100	266	285.000	276.000	-9.000
100	267	289.000	280.000	-9.000
100	268	285.000	276.000	-9.000
100	269	287.000	278.000	-9.000
100	270	285.000	276.000	-9.000
100	271	286.000	277.000	-9.000
100	273	283.000	274.000	-9.000
105	274	286.000	282.000	-4.000
105	275	285.000	280.000	-5.000
105	276	285.000	279.000	-6.000
105	277	285.000	280.000	-5.000
105	278	285.000	281.000	-4.000
Max		290.000	288.000	1.000
Average		285.465	279.310	-6.155
Min		282.000	269.000	-17.000
Std Dev		1.896	3.879	3.733

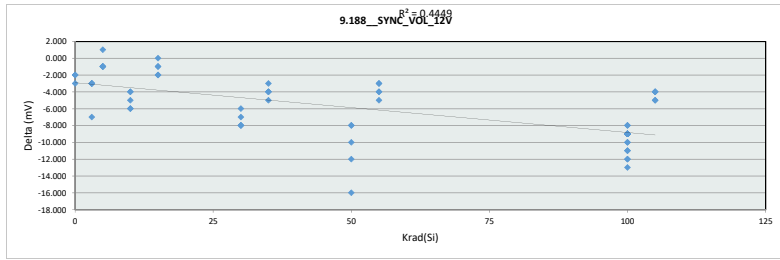


9.187_SYNC_VOL											
Test Site											
Tester											
Test Number											
Max Limit	500	mV									
Min Limit		mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	281.000	277.000	284.000	279.000	282.000	275.000	280.000	269.000	280.000	274.000	279.000
Min	283.000	279.000	286.400	279.800	283.800	277.200	282.600	273.600	282.200	276.391	280.400
Average	284.000	281.000	288.000	281.000	286.000	279.000	285.000	285.000	280.000	280.000	282.000
Max	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

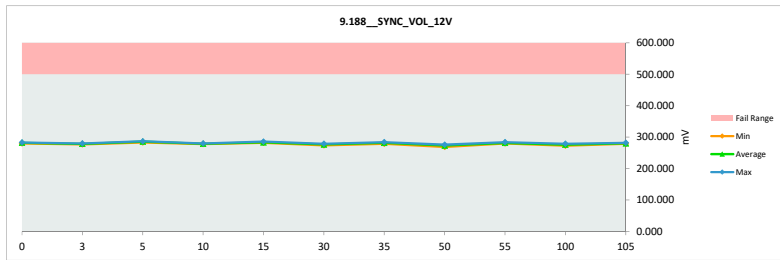


HDR TID Report
TPS7H5002-SP

9.188_SYNC_VOL_12V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	500	500		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	282.000	280.000	-2.000
0	190	285.000	283.000	-2.000
0	191	286.000	283.000	-3.000
3	300	284.000	277.000	-7.000
3	305	282.000	279.000	-3.000
3	309	281.000	278.000	-3.000
3	311	282.000	279.000	-3.000
3	313	283.000	280.000	-3.000
5	319	287.000	286.000	-1.000
5	321	284.000	283.000	-1.000
5	324	287.000	286.000	-1.000
5	327	288.000	287.000	-1.000
5	331	286.000	287.000	1.000
10	234	282.000	278.000	-4.000
10	235	283.000	279.000	-4.000
10	236	284.000	278.000	-6.000
10	237	285.000	280.000	-5.000
10	238	285.000	279.000	-6.000
15	239	282.000	282.000	0.000
15	240	283.000	282.000	-1.000
15	241	287.000	286.000	-1.000
15	243	284.000	282.000	-2.000
15	245	287.000	285.000	-2.000
30	222	282.000	274.000	-8.000
30	223	286.000	279.000	-7.000
30	225	284.000	276.000	-8.000
30	226	284.000	276.000	-8.000
30	227	284.000	276.000	-8.000
35	228	289.000	284.000	-5.000
35	229	285.000	282.000	-3.000
35	231	284.000	280.000	-4.000
35	232	283.000	279.000	-4.000
35	233	288.000	284.000	-4.000
50	210	285.000	269.000	-16.000
50	211	284.000	272.000	-12.000
50	212	285.000	275.000	-10.000
50	213	284.000	276.000	-8.000
50	214	282.000	274.000	-8.000
55	215	289.000	284.000	-5.000
55	216	286.000	282.000	-4.000
55	217	283.000	280.000	-3.000
55	218	285.000	282.000	-3.000
55	220	284.000	280.000	-4.000
100	247	288.000	275.000	-13.000
100	249	286.000	275.000	-11.000
100	250	287.000	275.000	-12.000
100	251	285.000	273.000	-12.000
100	253	285.000	275.000	-10.000
100	255	285.000	276.000	-9.000
100	256	284.000	276.000	-8.000
100	257	289.000	278.000	-11.000
100	258	287.000	278.000	-9.000
100	259	283.000	274.000	-9.000
100	260	284.000	275.000	-9.000
100	261	285.000	276.000	-9.000
100	262	286.000	277.000	-9.000
100	263	287.000	278.000	-9.000
100	264	284.000	275.000	-9.000
100	265	285.000	275.000	-10.000
100	266	285.000	276.000	-9.000
100	267	288.000	279.000	-9.000
100	268	284.000	275.000	-9.000
100	269	286.000	277.000	-9.000
100	270	285.000	276.000	-9.000
100	271	285.000	276.000	-9.000
100	273	282.000	274.000	-8.000
105	274	286.000	282.000	-4.000
105	275	284.000	280.000	-4.000
105	276	284.000	279.000	-5.000
105	277	284.000	280.000	-4.000
105	278	285.000	280.000	-5.000
Max		289.000	287.000	1.000
Average		284.831	278.732	-6.099
Min		281.000	269.000	-16.000
Std Dev		1.882	3.836	3.638

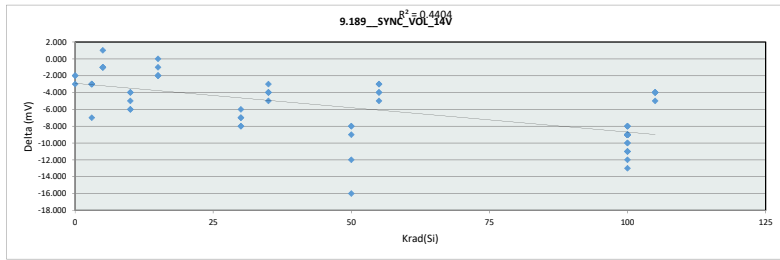


9.188_SYNC_VOL											
Test Site											
Tester											
Test Number											
Max Limit	500	mV									
Min Limit		mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	280.000	277.000	283.000	278.000	282.000	274.000	279.000	269.000	280.000	273.000	279.000
Min	282.000	278.600	285.800	278.800	283.400	276.600	281.800	273.200	281.600	275.826	280.200
Average	283.000	280.000	287.000	280.000	286.000	279.000	284.000	276.000	284.000	279.000	282.000
Max	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

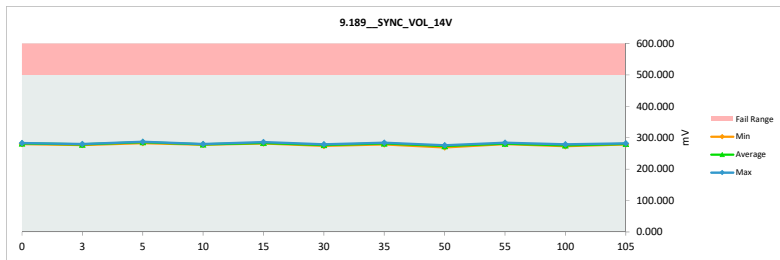


HDR TID Report
TPS7H5002-SP

9.189_SYNC_VOL_14V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	500	500		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	282.000	280.000	-2.000
0	190	285.000	283.000	-2.000
0	191	286.000	283.000	-3.000
3	300	284.000	277.000	-7.000
3	305	282.000	279.000	-3.000
3	309	281.000	278.000	-3.000
3	311	282.000	279.000	-3.000
3	313	283.000	280.000	-3.000
5	319	287.000	286.000	-1.000
5	321	284.000	283.000	-1.000
5	324	287.000	286.000	-1.000
5	327	288.000	287.000	-1.000
5	331	286.000	287.000	1.000
10	234	282.000	278.000	-4.000
10	235	283.000	279.000	-4.000
10	236	284.000	278.000	-6.000
10	237	285.000	280.000	-5.000
10	238	285.000	279.000	-6.000
15	239	282.000	282.000	0.000
15	240	283.000	282.000	-1.000
15	241	288.000	286.000	-2.000
15	243	284.000	282.000	-2.000
15	245	287.000	285.000	-2.000
30	222	282.000	274.000	-8.000
30	223	286.000	279.000	-7.000
30	225	283.000	276.000	-7.000
30	226	284.000	276.000	-6.000
30	227	284.000	276.000	-8.000
35	228	289.000	284.000	-5.000
35	229	285.000	282.000	-3.000
35	231	284.000	280.000	-4.000
35	232	283.000	279.000	-4.000
35	233	288.000	284.000	-4.000
50	210	285.000	269.000	-16.000
50	211	284.000	272.000	-12.000
50	212	285.000	276.000	-9.000
50	213	284.000	276.000	-8.000
50	214	282.000	274.000	-8.000
55	215	289.000	284.000	-5.000
55	216	286.000	282.000	-4.000
55	217	283.000	280.000	-3.000
55	218	285.000	282.000	-3.000
55	220	284.000	280.000	-4.000
100	247	288.000	275.000	-13.000
100	249	286.000	275.000	-11.000
100	250	286.000	275.000	-11.000
100	251	285.000	273.000	-12.000
100	253	285.000	275.000	-10.000
100	255	285.000	276.000	-9.000
100	256	284.000	276.000	-8.000
100	257	289.000	278.000	-11.000
100	258	287.000	278.000	-9.000
100	259	283.000	274.000	-9.000
100	260	284.000	275.000	-9.000
100	261	285.000	276.000	-9.000
100	262	286.000	278.000	-8.000
100	263	287.000	278.000	-9.000
100	264	284.000	275.000	-9.000
100	265	285.000	275.000	-10.000
100	266	285.000	276.000	-9.000
100	267	288.000	279.000	-9.000
100	268	284.000	275.000	-9.000
100	269	286.000	277.000	-9.000
100	270	285.000	276.000	-9.000
100	271	285.000	276.000	-9.000
100	273	282.000	274.000	-8.000
105	274	286.000	282.000	-4.000
105	275	284.000	280.000	-4.000
105	276	284.000	279.000	-5.000
105	277	284.000	280.000	-4.000
105	278	285.000	281.000	-4.000
Max		289.000	287.000	1.000
Average		284.817	278.775	-6.042
Min		281.000	269.000	-16.000
Std Dev		1.900	3.825	3.575

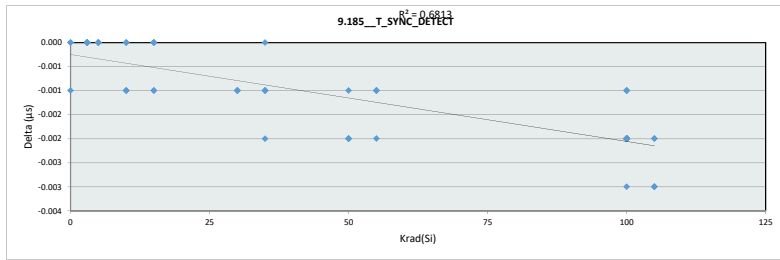


9.189_SYNC_VOL											
Test Site											
Tester											
Test Number											
Max Limit	500	mV									
Min Limit		mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	280.000	277.000	283.000	278.000	282.000	274.000	279.000	269.000	280.000	273.000	279.000
Average	282.000	278.600	285.800	278.800	283.400	276.600	281.800	273.400	281.600	275.870	280.400
Max	283.000	280.000	287.000	280.000	286.000	279.000	284.000	276.000	284.000	279.000	282.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

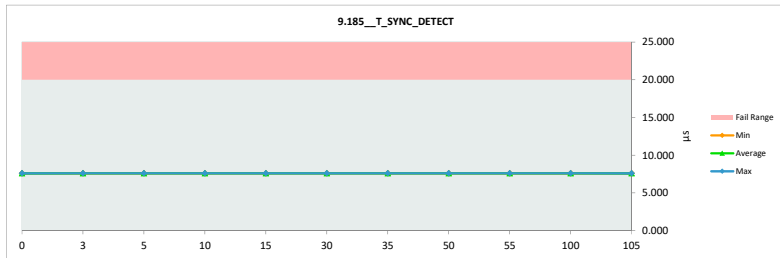


HDR TID Report
TPS7H5002-SP

9.185_T_SYNC_DETECT				
Test Site				
Tester				
Test Number				
Unit	µs	µs		
Max Limit	20	20		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.589	7.589	0.000
0	190	7.587	7.587	0.000
0	191	7.589	7.588	-0.001
3	300	7.588	7.588	0.000
3	305	7.587	7.587	0.000
3	309	7.587	7.587	0.000
3	311	7.587	7.587	0.000
3	313	7.587	7.587	0.000
5	319	7.587	7.587	0.000
5	321	7.589	7.589	0.000
5	324	7.587	7.587	0.000
5	327	7.587	7.587	0.000
5	331	7.587	7.587	0.000
10	234	7.588	7.587	-0.001
10	235	7.589	7.588	-0.001
10	236	7.587	7.587	0.000
10	237	7.589	7.589	0.000
10	238	7.589	7.588	-0.001
15	239	7.588	7.587	-0.001
15	240	7.587	7.587	0.000
15	241	7.587	7.587	0.000
15	243	7.588	7.588	0.000
15	245	7.588	7.587	-0.001
30	222	7.588	7.587	-0.001
30	223	7.587	7.586	-0.001
30	225	7.588	7.587	-0.001
30	226	7.589	7.588	-0.001
30	227	7.589	7.588	-0.001
35	228	7.588	7.587	-0.001
35	229	7.588	7.588	0.000
35	231	7.588	7.587	-0.001
35	232	7.589	7.587	-0.002
35	233	7.588	7.587	-0.001
50	210	7.587	7.585	-0.002
50	211	7.590	7.588	-0.002
50	212	7.588	7.587	-0.001
50	213	7.588	7.586	-0.002
50	214	7.589	7.587	-0.002
55	215	7.586	7.585	-0.001
55	216	7.587	7.585	-0.002
55	217	7.588	7.587	-0.001
55	218	7.587	7.586	-0.001
55	220	7.589	7.588	-0.001
100	247	7.589	7.587	-0.002
100	249	7.587	7.586	-0.001
100	250	7.588	7.586	-0.002
100	251	7.588	7.586	-0.002
100	253	7.588	7.586	-0.002
100	255	7.589	7.587	-0.002
100	256	7.589	7.586	-0.003
100	257	7.587	7.585	-0.002
100	258	7.587	7.586	-0.001
100	259	7.588	7.586	-0.002
100	260	7.589	7.587	-0.002
100	261	7.589	7.587	-0.002
100	262	7.589	7.587	-0.002
100	263	7.587	7.586	-0.001
100	264	7.589	7.587	-0.002
100	265	7.589	7.587	-0.002
100	266	7.590	7.588	-0.002
100	267	7.588	7.586	-0.002
100	268	7.588	7.586	-0.002
100	269	7.588	7.586	-0.002
100	270	7.588	7.586	-0.002
100	271	7.589	7.587	-0.002
100	273	7.587	7.586	-0.001
105	274	7.587	7.585	-0.002
105	275	7.589	7.586	-0.003
105	276	7.589	7.586	-0.003
105	277	7.587	7.585	-0.002
105	278	7.589	7.586	-0.003
Max		7.590	7.589	0.000
Average		7.588	7.587	-0.001
Min		7.586	7.585	-0.003
Std Dev		0.001	0.001	0.001

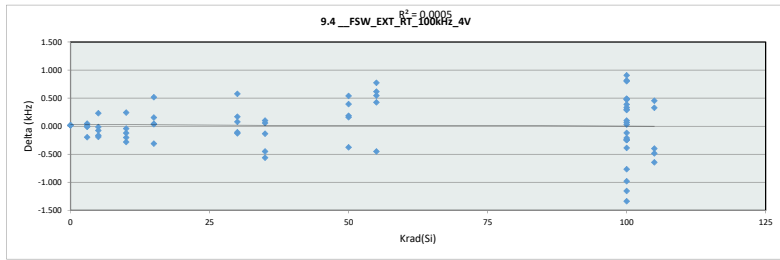


9.185_T_SYNC_DETECT											
Test Site											
Tester											
Test Number											
Max Limit	20	µs									
Min Limit		µs									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.587	7.587	7.587	7.587	7.587	7.587	7.586	7.585	7.585	7.585	7.585
Average	7.588	7.587	7.587	7.588	7.587	7.587	7.587	7.587	7.586	7.586	7.586
Max	7.589	7.588	7.589	7.589	7.589	7.588	7.588	7.588	7.588	7.588	7.586
UL	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000

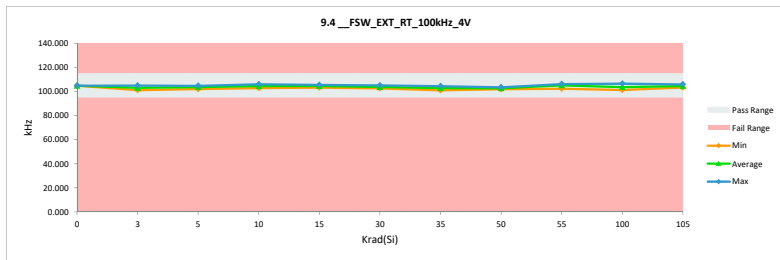


HDR TID Report TPS7H5002-SP

9.4_FSW_EXT_RT_100kHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	115	115		
Min Limit	95	95		
Krad(S)	Serial #	PRE	POST	Delta
0	189	104.830	104.842	0.012
0	190	104.769	104.782	0.013
0	191	104.893	104.906	0.013
3	300	103.259	103.255	-0.004
3	305	101.120	101.162	0.042
3	309	104.931	104.927	-0.004
3	311	103.658	103.459	-0.199
3	313	102.809	102.795	-0.014
5	319	104.503	104.308	-0.195
5	321	102.861	102.844	-0.017
5	324	104.814	104.733	-0.081
5	327	102.171	102.003	-0.168
5	331	104.226	104.454	0.228
10	234	105.273	105.511	0.238
10	235	103.123	102.917	-0.206
10	236	104.098	104.051	-0.047
10	237	106.262	105.978	-0.284
10	238	104.385	104.259	-0.126
15	239	104.144	104.175	0.031
15	240	103.157	103.307	0.150
15	241	105.781	105.467	-0.314
15	243	104.673	105.185	0.512
15	245	104.615	104.655	0.040
30	222	104.966	104.835	-0.131
30	223	102.681	102.571	-0.110
30	225	102.667	102.742	0.075
30	226	103.613	103.779	0.166
30	227	104.493	105.066	0.573
35	228	104.367	104.463	0.096
35	229	103.446	103.500	0.054
35	231	102.683	102.546	-0.137
35	232	101.386	100.933	-0.453
35	233	103.697	103.133	-0.564
50	210	101.472	101.863	0.391
50	211	103.144	103.326	0.182
50	212	103.065	102.686	-0.379
50	213	102.977	103.514	0.537
50	214	102.204	102.362	0.158
55	215	105.697	106.239	0.542
55	216	101.766	102.378	0.612
55	217	105.787	106.209	0.422
55	218	105.289	104.835	-0.454
55	220	105.093	105.863	0.770
100	247	105.675	106.474	0.799
100	249	104.164	103.395	-0.769
100	250	105.966	106.350	0.384
100	251	103.210	101.870	-1.340
100	253	104.739	105.550	0.811
100	255	104.890	105.174	0.284
100	256	102.303	102.633	0.330
100	257	105.486	104.328	-1.158
100	258	104.235	103.988	-0.247
100	259	101.507	101.293	-0.214
100	260	101.655	102.142	0.487
100	261	104.464	104.215	-0.249
100	262	101.960	102.058	0.098
100	263	103.697	103.305	-0.392
100	264	103.432	103.728	0.296
100	265	103.432	103.309	-0.123
100	266	103.753	104.224	0.471
100	267	103.432	103.910	0.478
100	268	104.357	103.373	-0.984
100	269	103.514	103.576	0.062
100	270	102.492	102.517	0.025
100	271	102.566	102.318	-0.248
100	273	103.390	104.294	0.904
105	274	104.575	103.929	-0.646
105	275	105.095	105.419	0.324
105	276	104.346	103.943	-0.403
105	277	103.828	103.341	-0.487
105	278	105.489	105.938	0.449
Max		106.262	106.474	0.904
Average		103.838	103.851	0.013
Min		101.120	100.933	-1.340
Std Dev		1.251	1.333	0.438

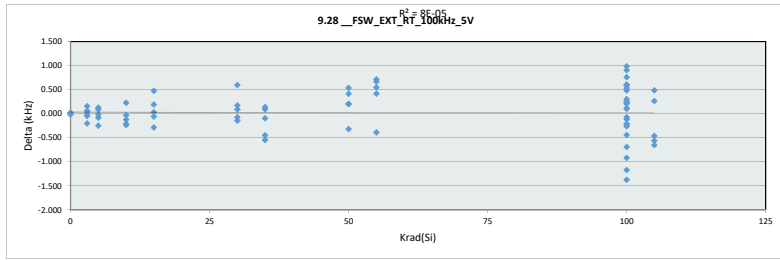


9.4_FSW_EXT_RT_100kHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	115	kHz									
Min Limit	95	kHz									
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	104.782	101.162	102.003	102.917	103.307	102.571	100.933	101.863	102.378	101.293	103.341
Average	104.843	103.120	103.668	104.543	104.558	103.799	102.915	102.750	105.105	103.653	104.514
Max	104.906	104.927	104.733	105.978	105.467	105.066	104.463	103.514	106.239	106.474	105.938
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

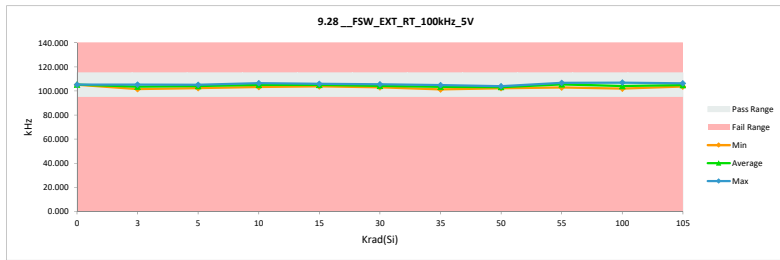


HDR TID Report TPS7H5002-SP

9.28_FSW_EXT_RT_100kHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	115	115		
Min Limit	95	95		
Krad(S)	Serial #	PRE	POST	Delta
0	189	105.189	105.193	0.004
0	190	105.053	105.069	0.016
0	191	105.271	105.247	-0.024
3	300	103.662	103.610	-0.052
3	305	101.374	101.526	0.152
3	309	105.186	105.242	0.056
3	311	103.967	103.759	-0.208
3	313	103.118	103.117	-0.001
5	319	104.742	104.719	-0.023
5	321	103.189	103.273	0.084
5	324	105.156	105.067	-0.089
5	327	102.570	102.314	-0.256
5	331	104.667	104.788	0.121
10	234	105.639	105.860	0.221
10	235	103.465	103.249	-0.216
10	236	104.456	104.416	-0.040
10	237	106.600	106.358	-0.242
10	238	104.720	104.592	-0.128
15	239	104.550	104.484	-0.066
15	240	103.509	103.694	0.185
15	241	106.133	105.842	-0.291
15	243	105.022	105.494	0.472
15	245	105.073	105.096	0.023
30	222	105.334	105.187	-0.147
30	223	103.003	102.924	-0.079
30	225	103.020	103.104	0.084
30	226	103.976	104.142	0.166
30	227	104.826	105.415	0.589
35	228	104.746	104.830	0.084
35	229	103.749	103.881	0.132
35	231	103.042	102.940	-0.102
35	232	101.712	101.256	-0.456
35	233	104.029	103.478	-0.551
50	210	101.796	102.203	0.407
50	211	103.549	103.748	0.199
50	212	103.386	103.060	-0.326
50	213	103.370	103.901	0.531
50	214	102.527	102.726	0.199
55	215	106.096	106.637	0.541
55	216	102.087	102.749	0.662
55	217	106.125	106.537	0.412
55	218	105.558	105.163	-0.395
55	220	105.482	106.193	0.711
100	247	106.078	106.830	0.752
100	249	104.464	103.767	-0.697
100	250	106.276	106.864	0.588
100	251	103.637	102.255	-1.382
100	253	104.974	105.955	0.981
100	255	105.260	105.510	0.250
100	256	102.690	102.915	0.225
100	257	105.881	104.703	-1.178
100	258	104.628	104.405	-0.223
100	259	101.908	101.830	-0.078
100	260	102.005	102.532	0.527
100	261	104.817	104.549	-0.268
100	262	102.205	102.405	0.200
100	263	104.049	103.600	-0.449
100	264	103.777	104.073	0.296
100	265	103.732	103.609	-0.123
100	266	104.041	104.518	0.477
100	267	103.724	104.324	0.600
100	268	104.721	103.796	-0.925
100	269	103.910	104.005	0.095
100	270	102.826	102.938	0.112
100	271	102.874	102.653	-0.221
100	273	103.721	104.621	0.900
105	274	104.900	104.241	-0.659
105	275	105.450	105.707	0.257
105	276	104.625	104.158	-0.467
105	277	104.188	103.620	-0.568
105	278	105.806	106.288	0.482
Max		106.600	106.864	0.981
Average		104.182	104.208	0.026
Min		101.374	101.256	-1.382
Std Dev		1.256	1.331	0.446

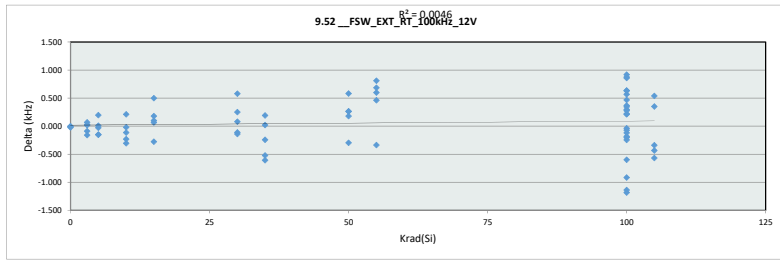


9.28_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	115	kHz									
Min Limit	95	kHz									
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	105.069	101.526	102.314	103.249	103.694	102.924	101.256	102.203	102.749	101.830	103.620
Average	105.170	103.451	104.032	104.895	104.922	104.154	103.277	103.128	105.456	104.029	104.803
Max	105.247	105.242	105.067	106.358	105.842	105.415	104.830	103.901	106.637	106.864	106.288
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

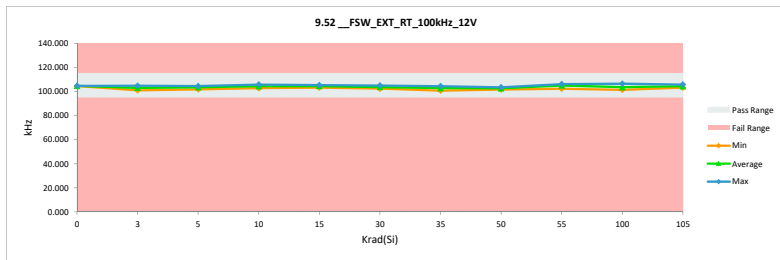


HDR TID Report TPS7H5002-SP

		9.52_FSW_EXT_RT_100kHz_12V		
Test Site	Tester			
Test Number	Unit	kHz	kHz	
Max Limit	Min Limit	115	95	115
Min Limit		95		95
Krad(Si)	Serial #	PRE	POST	Delta
0	189	104.692	104.693	0.001
0	190	104.541	104.517	-0.024
0	191	104.722	104.713	-0.009
3	300	103.129	103.040	-0.089
3	305	100.943	101.012	0.069
3	309	104.741	104.759	0.018
3	311	103.495	103.336	-0.159
3	313	102.652	102.679	0.027
5	319	104.206	104.176	-0.030
5	321	102.750	102.757	0.007
5	324	104.650	104.498	-0.152
5	327	101.972	101.820	-0.152
5	331	104.094	104.291	0.197
10	234	105.150	105.362	0.212
10	235	102.997	102.766	-0.231
10	236	103.985	103.964	-0.021
10	237	106.099	105.794	-0.305
10	238	104.275	104.162	-0.113
15	239	103.939	104.039	0.100
15	240	103.044	103.224	0.180
15	241	105.584	105.307	-0.277
15	243	104.564	105.063	0.499
15	245	104.509	104.573	0.064
30	222	104.837	104.697	-0.140
30	223	102.503	102.391	-0.112
30	225	102.526	102.607	0.081
30	226	103.444	103.695	0.251
30	227	104.374	104.952	0.578
35	228	104.265	104.455	0.190
35	229	103.374	103.394	0.020
35	231	102.650	102.408	-0.242
35	232	101.238	100.717	-0.521
35	233	103.543	102.937	-0.606
50	210	101.414	101.680	0.266
50	211	103.065	103.245	0.180
50	212	102.882	102.586	-0.296
50	213	102.910	103.491	0.581
50	214	102.029	102.291	0.262
55	215	105.585	106.184	0.599
55	216	101.621	102.304	0.683
55	217	105.607	106.069	0.462
55	218	104.995	104.658	-0.337
55	220	104.874	105.783	0.809
100	247	105.604	106.463	0.859
100	249	103.921	103.322	-0.599
100	250	105.845	106.411	0.566
100	251	103.097	101.913	-1.184
100	253	104.545	105.419	0.874
100	255	104.756	105.033	0.277
100	256	102.238	102.535	0.297
100	257	105.400	104.262	-1.138
100	258	104.130	103.946	-0.184
100	259	101.423	101.385	-0.038
100	260	101.567	102.198	0.631
100	261	104.361	104.157	-0.204
100	262	101.764	101.974	0.210
100	263	103.477	103.233	-0.244
100	264	103.327	103.694	0.367
100	265	103.332	103.213	-0.119
100	266	103.649	104.116	0.467
100	267	103.224	103.861	0.637
100	268	104.237	103.322	-0.915
100	269	103.294	103.638	0.344
100	270	102.356	102.575	0.219
100	271	102.342	102.271	-0.071
100	273	103.276	104.194	0.918
105	274	104.420	103.852	-0.568
105	275	104.981	105.331	0.350
105	276	104.127	103.788	-0.339
105	277	103.666	103.233	-0.433
105	278	105.278	105.818	0.540
	Max	106.099	106.463	0.918
	Average	103.693	103.750	0.057
	Min	100.943	100.717	-1.184
	Std Dev	1.244	1.326	0.436

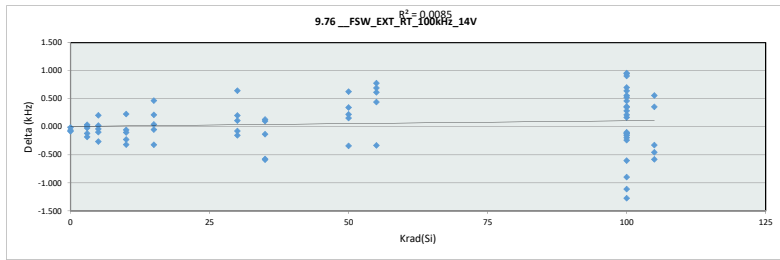


		9.52_FSW_EXT											
Test Site	Tester												
Test Number	Unit	kHz	kHz										
Max Limit	Min Limit	115	95										
		kHz	kHz										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000		
Min	104.517	101.012	101.820	102.766	103.224	102.391	100.717	101.680	102.304	101.385	103.233		
Average	104.641	102.965	103.508	104.410	104.441	103.668	102.782	102.659	105.000	103.615	104.404		
Max	104.713	104.759	104.498	105.794	105.307	104.952	104.455	103.491	106.184	106.463	105.818		
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000		

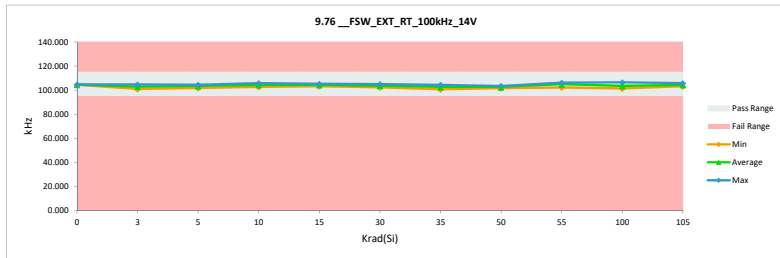


HDR TID Report
TPS7H5002-SP

9.76_FSW_EXT_RT_100kHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	115	115		
Min Limit	95	95		
Krad(S)	Serial #	PRE	POST	Delta
0	189	104.712	104.657	-0.055
0	190	104.568	104.492	-0.076
0	191	104.706	104.698	-0.008
3	300	103.135	103.021	-0.114
3	305	100.952	100.994	0.042
3	309	104.764	104.778	0.014
3	311	103.456	103.279	-0.177
3	313	102.683	102.667	-0.016
5	319	104.246	104.212	-0.034
5	321	102.704	102.730	0.026
5	324	104.600	104.508	-0.092
5	327	102.086	101.826	-0.260
5	331	104.110	104.318	0.208
10	234	105.124	105.356	0.232
10	235	103.003	102.780	-0.223
10	236	103.995	103.945	-0.050
10	237	106.092	105.780	-0.312
10	238	104.256	104.161	-0.095
15	239	103.970	104.017	0.047
15	240	103.030	103.248	0.218
15	241	105.589	105.272	-0.317
15	243	104.552	105.024	0.472
15	245	104.555	104.511	-0.044
30	222	104.850	104.703	-0.147
30	223	102.480	102.408	-0.072
30	225	102.518	102.633	0.115
30	226	103.484	103.689	0.205
30	227	104.309	104.958	0.649
35	228	104.270	104.404	0.134
35	229	103.288	103.394	0.106
35	231	102.539	102.411	-0.128
35	232	101.259	100.690	-0.569
35	233	103.546	102.965	-0.581
50	210	101.367	101.716	0.349
50	211	103.089	103.250	0.161
50	212	102.927	102.590	-0.337
50	213	102.866	103.496	0.630
50	214	102.024	102.249	0.225
55	215	105.563	106.184	0.621
55	216	101.581	102.279	0.698
55	217	105.638	106.085	0.447
55	218	105.033	104.706	-0.327
55	220	104.972	105.755	0.783
100	247	105.486	106.436	0.950
100	249	103.959	103.361	-0.598
100	250	105.804	106.510	0.706
100	251	103.129	101.861	-1.268
100	253	104.519	105.429	0.910
100	255	104.727	105.081	0.354
100	256	102.213	102.578	0.365
100	257	105.373	104.266	-1.107
100	258	104.076	103.964	-0.112
100	259	101.454	101.361	-0.093
100	260	101.571	102.133	0.562
100	261	104.331	104.180	-0.151
100	262	101.764	102.054	0.290
100	263	103.435	103.202	-0.233
100	264	103.272	103.739	0.467
100	265	103.308	103.187	-0.121
100	266	103.623	104.154	0.531
100	267	103.206	103.852	0.646
100	268	104.232	103.339	-0.893
100	269	103.350	103.567	0.217
100	270	102.332	102.503	0.171
100	271	102.413	102.223	-0.190
100	273	103.258	104.219	0.961
105	274	104.406	103.831	-0.575
105	275	104.971	105.331	0.360
105	276	104.141	103.819	-0.322
105	277	103.689	103.238	-0.451
105	278	105.282	105.844	0.562
Max		106.092	106.510	0.961
Average		103.688	103.748	0.060
Min		100.952	100.690	-1.268
Std Dev		1.240	1.332	0.451

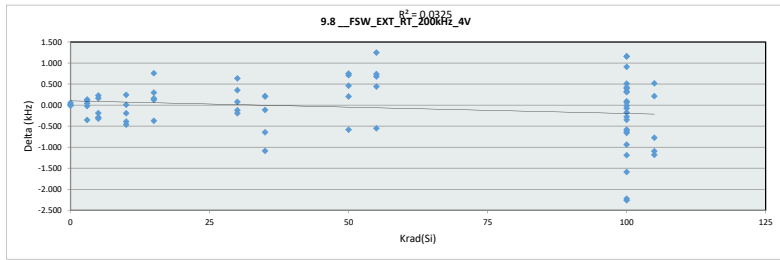


9.76_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	115	kHz									
Min Limit	95	kHz									
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	104.492	100.994	101.826	102.780	103.248	102.408	100.690	101.716	102.279	101.361	103.238
Average	104.616	102.948	103.519	104.404	104.414	103.678	102.773	102.660	105.002	103.617	104.413
Max	104.698	104.778	104.508	105.780	105.272	104.958	104.404	103.496	106.184	106.510	105.844
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

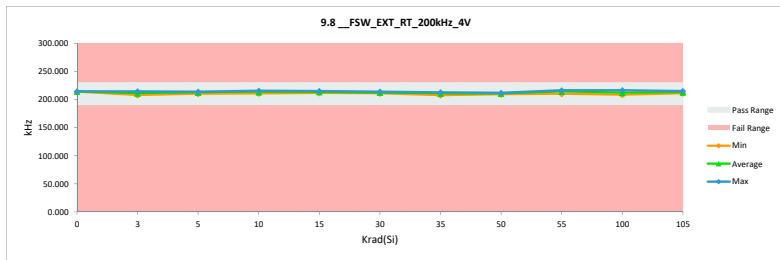


HDR TID Report TPS7H5002-SP

9.8_FSW_EXT_RT_200kHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	214.077	214.105	0.028
0	190	214.598	214.661	0.063
0	191	214.758	214.753	-0.005
3	300	211.534	211.658	0.124
3	305	208.000	208.143	0.143
3	309	214.633	214.617	-0.016
3	311	211.946	211.599	-0.347
3	313	210.699	210.749	0.050
5	319	214.364	214.182	-0.182
5	321	211.267	211.438	0.171
5	324	214.440	214.160	-0.280
5	327	210.632	210.317	-0.315
5	331	213.734	213.969	0.235
10	234	214.942	215.195	0.253
10	235	211.423	211.041	-0.382
10	236	213.166	213.182	0.016
10	237	216.222	215.765	-0.457
10	238	213.089	212.904	-0.185
15	239	212.744	212.912	0.168
15	240	211.462	211.768	0.306
15	241	215.569	215.203	-0.366
15	243	214.201	214.965	0.764
15	245	214.339	214.471	0.132
30	222	214.277	214.162	-0.115
30	223	211.312	211.126	-0.186
30	225	210.981	211.066	0.085
30	226	212.275	212.635	0.360
30	227	213.528	214.173	0.645
35	228	212.697	212.910	0.213
35	229	211.919	212.140	0.221
35	231	210.615	210.511	-0.104
35	232	208.504	207.867	-0.637
35	233	212.304	211.227	-1.077
50	210	208.675	209.431	0.756
50	211	211.123	211.592	0.469
50	212	211.847	211.270	-0.577
50	213	211.226	211.947	0.721
50	214	210.443	210.656	0.213
55	215	215.597	216.286	0.689
55	216	209.458	210.203	0.745
55	217	216.034	216.483	0.449
55	218	214.615	214.074	-0.541
55	220	214.468	215.723	1.255
100	247	215.583	216.499	0.916
100	249	213.829	212.649	-1.180
100	250	216.294	216.611	0.317
100	251	211.851	209.596	-2.255
100	253	213.821	214.987	1.166
100	255	215.150	215.142	-0.008
100	256	210.456	210.855	0.399
100	257	214.796	212.582	-2.214
100	258	213.814	213.240	-0.574
100	259	208.928	208.657	-0.271
100	260	208.925	209.255	0.330
100	261	213.814	213.161	-0.653
100	262	209.905	210.004	0.099
100	263	212.099	211.170	-0.929
100	264	211.768	211.841	0.073
100	265	212.548	212.383	-0.165
100	266	212.516	213.034	0.518
100	267	212.273	212.707	0.434
100	268	213.749	212.167	-1.582
100	269	211.635	211.568	-0.067
100	270	210.605	210.265	-0.340
100	271	210.499	209.886	-0.613
100	273	211.716	212.881	1.165
105	274	214.237	213.150	-1.087
105	275	215.105	215.325	0.220
105	276	213.322	212.150	-1.172
105	277	212.048	211.281	-0.767
105	278	214.565	215.090	0.525
Max		216.294	216.611	1.255
Average		212.670	212.611	-0.059
Min		208.000	207.867	-2.255
Std Dev		2.029	2.119	0.682

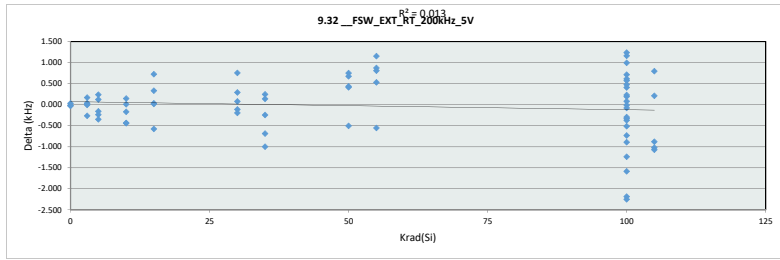


9.8_FSW_EXT_RT_200kHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	214.105	208.143	210.317	211.041	211.768	211.066	207.867	209.431	210.203	208.657	211.281
Average	214.506	211.353	212.813	213.617	213.864	212.632	210.931	210.979	214.554	212.223	213.399
Max	214.753	214.617	214.182	215.765	215.203	214.173	212.910	211.947	216.483	216.611	215.325
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

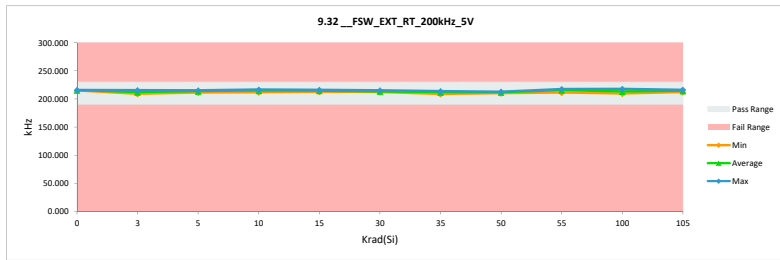


HDR TID Report TPS7H5002-SP

9.32 __FSW_EXT_RT_200kHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	215.063	215.102	0.039
0	190	215.541	215.519	-0.022
0	191	216.096	216.081	-0.015
3	300	212.536	212.576	0.040
3	305	208.923	209.097	0.174
3	309	215.587	215.586	-0.001
3	311	212.855	212.595	-0.260
3	313	211.742	211.759	0.017
5	319	215.486	215.255	-0.231
5	321	212.270	212.398	0.128
5	324	215.375	215.221	-0.154
5	327	211.704	211.360	-0.344
5	331	214.823	215.066	0.243
10	234	216.075	216.226	0.151
10	235	212.488	212.058	-0.430
10	236	214.231	214.247	0.016
10	237	217.302	216.868	-0.434
10	238	214.142	213.978	-0.164
15	239	213.862	213.883	0.021
15	240	212.456	212.793	0.337
15	241	216.671	216.098	-0.573
15	243	215.288	216.016	0.728
15	245	215.441	215.478	0.037
30	222	215.404	215.213	-0.191
30	223	212.240	212.133	-0.107
30	225	212.035	212.119	0.084
30	226	213.362	213.657	0.295
30	227	214.557	215.319	0.762
35	228	213.849	213.992	0.143
35	229	212.931	213.180	0.249
35	231	211.705	211.465	-0.240
35	232	209.538	208.857	-0.681
35	233	213.325	212.332	-0.993
50	210	209.669	210.350	0.681
50	211	212.209	212.650	0.441
50	212	212.843	212.342	-0.501
50	213	212.248	213.006	0.758
50	214	211.183	211.599	0.416
55	215	216.685	217.498	0.813
55	216	210.398	211.277	0.879
55	217	217.026	217.561	0.535
55	218	215.678	215.128	-0.550
55	220	215.590	216.759	1.160
100	247	216.713	217.712	0.999
100	249	215.026	213.794	-1.232
100	250	217.327	217.948	0.621
100	251	212.991	210.811	-2.180
100	253	214.746	215.914	1.168
100	255	216.212	216.298	0.086
100	256	211.517	211.998	0.481
100	257	215.906	213.659	-2.247
100	258	214.861	214.354	-0.507
100	259	210.097	209.809	-0.288
100	260	209.948	210.362	0.414
100	261	214.785	214.420	-0.365
100	262	210.791	211.025	0.234
100	263	213.192	212.303	-0.889
100	264	212.732	212.930	0.198
100	265	213.494	213.422	-0.072
100	266	213.518	214.235	0.717
100	267	213.220	213.799	0.579
100	268	214.802	213.223	-1.579
100	269	212.752	212.749	-0.003
100	270	211.737	211.412	-0.325
100	271	211.514	210.788	-0.726
100	273	212.696	213.941	1.245
105	274	215.285	214.220	-1.065
105	275	216.125	216.340	0.215
105	276	214.228	213.212	-1.016
105	277	213.077	212.201	-0.876
105	278	215.570	216.373	0.803
	Max	217.327	217.948	1.245
	Average	213.708	213.675	-0.033
	Min	208.923	208.857	-2.247
	Std Dev	2.054	2.147	0.697

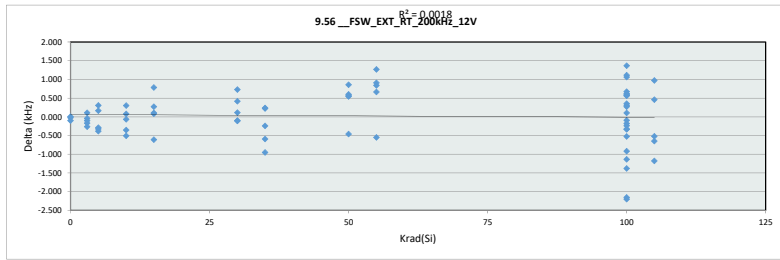


9.32 __FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	215.102	209.097	211.360	212.058	212.793	212.119	208.857	210.350	211.277	209.809	212.201
Average	215.567	212.323	213.860	214.675	214.854	213.688	211.965	211.989	215.645	213.344	214.469
Max	216.081	215.586	215.255	216.868	216.098	215.319	213.992	213.006	217.561	217.948	216.373
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

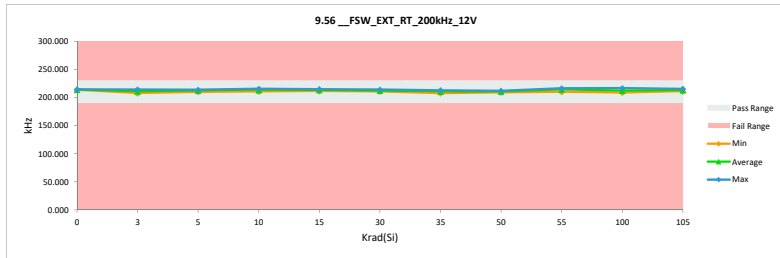


HDR TID Report
TPS7H5002-SP

9.56_FSW_EXT_RT_200kHz_12V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	213.907	213.891	-0.016
0	190	214.242	214.254	0.012
0	191	214.803	214.709	-0.094
3	300	211.317	211.282	-0.035
3	305	207.816	207.933	0.117
3	309	214.545	214.385	-0.160
3	311	211.740	211.486	-0.254
3	313	210.671	210.578	-0.093
5	319	214.238	213.923	-0.315
5	321	211.175	211.347	0.172
5	324	214.235	213.952	-0.283
5	327	210.523	210.149	-0.374
5	331	213.593	213.908	0.315
10	234	214.976	215.283	0.307
10	235	211.337	210.991	-0.346
10	236	213.116	213.203	0.087
10	237	216.209	215.711	-0.498
10	238	213.043	212.986	-0.057
15	239	212.737	212.851	0.114
15	240	211.369	211.648	0.279
15	241	215.534	214.934	-0.600
15	243	214.156	214.950	0.794
15	245	214.263	214.354	0.091
30	222	214.189	214.097	-0.092
30	223	211.103	211.001	-0.102
30	225	210.873	210.993	0.120
30	226	212.220	212.643	0.423
30	227	213.488	214.224	0.736
35	228	212.657	212.888	0.231
35	229	211.836	212.081	0.245
35	231	210.626	210.390	-0.236
35	232	208.387	207.804	-0.583
35	233	212.167	211.222	-0.945
50	210	208.621	209.230	0.609
50	211	211.016	211.568	0.552
50	212	211.693	211.243	-0.450
50	213	211.156	212.021	0.865
50	214	210.039	210.611	0.572
55	215	215.544	216.394	0.850
55	216	209.319	210.236	0.917
55	217	215.803	216.474	0.671
55	218	214.433	213.833	-0.540
55	220	214.414	215.689	1.275
100	247	215.648	216.717	1.069
100	249	213.852	212.723	-1.129
100	250	216.202	216.822	0.620
100	251	211.884	209.690	-2.194
100	253	213.746	214.867	1.121
100	255	214.986	215.264	0.278
100	256	210.408	210.989	0.581
100	257	214.713	212.568	-2.145
100	258	213.651	213.423	-0.228
100	259	208.977	208.891	-0.086
100	260	208.881	209.497	0.616
100	261	213.710	213.389	-0.321
100	262	209.684	210.000	0.316
100	263	212.068	211.157	-0.911
100	264	211.667	212.026	0.359
100	265	212.464	212.139	-0.325
100	266	212.479	213.049	0.570
100	267	212.122	212.802	0.680
100	268	213.708	212.336	-1.372
100	269	211.615	211.727	0.112
100	270	210.673	210.504	-0.169
100	271	210.386	209.869	-0.517
100	273	211.614	212.990	1.376
105	274	214.466	213.296	-1.170
105	275	215.001	215.467	0.466
105	276	212.767	212.126	-0.641
105	277	211.831	211.317	-0.514
105	278	214.451	215.431	0.980
Max		216.209	216.822	1.376
Average		212.574	212.598	0.024
Min		207.816	207.804	-2.194
Std Dev		2.039	2.130	0.695

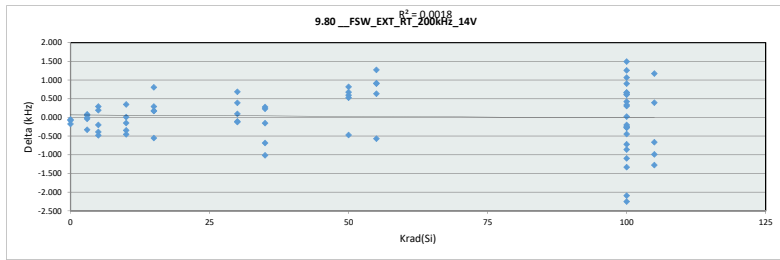


9.56_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	213.891	207.933	210.149	210.991	211.648	210.993	207.804	209.230	210.236	208.891	211.317
Average	214.285	211.133	212.656	213.635	213.747	212.592	210.877	210.935	214.537	212.323	213.527
Max	214.709	214.385	213.952	215.711	214.950	214.224	212.888	212.021	216.474	216.822	215.467
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

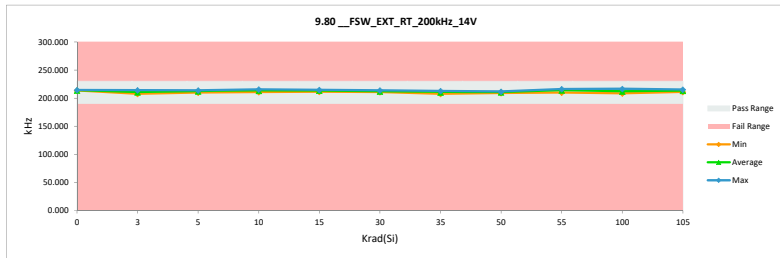


HDR TID Report TPS7H5002-SP

9.80_FSW_EXT_RT_200kHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(S)	Serial #	PRE	POST	Delta
0	189	213.929	213.880	-0.049
0	190	214.309	214.235	-0.074
0	191	214.878	214.711	-0.167
3	300	211.263	211.266	0.003
3	305	207.802	207.891	0.089
3	309	214.423	214.393	-0.030
3	311	211.755	211.433	-0.322
3	313	210.557	210.625	0.068
5	319	214.380	213.909	-0.471
5	321	211.152	211.354	0.202
5	324	214.206	214.011	-0.195
5	327	210.557	210.177	-0.380
5	331	213.554	213.855	0.301
10	234	214.923	215.279	0.356
10	235	211.381	210.936	-0.445
10	236	213.137	213.152	0.015
10	237	216.123	215.784	-0.339
10	238	213.046	212.906	-0.140
15	239	212.665	212.846	0.181
15	240	211.360	211.659	0.299
15	241	215.466	214.921	-0.545
15	243	214.055	214.867	0.812
15	245	214.179	214.362	0.183
30	222	214.193	214.092	-0.101
30	223	211.076	210.960	-0.116
30	225	210.898	211.002	0.104
30	226	212.247	212.643	0.396
30	227	213.436	214.129	0.693
35	228	212.703	212.942	0.239
35	229	211.814	212.101	0.287
35	231	210.540	210.392	-0.148
35	232	208.421	207.746	-0.675
35	233	212.188	211.180	-1.008
50	210	208.565	209.248	0.683
50	211	211.047	211.582	0.535
50	212	211.647	211.183	-0.464
50	213	211.185	212.012	0.827
50	214	209.969	210.570	0.601
55	215	215.515	216.426	0.911
55	216	209.258	210.183	0.925
55	217	215.789	216.429	0.640
55	218	214.385	213.824	-0.561
55	220	214.438	215.718	1.280
100	247	215.634	216.708	1.074
100	249	213.903	212.813	-1.090
100	250	216.100	216.775	0.675
100	251	211.946	209.703	-2.243
100	253	213.568	214.833	1.265
100	255	214.960	215.273	0.313
100	256	210.381	211.008	0.627
100	257	214.691	212.607	-2.084
100	258	213.650	213.385	-0.265
100	259	209.003	208.808	-0.195
100	260	208.865	209.480	0.615
100	261	213.704	213.474	-0.230
100	262	209.670	210.102	0.432
100	263	212.042	211.191	-0.851
100	264	211.570	211.913	0.343
100	265	212.442	212.178	-0.264
100	266	212.420	213.085	0.665
100	267	212.013	212.922	0.909
100	268	213.638	212.313	-1.325
100	269	211.670	211.701	0.031
100	270	210.921	210.484	-0.437
100	271	210.433	209.720	-0.713
100	273	211.603	213.106	1.503
105	274	214.461	213.192	-1.269
105	275	215.037	215.440	0.403
105	276	213.159	212.180	-0.979
105	277	212.009	211.354	-0.655
105	278	214.371	215.552	1.181
Max		216.123	216.775	1.503
Average		212.567	212.593	0.026
Min		207.802	207.746	-2.243
Std Dev		2.030	2.137	0.727

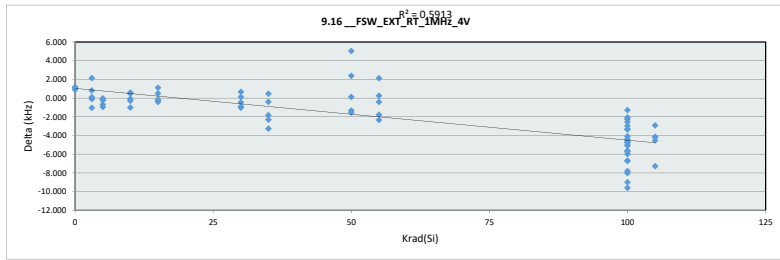


9.80_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	213.880	207.891	210.177	210.936	211.659	210.960	207.746	209.248	210.183	208.808	211.354
Average	214.275	211.122	212.661	213.611	213.731	212.565	210.872	210.919	214.516	212.330	213.544
Max	214.711	214.393	214.011	215.784	214.921	214.129	212.942	212.012	216.429	216.775	215.552
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

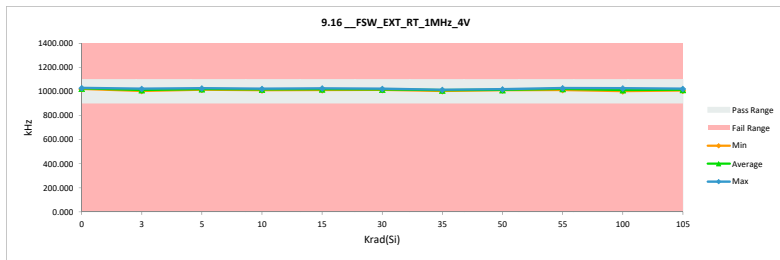


HDR TID Report
TPS7H5002-SP

9.16_FSW_EXT_RT_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	1100	1100		
Min Limit	900	900		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1020.267	1021.191	0.924
0	190	1027.422	1028.636	1.214
0	191	1028.904	1030.017	1.113
3	300	1014.384	1016.555	2.171
3	305	1004.926	1005.768	0.842
3	309	1023.285	1023.421	0.136
3	311	1012.036	1011.035	-1.001
3	313	1010.997	1010.939	-0.058
5	319	1027.798	1027.654	-0.144
5	321	1016.230	1016.234	0.004
5	324	1023.538	1022.627	-0.911
5	327	1017.873	1017.221	-0.652
5	331	1023.700	1023.524	-0.176
10	234	1022.508	1023.023	0.515
10	235	1012.092	1011.129	-0.963
10	236	1018.668	1019.290	0.622
10	237	1023.401	1023.156	-0.245
10	238	1015.970	1015.949	-0.021
15	239	1015.083	1014.968	-0.115
15	240	1012.813	1012.550	-0.263
15	241	1025.587	1025.225	-0.362
15	243	1019.547	1020.704	1.157
15	245	1026.167	1026.699	0.532
30	222	1022.689	1023.400	0.711
30	223	1017.156	1016.734	-0.422
30	225	1014.778	1013.916	-0.862
30	226	1018.025	1018.185	0.160
30	227	1019.789	1018.800	-0.989
35	228	1012.356	1012.852	0.496
35	229	1014.481	1014.106	-0.375
35	231	1013.215	1011.426	-1.789
35	232	1007.184	1004.912	-2.272
35	233	1018.297	1015.069	-3.228
50	210	1006.167	1011.240	5.073
50	211	1008.936	1011.352	2.416
50	212	1020.647	1019.352	-1.295
50	213	1012.104	1012.263	0.159
50	214	1018.327	1016.825	-1.502
55	215	1028.595	1028.898	0.303
55	216	1013.873	1013.503	-0.370
55	217	1027.987	1026.253	-1.734
55	218	1025.288	1022.984	-2.304
55	220	1020.183	1022.336	2.153
100	247	1026.011	1024.776	-1.235
100	249	1027.756	1022.756	-5.000
100	250	1031.813	1027.136	-4.677
100	251	1014.950	1005.986	-8.964
100	253	1021.519	1019.308	-2.211
100	255	1029.877	1024.210	-5.667
100	256	1012.861	1009.588	-3.273
100	257	1022.016	1012.459	-9.557
100	258	1025.447	1019.498	-5.949
100	259	1008.826	1004.773	-4.053
100	260	1007.459	1002.722	-4.737
100	261	1021.037	1015.455	-5.582
100	262	1016.308	1011.950	-4.358
100	263	1018.873	1010.899	-7.974
100	264	1013.206	1008.171	-5.035
100	265	1021.932	1018.612	-3.320
100	266	1015.672	1013.640	-2.032
100	267	1016.459	1013.524	-2.935
100	268	1021.642	1013.875	-7.767
100	269	1012.572	1008.037	-4.535
100	270	1015.156	1008.495	-6.661
100	271	1012.585	1005.899	-6.686
100	273	1011.771	1009.254	-2.517
105	274	1024.825	1020.326	-4.499
105	275	1027.588	1023.476	-4.112
105	276	1018.764	1011.523	-7.241
105	277	1013.169	1009.041	-4.128
105	278	1017.365	1014.478	-2.887
	Max	1031.813	1030.017	5.073
	Average	1018.461	1016.504	-1.957
	Min	1004.926	1002.722	-9.557
	Std Dev	6.354	6.744	2.950

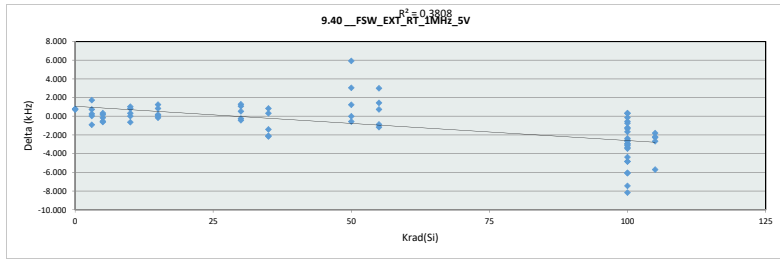


9.16_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	1100	kHz									
Min Limit	900	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1021.191	1005.768	1016.234	1011.129	1012.550	1013.916	1004.912	1011.240	1013.503	1002.722	1009.041
Average	1026.615	1013.544	1021.452	1018.509	1020.029	1018.207	1011.673	1014.206	1022.795	1013.523	1015.769
Max	1030.017	1023.421	1027.654	1023.156	1026.699	1023.400	1015.069	1019.352	1028.898	1027.136	1023.476
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

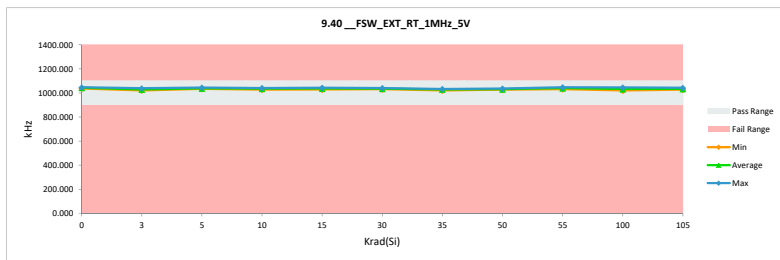


HDR TID Report TPS7H5002-SP

9.40_FSW_EXT_RT_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	1100	1100		
Min Limit	900	900		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1036.452	1037.194	0.742
0	190	1043.987	1044.816	0.829
0	191	1046.072	1046.854	0.782
3	300	1031.266	1033.035	1.769
3	305	1019.814	1020.574	0.760
3	309	1038.373	1038.659	0.286
3	311	1028.202	1027.329	-0.873
3	313	1026.407	1026.481	0.074
5	319	1045.163	1045.039	-0.124
5	321	1033.329	1033.704	0.375
5	324	1039.715	1039.217	-0.498
5	327	1034.706	1034.127	-0.579
5	331	1040.573	1040.778	0.205
10	234	1039.376	1040.434	1.058
10	235	1028.444	1027.840	-0.604
10	236	1035.577	1036.436	0.859
10	237	1040.655	1041.013	0.358
10	238	1033.795	1033.839	0.044
15	239	1031.228	1031.451	0.223
15	240	1028.180	1028.264	0.084
15	241	1043.161	1043.039	-0.122
15	243	1035.338	1036.623	1.285
15	245	1042.724	1043.599	0.875
30	222	1039.512	1040.826	1.314
30	223	1033.804	1034.374	0.570
30	225	1030.900	1030.695	-0.205
30	226	1034.778	1035.886	1.108
30	227	1037.033	1036.635	-0.398
35	228	1027.451	1028.319	0.868
35	229	1031.505	1031.860	0.355
35	231	1029.948	1028.579	-1.369
35	232	1023.341	1021.207	-2.134
35	233	1034.711	1032.661	-2.050
50	210	1022.019	1027.983	5.964
50	211	1023.600	1026.686	3.086
50	212	1037.046	1037.074	0.028
50	213	1028.703	1029.975	1.272
50	214	1035.068	1034.542	-0.526
55	215	1045.672	1047.142	1.470
55	216	1031.016	1031.776	0.760
55	217	1043.111	1042.268	-0.843
55	218	1042.955	1041.816	-1.139
55	220	1036.053	1039.076	3.023
100	247	1043.703	1044.073	0.370
100	249	1044.813	1041.936	-2.877
100	250	1049.505	1046.425	-3.080
100	251	1030.992	1023.578	-7.414
100	253	1039.254	1039.150	-0.104
100	255	1047.504	1044.070	-3.434
100	256	1029.895	1028.718	-1.177
100	257	1038.833	1030.698	-8.135
100	258	1042.102	1037.760	-4.342
100	259	1025.901	1024.277	-1.624
100	260	1023.164	1020.214	-2.950
100	261	1038.035	1034.738	-3.297
100	262	1032.313	1029.977	-2.336
100	263	1036.901	1030.880	-6.021
100	264	1029.894	1027.255	-2.639
100	265	1039.487	1038.178	-1.309
100	266	1032.312	1032.656	0.344
100	267	1032.376	1031.851	-0.525
100	268	1038.795	1032.737	-6.058
100	269	1028.742	1026.223	-2.519
100	270	1032.394	1027.601	-4.793
100	271	1029.136	1024.329	-4.807
100	273	1026.866	1026.164	-0.702
105	274	1041.003	1038.831	-2.172
105	275	1044.888	1042.653	-2.235
105	276	1036.064	1030.397	-5.667
105	277	1030.561	1027.921	-2.640
105	278	1031.990	1030.229	-1.761
	Max	1049.505	1047.142	5.964
	Average	1035.045	1034.130	-0.915
	Min	1019.814	1020.214	-8.135
	Std Dev	6.752	6.868	2.432

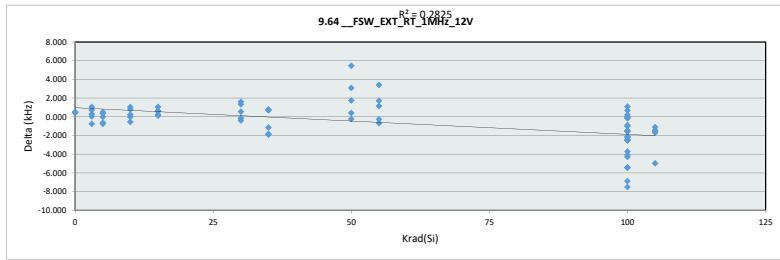


9.40_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	1100	kHz									
Min Limit	900	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1037.194	1020.574	1033.704	1027.840	1028.264	1030.695	1021.207	1026.686	1031.776	1020.214	1027.921
Average	1042.955	1029.216	1038.573	1035.912	1036.595	1035.683	1028.525	1031.252	1040.416	1032.326	1034.006
Max	1046.854	1038.659	1045.039	1041.013	1043.599	1040.826	1032.661	1037.074	1047.142	1046.425	1042.653
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

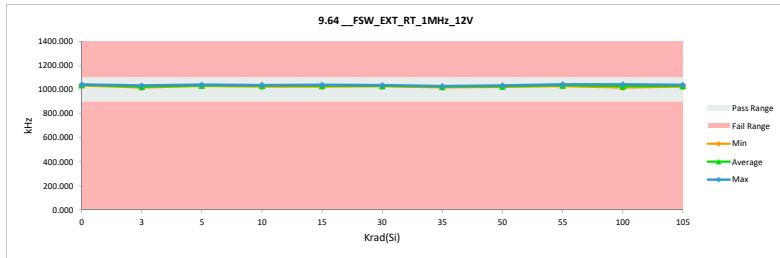


HDR TID Report
TPS7H5002-SP

9.64_FSW_EXT_RT_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	1100	1100		
Min Limit	900	900		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1032.561	1033.078	0.517
0	190	1040.004	1040.554	0.550
0	191	1041.993	1042.456	0.463
3	300	1027.487	1028.580	1.093
3	305	1015.922	1016.674	0.752
3	309	1034.261	1034.590	0.329
3	311	1024.373	1023.667	-0.706
3	313	1022.335	1022.406	0.071
5	319	1040.952	1040.971	0.019
5	321	1029.768	1030.295	0.527
5	324	1035.776	1035.060	-0.716
5	327	1030.769	1030.180	-0.589
5	331	1036.785	1037.188	0.403
10	234	1035.858	1036.944	1.086
10	235	1024.768	1024.265	-0.503
10	236	1031.764	1032.605	0.841
10	237	1037.000	1037.259	0.259
10	238	1030.265	1030.287	0.022
15	239	1027.257	1027.880	0.623
15	240	1024.307	1024.574	0.267
15	241	1038.925	1039.079	0.154
15	243	1031.621	1032.717	1.096
15	245	1038.797	1039.459	0.662
30	222	1035.521	1036.887	1.366
30	223	1029.801	1030.391	0.590
30	225	1027.133	1027.011	-0.122
30	226	1030.671	1032.316	1.645
30	227	1033.396	1033.043	-0.353
35	228	1023.615	1024.447	0.832
35	229	1027.799	1028.519	0.720
35	231	1026.240	1025.136	-1.104
35	232	1019.551	1017.672	-1.879
35	233	1030.704	1028.909	-1.795
50	210	1018.540	1024.034	5.494
50	211	1019.609	1022.719	3.110
50	212	1033.235	1033.678	0.443
50	213	1025.075	1026.865	1.790
50	214	1031.432	1031.236	-0.196
55	215	1041.683	1043.426	1.743
55	216	1027.183	1028.360	1.177
55	217	1038.918	1038.689	-0.229
55	218	1039.334	1038.687	-0.647
55	220	1031.844	1035.281	3.437
100	247	1040.216	1040.938	0.722
100	249	1040.920	1038.727	-2.193
100	250	1045.494	1042.979	-2.515
100	251	1026.980	1020.135	-6.845
100	253	1035.762	1036.003	0.241
100	255	1043.513	1041.081	-2.432
100	256	1026.330	1026.208	-0.122
100	257	1034.755	1027.285	-7.470
100	258	1038.126	1034.461	-3.665
100	259	1022.320	1021.346	-0.974
100	260	1019.408	1017.332	-2.076
100	261	1034.206	1031.812	-2.394
100	262	1028.308	1026.875	-1.433
100	263	1033.098	1027.677	-5.421
100	264	1026.285	1024.774	-1.511
100	265	1035.888	1035.029	-0.859
100	266	1028.646	1029.782	1.136
100	267	1028.447	1028.415	-0.032
100	268	1034.992	1029.631	-5.361
100	269	1024.818	1023.313	-1.505
100	270	1028.749	1024.488	-4.261
100	271	1025.323	1021.275	-4.048
100	273	1023.019	1023.059	0.040
105	274	1037.058	1035.557	-1.501
105	275	1040.903	1039.482	-1.421
105	276	1032.486	1027.543	-4.943
105	277	1026.769	1025.114	-1.655
105	278	1027.961	1026.882	-1.079
	Max	1045.494	1043.426	5.494
	Average	1031.206	1030.638	-0.568
	Min	1015.922	1016.674	-7.470
	Std Dev	6.700	6.745	2.200

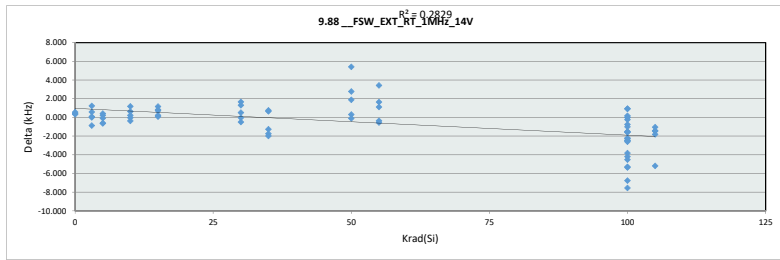


9.64_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	1100	kHz									
Min Limit	900	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1033.078	1016.674	1030.180	1024.265	1024.574	1027.011	1017.672	1022.719	1028.360	1017.332	1025.114
Average	1038.696	1025.183	1034.739	1032.272	1032.742	1031.930	1024.937	1027.706	1036.889	1029.245	1030.916
Max	1042.456	1034.590	1040.971	1037.259	1039.459	1036.887	1028.909	1033.678	1043.426	1042.979	1039.482
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

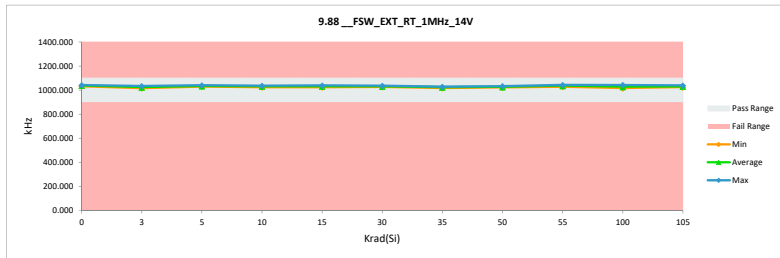


HDR TID Report
TPS7H5002-SP

9.88_FSW_EXT_RT_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	1100	1100		
Min Limit	900	900		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1032.475	1032.931	0.456
0	190	1040.042	1040.397	0.355
0	191	1041.883	1042.481	0.598
3	300	1027.211	1028.468	1.257
3	305	1015.956	1016.568	0.612
3	309	1034.188	1034.306	0.118
3	311	1024.334	1023.490	-0.844
3	313	1022.337	1022.362	0.025
5	319	1040.937	1040.880	-0.057
5	321	1029.721	1030.168	0.447
5	324	1035.569	1034.989	-0.580
5	327	1030.526	1029.931	-0.595
5	331	1036.808	1037.062	0.254
10	234	1035.694	1036.910	1.216
10	235	1024.603	1024.261	-0.342
10	236	1031.729	1032.388	0.659
10	237	1036.879	1037.137	0.258
10	238	1030.105	1030.098	-0.007
15	239	1027.085	1027.833	0.748
15	240	1024.206	1024.465	0.259
15	241	1038.810	1038.933	0.123
15	243	1031.381	1032.585	1.204
15	245	1038.564	1039.420	0.856
30	222	1035.540	1036.880	1.340
30	223	1029.786	1030.339	0.553
30	225	1027.038	1027.035	-0.003
30	226	1030.591	1032.284	1.693
30	227	1033.297	1032.829	-0.468
35	228	1023.526	1024.207	0.681
35	229	1027.630	1028.422	0.792
35	231	1026.223	1024.982	-1.241
35	232	1019.483	1017.532	-1.951
35	233	1030.545	1028.829	-1.716
50	210	1018.512	1023.953	5.441
50	211	1019.657	1022.469	2.812
50	212	1033.218	1033.564	0.346
50	213	1024.971	1026.882	1.911
50	214	1031.204	1031.134	-0.070
55	215	1041.684	1043.353	1.669
55	216	1027.100	1028.237	1.137
55	217	1038.899	1038.568	-0.331
55	218	1039.043	1038.524	-0.519
55	220	1031.768	1035.232	3.464
100	247	1039.971	1040.904	0.933
100	249	1040.799	1038.558	-2.241
100	250	1045.320	1042.751	-2.569
100	251	1026.800	1020.063	-6.737
100	253	1035.608	1035.805	0.197
100	255	1043.279	1040.805	-2.474
100	256	1026.304	1026.102	-0.202
100	257	1034.741	1027.230	-7.511
100	258	1038.151	1034.365	-3.786
100	259	1022.274	1021.298	-0.976
100	260	1019.320	1017.135	-2.185
100	261	1034.138	1031.723	-2.415
100	262	1028.243	1026.761	-1.482
100	263	1033.006	1027.745	-5.261
100	264	1026.257	1024.698	-1.559
100	265	1035.788	1035.065	-0.723
100	266	1028.554	1029.529	0.975
100	267	1028.311	1028.348	0.037
100	268	1034.910	1029.574	-5.336
100	269	1024.722	1023.188	-1.534
100	270	1028.674	1024.188	-4.486
100	271	1025.293	1021.132	-4.161
100	273	1022.900	1022.957	0.057
105	274	1036.844	1035.444	-1.400
105	275	1040.870	1039.442	-1.428
105	276	1032.369	1027.205	-5.164
105	277	1026.672	1024.882	-1.790
105	278	1027.841	1026.829	-1.012
Max		1045.320	1043.353	5.441
Average		1031.109	1030.522	-0.587
Min		1015.956	1016.568	-7.511
Std Dev		6.683	6.750	2.205

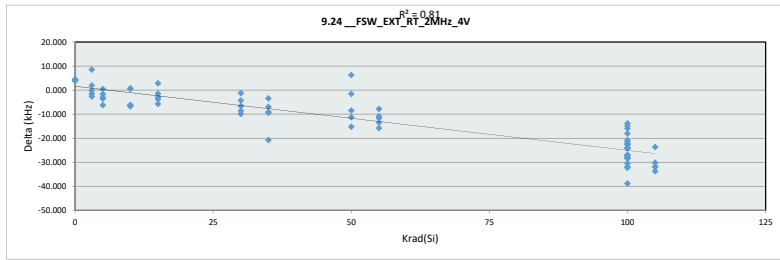


9.88_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	1100	kHz									
Min Limit	900	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1032.931	1016.568	1029.931	1024.261	1024.465	1027.035	1017.532	1022.469	1028.237	1017.135	1024.882
Average	1038.603	1025.039	1034.606	1032.159	1032.647	1031.873	1024.794	1027.600	1036.783	1029.127	1030.760
Max	1042.481	1034.306	1040.880	1037.137	1039.420	1036.880	1028.829	1033.564	1043.353	1042.751	1039.442
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

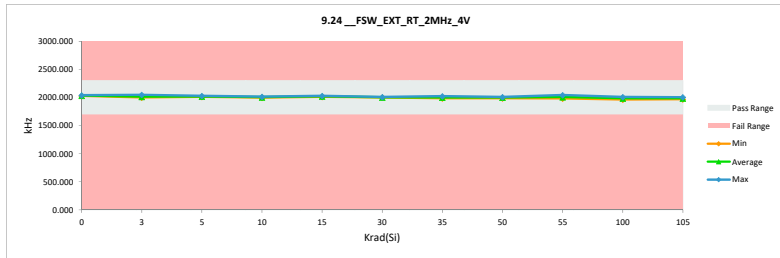


HDR TID Report TPS7H5002-SP

9.24_FSW_EXT_RT_2MHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	2300	2300		
Min Limit	1700	1700		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	2025.567	2029.736	4.169
0	190	2028.687	2033.346	4.659
0	191	2037.282	2041.237	3.955
3	300	2000.242	2008.885	8.643
3	305	1998.732	2000.786	2.054
3	309	2050.602	2049.232	-1.370
3	311	2005.530	2003.040	-2.490
3	313	2017.955	2017.973	0.018
5	319	2028.522	2029.087	0.565
5	321	2017.598	2016.110	-1.488
5	324	2029.180	2023.062	-6.118
5	327	2015.306	2012.413	-2.893
5	331	2025.245	2021.770	-3.475
10	234	2018.112	2012.100	-6.012
10	235	2005.274	1998.647	-6.627
10	236	2015.183	2015.863	0.680
10	237	2016.306	2009.997	-6.309
10	238	2001.198	2002.055	0.857
15	239	2022.890	2017.288	-5.602
15	240	2027.552	2024.884	-2.668
15	241	2016.749	2013.136	-3.613
15	243	2028.664	2031.600	2.936
15	245	2031.222	2029.937	-1.285
30	222	2010.302	2009.171	-1.131
30	223	2009.668	2001.158	-8.510
30	225	2010.582	2006.433	-4.149
30	226	2016.621	2006.880	-9.741
30	227	2007.435	2000.929	-6.506
35	228	2028.456	2025.113	-3.343
35	229	2004.550	1995.254	-9.296
35	231	2005.439	1996.458	-8.981
35	232	1995.641	1988.808	-6.833
35	233	2022.104	2001.473	-20.631
50	210	1995.533	2001.949	6.416
50	211	2011.050	2009.619	-1.431
50	212	2026.688	2011.618	-15.070
50	213	2000.067	1988.897	-11.170
50	214	2005.853	1997.458	-8.395
55	215	2021.607	2010.821	-10.786
55	216	1995.588	1984.100	-11.488
55	217	2060.218	2044.519	-15.699
55	218	2010.091	1996.707	-13.384
55	220	2028.518	2020.795	-7.723
100	247	2024.843	2006.883	-17.960
100	249	2033.522	2006.667	-26.855
100	250	2025.192	2009.348	-15.844
100	251	2034.350	1995.664	-38.686
100	253	1994.200	1980.511	-13.689
100	255	2025.770	1994.058	-31.712
100	256	2001.876	1980.656	-21.220
100	257	2007.605	1975.425	-32.180
100	258	2029.802	2009.100	-20.702
100	259	1992.438	1964.665	-27.773
100	260	1997.896	1975.344	-22.552
100	261	2024.050	1997.226	-26.824
100	262	2020.882	1998.397	-22.485
100	263	1995.049	1964.755	-30.294
100	264	1995.674	1967.422	-28.252
100	265	1999.170	1984.368	-14.802
100	266	2000.637	1976.523	-24.114
100	267	2016.294	1992.364	-23.930
100	268	2015.152	1987.948	-27.204
100	269	2010.280	1981.953	-28.327
100	270	1999.017	1976.925	-22.092
100	271	1998.200	1969.987	-28.213
100	273	2021.611	1997.797	-23.814
105	274	2038.698	2006.901	-31.797
105	275	2025.673	1993.826	-31.847
105	276	2005.179	1975.122	-30.057
105	277	1994.247	1970.682	-23.565
105	278	2025.919	1992.280	-33.639
	Max	2060.218	2049.232	8.643
	Average	2015.251	2002.439	-12.813
	Min	1992.438	1964.665	-38.686
	Std Dev	14.411	19.354	12.136

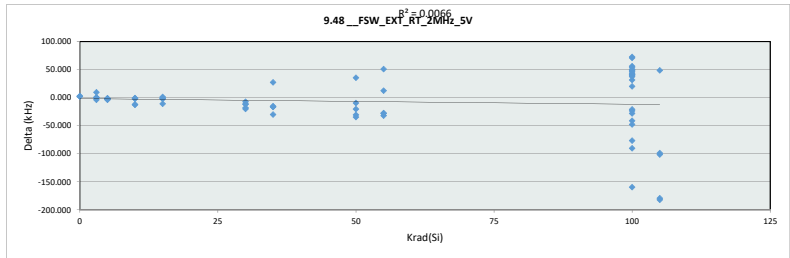


9.24_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	2300	kHz									
Min Limit	1700	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2029.736	2000.786	2012.413	1998.647	2013.136	2000.929	1988.808	1988.897	1984.100	1964.665	1970.682
Average	2034.773	2015.983	2020.488	2007.732	2023.369	2004.914	2001.421	2001.908	2011.388	1986.695	1987.762
Max	2041.237	2049.232	2029.087	2015.863	2031.600	2009.171	2025.113	2011.618	2044.519	2009.348	2006.901
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000

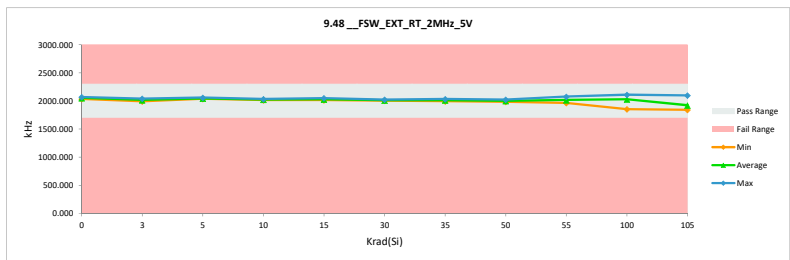


HDR TID Report
TPS7H5002-SP

9.48_FSW_EXT_RT_2MHz_5V				
Test Site	Tester	Test Number	Unit	
			kHz	kHz
Max Limit	2300	2300		
Min Limit	1700	1700		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	2035.012	2037.584	2.572
0	190	2056.161	2059.055	2.894
0	191	2066.698	2069.799	3.101
3	300	2015.374	2025.311	9.937
3	305	1992.720	1993.098	0.378
3	309	2041.045	2041.893	0.848
3	311	2017.857	2014.304	-3.553
3	313	2011.228	2012.147	0.919
5	319	2060.257	2059.716	-0.541
5	321	2041.494	2040.480	-1.014
5	324	2042.840	2039.396	-3.444
5	327	2039.374	2037.232	-2.142
5	331	2050.914	2049.084	-1.830
10	234	2042.763	2031.101	-11.662
10	235	2015.581	2014.225	-1.356
10	236	2034.923	2034.563	-0.360
10	237	2038.320	2035.964	-2.356
10	238	2020.981	2020.252	-0.729
15	239	2032.318	2030.482	-1.836
15	240	2016.042	2014.492	-1.550
15	241	2033.483	2022.836	-10.647
15	243	2025.187	2025.246	0.059
15	245	2048.355	2050.078	1.723
30	222	2016.504	2005.141	-11.363
30	223	2031.284	2011.640	-19.644
30	225	2020.945	2014.196	-6.749
30	226	2036.155	2025.256	-10.899
30	227	2024.411	2006.677	-17.734
35	228	2006.974	2034.581	27.607
35	229	2023.784	2009.087	-14.697
35	231	2026.531	2010.605	-15.926
35	232	2010.236	1994.238	-15.998
35	233	2045.708	2015.915	-29.793
50	210	2003.433	1994.396	-9.037
50	211	1989.070	2025.042	35.972
50	212	2046.638	2016.482	-30.156
50	213	2020.903	1986.770	-34.133
50	214	2005.424	1985.580	-19.844
55	215	2043.088	2011.292	-31.796
55	216	1992.864	1965.325	-27.539
55	217	2049.069	2061.967	12.898
55	218	2002.949	1975.967	-26.982
55	220	2025.841	2037.147	11.306
100	247	2046.714	2103.264	56.550
100	249	2058.731	1968.760	-89.971
100	250	2038.541	2111.592	73.051
100	251	2022.642	1994.920	-27.722
100	253	1974.814	1951.736	-23.078
100	255	2043.836	1967.566	-76.270
100	256	2025.078	2075.780	50.702
100	257	2027.607	2076.664	49.057
100	258	2049.953	2091.671	41.718
100	259	2012.910	1853.643	-159.267
100	260	2008.896	2040.978	32.082
100	261	2045.886	2089.721	43.835
100	262	2038.086	2085.026	46.940
100	263	1979.712	1932.229	-47.483
100	264	2012.486	2067.384	54.898
100	265	1986.661	1945.912	-40.749
100	266	2014.378	2034.842	20.464
100	267	2023.511	2063.438	39.927
100	268	2037.958	2079.812	41.854
100	269	2009.069	2047.010	37.941
100	270	2005.777	2077.258	71.481
100	271	2013.056	2063.215	50.159
100	273	2005.968	1985.546	-20.422
105	274	2049.147	2098.317	49.170
105	275	2050.416	1871.405	-179.011
105	276	2024.170	1842.365	-181.805
105	277	2003.915	1905.425	-98.490
105	278	2003.643	1902.506	-101.137
	Max	2066.698	2111.592	73.051
	Average	2025.554	2018.361	-7.193
	Min	1974.814	1842.365	-181.805
	Std Dev	20.140	55.372	50.429



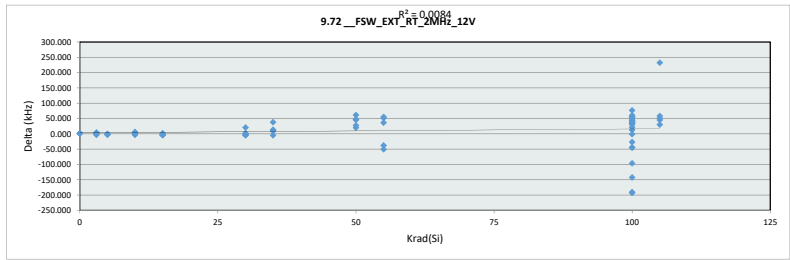
9.48_FSW_EXT											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			2300	1700	kHz	kHz					
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2037.584	1993.098	2037.232	2014.225	2014.492	2005.141	1994.238	1985.580	1965.325	1853.643	1842.365
Average	2055.479	2017.351	2045.182	2025.221	2028.627	2012.582	2012.885	2001.654	2018.340	2030.781	1924.004
Max	2069.799	2041.893	2059.716	2034.563	2050.078	2025.256	2034.581	2025.042	2077.147	2111.592	2098.317
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



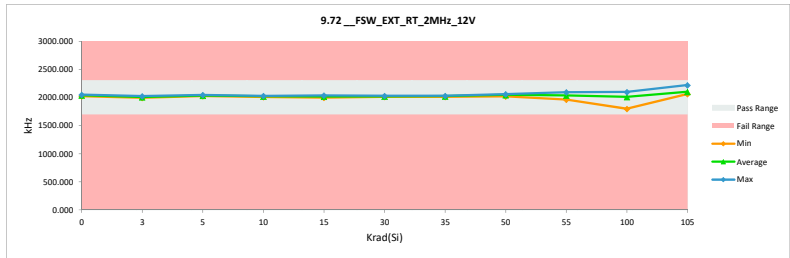
HDR TID Report

TPS7H5002-SP

		9.72 _FSW_EXT_RT_2MHz_12V		
Test Site	Tester			
Test Number	Unit	kHz	kHz	
Max Limit	2300		2300	
Min Limit	1700		1700	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	2022.009	2023.864	1.855
0	190	2047.262	2049.619	2.357
0	191	2049.079	2051.712	2.633
3	300	2012.171	2016.664	4.493
3	305	1998.977	2003.770	4.793
3	309	2025.164	2025.950	0.786
3	311	2005.321	2003.018	-2.303
3	313	1995.885	1996.419	0.534
5	319	2046.884	2047.000	0.116
5	321	2030.329	2030.602	0.273
5	324	2031.034	2029.942	-1.092
5	327	2030.261	2028.548	-1.713
5	331	2040.802	2040.100	-0.702
10	234	2030.987	2029.739	-1.248
10	235	2003.731	2010.944	7.213
10	236	2022.265	2023.445	1.180
10	237	2025.235	2023.308	-1.927
10	238	2024.993	2027.674	2.681
15	239	2017.300	2016.193	-1.107
15	240	2001.629	1998.969	-2.660
15	241	2033.536	2030.401	-3.135
15	243	2012.206	2010.426	-1.780
15	245	2035.927	2038.933	3.006
30	222	2016.649	2013.521	-3.128
30	223	2021.570	2021.596	0.026
30	225	2010.016	2031.590	21.574
30	226	2022.312	2025.627	3.315
30	227	2021.356	2017.956	-3.400
35	228	1992.734	2031.694	38.960
35	229	2013.822	2028.284	14.462
35	231	2017.323	2028.385	11.062
35	232	2001.024	2012.464	11.440
35	233	2033.734	2029.841	-3.893
50	210	1993.193	2055.861	62.668
50	211	1975.028	2021.740	46.712
50	212	2033.652	2062.294	28.642
50	213	2010.033	2058.856	48.823
50	214	2024.242	2045.392	21.150
55	215	2041.898	2094.629	52.731
55	216	2014.154	1964.977	-49.177
55	217	2036.432	2073.833	37.401
55	218	2034.917	1998.031	-36.886
55	220	2011.064	2067.223	56.159
100	247	2032.669	2086.030	53.361
100	249	2043.094	2099.956	56.862
100	250	2052.562	2094.274	41.712
100	251	2006.513	1816.856	-189.657
100	253	1985.851	1942.481	-43.370
100	255	2041.814	1900.206	-141.608
100	256	2014.412	2060.667	46.255
100	257	2020.149	2058.414	38.265
100	258	2037.719	2051.864	14.145
100	259	2006.983	2064.644	57.661
100	260	1998.600	1954.841	-43.759
100	261	2032.096	2064.674	32.578
100	262	2027.026	2049.619	22.593
100	263	1997.320	1901.906	-95.414
100	264	2003.439	2045.301	41.862
100	265	2007.106	2084.762	77.656
100	266	2001.900	2063.393	61.493
100	267	2011.265	2011.245	-0.020
100	268	2022.127	2054.651	32.524
100	269	1996.432	1970.532	-25.900
100	270	2017.236	2062.489	45.253
100	271	2002.705	2039.213	36.508
100	273	1992.686	1800.461	-192.225
105	274	2033.761	2064.881	31.120
105	275	2042.021	2094.171	52.150
105	276	2025.801	2071.893	46.092
105	277	2009.689	2068.843	59.154
105	278	1989.097	2222.253	233.156
	Max	2052.562	2222.253	233.156
	Average	2018.595	2028.754	10.160
	Min	1975.028	1800.461	-192.225
	Std Dev	17.167	58.530	55.927

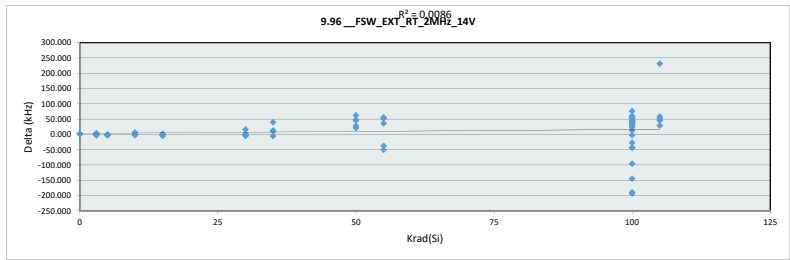


		9.72 _FSW_EXT										
Test Site	Tester											
Test Number	Unit	kHz	kHz									
Max Limit	2300		2300									
Min Limit	1700		1700									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2023.864	1996.419	2028.548	2010.944	1998.969	2013.521	2012.464	2021.740	1964.977	1800.461	2064.881	
Average	2041.732	2009.164	2035.238	2023.022	2018.984	2022.058	2026.134	2048.829	2039.739	2012.108	2104.408	
Max	2051.712	2025.950	2047.000	2029.739	2038.933	2031.590	2031.694	2062.294	2094.629	2099.956	2222.253	
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000

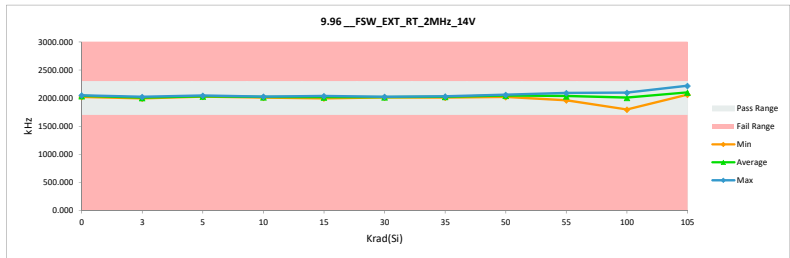


HDR TID Report
TPS7H5002-SP

9.96_FSW_EXT_RT_2MHz_14V				
Test Site	Tester	Max Limit	Min Limit	
		2300 kHz	1700 kHz	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	2021.381	2023.728	2.347
0	190	2046.968	2048.955	1.987
0	191	2048.750	2051.960	3.210
3	300	2011.785	2016.349	4.564
3	305	1999.360	2003.608	4.248
3	309	2025.567	2025.374	-0.193
3	311	2004.960	2002.490	-2.470
3	313	1995.406	1995.997	0.591
5	319	2046.793	2046.969	0.176
5	321	2029.930	2030.203	0.273
5	324	2030.863	2028.954	-1.909
5	327	2029.633	2028.201	-1.432
5	331	2040.461	2039.980	-0.481
10	234	2030.690	2029.320	-1.370
10	235	2003.478	2010.758	7.280
10	236	2022.034	2022.815	0.781
10	237	2024.240	2023.051	-1.189
10	238	2024.854	2027.305	2.451
15	239	2017.495	2016.303	-1.192
15	240	2001.221	1998.172	-3.049
15	241	2033.296	2029.531	-3.765
15	243	2012.040	2009.802	-2.238
15	245	2035.544	2038.791	3.247
30	222	2015.962	2013.004	-2.958
30	223	2021.486	2021.852	0.366
30	225	2009.514	2026.846	17.332
30	226	2022.198	2025.050	2.852
30	227	2020.750	2017.293	-3.457
35	228	1992.269	2033.207	40.938
35	229	2013.798	2027.590	13.792
35	231	2016.902	2028.613	11.711
35	232	2000.607	2012.540	11.933
35	233	2033.437	2028.950	-4.487
50	210	1993.077	2056.239	63.162
50	211	1974.750	2021.591	46.841
50	212	2033.254	2061.915	28.661
50	213	2009.704	2058.057	48.353
50	214	2023.575	2045.559	21.984
55	215	2041.446	2094.368	52.922
55	216	2013.345	1964.159	-49.186
55	217	2036.416	2073.502	37.086
55	218	2034.307	1997.590	-36.717
55	220	2010.768	2067.455	56.687
100	247	2032.932	2085.319	52.387
100	249	2042.588	2099.505	56.917
100	250	2052.302	2093.467	41.165
100	251	2006.684	1818.319	-188.365
100	253	1985.122	1943.070	-42.052
100	255	2041.475	1897.806	-143.669
100	256	2014.377	2059.901	45.524
100	257	2019.296	2058.208	38.912
100	258	2037.885	2051.891	14.006
100	259	2006.782	2064.827	58.045
100	260	1998.857	1955.585	-43.272
100	261	2032.503	2064.025	31.522
100	262	2026.548	2050.978	24.430
100	263	1996.569	1902.108	-94.461
100	264	2002.939	2045.865	42.926
100	265	2006.688	2083.636	76.948
100	266	2001.373	2063.209	61.836
100	267	2010.807	2009.438	-1.369
100	268	2021.986	2055.562	33.576
100	269	1996.213	1970.296	-25.917
100	270	2016.692	2061.622	44.930
100	271	2002.465	2038.902	36.437
100	273	1992.212	1799.733	-192.479
105	274	2033.518	2063.591	30.073
105	275	2041.829	2094.153	52.324
105	276	2025.008	2071.350	46.342
105	277	2009.279	2068.255	58.976
105	278	1988.985	2221.512	232.527
	Max	2052.302	2221.512	232.527
	Average	2018.285	2028.396	10.111
	Min	1974.750	1799.733	-192.479
	Std Dev	17.188	58.466	55.885

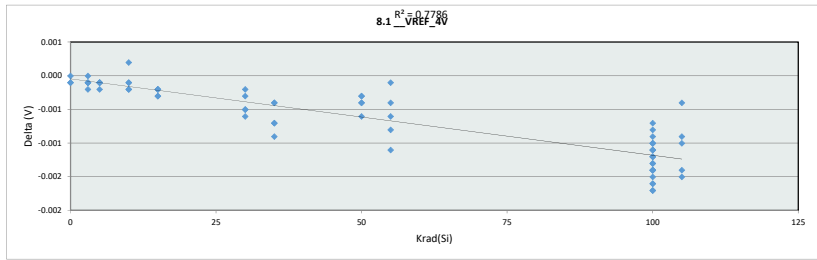


9.96_FSW_EXT										
Test Site	Tester	Max Limit	Min Limit							
		2300 kHz	1700 kHz							
Krad(Si)	LL	Min	Average	Max	UL					
0	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
3	2023.728	1995.997	2028.201	2010.758	1998.172	2013.004	2012.540	2021.591	1964.159	1799.733
5	2023.728	1995.997	2028.201	2010.758	1998.172	2013.004	2012.540	2021.591	1964.159	1799.733
10	2023.728	1995.997	2028.201	2010.758	1998.172	2013.004	2012.540	2021.591	1964.159	1799.733
15	2023.728	1995.997	2028.201	2010.758	1998.172	2013.004	2012.540	2021.591	1964.159	1799.733
30	2023.728	1995.997	2028.201	2010.758	1998.172	2013.004	2012.540	2021.591	1964.159	1799.733
35	2023.728	1995.997	2028.201	2010.758	1998.172	2013.004	2012.540	2021.591	1964.159	1799.733
50	2023.728	1995.997	2028.201	2010.758	1998.172	2013.004	2012.540	2021.591	1964.159	1799.733
55	2023.728	1995.997	2028.201	2010.758	1998.172	2013.004	2012.540	2021.591	1964.159	1799.733
100	2023.728	1995.997	2028.201	2010.758	1998.172	2013.004	2012.540	2021.591	1964.159	1799.733
105	2023.728	1995.997	2028.201	2010.758	1998.172	2013.004	2012.540	2021.591	1964.159	1799.733

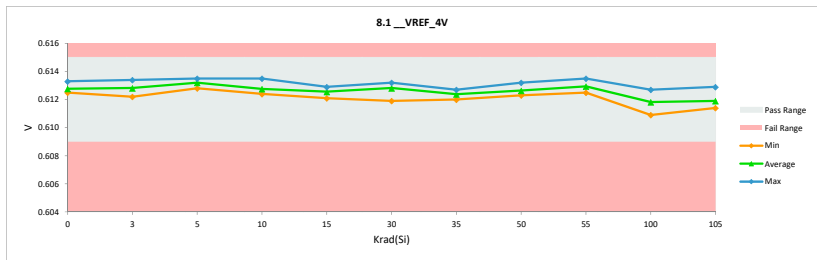


HDR TID Report
TPS7H5002-SP

		8.1_VREF_4V		
Test Site				
Tester				
Test Number				
Unit		V	V	
Max Limit		0.615	0.615	
Min Limit		0.609	0.609	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.613	0.613	0.000
0	190	0.613	0.613	0.000
0	191	0.613	0.613	0.000
3	300	0.612	0.612	0.000
3	305	0.613	0.613	0.000
3	309	0.613	0.613	0.000
3	311	0.613	0.613	0.000
3	313	0.612	0.612	0.000
5	319	0.613	0.613	0.000
5	321	0.613	0.613	0.000
5	324	0.614	0.613	0.000
5	327	0.614	0.613	0.000
5	331	0.613	0.613	0.000
10	234	0.612	0.612	0.000
10	235	0.613	0.613	0.000
10	236	0.613	0.612	0.000
10	237	0.614	0.613	0.000
10	238	0.613	0.613	0.000
15	239	0.612	0.612	0.000
15	240	0.613	0.613	0.000
15	241	0.613	0.613	0.000
15	243	0.613	0.613	0.000
15	245	0.613	0.613	0.000
30	222	0.613	0.613	0.000
30	223	0.614	0.613	0.000
30	225	0.613	0.613	0.000
30	226	0.614	0.613	0.000
30	227	0.613	0.612	-0.001
35	228	0.613	0.612	-0.001
35	229	0.613	0.613	0.000
35	231	0.613	0.613	0.000
35	232	0.613	0.612	-0.001
35	233	0.613	0.612	-0.001
50	210	0.613	0.612	0.000
50	211	0.613	0.613	0.000
50	212	0.613	0.613	0.000
50	213	0.613	0.613	0.000
50	214	0.613	0.612	-0.001
55	215	0.614	0.613	0.000
55	216	0.614	0.613	-0.001
55	217	0.613	0.613	-0.001
55	218	0.613	0.613	0.000
55	220	0.614	0.613	-0.001
100	247	0.614	0.612	-0.001
100	249	0.613	0.612	-0.001
100	250	0.613	0.612	-0.002
100	251	0.613	0.611	-0.002
100	253	0.613	0.612	-0.001
100	255	0.613	0.611	-0.001
100	256	0.613	0.612	-0.001
100	257	0.613	0.612	-0.001
100	258	0.613	0.612	-0.002
100	259	0.612	0.611	-0.001
100	260	0.613	0.612	-0.001
100	261	0.613	0.612	-0.001
100	262	0.613	0.612	-0.001
100	263	0.613	0.612	-0.002
100	264	0.613	0.612	-0.001
100	265	0.613	0.611	-0.001
100	266	0.613	0.612	-0.001
100	267	0.614	0.612	-0.002
100	268	0.613	0.611	-0.001
100	269	0.613	0.613	-0.001
100	270	0.613	0.611	-0.001
100	271	0.612	0.611	-0.001
100	273	0.613	0.612	-0.001
105	274	0.613	0.612	-0.001
105	275	0.613	0.612	-0.002
105	276	0.613	0.612	-0.001
105	277	0.613	0.613	0.000
105	278	0.612	0.611	-0.001
Max		0.614	0.613	0.000
Average		0.613	0.612	-0.001
Min		0.612	0.611	-0.002
Std Dev		0.000	0.001	0.001

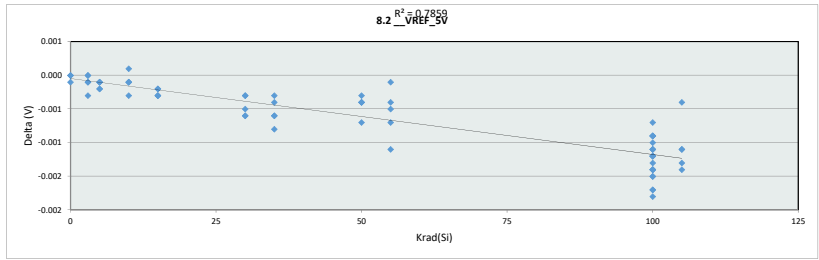


		8.1_VREF_4V										
Test Site												
Tester												
Test Number		0.615	V									
Max Limit		0.615	V									
Min Limit		0.609	V									
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min		0.613	0.612	0.613	0.612	0.612	0.612	0.612	0.612	0.613	0.611	0.611
Average		0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.613	0.613	0.612	0.612
Max		0.613	0.613	0.614	0.614	0.613	0.613	0.613	0.613	0.614	0.613	0.613
UL		0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615

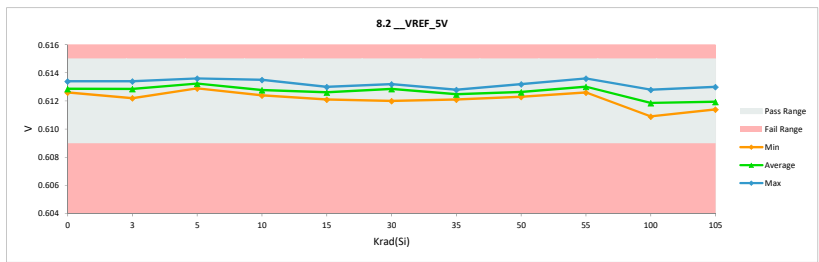


HDR TID Report TPS7H5002-SP

		8.2_VREF_5V		
Test Site				
Tester				
Test Number				
Unit		V	V	
Max Limit		0.615	0.615	
Min Limit		0.609	0.609	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.613	0.613	0.000
0	190	0.613	0.613	0.000
0	191	0.613	0.613	0.000
3	300	0.613	0.612	0.000
3	305	0.613	0.613	0.000
3	309	0.613	0.613	0.000
3	311	0.613	0.613	0.000
3	313	0.612	0.612	0.000
5	319	0.613	0.613	0.000
5	321	0.613	0.613	0.000
5	324	0.614	0.614	0.000
5	327	0.614	0.613	0.000
5	331	0.613	0.613	0.000
10	234	0.612	0.612	0.000
10	235	0.613	0.613	0.000
10	236	0.613	0.613	0.000
10	237	0.614	0.613	0.000
10	238	0.613	0.613	0.000
15	239	0.612	0.612	0.000
15	240	0.613	0.613	0.000
15	241	0.613	0.613	0.000
15	243	0.613	0.613	0.000
15	245	0.613	0.613	0.000
30	222	0.613	0.613	0.000
30	223	0.614	0.613	-0.001
30	225	0.613	0.613	0.000
30	226	0.614	0.613	-0.001
30	227	0.613	0.612	-0.001
35	228	0.613	0.612	-0.001
35	229	0.613	0.613	0.000
35	231	0.613	0.613	0.000
35	232	0.613	0.612	-0.001
35	233	0.613	0.612	-0.001
50	210	0.613	0.612	0.000
50	211	0.613	0.613	0.000
50	212	0.613	0.613	0.000
50	213	0.613	0.613	0.000
50	214	0.613	0.612	-0.001
55	215	0.614	0.614	0.000
55	216	0.614	0.613	0.000
55	217	0.613	0.613	-0.001
55	218	0.613	0.613	0.000
55	220	0.614	0.613	-0.001
100	247	0.614	0.612	-0.001
100	249	0.613	0.612	-0.001
100	250	0.613	0.612	-0.002
100	251	0.613	0.611	-0.002
100	253	0.613	0.612	-0.001
100	255	0.613	0.611	-0.001
100	256	0.613	0.612	-0.001
100	257	0.613	0.612	-0.001
100	258	0.613	0.612	-0.001
100	259	0.612	0.611	-0.001
100	260	0.613	0.612	-0.001
100	261	0.613	0.612	-0.001
100	262	0.613	0.613	-0.001
100	263	0.614	0.612	-0.002
100	264	0.613	0.612	-0.001
100	265	0.613	0.611	-0.002
100	266	0.613	0.612	-0.001
100	267	0.614	0.612	-0.002
100	268	0.613	0.612	-0.001
100	269	0.613	0.613	-0.001
100	270	0.613	0.611	-0.001
100	271	0.612	0.611	-0.001
100	273	0.613	0.612	-0.001
105	274	0.613	0.612	-0.001
105	275	0.613	0.612	-0.001
105	276	0.613	0.612	-0.001
105	277	0.613	0.613	0.000
105	278	0.613	0.611	-0.001
	Max	0.614	0.614	0.000
	Average	0.613	0.612	-0.001
	Min	0.612	0.611	-0.002
	Std Dev	0.000	0.001	0.001

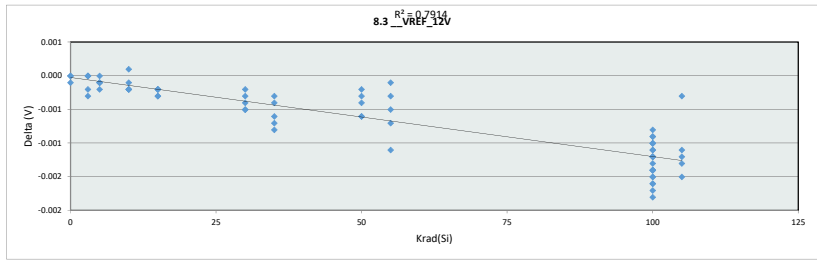


		8.2_VREF_5V										
Test Site												
Tester												
Test Number		0.615	V									
Max Limit		0.615	V									
Min Limit		0.609	V									
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min		0.613	0.612	0.613	0.612	0.612	0.612	0.612	0.612	0.613	0.611	0.611
Average		0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.613	0.613	0.612	0.612
Max		0.613	0.613	0.614	0.614	0.613	0.613	0.613	0.613	0.614	0.613	0.613
UL		0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615

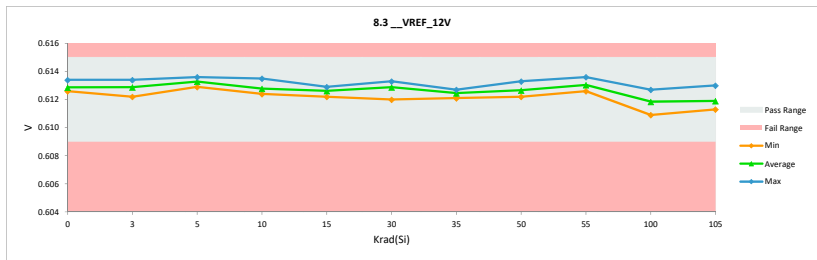


HDR TID Report
TPS7H5002-SP

8.3_VREF_12V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			0.615	0.615
Min Limit			0.609	0.609
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.613	0.613	0.000
0	190	0.613	0.613	0.000
0	191	0.613	0.613	0.000
3	300	0.613	0.612	0.000
3	305	0.613	0.613	0.000
3	309	0.613	0.613	0.000
3	311	0.613	0.613	0.000
3	313	0.612	0.612	0.000
5	319	0.613	0.613	0.000
5	321	0.613	0.613	0.000
5	324	0.614	0.614	0.000
5	327	0.614	0.613	0.000
5	331	0.613	0.613	0.000
10	234	0.612	0.613	0.000
10	235	0.613	0.613	0.000
10	236	0.613	0.612	0.000
10	237	0.614	0.613	0.000
10	238	0.613	0.613	0.000
15	239	0.612	0.612	0.000
15	240	0.613	0.613	0.000
15	241	0.613	0.613	0.000
15	243	0.613	0.613	0.000
15	245	0.613	0.613	0.000
30	222	0.613	0.613	0.000
30	223	0.614	0.613	0.000
30	225	0.613	0.613	0.000
30	226	0.614	0.613	0.000
30	227	0.613	0.612	-0.001
35	228	0.613	0.612	-0.001
35	229	0.613	0.613	0.000
35	231	0.613	0.613	0.000
35	232	0.613	0.612	-0.001
35	233	0.613	0.612	-0.001
50	210	0.613	0.612	-0.001
50	211	0.613	0.613	0.000
50	212	0.613	0.613	0.000
50	213	0.614	0.613	0.000
50	214	0.613	0.612	-0.001
55	215	0.614	0.614	0.000
55	216	0.614	0.613	0.000
55	217	0.613	0.613	-0.001
55	218	0.613	0.613	0.000
55	220	0.614	0.613	-0.001
100	247	0.614	0.612	-0.001
100	249	0.613	0.612	-0.001
100	250	0.613	0.612	-0.002
100	251	0.613	0.611	-0.002
100	253	0.613	0.612	-0.001
100	255	0.613	0.611	-0.001
100	256	0.613	0.612	-0.001
100	257	0.613	0.612	-0.001
100	258	0.613	0.612	-0.002
100	259	0.612	0.611	-0.001
100	260	0.613	0.612	-0.001
100	261	0.613	0.612	-0.001
100	262	0.613	0.613	-0.001
100	263	0.614	0.612	-0.002
100	264	0.613	0.612	-0.001
100	265	0.613	0.611	-0.001
100	266	0.613	0.612	-0.001
100	267	0.614	0.612	-0.002
100	268	0.613	0.611	-0.001
100	269	0.613	0.613	-0.001
100	270	0.613	0.611	-0.001
100	271	0.612	0.611	-0.001
100	273	0.613	0.612	-0.002
105	274	0.613	0.612	-0.001
105	275	0.613	0.612	-0.002
105	276	0.613	0.612	-0.001
105	277	0.613	0.613	0.000
105	278	0.612	0.611	-0.001
Max		0.614	0.614	0.000
Average		0.613	0.612	-0.001
Min		0.612	0.611	-0.002
Std Dev		0.000	0.001	0.001

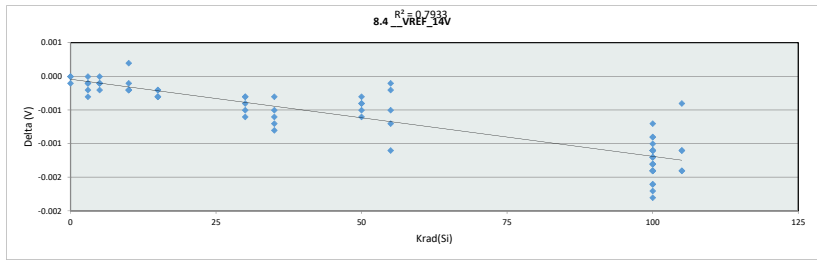


8.3_VREF_12V											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit			0.615	0.615							
Min Limit			0.609	0.609							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.612	0.613	0.612	0.612	0.612	0.612	0.612	0.613	0.611	0.611
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.613	0.613	0.612	0.612
Max	0.613	0.613	0.614	0.614	0.613	0.613	0.613	0.613	0.614	0.613	0.613
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615

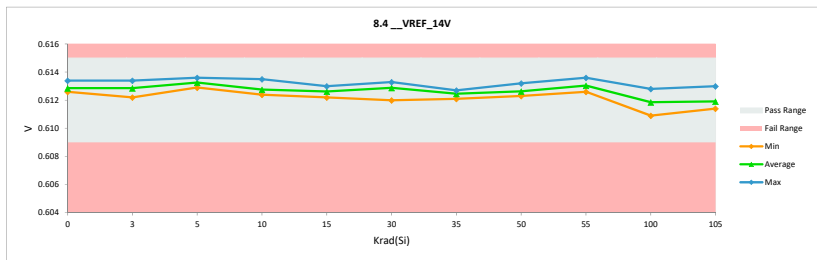


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		8.4_VREF_14V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit		0.615	0.615	
Min Limit		0.609	0.609	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.613	0.613	0.000
0	190	0.613	0.613	0.000
0	191	0.613	0.613	0.000
3	300	0.613	0.612	0.000
3	305	0.613	0.613	0.000
3	309	0.613	0.613	0.000
3	311	0.613	0.613	0.000
3	313	0.613	0.612	0.000
5	319	0.613	0.613	0.000
5	321	0.613	0.613	0.000
5	324	0.614	0.614	0.000
5	327	0.614	0.613	0.000
5	331	0.613	0.613	0.000
10	234	0.612	0.613	0.000
10	235	0.613	0.613	0.000
10	236	0.613	0.612	0.000
10	237	0.614	0.613	0.000
10	238	0.613	0.613	0.000
15	239	0.612	0.612	0.000
15	240	0.613	0.613	0.000
15	241	0.613	0.613	0.000
15	243	0.613	0.613	0.000
15	245	0.613	0.613	0.000
30	222	0.613	0.613	0.000
30	223	0.614	0.613	0.000
30	225	0.613	0.613	0.000
30	226	0.614	0.613	-0.001
30	227	0.613	0.612	-0.001
35	228	0.613	0.612	-0.001
35	229	0.613	0.613	-0.001
35	231	0.613	0.613	0.000
35	232	0.613	0.612	-0.001
35	233	0.613	0.612	-0.001
50	210	0.613	0.612	-0.001
50	211	0.613	0.613	0.000
50	212	0.613	0.613	0.000
50	213	0.613	0.613	0.000
50	214	0.613	0.612	-0.001
55	215	0.614	0.614	0.000
55	216	0.614	0.613	0.000
55	217	0.613	0.613	-0.001
55	218	0.613	0.613	0.000
55	220	0.614	0.613	-0.001
100	247	0.614	0.612	-0.001
100	249	0.613	0.612	-0.001
100	250	0.613	0.612	-0.002
100	251	0.613	0.611	-0.002
100	253	0.613	0.612	-0.001
100	255	0.613	0.611	-0.001
100	256	0.613	0.612	-0.001
100	257	0.613	0.612	-0.001
100	258	0.613	0.612	-0.001
100	259	0.612	0.611	-0.001
100	260	0.613	0.612	-0.001
100	261	0.613	0.612	-0.001
100	262	0.613	0.612	-0.001
100	263	0.614	0.612	-0.002
100	264	0.613	0.612	-0.001
100	265	0.613	0.611	-0.001
100	266	0.613	0.612	-0.001
100	267	0.614	0.612	-0.002
100	268	0.613	0.612	-0.001
100	269	0.613	0.613	-0.001
100	270	0.613	0.611	-0.001
100	271	0.612	0.611	-0.001
100	273	0.613	0.612	-0.001
105	274	0.613	0.612	-0.001
105	275	0.613	0.612	-0.001
105	276	0.613	0.612	-0.001
105	277	0.613	0.613	0.000
105	278	0.613	0.611	-0.001
Max		0.614	0.614	0.000
Average		0.613	0.612	-0.001
Min		0.612	0.611	-0.002
Std Dev		0.000	0.001	0.001

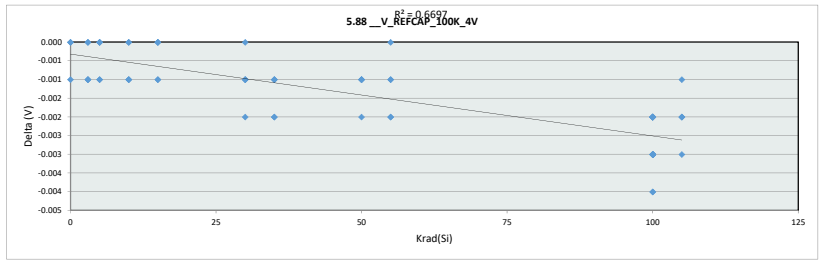


		8.4_VREF_14V										
Test Site	Tester											
Test Number	Unit	0.615	V									
Max Limit		0.615	V									
Min Limit		0.609	V									
Krad(Si)	LL	3	5	10	15	30	35	50	55	100	105	
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	
Min	0.613	0.612	0.613	0.612	0.612	0.612	0.612	0.612	0.613	0.611	0.611	
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.613	0.613	0.612	0.612	
Max	0.613	0.613	0.614	0.614	0.613	0.613	0.613	0.613	0.614	0.613	0.613	
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	

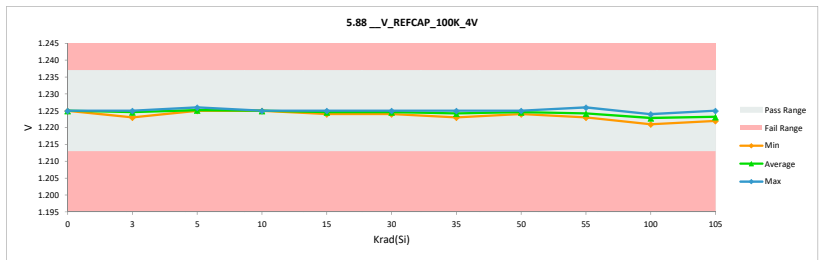


HDR TID Report TPS7H5002-SP

5.88_V_REFCAP_100K_4V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.225	1.225	0.000	
0	190	1.226	1.225	-0.001	
0	191	1.225	1.225	0.000	
3	300	1.224	1.223	-0.001	
3	305	1.225	1.225	0.000	
3	309	1.226	1.225	-0.001	
3	311	1.226	1.225	-0.001	
3	313	1.225	1.225	0.000	
5	319	1.226	1.225	-0.001	
5	321	1.226	1.225	-0.001	
5	324	1.225	1.225	0.000	
5	327	1.226	1.226	0.000	
5	331	1.225	1.225	0.000	
10	234	1.225	1.225	0.000	
10	235	1.226	1.225	-0.001	
10	236	1.226	1.225	-0.001	
10	237	1.226	1.225	-0.001	
10	238	1.225	1.225	0.000	
15	239	1.225	1.224	-0.001	
15	240	1.225	1.225	0.000	
15	241	1.225	1.224	-0.001	
15	243	1.225	1.225	0.000	
15	245	1.225	1.225	0.000	
30	222	1.225	1.225	0.000	
30	223	1.226	1.224	-0.002	
30	225	1.226	1.225	-0.001	
30	226	1.226	1.225	-0.001	
30	227	1.225	1.224	-0.001	
35	228	1.226	1.225	-0.001	
35	229	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
35	232	1.226	1.224	-0.002	
35	233	1.225	1.223	-0.002	
50	210	1.226	1.225	-0.001	
50	211	1.226	1.225	-0.001	
50	212	1.225	1.224	-0.001	
50	213	1.226	1.225	-0.001	
50	214	1.226	1.224	-0.002	
55	215	1.226	1.226	0.000	
55	216	1.225	1.224	-0.001	
55	217	1.226	1.224	-0.002	
55	218	1.225	1.224	-0.001	
55	220	1.225	1.223	-0.002	
100	247	1.226	1.223	-0.003	
100	249	1.226	1.224	-0.002	
100	250	1.225	1.222	-0.003	
100	251	1.225	1.222	-0.003	
100	253	1.225	1.223	-0.002	
100	255	1.225	1.222	-0.003	
100	256	1.226	1.223	-0.003	
100	257	1.226	1.223	-0.003	
100	258	1.225	1.222	-0.003	
100	259	1.225	1.223	-0.002	
100	260	1.226	1.224	-0.002	
100	261	1.226	1.224	-0.002	
100	262	1.226	1.224	-0.002	
100	263	1.225	1.221	-0.004	
100	264	1.226	1.223	-0.003	
100	265	1.225	1.222	-0.003	
100	266	1.226	1.223	-0.003	
100	267	1.226	1.222	-0.004	
100	268	1.225	1.223	-0.002	
100	269	1.226	1.224	-0.002	
100	270	1.225	1.223	-0.002	
100	271	1.226	1.223	-0.003	
100	273	1.226	1.223	-0.003	
105	274	1.225	1.223	-0.002	
105	275	1.225	1.222	-0.003	
105	276	1.225	1.223	-0.002	
105	277	1.226	1.225	-0.001	
105	278	1.225	1.223	-0.002	
	Max	1.226	1.226	0.000	
	Average	1.225	1.224	-0.002	
	Min	1.224	1.221	-0.004	
	Std Dev	0.001	0.001	0.001	

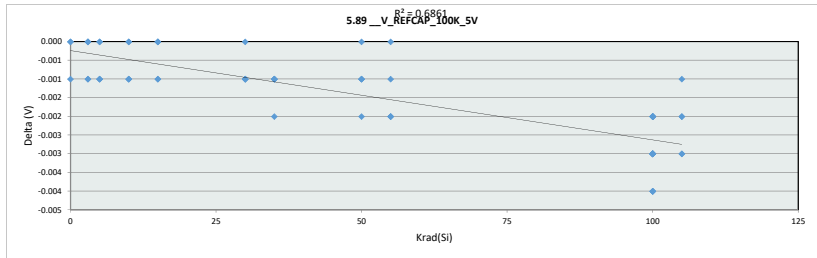


5.88_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.223	1.225	1.225	1.224	1.224	1.223	1.224	1.223	1.221	1.222
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.225	1.224	1.223	1.223
Max	1.225	1.225	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.226	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

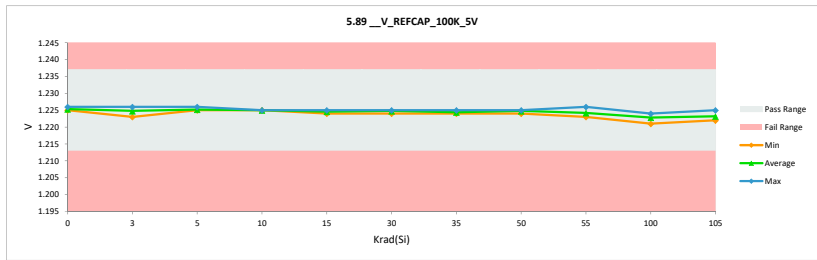


HDR TID Report TPS7H5002-SP

5.89_V_REFCAP_100K_5V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.225	1.225	0.000	
0	190	1.226	1.226	0.000	
0	191	1.226	1.225	-0.001	
3	300	1.224	1.223	-0.001	
3	305	1.225	1.225	0.000	
3	309	1.226	1.225	-0.001	
3	311	1.226	1.226	0.000	
3	313	1.225	1.225	0.000	
5	319	1.226	1.225	-0.001	
5	321	1.226	1.225	-0.001	
5	324	1.225	1.225	0.000	
5	327	1.226	1.226	0.000	
5	331	1.226	1.225	-0.001	
10	234	1.225	1.225	0.000	
10	235	1.226	1.225	-0.001	
10	236	1.226	1.225	-0.001	
10	237	1.226	1.225	-0.001	
10	238	1.225	1.225	0.000	
15	239	1.225	1.224	-0.001	
15	240	1.225	1.225	0.000	
15	241	1.225	1.224	-0.001	
15	243	1.225	1.225	0.000	
15	245	1.225	1.225	0.000	
30	222	1.225	1.225	0.000	
30	223	1.226	1.225	-0.001	
30	225	1.226	1.225	-0.001	
30	226	1.226	1.225	-0.001	
30	227	1.225	1.224	-0.001	
35	228	1.226	1.225	-0.001	
35	229	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
35	232	1.226	1.224	-0.002	
35	233	1.225	1.224	-0.001	
50	210	1.226	1.225	-0.001	
50	211	1.226	1.225	-0.001	
50	212	1.225	1.225	0.000	
50	213	1.226	1.225	-0.001	
50	214	1.226	1.224	-0.002	
55	215	1.226	1.226	0.000	
55	216	1.226	1.224	-0.002	
55	217	1.226	1.224	-0.002	
55	218	1.225	1.224	-0.001	
55	220	1.225	1.223	-0.002	
100	247	1.226	1.223	-0.003	
100	249	1.226	1.224	-0.002	
100	250	1.226	1.222	-0.004	
100	251	1.226	1.222	-0.004	
100	253	1.225	1.223	-0.002	
100	255	1.225	1.222	-0.003	
100	256	1.226	1.223	-0.003	
100	257	1.226	1.223	-0.003	
100	258	1.225	1.222	-0.003	
100	259	1.225	1.222	-0.003	
100	260	1.226	1.224	-0.002	
100	261	1.226	1.224	-0.002	
100	262	1.226	1.224	-0.002	
100	263	1.225	1.221	-0.004	
100	264	1.226	1.223	-0.003	
100	265	1.225	1.222	-0.003	
100	266	1.226	1.223	-0.003	
100	267	1.226	1.222	-0.004	
100	268	1.225	1.223	-0.002	
100	269	1.226	1.224	-0.002	
100	270	1.225	1.223	-0.002	
100	271	1.226	1.223	-0.003	
100	273	1.226	1.223	-0.003	
105	274	1.226	1.223	-0.003	
105	275	1.225	1.222	-0.003	
105	276	1.225	1.223	-0.002	
105	277	1.226	1.225	-0.001	
105	278	1.225	1.223	-0.002	
	Max	1.226	1.226	0.000	
	Average	1.226	1.224	-0.002	
	Min	1.224	1.221	-0.004	
	Std Dev	0.001	0.001	0.001	

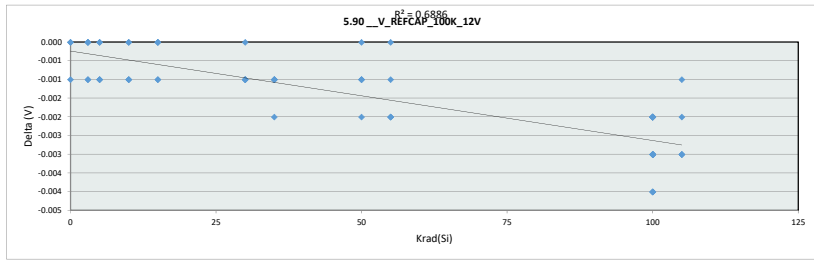


5.89_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.223	1.225	1.225	1.224	1.224	1.224	1.224	1.224	1.223	1.222
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.225	1.224	1.223	1.223
Max	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.226	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

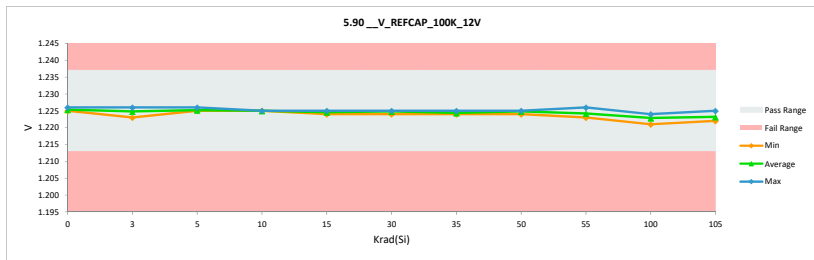


HDR TID Report
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5.90_V_REFCAP_100K_12V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.225	1.225	0.000	
0	190	1.226	1.226	0.000	
0	191	1.226	1.225	-0.001	
3	300	1.224	1.223	-0.001	
3	305	1.225	1.225	0.000	
3	309	1.226	1.225	-0.001	
3	311	1.226	1.226	0.000	
3	313	1.225	1.225	0.000	
5	319	1.226	1.225	-0.001	
5	321	1.226	1.225	-0.001	
5	324	1.225	1.225	0.000	
5	327	1.226	1.226	0.000	
5	331	1.226	1.225	-0.001	
10	234	1.225	1.225	0.000	
10	235	1.226	1.225	-0.001	
10	236	1.226	1.225	-0.001	
10	237	1.226	1.225	-0.001	
10	238	1.225	1.225	0.000	
15	239	1.225	1.224	-0.001	
15	240	1.225	1.225	0.000	
15	241	1.225	1.224	-0.001	
15	243	1.225	1.225	0.000	
15	245	1.225	1.225	0.000	
30	222	1.225	1.225	0.000	
30	223	1.226	1.225	-0.001	
30	225	1.226	1.225	-0.001	
30	226	1.226	1.225	-0.001	
30	227	1.225	1.224	-0.001	
35	228	1.226	1.225	-0.001	
35	229	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
35	232	1.226	1.224	-0.002	
35	233	1.225	1.224	-0.001	
50	210	1.226	1.225	-0.001	
50	211	1.226	1.225	-0.001	
50	212	1.225	1.225	0.000	
50	213	1.226	1.225	-0.001	
50	214	1.226	1.224	-0.002	
55	215	1.226	1.226	0.000	
55	216	1.226	1.224	-0.002	
55	217	1.226	1.224	-0.002	
55	218	1.225	1.224	-0.001	
55	220	1.225	1.223	-0.002	
100	247	1.226	1.223	-0.003	
100	249	1.226	1.224	-0.002	
100	250	1.225	1.222	-0.003	
100	251	1.226	1.222	-0.004	
100	253	1.225	1.223	-0.002	
100	255	1.225	1.222	-0.003	
100	256	1.226	1.223	-0.003	
100	257	1.226	1.223	-0.003	
100	258	1.226	1.222	-0.004	
100	259	1.225	1.223	-0.002	
100	260	1.226	1.224	-0.002	
100	261	1.226	1.224	-0.002	
100	262	1.226	1.224	-0.002	
100	263	1.225	1.221	-0.004	
100	264	1.226	1.223	-0.003	
100	265	1.225	1.222	-0.003	
100	266	1.226	1.223	-0.003	
100	267	1.226	1.222	-0.004	
100	268	1.225	1.223	-0.002	
100	269	1.226	1.224	-0.002	
100	270	1.225	1.223	-0.002	
100	271	1.226	1.223	-0.003	
100	273	1.226	1.223	-0.003	
105	274	1.226	1.223	-0.003	
105	275	1.225	1.222	-0.003	
105	276	1.225	1.223	-0.002	
105	277	1.226	1.225	-0.001	
105	278	1.226	1.223	-0.003	
	Max	1.226	1.226	0.000	
	Average	1.226	1.224	-0.002	
	Min	1.224	1.221	-0.004	
	Std Dev	0.001	0.001	0.001	

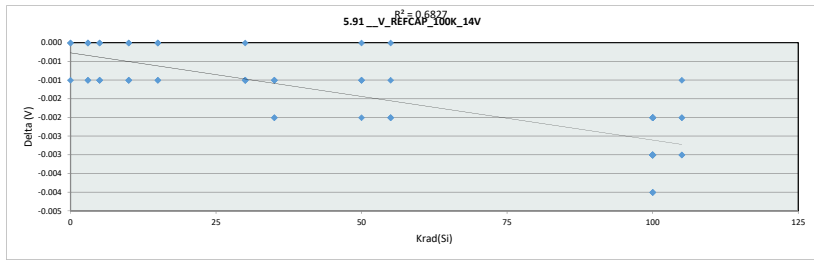


5.90_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.223	1.225	1.225	1.224	1.224	1.224	1.224	1.224	1.223	1.222
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.225	1.224	1.224	1.223
Max	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.226	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

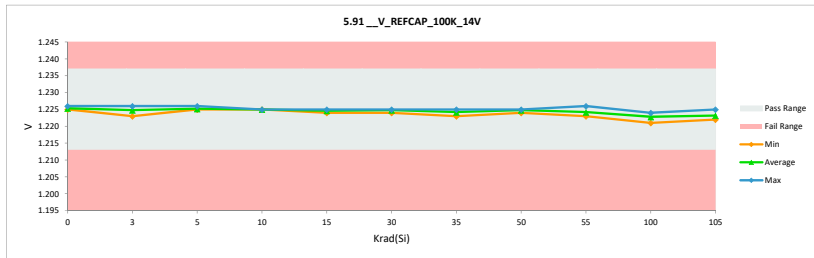


HDR TID Report TPS7H5002-SP

5.91_V_REFCAP_100K_14V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.225	1.225	0.000	
0	190	1.226	1.226	0.000	
0	191	1.226	1.225	-0.001	
3	300	1.224	1.223	-0.001	
3	305	1.225	1.225	0.000	
3	309	1.226	1.225	-0.001	
3	311	1.226	1.226	0.000	
3	313	1.225	1.225	0.000	
5	319	1.226	1.225	-0.001	
5	321	1.226	1.225	-0.001	
5	324	1.225	1.225	0.000	
5	327	1.226	1.226	0.000	
5	331	1.226	1.225	-0.001	
10	234	1.225	1.225	0.000	
10	235	1.226	1.225	-0.001	
10	236	1.226	1.225	-0.001	
10	237	1.226	1.225	-0.001	
10	238	1.225	1.225	0.000	
15	239	1.225	1.224	-0.001	
15	240	1.225	1.225	0.000	
15	241	1.225	1.224	-0.001	
15	243	1.225	1.225	0.000	
15	245	1.225	1.225	0.000	
30	222	1.225	1.225	0.000	
30	223	1.226	1.225	-0.001	
30	225	1.226	1.225	-0.001	
30	226	1.226	1.225	-0.001	
30	227	1.225	1.224	-0.001	
35	228	1.226	1.225	-0.001	
35	229	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
35	232	1.226	1.224	-0.002	
35	233	1.225	1.223	-0.002	
50	210	1.226	1.225	-0.001	
50	211	1.226	1.225	-0.001	
50	212	1.225	1.225	0.000	
50	213	1.226	1.225	-0.001	
50	214	1.226	1.224	-0.002	
55	215	1.226	1.226	0.000	
55	216	1.226	1.224	-0.002	
55	217	1.226	1.224	-0.002	
55	218	1.225	1.224	-0.001	
55	220	1.225	1.223	-0.002	
100	247	1.226	1.223	-0.003	
100	249	1.226	1.224	-0.002	
100	250	1.225	1.222	-0.003	
100	251	1.226	1.222	-0.004	
100	253	1.225	1.223	-0.002	
100	255	1.225	1.222	-0.003	
100	256	1.226	1.223	-0.003	
100	257	1.226	1.223	-0.003	
100	258	1.225	1.222	-0.003	
100	259	1.225	1.222	-0.003	
100	260	1.226	1.224	-0.002	
100	261	1.226	1.224	-0.002	
100	262	1.226	1.224	-0.002	
100	263	1.225	1.221	-0.004	
100	264	1.226	1.223	-0.003	
100	265	1.225	1.222	-0.003	
100	266	1.226	1.223	-0.003	
100	267	1.226	1.222	-0.004	
100	268	1.225	1.223	-0.002	
100	269	1.226	1.224	-0.002	
100	270	1.225	1.223	-0.002	
100	271	1.226	1.223	-0.003	
100	273	1.226	1.223	-0.003	
105	274	1.226	1.223	-0.003	
105	275	1.225	1.222	-0.003	
105	276	1.225	1.223	-0.002	
105	277	1.226	1.225	-0.001	
105	278	1.225	1.223	-0.002	
Max		1.226	1.226	0.000	
Average		1.226	1.224	-0.002	
Min		1.224	1.221	-0.004	
Std Dev		0.001	0.001	0.001	

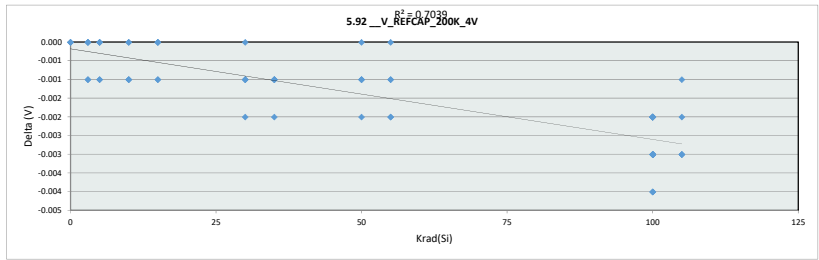


5.91_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.223	1.225	1.225	1.224	1.224	1.223	1.224	1.223	1.221	1.222
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.225	1.224	1.223	1.223
Max	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

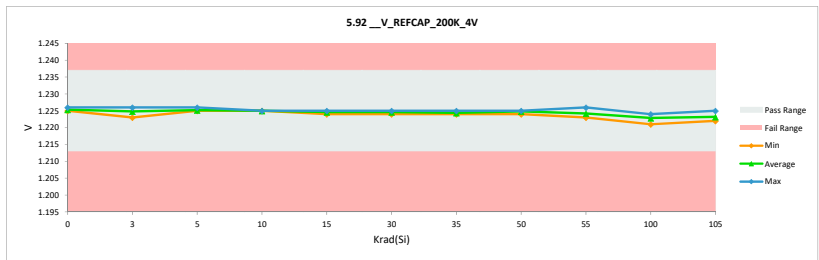


HDR TID Report
TPS7H5002-SP

5.92_V_REFCAP_200K_4V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.225	1.225	0.000
0	190	1.226	1.226	0.000
0	191	1.225	1.225	0.000
3	300	1.224	1.223	-0.001
3	305	1.225	1.225	0.000
3	309	1.226	1.225	-0.001
3	311	1.226	1.226	0.000
3	313	1.225	1.225	0.000
5	319	1.226	1.225	-0.001
5	321	1.225	1.225	0.000
5	324	1.225	1.225	0.000
5	327	1.226	1.226	0.000
5	331	1.226	1.225	-0.001
10	234	1.225	1.225	0.000
10	235	1.226	1.225	-0.001
10	236	1.226	1.225	-0.001
10	237	1.226	1.225	-0.001
10	238	1.225	1.225	0.000
15	239	1.225	1.224	-0.001
15	240	1.225	1.225	0.000
15	241	1.225	1.224	-0.001
15	243	1.225	1.225	0.000
15	245	1.225	1.225	0.000
30	222	1.225	1.225	0.000
30	223	1.226	1.224	-0.002
30	225	1.226	1.225	-0.001
30	226	1.226	1.225	-0.001
30	227	1.225	1.224	-0.001
35	228	1.226	1.225	-0.001
35	229	1.225	1.224	-0.001
35	231	1.226	1.225	-0.001
35	232	1.226	1.224	-0.002
35	233	1.225	1.224	-0.001
50	210	1.226	1.225	-0.001
50	211	1.226	1.225	-0.001
50	212	1.225	1.225	0.000
50	213	1.226	1.225	-0.001
50	214	1.226	1.224	-0.002
55	215	1.226	1.226	0.000
55	216	1.225	1.224	-0.001
55	217	1.226	1.224	-0.002
55	218	1.225	1.224	-0.001
55	220	1.225	1.223	-0.002
100	247	1.226	1.223	-0.003
100	249	1.226	1.224	-0.002
100	250	1.225	1.222	-0.003
100	251	1.226	1.222	-0.004
100	253	1.225	1.223	-0.002
100	255	1.225	1.222	-0.003
100	256	1.226	1.223	-0.003
100	257	1.226	1.223	-0.003
100	258	1.225	1.222	-0.003
100	259	1.225	1.223	-0.002
100	260	1.226	1.224	-0.002
100	261	1.226	1.224	-0.002
100	262	1.226	1.224	-0.002
100	263	1.225	1.221	-0.004
100	264	1.226	1.223	-0.003
100	265	1.225	1.222	-0.003
100	266	1.226	1.223	-0.003
100	267	1.226	1.222	-0.004
100	268	1.225	1.223	-0.002
100	269	1.226	1.224	-0.002
100	270	1.225	1.223	-0.002
100	271	1.226	1.223	-0.003
100	273	1.226	1.223	-0.003
105	274	1.226	1.223	-0.003
105	275	1.225	1.222	-0.003
105	276	1.225	1.223	-0.002
105	277	1.226	1.225	-0.001
105	278	1.226	1.223	-0.003
	Max	1.226	1.226	0.000
	Average	1.226	1.224	-0.001
	Min	1.224	1.221	-0.004
	Std Dev	0.001	0.001	0.001

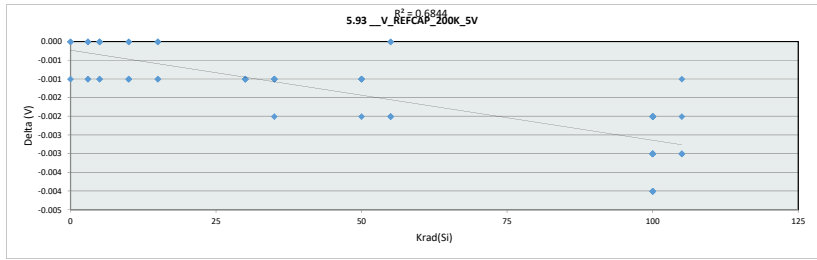


5.92_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.223	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.221	1.222
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.225	1.224	1.223	1.223
Max	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.226	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

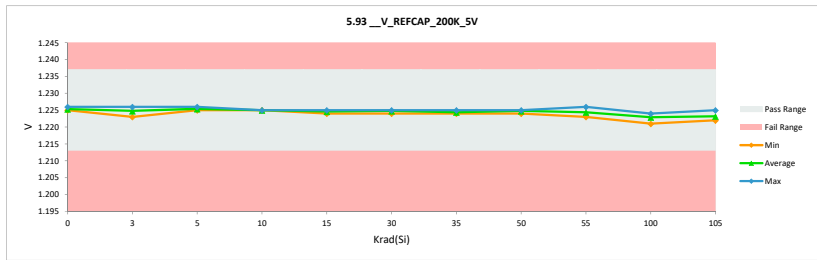


HDR TID Report TPS7H5002-SP

5.93_V_REFCAP_200K_5V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.225	1.225	0.000	
0	190	1.226	1.226	0.000	
0	191	1.226	1.225	-0.001	
3	300	1.224	1.223	-0.001	
3	305	1.225	1.225	0.000	
3	309	1.226	1.225	-0.001	
3	311	1.226	1.226	0.000	
3	313	1.225	1.225	0.000	
5	319	1.226	1.225	-0.001	
5	321	1.226	1.226	0.000	
5	324	1.225	1.225	0.000	
5	327	1.226	1.226	0.000	
5	331	1.226	1.225	-0.001	
10	234	1.225	1.225	0.000	
10	235	1.226	1.225	-0.001	
10	236	1.226	1.225	-0.001	
10	237	1.226	1.225	-0.001	
10	238	1.225	1.225	0.000	
15	239	1.225	1.224	-0.001	
15	240	1.225	1.225	0.000	
15	241	1.225	1.224	-0.001	
15	243	1.225	1.225	0.000	
15	245	1.225	1.225	0.000	
30	222	1.226	1.225	-0.001	
30	223	1.226	1.225	-0.001	
30	225	1.226	1.225	-0.001	
30	226	1.226	1.225	-0.001	
30	227	1.225	1.224	-0.001	
35	228	1.226	1.225	-0.001	
35	229	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
35	232	1.226	1.224	-0.002	
35	233	1.225	1.224	-0.001	
50	210	1.226	1.225	-0.001	
50	211	1.226	1.225	-0.001	
50	212	1.226	1.225	-0.001	
50	213	1.226	1.225	-0.001	
50	214	1.226	1.224	-0.002	
55	215	1.226	1.226	0.000	
55	216	1.226	1.224	-0.002	
55	217	1.226	1.224	-0.002	
55	218	1.225	1.225	0.000	
55	220	1.225	1.223	-0.002	
100	247	1.226	1.223	-0.003	
100	249	1.226	1.224	-0.002	
100	250	1.226	1.222	-0.004	
100	251	1.226	1.222	-0.004	
100	253	1.225	1.223	-0.002	
100	255	1.225	1.222	-0.003	
100	256	1.226	1.224	-0.002	
100	257	1.226	1.223	-0.003	
100	258	1.226	1.222	-0.004	
100	259	1.225	1.223	-0.002	
100	260	1.226	1.224	-0.002	
100	261	1.226	1.224	-0.002	
100	262	1.226	1.224	-0.002	
100	263	1.225	1.221	-0.004	
100	264	1.226	1.223	-0.003	
100	265	1.225	1.222	-0.003	
100	266	1.226	1.223	-0.003	
100	267	1.226	1.222	-0.004	
100	268	1.225	1.223	-0.002	
100	269	1.226	1.224	-0.002	
100	270	1.225	1.223	-0.002	
100	271	1.226	1.223	-0.003	
100	273	1.226	1.223	-0.003	
105	274	1.226	1.223	-0.003	
105	275	1.225	1.222	-0.003	
105	276	1.225	1.223	-0.002	
105	277	1.226	1.225	-0.001	
105	278	1.226	1.223	-0.003	
	Max	1.226	1.226	0.000	
	Average	1.226	1.224	-0.002	
	Min	1.224	1.221	-0.004	
	Std Dev	0.001	0.001	0.001	

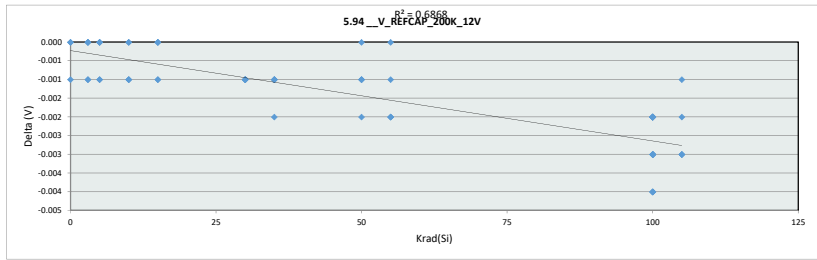


5.93_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.223	1.225	1.225	1.224	1.224	1.224	1.224	1.224	1.223	1.222
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.225	1.224	1.224	1.223
Max	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.226	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

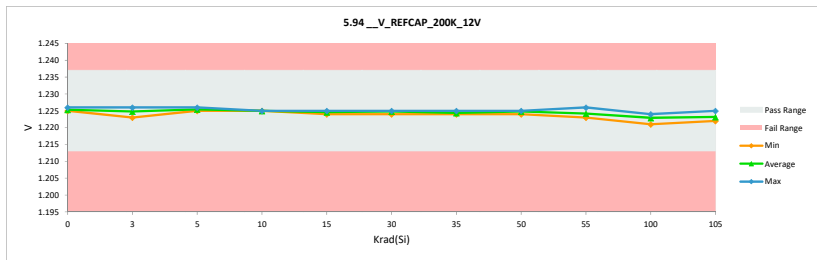


HDR TID Report TPS7H5002-SP

5.94_V_REFCAP_200K_12V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.225	1.225	0.000	
0	190	1.226	1.226	0.000	
0	191	1.226	1.225	-0.001	
3	300	1.224	1.223	-0.001	
3	305	1.225	1.225	0.000	
3	309	1.226	1.225	-0.001	
3	311	1.226	1.226	0.000	
3	313	1.225	1.225	0.000	
5	319	1.226	1.225	-0.001	
5	321	1.226	1.226	0.000	
5	324	1.225	1.225	0.000	
5	327	1.226	1.226	0.000	
5	331	1.226	1.225	-0.001	
10	234	1.225	1.225	0.000	
10	235	1.226	1.225	-0.001	
10	236	1.226	1.225	-0.001	
10	237	1.226	1.225	-0.001	
10	238	1.225	1.225	0.000	
15	239	1.225	1.224	-0.001	
15	240	1.225	1.225	0.000	
15	241	1.225	1.224	-0.001	
15	243	1.225	1.225	0.000	
15	245	1.225	1.225	0.000	
30	222	1.226	1.225	-0.001	
30	223	1.226	1.225	-0.001	
30	225	1.226	1.225	-0.001	
30	226	1.226	1.225	-0.001	
30	227	1.225	1.224	-0.001	
35	228	1.226	1.225	-0.001	
35	229	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
35	232	1.226	1.224	-0.002	
35	233	1.225	1.224	-0.001	
50	210	1.226	1.225	-0.001	
50	211	1.226	1.225	-0.001	
50	212	1.225	1.225	0.000	
50	213	1.226	1.225	-0.001	
50	214	1.226	1.224	-0.002	
55	215	1.226	1.226	0.000	
55	216	1.226	1.224	-0.002	
55	217	1.226	1.224	-0.002	
55	218	1.225	1.224	-0.001	
55	220	1.225	1.223	-0.002	
100	247	1.226	1.223	-0.003	
100	249	1.226	1.224	-0.002	
100	250	1.226	1.222	-0.004	
100	251	1.226	1.222	-0.004	
100	253	1.225	1.223	-0.002	
100	255	1.225	1.222	-0.003	
100	256	1.226	1.224	-0.002	
100	257	1.226	1.223	-0.003	
100	258	1.226	1.222	-0.004	
100	259	1.225	1.223	-0.002	
100	260	1.226	1.224	-0.002	
100	261	1.226	1.224	-0.002	
100	262	1.226	1.224	-0.002	
100	263	1.225	1.221	-0.004	
100	264	1.226	1.223	-0.003	
100	265	1.225	1.222	-0.003	
100	266	1.226	1.223	-0.003	
100	267	1.226	1.222	-0.004	
100	268	1.225	1.223	-0.002	
100	269	1.226	1.224	-0.002	
100	270	1.225	1.223	-0.002	
100	271	1.226	1.223	-0.003	
100	273	1.226	1.223	-0.003	
105	274	1.226	1.223	-0.003	
105	275	1.225	1.222	-0.003	
105	276	1.225	1.223	-0.002	
105	277	1.226	1.225	-0.001	
105	278	1.226	1.223	-0.003	
	Max	1.226	1.226	0.000	
	Average	1.226	1.224	-0.002	
	Min	1.224	1.221	-0.004	
	Std Dev	0.001	0.001	0.001	

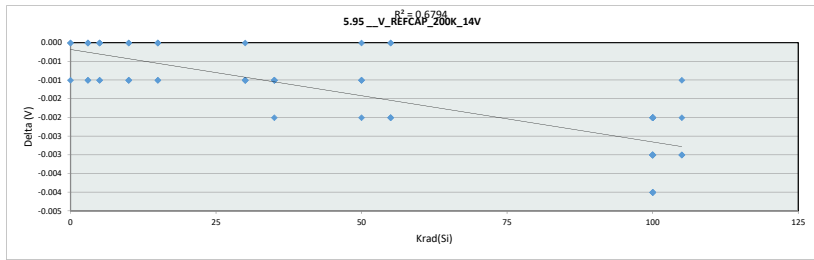


5.94_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.223	1.225	1.225	1.224	1.224	1.224	1.224	1.224	1.223	1.222
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.225	1.224	1.224	1.223
Max	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.226	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

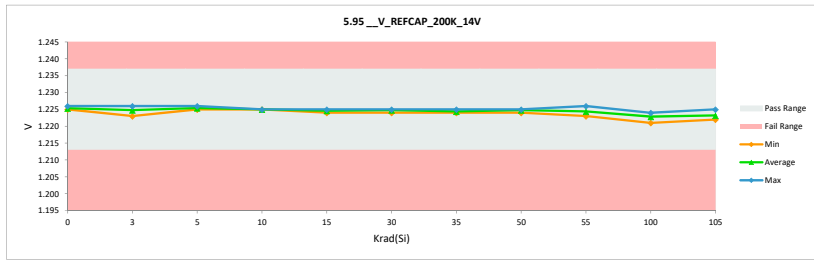


HDR TID Report
TPS7H5002-SP

5.95_V_REFCAP_200K_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.225	1.225	0.000
0	190	1.226	1.226	0.000
0	191	1.226	1.225	-0.001
3	300	1.224	1.223	-0.001
3	305	1.225	1.225	0.000
3	309	1.226	1.225	-0.001
3	311	1.226	1.226	0.000
3	313	1.225	1.225	0.000
5	319	1.226	1.225	-0.001
5	321	1.226	1.226	0.000
5	324	1.225	1.225	0.000
5	327	1.226	1.226	0.000
5	331	1.226	1.225	-0.001
10	234	1.225	1.225	0.000
10	235	1.226	1.225	-0.001
10	236	1.226	1.225	-0.001
10	237	1.226	1.225	-0.001
10	238	1.225	1.225	0.000
15	239	1.225	1.224	-0.001
15	240	1.225	1.225	0.000
15	241	1.225	1.224	-0.001
15	243	1.225	1.225	0.000
15	245	1.225	1.225	0.000
30	222	1.225	1.225	0.000
30	223	1.226	1.225	-0.001
30	225	1.226	1.225	-0.001
30	226	1.226	1.225	-0.001
30	227	1.225	1.224	-0.001
35	228	1.226	1.225	-0.001
35	229	1.225	1.224	-0.001
35	231	1.226	1.225	-0.001
35	232	1.226	1.224	-0.002
35	233	1.225	1.224	-0.001
50	210	1.226	1.225	-0.001
50	211	1.226	1.225	-0.001
50	212	1.225	1.225	0.000
50	213	1.226	1.225	-0.001
50	214	1.226	1.224	-0.002
55	215	1.226	1.226	0.000
55	216	1.226	1.224	-0.002
55	217	1.226	1.224	-0.002
55	218	1.225	1.225	0.000
55	220	1.225	1.223	-0.002
100	247	1.226	1.223	-0.003
100	249	1.226	1.224	-0.002
100	250	1.226	1.222	-0.004
100	251	1.226	1.222	-0.004
100	253	1.225	1.223	-0.002
100	255	1.225	1.222	-0.003
100	256	1.226	1.223	-0.003
100	257	1.226	1.223	-0.003
100	258	1.226	1.222	-0.004
100	259	1.225	1.223	-0.002
100	260	1.226	1.224	-0.002
100	261	1.226	1.224	-0.002
100	262	1.226	1.224	-0.002
100	263	1.225	1.221	-0.004
100	264	1.226	1.223	-0.003
100	265	1.225	1.222	-0.003
100	266	1.226	1.223	-0.003
100	267	1.226	1.222	-0.004
100	268	1.225	1.223	-0.002
100	269	1.226	1.224	-0.002
100	270	1.225	1.223	-0.002
100	271	1.226	1.223	-0.003
100	273	1.226	1.223	-0.003
105	274	1.226	1.223	-0.003
105	275	1.225	1.222	-0.003
105	276	1.225	1.223	-0.002
105	277	1.226	1.225	-0.001
105	278	1.226	1.223	-0.003
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.002
Min		1.224	1.221	-0.004
Std Dev		0.001	0.001	0.001

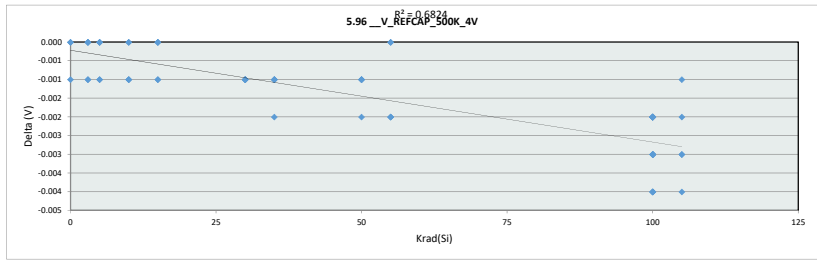


5.95_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.223	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.221	1.222
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.225	1.224	1.223	1.223
Max	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.226	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

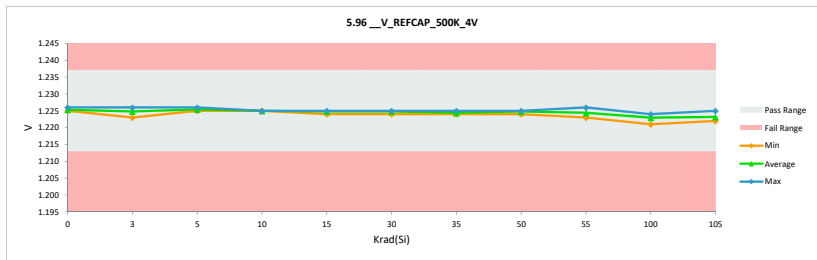


HDR TID Report
TPS7H5002-SP

5.96_V_REFCAP_500K_4V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.225	1.225	0.000	
0	190	1.226	1.226	0.000	
0	191	1.226	1.225	-0.001	
3	300	1.224	1.223	-0.001	
3	305	1.225	1.225	0.000	
3	309	1.226	1.225	-0.001	
3	311	1.226	1.226	0.000	
3	313	1.225	1.225	0.000	
5	319	1.226	1.225	-0.001	
5	321	1.226	1.226	0.000	
5	324	1.225	1.225	0.000	
5	327	1.226	1.226	0.000	
5	331	1.226	1.225	-0.001	
10	234	1.225	1.225	0.000	
10	235	1.226	1.225	-0.001	
10	236	1.226	1.225	-0.001	
10	237	1.226	1.225	-0.001	
10	238	1.225	1.225	0.000	
10	239	1.225	1.225	0.000	
15	240	1.226	1.225	-0.001	
15	241	1.225	1.224	-0.001	
15	243	1.225	1.225	0.000	
15	245	1.225	1.225	0.000	
30	222	1.226	1.225	-0.001	
30	223	1.226	1.225	-0.001	
30	225	1.226	1.225	-0.001	
30	226	1.226	1.225	-0.001	
30	227	1.225	1.224	-0.001	
35	228	1.226	1.225	-0.001	
35	229	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
35	232	1.226	1.224	-0.002	
35	233	1.225	1.224	-0.001	
50	210	1.226	1.225	-0.001	
50	211	1.226	1.225	-0.001	
50	212	1.226	1.225	-0.001	
50	213	1.226	1.225	-0.001	
50	214	1.226	1.224	-0.002	
55	215	1.226	1.226	0.000	
55	216	1.226	1.224	-0.002	
55	217	1.226	1.224	-0.002	
55	218	1.225	1.225	0.000	
55	220	1.225	1.223	-0.002	
100	247	1.226	1.223	-0.003	
100	249	1.226	1.224	-0.002	
100	250	1.226	1.222	-0.004	
100	251	1.226	1.222	-0.004	
100	253	1.225	1.223	-0.002	
100	255	1.225	1.223	-0.002	
100	256	1.226	1.224	-0.002	
100	257	1.226	1.223	-0.003	
100	258	1.226	1.222	-0.004	
100	259	1.225	1.223	-0.002	
100	260	1.226	1.224	-0.002	
100	261	1.226	1.224	-0.002	
100	262	1.226	1.224	-0.002	
100	263	1.225	1.221	-0.004	
100	264	1.226	1.223	-0.003	
100	265	1.225	1.222	-0.003	
100	266	1.226	1.223	-0.003	
100	267	1.226	1.222	-0.004	
100	268	1.226	1.223	-0.003	
100	269	1.226	1.224	-0.002	
100	270	1.225	1.223	-0.002	
100	271	1.226	1.223	-0.003	
100	273	1.226	1.223	-0.003	
105	274	1.226	1.223	-0.003	
105	275	1.226	1.222	-0.004	
105	276	1.225	1.223	-0.002	
105	277	1.226	1.225	-0.001	
105	278	1.226	1.223	-0.003	
	Max	1.226	1.226	0.000	
	Average	1.226	1.224	-0.002	
	Min	1.224	1.221	-0.004	
	Std Dev	0.001	0.001	0.001	

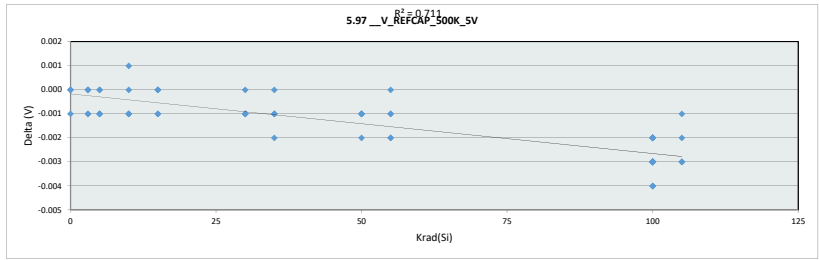


5.96_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.223	1.225	1.225	1.224	1.224	1.224	1.224	1.224	1.223	1.222
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.225	1.224	1.224	1.223
Max	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.226	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

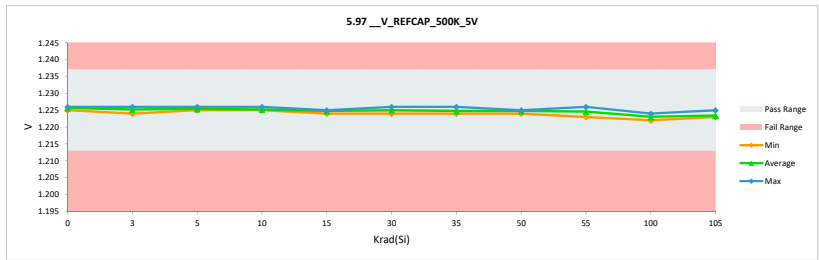


HDR TID Report
TPS7H5002-SP

5.97_V_REFCAP_500K_5V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.226	1.225	-0.001	
0	190	1.226	1.226	0.000	
0	191	1.226	1.226	0.000	
3	300	1.224	1.224	0.000	
3	305	1.226	1.225	-0.001	
3	309	1.226	1.226	0.000	
3	311	1.226	1.226	0.000	
3	313	1.226	1.225	-0.001	
5	319	1.226	1.225	-0.001	
5	321	1.226	1.226	0.000	
5	324	1.226	1.225	-0.001	
5	327	1.226	1.226	0.000	
5	331	1.226	1.225	-0.001	
10	234	1.225	1.226	0.001	
10	235	1.226	1.225	-0.001	
10	236	1.226	1.225	-0.001	
10	237	1.226	1.225	-0.001	
10	238	1.225	1.225	0.000	
15	239	1.225	1.225	0.000	
15	240	1.226	1.225	-0.001	
15	241	1.225	1.224	-0.001	
15	243	1.225	1.225	0.000	
15	245	1.225	1.225	0.000	
30	222	1.226	1.225	-0.001	
30	223	1.226	1.225	-0.001	
30	225	1.226	1.226	0.000	
30	226	1.226	1.225	-0.001	
30	227	1.225	1.224	-0.001	
35	228	1.226	1.225	-0.001	
35	229	1.225	1.224	-0.001	
35	231	1.226	1.226	0.000	
35	232	1.226	1.225	-0.001	
35	233	1.226	1.224	-0.002	
50	210	1.226	1.225	-0.001	
50	211	1.226	1.225	-0.001	
50	212	1.226	1.225	-0.001	
50	213	1.226	1.225	-0.001	
50	214	1.226	1.224	-0.002	
55	215	1.226	1.226	0.000	
55	216	1.226	1.225	-0.001	
55	217	1.226	1.224	-0.002	
55	218	1.226	1.225	-0.001	
55	220	1.225	1.223	-0.002	
100	247	1.226	1.223	-0.003	
100	249	1.226	1.224	-0.002	
100	250	1.226	1.222	-0.004	
100	251	1.226	1.222	-0.004	
100	253	1.225	1.223	-0.002	
100	255	1.225	1.223	-0.002	
100	256	1.226	1.224	-0.002	
100	257	1.226	1.223	-0.003	
100	258	1.226	1.223	-0.003	
100	259	1.226	1.223	-0.003	
100	260	1.227	1.224	-0.003	
100	261	1.226	1.224	-0.002	
100	262	1.226	1.224	-0.002	
100	263	1.225	1.222	-0.003	
100	264	1.226	1.223	-0.003	
100	265	1.225	1.222	-0.003	
100	266	1.226	1.223	-0.003	
100	267	1.226	1.222	-0.004	
100	268	1.226	1.223	-0.003	
100	269	1.226	1.224	-0.002	
100	270	1.226	1.223	-0.003	
100	271	1.226	1.223	-0.003	
100	273	1.226	1.223	-0.003	
105	274	1.226	1.223	-0.003	
105	275	1.226	1.223	-0.003	
105	276	1.225	1.223	-0.002	
105	277	1.226	1.225	-0.001	
105	278	1.226	1.223	-0.003	
	Max	1.227	1.226	0.001	
	Average	1.226	1.224	-0.002	
	Min	1.224	1.222	-0.004	
	Std Dev	0.000	0.001	0.001	

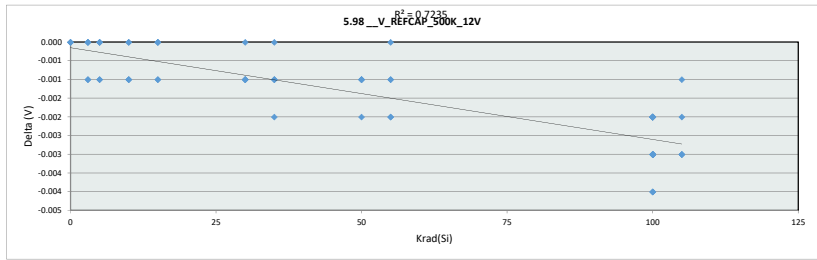


5.97_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.224	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.222	1.223
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.225
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.226	1.226	1.226	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

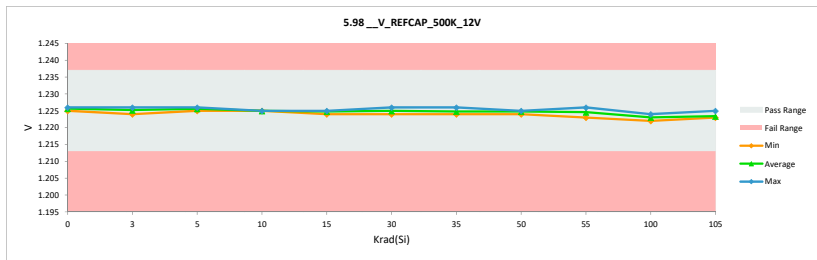


HDR TID Report TPS7H5002-SP

5.98_V_REFCAP_500K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.225	1.225	0.000
0	190	1.226	1.226	0.000
0	191	1.226	1.226	0.000
3	300	1.224	1.224	0.000
3	305	1.226	1.225	-0.001
3	309	1.226	1.226	0.000
3	311	1.226	1.226	0.000
3	313	1.226	1.225	-0.001
5	319	1.226	1.225	-0.001
5	321	1.226	1.226	0.000
5	324	1.226	1.225	-0.001
5	327	1.226	1.226	0.000
5	331	1.226	1.226	0.000
10	234	1.225	1.225	0.000
10	235	1.226	1.225	-0.001
10	236	1.226	1.225	-0.001
10	237	1.226	1.225	-0.001
10	238	1.225	1.225	0.000
10	239	1.225	1.225	0.000
15	240	1.226	1.225	-0.001
15	241	1.225	1.224	-0.001
15	243	1.225	1.225	0.000
15	245	1.225	1.225	0.000
30	222	1.226	1.225	-0.001
30	223	1.226	1.225	-0.001
30	225	1.226	1.226	0.000
30	226	1.226	1.225	-0.001
30	227	1.225	1.224	-0.001
35	228	1.226	1.225	-0.001
35	229	1.225	1.224	-0.001
35	231	1.226	1.226	0.000
35	232	1.226	1.225	-0.001
35	233	1.226	1.224	-0.002
50	210	1.226	1.225	-0.001
50	211	1.226	1.225	-0.001
50	212	1.226	1.225	-0.001
50	213	1.226	1.225	-0.001
50	214	1.226	1.224	-0.002
55	215	1.226	1.226	0.000
55	216	1.226	1.225	-0.001
55	217	1.226	1.224	-0.002
55	218	1.226	1.225	-0.001
55	220	1.225	1.223	-0.002
55	247	1.226	1.223	-0.003
100	249	1.226	1.224	-0.002
100	250	1.226	1.222	-0.004
100	251	1.226	1.222	-0.004
100	253	1.225	1.223	-0.002
100	255	1.225	1.223	-0.002
100	256	1.226	1.224	-0.002
100	257	1.226	1.223	-0.003
100	258	1.226	1.223	-0.003
100	259	1.225	1.223	-0.002
100	260	1.226	1.224	-0.002
100	261	1.226	1.224	-0.002
100	262	1.226	1.224	-0.002
100	263	1.225	1.222	-0.003
100	264	1.226	1.223	-0.003
100	265	1.225	1.222	-0.003
100	266	1.226	1.223	-0.003
100	267	1.226	1.222	-0.004
100	268	1.226	1.223	-0.003
100	269	1.226	1.224	-0.002
100	270	1.226	1.223	-0.003
100	271	1.226	1.223	-0.003
100	273	1.226	1.223	-0.003
105	274	1.226	1.223	-0.003
105	275	1.226	1.223	-0.003
105	276	1.225	1.223	-0.002
105	277	1.226	1.225	-0.001
105	278	1.226	1.223	-0.003
	Max	1.226	1.226	0.000
	Average	1.226	1.224	-0.001
	Min	1.224	1.222	-0.004
	Std Dev	0.000	0.001	0.001

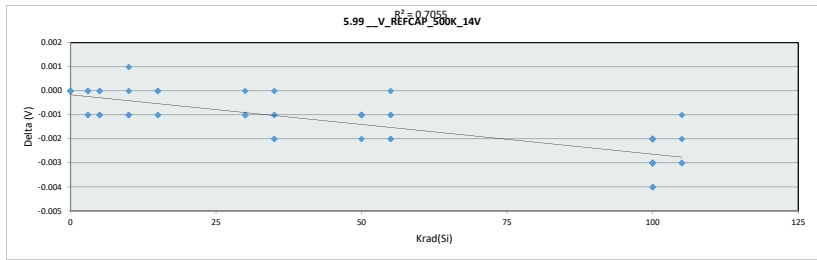


5.98_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.224	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.222	1.223
Average	1.226	1.225	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.225
Max	1.226	1.226	1.226	1.225	1.225	1.226	1.226	1.226	1.226	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

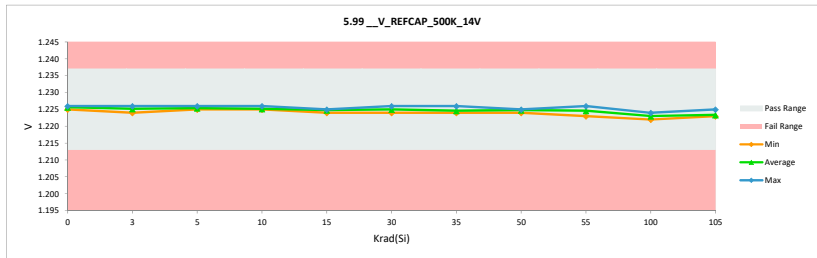


HDR TID Report TPS7H5002-SP

5.99_V_REFCAP_500K_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.225	1.225	0.000
0	190	1.226	1.226	0.000
0	191	1.226	1.226	0.000
3	300	1.224	1.224	0.000
3	305	1.226	1.225	-0.001
3	309	1.226	1.226	0.000
3	311	1.226	1.226	0.000
3	313	1.226	1.225	-0.001
5	319	1.226	1.225	-0.001
5	321	1.226	1.226	0.000
5	324	1.226	1.225	-0.001
5	327	1.226	1.226	0.000
5	331	1.226	1.225	-0.001
10	234	1.225	1.226	0.001
10	235	1.226	1.225	-0.001
10	236	1.226	1.225	-0.001
10	237	1.226	1.225	-0.001
10	238	1.225	1.225	0.000
10	239	1.225	1.225	0.000
15	240	1.226	1.225	-0.001
15	241	1.225	1.224	-0.001
15	243	1.225	1.225	0.000
15	245	1.225	1.225	0.000
30	222	1.226	1.225	-0.001
30	223	1.226	1.225	-0.001
30	225	1.226	1.226	0.000
30	226	1.226	1.225	-0.001
30	227	1.225	1.224	-0.001
35	228	1.226	1.225	-0.001
35	229	1.225	1.224	-0.001
35	231	1.226	1.226	0.000
35	232	1.226	1.224	-0.002
35	233	1.226	1.224	-0.002
50	210	1.226	1.225	-0.001
50	211	1.226	1.225	-0.001
50	212	1.226	1.225	-0.001
50	213	1.226	1.225	-0.001
50	214	1.226	1.224	-0.002
55	215	1.226	1.226	0.000
55	216	1.226	1.225	-0.001
55	217	1.226	1.224	-0.002
55	218	1.226	1.225	-0.001
55	220	1.225	1.223	-0.002
55	247	1.226	1.223	-0.003
100	249	1.226	1.224	-0.002
100	250	1.226	1.222	-0.004
100	251	1.226	1.222	-0.004
100	253	1.225	1.223	-0.002
100	255	1.225	1.223	-0.002
100	256	1.226	1.224	-0.002
100	257	1.226	1.223	-0.003
100	258	1.226	1.223	-0.003
100	259	1.226	1.223	-0.003
100	260	1.226	1.224	-0.002
100	261	1.226	1.224	-0.002
100	262	1.226	1.224	-0.002
100	263	1.225	1.222	-0.003
100	264	1.226	1.223	-0.003
100	265	1.225	1.222	-0.003
100	266	1.226	1.223	-0.003
100	267	1.226	1.222	-0.004
100	268	1.226	1.223	-0.003
100	269	1.226	1.224	-0.002
100	270	1.226	1.223	-0.003
100	271	1.226	1.223	-0.003
100	273	1.226	1.223	-0.003
105	274	1.226	1.223	-0.003
105	275	1.226	1.223	-0.003
105	276	1.225	1.223	-0.002
105	277	1.226	1.225	-0.001
105	278	1.226	1.223	-0.003
Max		1.226	1.226	0.001
Average		1.226	1.224	-0.002
Min		1.224	1.222	-0.004
Std Dev		0.000	0.001	0.001

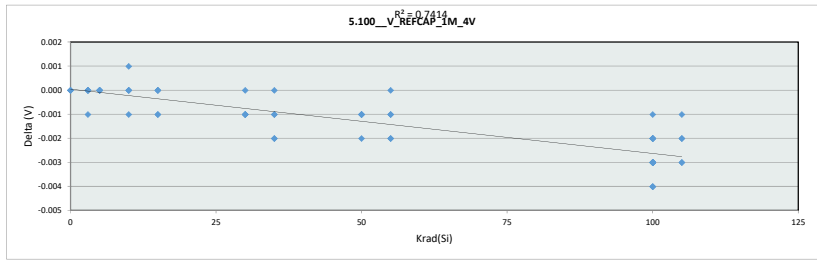


5.99_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.224	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.222	1.223
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.225	1.225	1.223	1.223
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.226	1.226	1.226	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

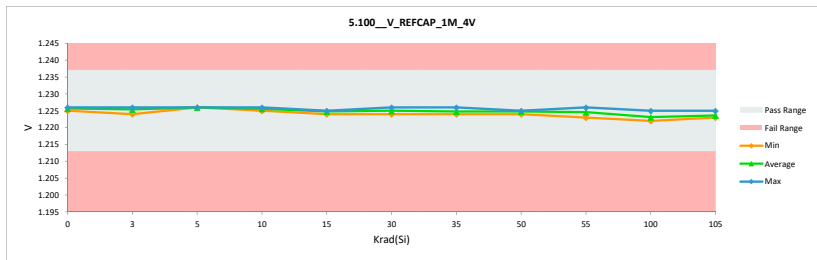


HDR TID Report
TPS7H5002-SP

5.100_V_REFCAP_1M_4V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit		1.237		1.237
Min Limit		1.213		1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.225	1.225	0.000
0	190	1.226	1.226	0.000
0	191	1.226	1.226	0.000
3	300	1.224	1.224	0.000
3	305	1.226	1.225	-0.001
3	309	1.226	1.226	0.000
3	311	1.226	1.226	0.000
3	313	1.226	1.226	0.000
5	319	1.226	1.226	0.000
5	321	1.226	1.226	0.000
5	324	1.226	1.226	0.000
5	327	1.226	1.226	0.000
5	331	1.226	1.226	0.000
10	234	1.225	1.226	0.001
10	235	1.226	1.226	0.000
10	236	1.226	1.226	0.000
10	237	1.226	1.225	-0.001
10	238	1.225	1.225	0.000
15	239	1.225	1.225	0.000
15	240	1.226	1.225	-0.001
15	241	1.225	1.224	-0.001
15	243	1.225	1.225	0.000
15	245	1.225	1.225	0.000
30	222	1.226	1.225	-0.001
30	223	1.226	1.225	-0.001
30	225	1.226	1.226	0.000
30	226	1.226	1.225	-0.001
30	227	1.225	1.224	-0.001
35	228	1.227	1.225	-0.002
35	229	1.225	1.224	-0.001
35	231	1.226	1.226	0.000
35	232	1.226	1.225	-0.001
35	233	1.226	1.224	-0.002
50	210	1.226	1.225	-0.001
50	211	1.226	1.225	-0.001
50	212	1.226	1.225	-0.001
50	213	1.226	1.225	-0.001
50	214	1.226	1.224	-0.002
55	215	1.226	1.226	0.000
55	216	1.226	1.225	-0.001
55	217	1.226	1.224	-0.002
55	218	1.226	1.225	-0.001
55	220	1.225	1.223	-0.002
100	247	1.226	1.223	-0.003
100	249	1.226	1.224	-0.002
100	250	1.226	1.222	-0.004
100	251	1.226	1.222	-0.004
100	253	1.225	1.223	-0.002
100	255	1.226	1.223	-0.003
100	256	1.226	1.224	-0.002
100	257	1.226	1.223	-0.003
100	258	1.226	1.223	-0.003
100	259	1.226	1.223	-0.003
100	260	1.227	1.224	-0.003
100	261	1.226	1.225	-0.001
100	262	1.226	1.224	-0.002
100	263	1.225	1.222	-0.003
100	264	1.226	1.224	-0.002
100	265	1.225	1.222	-0.003
100	266	1.226	1.223	-0.003
100	267	1.226	1.222	-0.004
100	268	1.226	1.223	-0.003
100	269	1.226	1.224	-0.002
100	270	1.226	1.223	-0.003
100	271	1.226	1.223	-0.003
100	273	1.226	1.223	-0.003
105	274	1.226	1.223	-0.003
105	275	1.226	1.223	-0.003
105	276	1.225	1.223	-0.002
105	277	1.226	1.225	-0.001
105	278	1.226	1.224	-0.002
	Max	1.227	1.226	0.001
	Average	1.226	1.224	-0.001
	Min	1.224	1.222	-0.004
	Std Dev	0.000	0.001	0.001

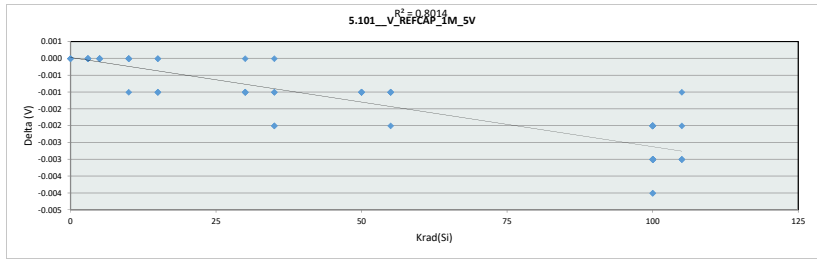


5.100_V_REFCAP											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit		1.237		1.237							
Min Limit		1.213		1.213							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.224	1.226	1.225	1.224	1.224	1.224	1.224	1.223	1.222	1.223
Average	1.226	1.225	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.226	1.226	1.225	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

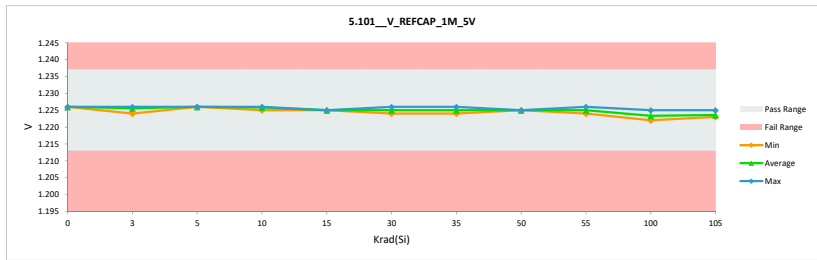


HDR TID Report TPS7H5002-SP

5.101_V_REFCAP_1M_5V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			1.237	1.237
Min Limit			1.213	1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.226	1.226	0.000
0	190	1.226	1.226	0.000
0	191	1.226	1.226	0.000
3	300	1.224	1.224	0.000
3	305	1.226	1.226	0.000
3	309	1.226	1.226	0.000
3	311	1.226	1.226	0.000
3	313	1.226	1.226	0.000
5	319	1.226	1.226	0.000
5	321	1.226	1.226	0.000
5	324	1.226	1.226	0.000
5	327	1.226	1.226	0.000
5	331	1.226	1.226	0.000
10	234	1.226	1.226	0.000
10	235	1.226	1.226	0.000
10	236	1.226	1.226	0.000
10	237	1.226	1.226	0.000
10	238	1.226	1.225	-0.001
15	239	1.225	1.225	0.000
15	240	1.226	1.225	-0.001
15	241	1.225	1.225	0.000
15	243	1.226	1.225	-0.001
15	245	1.226	1.225	-0.001
30	222	1.226	1.225	-0.001
30	223	1.226	1.225	-0.001
30	225	1.226	1.226	0.000
30	226	1.226	1.225	-0.001
30	227	1.225	1.224	-0.001
35	228	1.227	1.225	-0.002
35	229	1.225	1.225	0.000
35	231	1.227	1.226	-0.001
35	232	1.226	1.225	-0.001
35	233	1.226	1.224	-0.002
50	210	1.226	1.225	-0.001
50	211	1.226	1.225	-0.001
50	212	1.226	1.225	-0.001
50	213	1.226	1.225	-0.001
50	214	1.226	1.225	-0.001
55	215	1.227	1.226	-0.001
55	216	1.226	1.225	-0.001
55	217	1.226	1.225	-0.001
55	218	1.226	1.225	-0.001
55	220	1.226	1.224	-0.002
100	247	1.226	1.223	-0.003
100	249	1.227	1.224	-0.003
100	250	1.226	1.222	-0.004
100	251	1.226	1.222	-0.004
100	253	1.226	1.224	-0.002
100	255	1.226	1.223	-0.003
100	256	1.226	1.224	-0.002
100	257	1.226	1.223	-0.003
100	258	1.226	1.223	-0.003
100	259	1.226	1.223	-0.003
100	260	1.227	1.224	-0.003
100	261	1.227	1.225	-0.002
100	262	1.226	1.224	-0.002
100	263	1.225	1.222	-0.003
100	264	1.226	1.224	-0.002
100	265	1.226	1.223	-0.003
100	266	1.226	1.224	-0.002
100	267	1.226	1.223	-0.003
100	268	1.226	1.224	-0.002
100	269	1.226	1.224	-0.002
100	270	1.226	1.223	-0.003
100	271	1.226	1.223	-0.003
100	273	1.226	1.223	-0.003
105	274	1.226	1.223	-0.003
105	275	1.226	1.223	-0.003
105	276	1.226	1.223	-0.003
105	277	1.226	1.225	-0.001
105	278	1.226	1.224	-0.002
	Max	1.227	1.226	0.000
	Average	1.226	1.225	-0.001
	Min	1.224	1.222	-0.004
	Std Dev	0.006	0.001	0.001

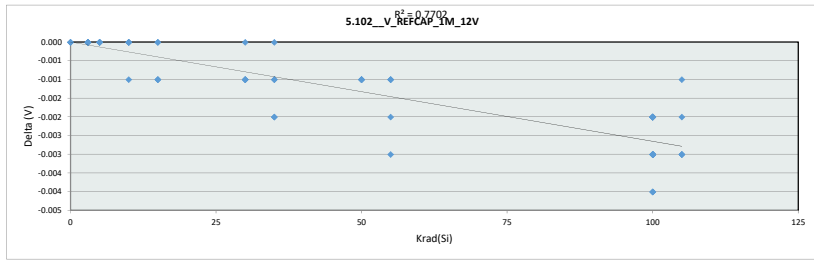


5.101_V_REFCAP											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit			1.237	1.237							
Min Limit			1.213	1.213							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.224	1.226	1.225	1.225	1.224	1.224	1.225	1.224	1.222	1.223
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.226	1.226	1.225	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

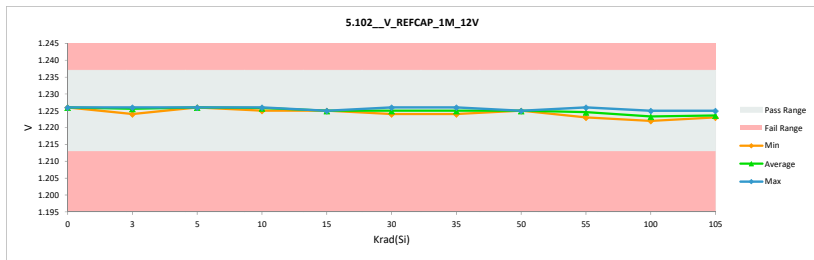


HDR TID Report TPS7H5002-SP

		5.102_V_REFCAP_1M_12V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.226	1.226	0.000
0	190	1.226	1.226	0.000
0	191	1.226	1.226	0.000
3	300	1.224	1.224	0.000
3	305	1.226	1.226	0.000
3	309	1.226	1.226	0.000
3	311	1.226	1.226	0.000
3	313	1.226	1.226	0.000
5	319	1.226	1.226	0.000
5	321	1.226	1.226	0.000
5	324	1.226	1.226	0.000
5	327	1.226	1.226	0.000
5	331	1.226	1.226	0.000
10	234	1.226	1.226	0.000
10	235	1.226	1.226	0.000
10	236	1.226	1.226	0.000
10	237	1.226	1.226	0.000
10	238	1.226	1.225	-0.001
15	239	1.225	1.225	0.000
15	240	1.226	1.225	-0.001
15	241	1.225	1.225	0.000
15	243	1.226	1.225	-0.001
15	245	1.226	1.225	-0.001
30	222	1.226	1.225	-0.001
30	223	1.226	1.225	-0.001
30	225	1.226	1.226	0.000
30	226	1.226	1.225	-0.001
30	227	1.225	1.224	-0.001
35	228	1.227	1.225	-0.002
35	229	1.225	1.225	0.000
35	231	1.227	1.226	-0.001
35	232	1.226	1.225	-0.001
35	233	1.226	1.224	-0.002
50	210	1.226	1.225	-0.001
50	211	1.226	1.225	-0.001
50	212	1.226	1.225	-0.001
50	213	1.226	1.225	-0.001
50	214	1.226	1.225	-0.001
55	215	1.227	1.226	-0.001
55	216	1.226	1.225	-0.001
55	217	1.226	1.224	-0.002
55	218	1.226	1.225	-0.001
55	220	1.226	1.223	-0.003
100	247	1.226	1.223	-0.003
100	249	1.226	1.224	-0.002
100	250	1.226	1.222	-0.004
100	251	1.226	1.222	-0.004
100	253	1.226	1.224	-0.002
100	255	1.226	1.223	-0.003
100	256	1.226	1.224	-0.002
100	257	1.226	1.223	-0.003
100	258	1.226	1.223	-0.003
100	259	1.226	1.223	-0.003
100	260	1.227	1.224	-0.003
100	261	1.227	1.225	-0.002
100	262	1.226	1.224	-0.002
100	263	1.225	1.222	-0.003
100	264	1.226	1.224	-0.002
100	265	1.226	1.222	-0.004
100	266	1.226	1.224	-0.002
100	267	1.226	1.223	-0.003
100	268	1.226	1.224	-0.002
100	269	1.226	1.224	-0.002
100	270	1.226	1.223	-0.003
100	271	1.226	1.223	-0.003
100	273	1.226	1.223	-0.003
105	274	1.226	1.223	-0.003
105	275	1.226	1.223	-0.003
105	276	1.226	1.223	-0.003
105	277	1.226	1.225	-0.001
105	278	1.226	1.224	-0.002
	Max	1.227	1.226	0.000
	Average	1.226	1.225	-0.001
	Min	1.224	1.222	-0.004
	Std Dev	0.006	0.001	0.001

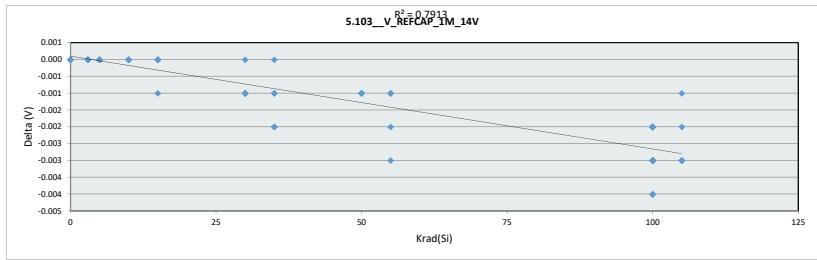


		5.102_V_REFCAP													
Test Site	Tester														
Test Number	Unit	1.237	V												
Max Limit	1.237	V													
Min Limit	1.213	V													
Krad(Si)	LL	3	5	10	15	30	35	50	55	100	105	Min	Average	Max	UL
1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.237
1.226	1.224	1.226	1.225	1.225	1.225	1.224	1.224	1.225	1.225	1.225	1.225	1.223	1.223	1.224	1.237
1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.225	1.225	1.223	1.224	1.224	1.237
1.226	1.226	1.226	1.226	1.225	1.225	1.226	1.226	1.226	1.225	1.226	1.226	1.225	1.225	1.225	1.237

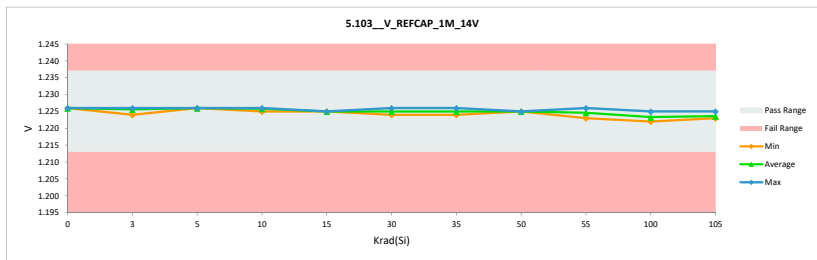


HDR TID Report
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5.103_V_REFCAP_1M_14V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.226	1.226	0.000	
0	190	1.226	1.226	0.000	
0	191	1.226	1.226	0.000	
3	300	1.224	1.224	0.000	
3	305	1.226	1.226	0.000	
3	309	1.226	1.226	0.000	
3	311	1.226	1.226	0.000	
3	313	1.226	1.226	0.000	
5	319	1.226	1.226	0.000	
5	321	1.226	1.226	0.000	
5	324	1.226	1.226	0.000	
5	327	1.226	1.226	0.000	
5	331	1.226	1.226	0.000	
10	234	1.226	1.226	0.000	
10	235	1.226	1.226	0.000	
10	236	1.226	1.226	0.000	
10	237	1.226	1.226	0.000	
10	238	1.225	1.225	0.000	
10	239	1.225	1.225	0.000	
15	240	1.226	1.225	-0.001	
15	241	1.225	1.225	0.000	
15	243	1.225	1.225	0.000	
15	245	1.225	1.225	0.000	
30	222	1.226	1.225	-0.001	
30	223	1.226	1.225	-0.001	
30	225	1.226	1.226	0.000	
30	226	1.226	1.225	-0.001	
30	227	1.225	1.224	-0.001	
35	228	1.227	1.225	-0.002	
35	229	1.225	1.225	0.000	
35	231	1.227	1.226	-0.001	
35	232	1.226	1.225	-0.001	
35	233	1.226	1.224	-0.002	
50	210	1.226	1.225	-0.001	
50	211	1.226	1.225	-0.001	
50	212	1.226	1.225	-0.001	
50	213	1.226	1.225	-0.001	
50	214	1.226	1.225	-0.001	
55	215	1.227	1.226	-0.001	
55	216	1.226	1.225	-0.001	
55	217	1.226	1.224	-0.002	
55	218	1.226	1.225	-0.001	
55	220	1.226	1.223	-0.003	
100	247	1.226	1.223	-0.003	
100	249	1.226	1.224	-0.002	
100	250	1.226	1.222	-0.004	
100	251	1.226	1.222	-0.004	
100	253	1.226	1.224	-0.002	
100	255	1.226	1.223	-0.003	
100	256	1.226	1.224	-0.002	
100	257	1.226	1.223	-0.003	
100	258	1.226	1.223	-0.003	
100	259	1.226	1.223	-0.003	
100	260	1.227	1.224	-0.003	
100	261	1.227	1.225	-0.002	
100	262	1.226	1.224	-0.002	
100	263	1.225	1.222	-0.003	
100	264	1.226	1.224	-0.002	
100	265	1.226	1.222	-0.004	
100	266	1.226	1.224	-0.002	
100	267	1.226	1.223	-0.003	
100	268	1.226	1.224	-0.002	
100	269	1.226	1.224	-0.002	
100	270	1.226	1.223	-0.003	
100	271	1.226	1.223	-0.003	
100	273	1.226	1.223	-0.003	
105	274	1.226	1.223	-0.003	
105	275	1.226	1.223	-0.003	
105	276	1.226	1.223	-0.003	
105	277	1.226	1.225	-0.001	
105	278	1.226	1.224	-0.002	
Max		1.227	1.226	0.000	
Average		1.226	1.225	-0.001	
Min		1.224	1.222	-0.004	
Std Dev		0.006	0.001	0.001	

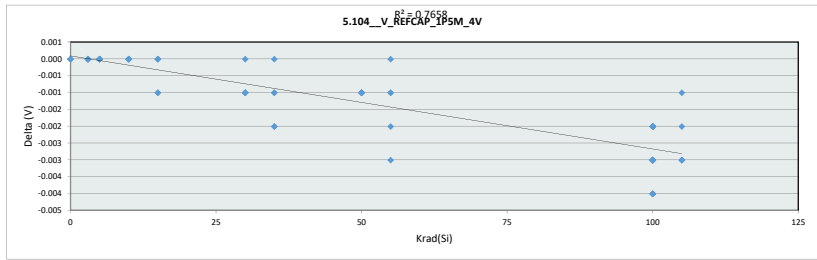


5.103_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.224	1.226	1.225	1.225	1.224	1.224	1.225	1.223	1.222	1.223
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.224
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.226	1.226	1.225	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

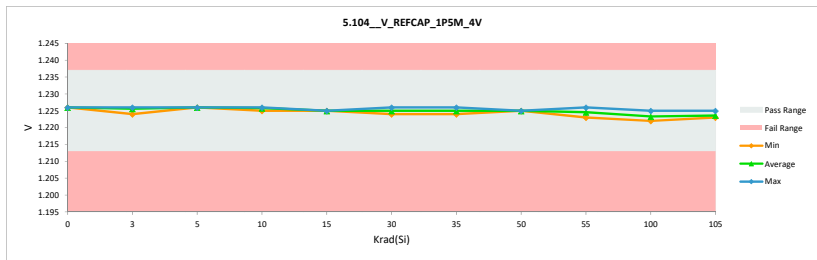


HDR TID Report
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5.104_V_REFCAP_1P5M_4V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.226	1.226	0.000	
0	190	1.226	1.226	0.000	
0	191	1.226	1.226	0.000	
3	300	1.224	1.224	0.000	
3	305	1.226	1.226	0.000	
3	309	1.226	1.226	0.000	
3	311	1.226	1.226	0.000	
3	313	1.226	1.226	0.000	
5	319	1.226	1.226	0.000	
5	321	1.226	1.226	0.000	
5	324	1.226	1.226	0.000	
5	327	1.226	1.226	0.000	
5	331	1.226	1.226	0.000	
10	234	1.226	1.226	0.000	
10	235	1.226	1.226	0.000	
10	236	1.226	1.226	0.000	
10	237	1.226	1.226	0.000	
10	238	1.225	1.225	0.000	
10	239	1.225	1.225	0.000	
15	240	1.226	1.225	-0.001	
15	241	1.225	1.225	0.000	
15	243	1.225	1.225	0.000	
15	245	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.226	1.225	-0.001	
30	225	1.226	1.226	0.000	
30	226	1.226	1.225	-0.001	
30	227	1.225	1.224	-0.001	
35	228	1.227	1.225	-0.002	
35	229	1.225	1.225	0.000	
35	231	1.227	1.226	-0.001	
35	232	1.226	1.225	-0.001	
35	233	1.226	1.224	-0.002	
50	210	1.226	1.225	-0.001	
50	211	1.226	1.225	-0.001	
50	212	1.226	1.225	-0.001	
50	213	1.226	1.225	-0.001	
50	214	1.226	1.225	-0.001	
55	215	1.226	1.226	0.000	
55	216	1.226	1.225	-0.001	
55	217	1.226	1.224	-0.002	
55	218	1.226	1.225	-0.001	
55	220	1.226	1.223	-0.003	
100	247	1.226	1.223	-0.003	
100	249	1.226	1.224	-0.002	
100	250	1.226	1.222	-0.004	
100	251	1.226	1.222	-0.004	
100	253	1.226	1.224	-0.002	
100	255	1.226	1.223	-0.003	
100	256	1.226	1.224	-0.002	
100	257	1.226	1.223	-0.003	
100	258	1.226	1.223	-0.003	
100	259	1.226	1.223	-0.003	
100	260	1.227	1.224	-0.003	
100	261	1.227	1.225	-0.002	
100	262	1.226	1.224	-0.002	
100	263	1.225	1.222	-0.003	
100	264	1.226	1.224	-0.002	
100	265	1.226	1.222	-0.004	
100	266	1.226	1.224	-0.002	
100	267	1.226	1.223	-0.003	
100	268	1.226	1.224	-0.002	
100	269	1.226	1.224	-0.002	
100	270	1.226	1.223	-0.003	
100	271	1.226	1.223	-0.003	
100	273	1.227	1.223	-0.004	
105	274	1.226	1.223	-0.003	
105	275	1.226	1.223	-0.003	
105	276	1.226	1.223	-0.003	
105	277	1.226	1.225	-0.001	
105	278	1.226	1.224	-0.002	
Max		1.227	1.226	0.000	
Average		1.226	1.225	-0.001	
Min		1.224	1.222	-0.004	
Std Dev		0.006	0.001	0.001	

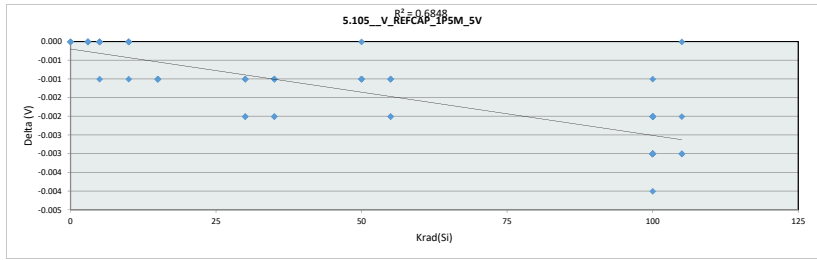


5.104_V_REFCAP											
Test Site											
Tester											
Test Number	1.237	V									
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.224	1.226	1.225	1.225	1.224	1.224	1.225	1.223	1.222	1.223
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.225	1.224
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.226	1.226	1.225	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

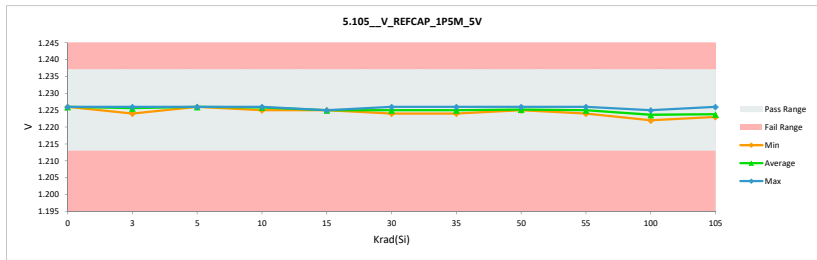


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5.105_V_REFCAP_1P5M_5V					
Test Site					
Tester					
Test Number					
Unit		V	V		
Max Limit		1.237	1.237		
Min Limit		1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.226	1.226	0.000	
0	190	1.226	1.226	0.000	
0	191	1.226	1.226	0.000	
3	300	1.224	1.224	0.000	
3	305	1.226	1.226	0.000	
3	309	1.226	1.226	0.000	
3	311	1.226	1.226	0.000	
3	313	1.226	1.226	0.000	
5	319	1.226	1.226	0.000	
5	321	1.226	1.226	0.000	
5	324	1.226	1.226	0.000	
5	327	1.227	1.226	-0.001	
5	331	1.226	1.226	0.000	
10	234	1.226	1.226	0.000	
10	235	1.226	1.226	0.000	
10	236	1.226	1.226	0.000	
10	237	1.226	1.226	0.000	
10	238	1.226	1.225	-0.001	
15	239	1.226	1.225	-0.001	
15	240	1.226	1.225	-0.001	
15	241	1.226	1.225	-0.001	
15	243	1.226	1.225	-0.001	
15	245	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.226	1.225	-0.001	
30	225	1.227	1.226	-0.001	
30	226	1.227	1.225	-0.002	
30	227	1.226	1.224	-0.002	
35	228	1.227	1.225	-0.002	
35	229	1.226	1.225	-0.001	
35	231	1.227	1.226	-0.001	
35	232	1.226	1.225	-0.001	
35	233	1.226	1.224	-0.002	
50	210	1.226	1.225	-0.001	
50	211	1.226	1.225	-0.001	
50	212	1.226	1.225	-0.001	
50	213	1.226	1.226	0.000	
50	214	1.226	1.225	-0.001	
55	215	1.227	1.226	-0.001	
55	216	1.226	1.225	-0.001	
55	217	1.227	1.225	-0.002	
55	218	1.226	1.225	-0.001	
55	220	1.226	1.224	-0.002	
100	247	1.227	1.224	-0.003	
100	249	1.227	1.225	-0.002	
100	250	1.226	1.223	-0.003	
100	251	1.226	1.223	-0.003	
100	253	1.226	1.224	-0.002	
100	255	1.226	1.223	-0.003	
100	256	1.226	1.224	-0.002	
100	257	1.226	1.224	-0.002	
100	258	1.226	1.223	-0.003	
100	259	1.226	1.223	-0.003	
100	260	1.227	1.224	-0.003	
100	261	1.227	1.225	-0.002	
100	262	1.227	1.224	-0.003	
100	263	1.226	1.222	-0.004	
100	264	1.226	1.224	-0.002	
100	265	1.226	1.223	-0.003	
100	266	1.226	1.224	-0.002	
100	267	1.226	1.223	-0.003	
100	268	1.226	1.224	-0.002	
100	269	1.226	1.225	-0.001	
100	270	1.226	1.223	-0.003	
100	271	1.226	1.223	-0.003	
100	273	1.227	1.224	-0.003	
105	274	1.226	1.223	-0.003	
105	275	1.226	1.223	-0.003	
105	276	1.226	1.223	-0.003	
105	277	1.226	1.226	0.000	
105	278	1.226	1.224	-0.002	
	Max	1.227	1.226	0.000	
	Average	1.226	1.225	-0.001	
	Min	1.224	1.222	-0.004	
	Std Dev	0.000	0.001	0.001	

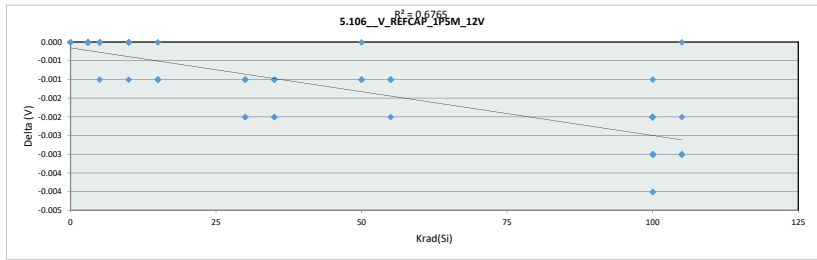


5.105_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit		1.237	V								
Min Limit		1.213	V								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.224	1.226	1.225	1.225	1.224	1.224	1.225	1.224	1.222	1.223
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.226	1.226	1.226	1.225	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

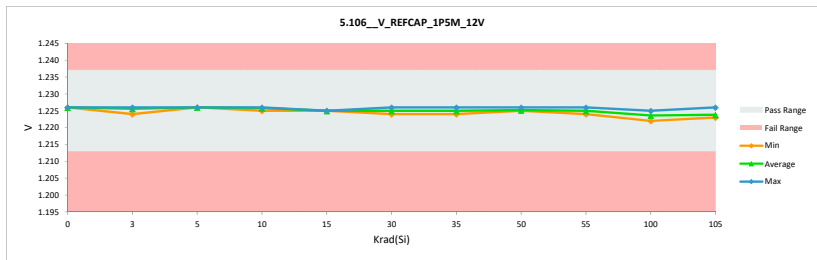


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		5.106_V_REFCAP_1P5M_12V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit		1.237	1.237	
Min Limit		1.213	1.213	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.226	1.226	0.000
0	190	1.226	1.226	0.000
0	191	1.226	1.226	0.000
3	300	1.224	1.224	0.000
3	305	1.226	1.226	0.000
3	309	1.226	1.226	0.000
3	311	1.226	1.226	0.000
3	313	1.226	1.226	0.000
5	319	1.226	1.226	0.000
5	321	1.226	1.226	0.000
5	324	1.226	1.226	0.000
5	327	1.227	1.226	-0.001
5	331	1.226	1.226	0.000
10	234	1.226	1.226	0.000
10	235	1.226	1.226	0.000
10	236	1.226	1.226	0.000
10	237	1.226	1.226	0.000
10	238	1.226	1.225	-0.001
15	239	1.226	1.225	-0.001
15	240	1.226	1.225	-0.001
15	241	1.225	1.225	0.000
15	243	1.226	1.225	-0.001
15	245	1.226	1.225	-0.001
30	222	1.226	1.225	-0.001
30	223	1.226	1.225	-0.001
30	225	1.227	1.226	-0.001
30	226	1.227	1.225	-0.002
30	227	1.226	1.224	-0.002
35	228	1.227	1.225	-0.002
35	229	1.226	1.225	-0.001
35	231	1.227	1.226	-0.001
35	232	1.226	1.225	-0.001
35	233	1.226	1.224	-0.002
50	210	1.226	1.225	-0.001
50	211	1.226	1.225	-0.001
50	212	1.226	1.225	-0.001
50	213	1.226	1.226	0.000
50	214	1.226	1.225	-0.001
55	215	1.227	1.226	-0.001
55	216	1.226	1.225	-0.001
55	217	1.226	1.225	-0.001
55	218	1.226	1.225	-0.001
55	220	1.226	1.224	-0.002
55	247	1.227	1.223	-0.004
100	249	1.227	1.225	-0.002
100	250	1.226	1.223	-0.003
100	251	1.226	1.223	-0.003
100	253	1.226	1.224	-0.002
100	255	1.226	1.223	-0.003
100	256	1.226	1.224	-0.002
100	257	1.226	1.224	-0.002
100	258	1.226	1.223	-0.003
100	259	1.226	1.223	-0.003
100	260	1.227	1.224	-0.003
100	261	1.227	1.225	-0.002
100	262	1.226	1.224	-0.002
100	263	1.226	1.222	-0.004
100	264	1.226	1.224	-0.002
100	265	1.226	1.223	-0.003
100	266	1.226	1.224	-0.002
100	267	1.226	1.223	-0.003
100	268	1.226	1.224	-0.002
100	269	1.226	1.225	-0.001
100	270	1.226	1.223	-0.003
100	271	1.226	1.223	-0.003
100	273	1.227	1.224	-0.003
105	274	1.226	1.223	-0.003
105	275	1.226	1.223	-0.003
105	276	1.226	1.223	-0.003
105	277	1.226	1.226	0.000
105	278	1.226	1.224	-0.002
	Max	1.227	1.226	0.000
	Average	1.226	1.225	-0.001
	Min	1.224	1.222	-0.004
	Std Dev	0.000	0.001	0.001

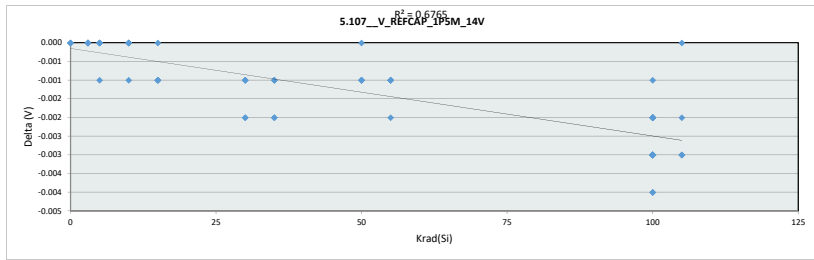


		5.106_V_REFCAP										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit		1.237	1.237									
Min Limit		1.213	1.213									
Krad(Si)	Serial #	PRE	POST	Delta								
0	1	1.213	1.213	0.000	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
3	3	1.226	1.224	-0.002	1.224	1.224	1.224	1.224	1.224	1.224	1.224	1.224
5	5	1.226	1.225	-0.001	1.225	1.225	1.225	1.225	1.225	1.225	1.225	1.225
10	10	1.226	1.225	-0.001	1.225	1.225	1.225	1.225	1.225	1.225	1.225	1.225
15	15	1.226	1.225	-0.001	1.225	1.225	1.225	1.225	1.225	1.225	1.225	1.225
30	30	1.226	1.225	-0.001	1.225	1.225	1.225	1.225	1.225	1.225	1.225	1.225
35	35	1.226	1.225	-0.001	1.225	1.225	1.225	1.225	1.225	1.225	1.225	1.225
50	50	1.226	1.226	0.000	1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.226
55	55	1.226	1.226	0.000	1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.226
100	100	1.226	1.226	0.000	1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.226
105	105	1.226	1.226	0.000	1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.226

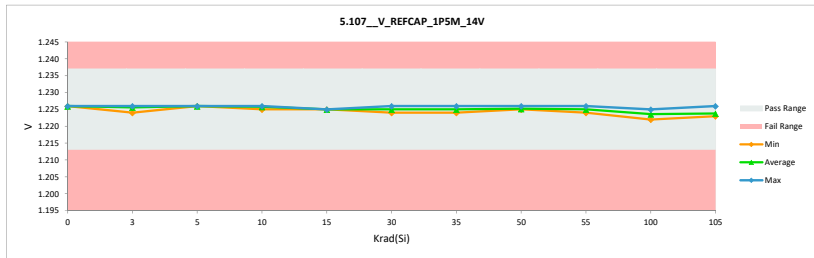


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5.107_V_REFCAP_1PSM_14V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit		1.237		1.237
Min Limit		1.213		1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.226	1.226	0.000
0	190	1.226	1.226	0.000
0	191	1.226	1.226	0.000
3	300	1.224	1.224	0.000
3	305	1.226	1.226	0.000
3	309	1.226	1.226	0.000
3	311	1.226	1.226	0.000
3	313	1.226	1.226	0.000
5	319	1.226	1.226	0.000
5	321	1.226	1.226	0.000
5	324	1.226	1.226	0.000
5	327	1.227	1.226	-0.001
5	331	1.226	1.226	0.000
10	234	1.226	1.226	0.000
10	235	1.226	1.226	0.000
10	236	1.226	1.226	0.000
10	237	1.226	1.226	0.000
10	238	1.226	1.225	-0.001
15	239	1.226	1.225	-0.001
15	240	1.226	1.225	-0.001
15	241	1.225	1.225	0.000
15	243	1.226	1.225	-0.001
15	245	1.226	1.225	-0.001
30	222	1.226	1.225	-0.001
30	223	1.226	1.225	-0.001
30	225	1.227	1.226	-0.001
30	226	1.227	1.225	-0.002
30	227	1.226	1.224	-0.002
35	228	1.227	1.225	-0.002
35	229	1.226	1.225	-0.001
35	231	1.227	1.226	-0.001
35	232	1.226	1.225	-0.001
35	233	1.226	1.224	-0.002
50	210	1.226	1.225	-0.001
50	211	1.226	1.225	-0.001
50	212	1.226	1.225	-0.001
50	213	1.226	1.226	0.000
50	214	1.226	1.225	-0.001
55	215	1.227	1.226	-0.001
55	216	1.226	1.225	-0.001
55	217	1.226	1.225	-0.001
55	218	1.226	1.225	-0.001
55	220	1.226	1.224	-0.002
100	247	1.227	1.223	-0.004
100	249	1.227	1.225	-0.002
100	250	1.226	1.223	-0.003
100	251	1.226	1.223	-0.003
100	253	1.226	1.224	-0.002
100	255	1.226	1.223	-0.003
100	256	1.226	1.224	-0.002
100	257	1.226	1.224	-0.002
100	258	1.226	1.223	-0.003
100	259	1.226	1.223	-0.003
100	260	1.227	1.224	-0.003
100	261	1.227	1.225	-0.002
100	262	1.226	1.224	-0.002
100	263	1.226	1.222	-0.004
100	264	1.226	1.224	-0.002
100	265	1.226	1.223	-0.003
100	266	1.226	1.224	-0.002
100	267	1.226	1.223	-0.003
100	268	1.226	1.224	-0.002
100	269	1.226	1.225	-0.001
100	270	1.226	1.223	-0.003
100	271	1.226	1.223	-0.003
100	273	1.227	1.224	-0.003
105	274	1.226	1.223	-0.003
105	275	1.226	1.223	-0.003
105	276	1.226	1.223	-0.003
105	277	1.226	1.226	0.000
105	278	1.226	1.224	-0.002
	Max	1.227	1.226	0.000
	Average	1.226	1.225	-0.001
	Min	1.224	1.222	-0.004
	Std Dev	0.000	0.001	0.001

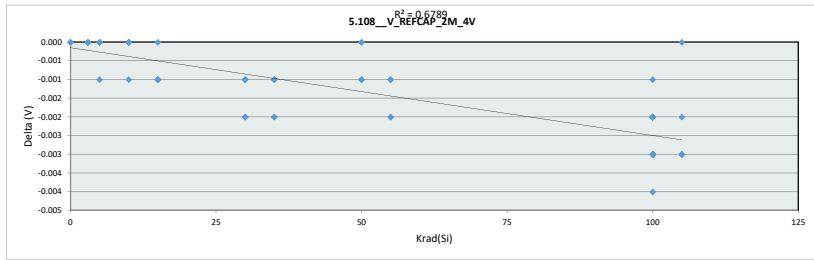


5.107_V_REFCAP											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit		1.237		1.237							
Min Limit		1.213		1.213							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.224	1.226	1.225	1.225	1.224	1.224	1.225	1.224	1.222	1.223
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.226	1.226	1.226	1.225	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

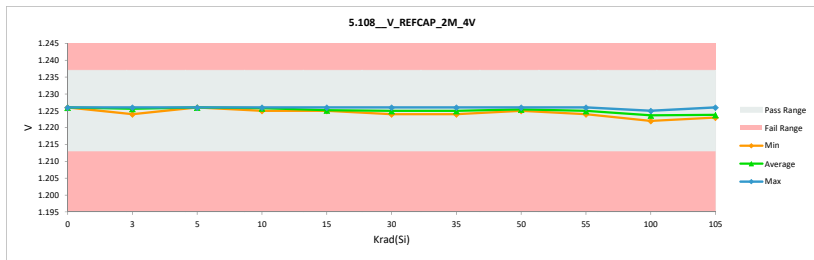


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5.108_V_REFCAP_2M_4V					
Test Site					
Tester					
Test Number					
Unit		V	V		
Max Limit		1.237	1.237		
Min Limit		1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.226	1.226	0.000	
0	190	1.226	1.226	0.000	
0	191	1.226	1.226	0.000	
3	300	1.224	1.224	0.000	
3	305	1.226	1.226	0.000	
3	309	1.226	1.226	0.000	
3	311	1.226	1.226	0.000	
3	313	1.226	1.226	0.000	
5	319	1.226	1.226	0.000	
5	321	1.226	1.226	0.000	
5	324	1.226	1.226	0.000	
5	327	1.227	1.226	-0.001	
5	331	1.226	1.226	0.000	
10	234	1.226	1.226	0.000	
10	235	1.226	1.226	0.000	
10	236	1.226	1.226	0.000	
10	237	1.226	1.226	0.000	
10	238	1.226	1.225	-0.001	
15	239	1.226	1.225	-0.001	
15	240	1.226	1.226	0.000	
15	241	1.226	1.225	-0.001	
15	243	1.226	1.225	-0.001	
15	245	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.226	1.225	-0.001	
30	225	1.227	1.226	-0.001	
30	226	1.227	1.225	-0.002	
30	227	1.226	1.224	-0.002	
35	228	1.227	1.225	-0.002	
35	229	1.226	1.225	-0.001	
35	231	1.227	1.226	-0.001	
35	232	1.226	1.225	-0.001	
35	233	1.226	1.224	-0.002	
50	210	1.226	1.225	-0.001	
50	211	1.226	1.226	0.000	
50	212	1.226	1.225	-0.001	
50	213	1.226	1.226	0.000	
50	214	1.226	1.225	-0.001	
55	215	1.227	1.226	-0.001	
55	216	1.226	1.225	-0.001	
55	217	1.227	1.225	-0.002	
55	218	1.226	1.225	-0.001	
55	220	1.226	1.224	-0.002	
100	247	1.227	1.224	-0.003	
100	249	1.227	1.225	-0.002	
100	250	1.226	1.223	-0.003	
100	251	1.226	1.223	-0.003	
100	253	1.226	1.224	-0.002	
100	255	1.226	1.223	-0.003	
100	256	1.226	1.224	-0.002	
100	257	1.226	1.224	-0.002	
100	258	1.226	1.223	-0.003	
100	259	1.226	1.223	-0.003	
100	260	1.227	1.224	-0.003	
100	261	1.227	1.225	-0.002	
100	262	1.227	1.224	-0.003	
100	263	1.226	1.222	-0.004	
100	264	1.226	1.224	-0.002	
100	265	1.226	1.223	-0.003	
100	266	1.226	1.224	-0.002	
100	267	1.226	1.223	-0.003	
100	268	1.226	1.224	-0.002	
100	269	1.226	1.225	-0.001	
100	270	1.226	1.223	-0.003	
100	271	1.226	1.223	-0.003	
100	273	1.227	1.224	-0.003	
105	274	1.226	1.223	-0.003	
105	275	1.226	1.223	-0.003	
105	276	1.226	1.223	-0.003	
105	277	1.226	1.226	0.000	
105	278	1.226	1.224	-0.002	
	Max	1.227	1.226	0.000	
	Average	1.226	1.225	-0.001	
	Min	1.224	1.222	-0.004	
	Std Dev	0.000	0.001	0.001	

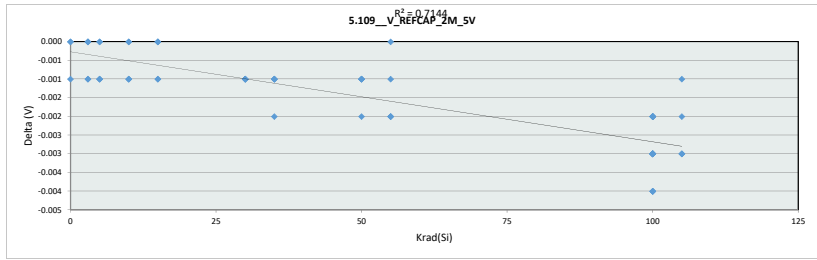


5.108_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit		1.237	V								
Min Limit		1.213	V								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.224	1.226	1.225	1.225	1.224	1.224	1.225	1.224	1.222	1.223
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.226	1.225	1.226
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

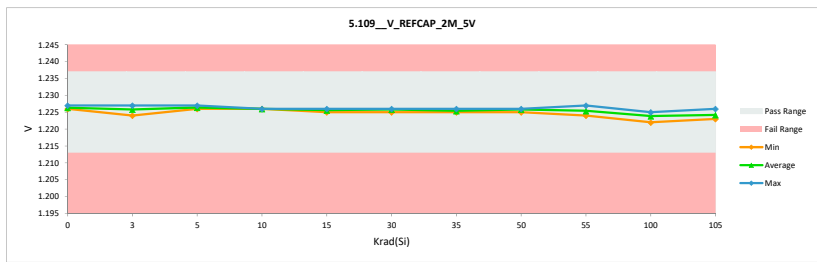


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5.109_V_REFCAP_2M_5V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.226	1.226	0.000	
0	190	1.227	1.227	0.000	
0	191	1.227	1.226	-0.001	
3	300	1.225	1.224	-0.001	
3	305	1.226	1.226	0.000	
3	309	1.227	1.226	-0.001	
3	311	1.227	1.227	0.000	
3	313	1.226	1.226	0.000	
5	319	1.227	1.226	-0.001	
5	321	1.227	1.227	0.000	
5	324	1.227	1.226	-0.001	
5	327	1.227	1.227	0.000	
5	331	1.227	1.226	-0.001	
10	234	1.226	1.226	0.000	
10	235	1.227	1.226	-0.001	
10	236	1.227	1.226	-0.001	
10	237	1.227	1.226	-0.001	
10	238	1.226	1.226	0.000	
15	239	1.226	1.225	-0.001	
15	240	1.226	1.226	0.000	
15	241	1.226	1.225	-0.001	
15	243	1.226	1.226	0.000	
15	245	1.226	1.226	0.000	
30	222	1.227	1.226	-0.001	
30	223	1.227	1.226	-0.001	
30	225	1.227	1.226	-0.001	
30	226	1.227	1.226	-0.001	
30	227	1.226	1.225	-0.001	
35	228	1.227	1.226	-0.001	
35	229	1.226	1.225	-0.001	
35	231	1.227	1.226	-0.001	
35	232	1.227	1.225	-0.002	
35	233	1.226	1.225	-0.001	
50	210	1.227	1.226	-0.001	
50	211	1.227	1.226	-0.001	
50	212	1.227	1.226	-0.001	
50	213	1.227	1.226	-0.001	
50	214	1.227	1.225	-0.002	
55	215	1.227	1.227	0.000	
55	216	1.227	1.225	-0.002	
55	217	1.227	1.225	-0.002	
55	218	1.227	1.226	-0.001	
55	220	1.226	1.224	-0.002	
100	247	1.227	1.224	-0.003	
100	249	1.227	1.225	-0.002	
100	250	1.227	1.223	-0.004	
100	251	1.227	1.223	-0.004	
100	253	1.226	1.224	-0.002	
100	255	1.226	1.223	-0.003	
100	256	1.227	1.224	-0.003	
100	257	1.227	1.224	-0.003	
100	258	1.226	1.223	-0.003	
100	259	1.227	1.224	-0.003	
100	260	1.227	1.225	-0.002	
100	261	1.227	1.225	-0.002	
100	262	1.227	1.225	-0.002	
100	263	1.226	1.222	-0.004	
100	264	1.227	1.224	-0.003	
100	265	1.226	1.223	-0.003	
100	266	1.227	1.224	-0.003	
100	267	1.227	1.223	-0.004	
100	268	1.226	1.224	-0.002	
100	269	1.227	1.225	-0.002	
100	270	1.226	1.224	-0.002	
100	271	1.227	1.224	-0.003	
100	273	1.227	1.224	-0.003	
105	274	1.227	1.224	-0.003	
105	275	1.226	1.223	-0.003	
105	276	1.226	1.224	-0.002	
105	277	1.227	1.226	-0.001	
105	278	1.227	1.224	-0.003	
	Max	1.227	1.227	0.000	
	Average	1.227	1.225	-0.002	
	Min	1.225	1.222	-0.004	
	Std Dev	0.001	0.001	0.001	

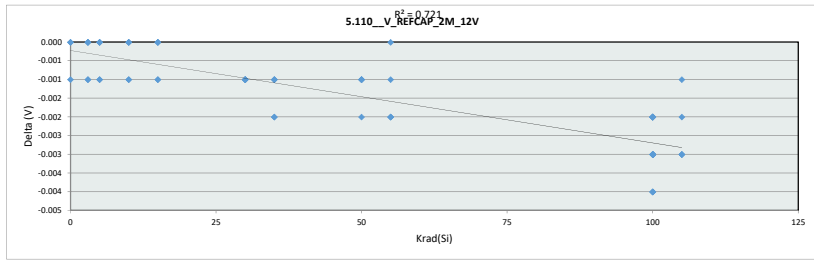


5.109_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.224	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.223
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.226	1.225	1.224	1.224
Max	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.226	1.227	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

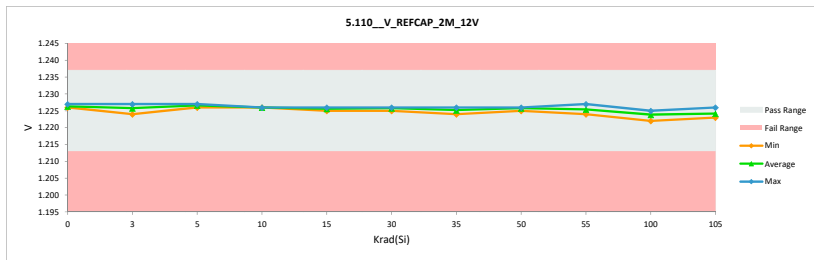


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5.110_V_REFCAP_2M_12V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			V	1.237	1.213
			V	1.237	1.213
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.226	1.226	0.000	
0	190	1.227	1.227	0.000	
0	191	1.227	1.226	-0.001	
3	300	1.225	1.224	-0.001	
3	305	1.226	1.226	0.000	
3	309	1.227	1.226	-0.001	
3	311	1.227	1.227	0.000	
3	313	1.226	1.226	0.000	
5	319	1.227	1.226	-0.001	
5	321	1.227	1.227	0.000	
5	324	1.227	1.227	0.000	
5	327	1.227	1.227	0.000	
5	331	1.227	1.226	-0.001	
10	234	1.226	1.226	0.000	
10	235	1.227	1.226	-0.001	
10	236	1.226	1.226	0.000	
10	237	1.227	1.226	-0.001	
10	238	1.226	1.226	0.000	
15	239	1.226	1.225	-0.001	
15	240	1.226	1.226	0.000	
15	241	1.226	1.225	-0.001	
15	243	1.226	1.226	0.000	
15	245	1.226	1.226	0.000	
30	222	1.227	1.226	-0.001	
30	223	1.227	1.226	-0.001	
30	225	1.227	1.226	-0.001	
30	226	1.227	1.226	-0.001	
30	227	1.226	1.225	-0.001	
35	228	1.227	1.226	-0.001	
35	229	1.226	1.225	-0.001	
35	231	1.227	1.226	-0.001	
35	232	1.227	1.225	-0.002	
35	233	1.226	1.224	-0.002	
50	210	1.227	1.226	-0.001	
50	211	1.227	1.226	-0.001	
50	212	1.227	1.226	-0.001	
50	213	1.227	1.226	-0.001	
50	214	1.227	1.225	-0.002	
55	215	1.227	1.227	0.000	
55	216	1.227	1.225	-0.002	
55	217	1.227	1.225	-0.002	
55	218	1.227	1.226	-0.001	
55	220	1.226	1.224	-0.002	
100	247	1.227	1.224	-0.003	
100	249	1.227	1.225	-0.002	
100	250	1.227	1.223	-0.004	
100	251	1.227	1.223	-0.004	
100	253	1.226	1.224	-0.002	
100	255	1.226	1.223	-0.003	
100	256	1.227	1.224	-0.003	
100	257	1.227	1.224	-0.003	
100	258	1.226	1.223	-0.003	
100	259	1.227	1.224	-0.003	
100	260	1.227	1.225	-0.002	
100	261	1.227	1.225	-0.002	
100	262	1.227	1.225	-0.002	
100	263	1.226	1.222	-0.004	
100	264	1.227	1.224	-0.003	
100	265	1.226	1.223	-0.003	
100	266	1.227	1.224	-0.003	
100	267	1.227	1.223	-0.004	
100	268	1.226	1.224	-0.002	
100	269	1.227	1.225	-0.002	
100	270	1.226	1.224	-0.002	
100	271	1.227	1.224	-0.003	
100	273	1.227	1.224	-0.003	
105	274	1.227	1.224	-0.003	
105	275	1.226	1.223	-0.003	
105	276	1.226	1.224	-0.002	
105	277	1.227	1.226	-0.001	
105	278	1.227	1.224	-0.003	
	Max	1.227	1.227	0.000	
	Average	1.227	1.225	-0.002	
	Min	1.225	1.222	-0.004	
	Std Dev	0.001	0.001	0.001	

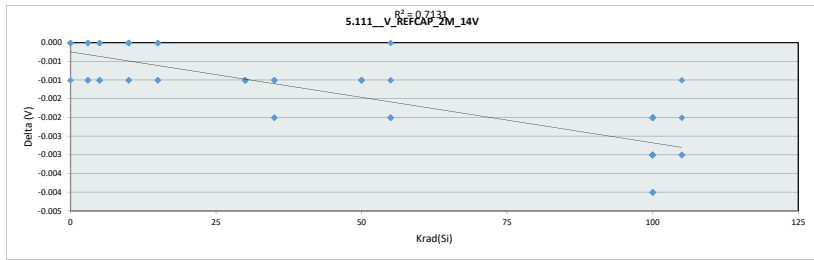


5.110_V_REFCAP											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	1.237	1.213						
			V	1.237	1.213						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.224	1.226	1.226	1.225	1.225	1.224	1.225	1.224	1.222	1.223
Average	1.226	1.226	1.227	1.226	1.226	1.226	1.225	1.226	1.225	1.224	1.224
Max	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.226	1.227	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

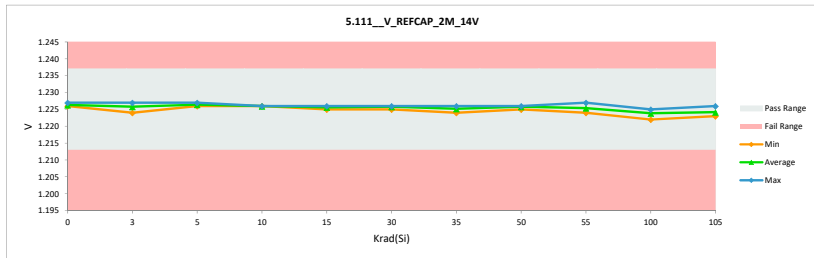


HDR TID Report
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5.111_V_REFCAP_2M_14V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			V	1.237	1.213
			V	1.237	1.213
Krad(Si)	Serial #	PRE	POST	Delta	
0	189	1.226	1.226	0.000	
0	190	1.227	1.227	0.000	
0	191	1.227	1.226	-0.001	
3	300	1.225	1.224	-0.001	
3	305	1.226	1.226	0.000	
3	309	1.227	1.226	-0.001	
3	311	1.227	1.227	0.000	
3	313	1.226	1.226	0.000	
5	319	1.227	1.226	-0.001	
5	321	1.227	1.227	0.000	
5	324	1.227	1.226	-0.001	
5	327	1.227	1.227	0.000	
5	331	1.227	1.226	-0.001	
10	234	1.226	1.226	0.000	
10	235	1.227	1.226	-0.001	
10	236	1.226	1.226	0.000	
10	237	1.227	1.226	-0.001	
10	238	1.226	1.226	0.000	
15	239	1.226	1.225	-0.001	
15	240	1.226	1.226	0.000	
15	241	1.226	1.225	-0.001	
15	243	1.226	1.226	0.000	
15	245	1.226	1.226	0.000	
30	222	1.227	1.226	-0.001	
30	223	1.227	1.226	-0.001	
30	225	1.227	1.226	-0.001	
30	226	1.227	1.226	-0.001	
30	227	1.226	1.225	-0.001	
35	228	1.227	1.226	-0.001	
35	229	1.226	1.225	-0.001	
35	231	1.227	1.226	-0.001	
35	232	1.227	1.225	-0.002	
35	233	1.226	1.224	-0.002	
50	210	1.227	1.226	-0.001	
50	211	1.227	1.226	-0.001	
50	212	1.227	1.226	-0.001	
50	213	1.227	1.226	-0.001	
50	214	1.226	1.225	-0.001	
55	215	1.227	1.227	0.000	
55	216	1.227	1.225	-0.002	
55	217	1.227	1.225	-0.002	
55	218	1.227	1.226	-0.001	
55	220	1.226	1.224	-0.002	
100	247	1.227	1.224	-0.003	
100	249	1.227	1.225	-0.002	
100	250	1.227	1.223	-0.004	
100	251	1.227	1.223	-0.004	
100	253	1.226	1.224	-0.002	
100	255	1.226	1.223	-0.003	
100	256	1.227	1.224	-0.003	
100	257	1.227	1.224	-0.003	
100	258	1.226	1.223	-0.003	
100	259	1.227	1.224	-0.003	
100	260	1.227	1.225	-0.002	
100	261	1.227	1.225	-0.002	
100	262	1.227	1.225	-0.002	
100	263	1.226	1.222	-0.004	
100	264	1.227	1.224	-0.003	
100	265	1.226	1.223	-0.003	
100	266	1.227	1.224	-0.003	
100	267	1.227	1.223	-0.004	
100	268	1.226	1.224	-0.002	
100	269	1.227	1.225	-0.002	
100	270	1.226	1.224	-0.002	
100	271	1.227	1.224	-0.003	
100	273	1.227	1.224	-0.003	
105	274	1.227	1.224	-0.003	
105	275	1.226	1.223	-0.003	
105	276	1.226	1.224	-0.002	
105	277	1.227	1.226	-0.001	
105	278	1.227	1.224	-0.003	
	Max	1.227	1.227	0.000	
	Average	1.227	1.225	-0.002	
	Min	1.225	1.222	-0.004	
	Std Dev	0.001	0.001	0.001	

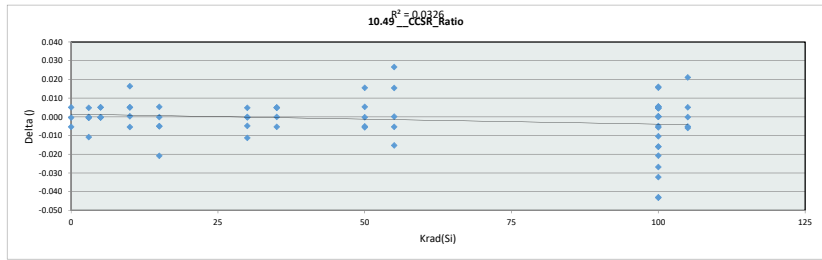


5.111_V_REFCAP											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	1.237	1.213						
			V	1.237	1.213						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.224	1.226	1.226	1.225	1.225	1.224	1.225	1.224	1.222	1.223
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.226	1.225	1.224	1.224
Max	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.227	1.225	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

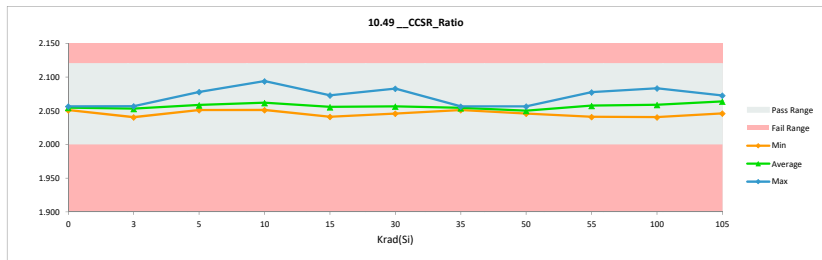


HDR TID Report
TPS7H5002-SP

		10.49_CCSR_Ratio		
Test Site				
Tester				
Test Number				
Unit				
Max Limit		2.12	2.12	
Min Limit		2	2	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	2.062	2.056	-0.005
0	190	2.051	2.051	0.000
0	191	2.051	2.057	0.005
3	300	2.051	2.041	-0.011
3	305	2.056	2.056	0.000
3	309	2.051	2.056	0.005
3	311	2.056	2.056	0.000
3	313	2.057	2.057	0.000
5	319	2.056	2.056	0.000
5	321	2.051	2.056	0.005
5	324	2.051	2.051	0.000
5	327	2.051	2.051	0.000
5	331	2.073	2.078	0.005
10	234	2.051	2.056	0.005
10	235	2.051	2.051	0.000
10	236	2.051	2.056	0.005
10	237	2.077	2.094	0.017
10	238	2.056	2.051	-0.005
15	239	2.056	2.062	0.005
15	240	2.057	2.057	0.000
15	241	2.062	2.041	-0.021
15	243	2.078	2.073	-0.005
15	245	2.051	2.046	-0.005
30	222	2.061	2.057	-0.005
30	223	2.083	2.083	0.000
30	225	2.046	2.046	0.000
30	226	2.046	2.051	0.005
30	227	2.057	2.046	-0.011
35	228	2.056	2.056	0.000
35	229	2.046	2.051	0.005
35	231	2.057	2.051	-0.005
35	232	2.051	2.056	0.005
35	233	2.051	2.056	0.005
50	210	2.051	2.051	0.000
50	211	2.051	2.046	-0.005
50	212	2.046	2.051	0.006
50	213	2.051	2.046	-0.005
50	214	2.041	2.057	0.016
55	215	2.056	2.041	-0.015
55	216	2.056	2.057	0.000
55	217	2.040	2.067	0.027
55	218	2.062	2.078	0.016
55	220	2.051	2.046	-0.005
100	247	2.083	2.051	-0.032
100	249	2.056	2.051	-0.005
100	250	2.078	2.083	0.006
100	251	2.084	2.041	-0.043
100	253	2.067	2.067	0.000
100	255	2.057	2.061	0.005
100	256	2.089	2.046	-0.043
100	257	2.072	2.073	0.000
100	258	2.067	2.051	-0.016
100	259	2.067	2.067	0.000
100	260	2.046	2.046	0.000
100	261	2.061	2.062	0.000
100	262	2.067	2.062	-0.005
100	263	2.051	2.056	0.005
100	264	2.046	2.052	0.006
100	265	2.078	2.062	-0.016
100	266	2.067	2.046	-0.021
100	267	2.041	2.057	0.016
100	268	2.067	2.057	-0.010
100	269	2.057	2.062	0.005
100	270	2.083	2.057	-0.027
100	271	2.067	2.083	0.016
100	273	2.056	2.062	0.006
105	274	2.073	2.067	-0.006
105	275	2.046	2.046	0.000
105	276	2.067	2.062	-0.005
105	277	2.067	2.072	0.005
105	278	2.051	2.073	0.021
	Max	2.089	2.094	0.027
	Average	2.059	2.057	-0.001
	Min	2.040	2.041	-0.043
	Std Dev	0.011	0.011	0.012

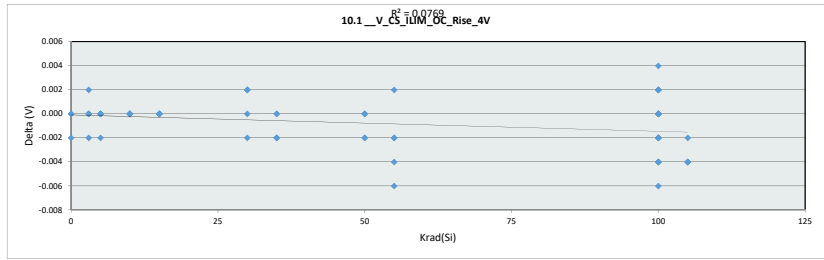


		10.49_CCSR_Rat										
Test Site												
Tester												
Test Number												
Max Limit		2.12										
Min Limit		2										
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000
Min		2.051	2.041	2.051	2.051	2.041	2.046	2.051	2.046	2.041	2.041	2.046
Average		2.055	2.053	2.059	2.062	2.056	2.057	2.054	2.050	2.058	2.059	2.064
Max		2.057	2.057	2.078	2.094	2.073	2.083	2.056	2.057	2.078	2.083	2.073
UL		2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120

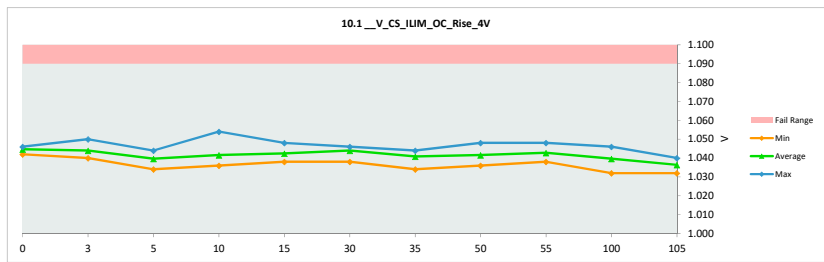


HDR TID Report
TPS7H5002-SP

10.1_V_CS_ILIM_OC_Rise_4V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.046	1.046	0.000
0	190	1.046	1.046	0.000
0	191	1.044	1.042	-0.002
3	300	1.040	1.040	0.000
3	305	1.050	1.050	0.000
3	309	1.044	1.044	0.000
3	311	1.048	1.046	-0.002
3	313	1.038	1.040	0.002
5	319	1.044	1.044	0.000
5	321	1.044	1.044	0.000
5	324	1.034	1.034	0.000
5	327	1.040	1.038	-0.002
5	331	1.038	1.038	0.000
10	234	1.036	1.036	0.000
10	235	1.040	1.040	0.000
10	236	1.054	1.054	0.000
10	237	1.040	1.040	0.000
10	238	1.038	1.038	0.000
15	239	1.038	1.038	0.000
15	240	1.046	1.046	0.000
15	241	1.042	1.042	0.000
15	243	1.038	1.038	0.000
15	245	1.048	1.048	0.000
30	222	1.044	1.046	0.002
30	223	1.044	1.044	0.000
30	225	1.044	1.046	0.002
30	226	1.044	1.046	0.002
30	227	1.040	1.038	-0.002
35	228	1.044	1.042	-0.002
35	229	1.036	1.034	-0.002
35	231	1.042	1.042	0.000
35	232	1.044	1.044	0.000
35	233	1.044	1.042	-0.002
50	210	1.036	1.036	0.000
50	211	1.044	1.042	-0.002
50	212	1.040	1.040	0.000
50	213	1.044	1.042	-0.002
50	214	1.048	1.048	0.000
55	215	1.038	1.040	0.002
55	216	1.044	1.038	-0.006
55	217	1.048	1.044	-0.004
55	218	1.050	1.048	-0.002
55	220	1.046	1.044	-0.002
100	247	1.046	1.046	0.000
100	249	1.036	1.032	-0.004
100	250	1.042	1.040	-0.002
100	251	1.046	1.042	-0.004
100	253	1.042	1.040	-0.002
100	255	1.036	1.036	0.000
100	256	1.042	1.044	0.002
100	257	1.040	1.042	0.002
100	258	1.038	1.034	-0.004
100	259	1.044	1.042	-0.002
100	260	1.040	1.038	-0.002
100	261	1.038	1.038	0.000
100	262	1.036	1.036	0.000
100	263	1.042	1.042	0.000
100	264	1.042	1.046	0.004
100	265	1.038	1.036	-0.002
100	266	1.042	1.038	-0.004
100	267	1.036	1.036	0.000
100	268	1.038	1.038	0.000
100	269	1.042	1.044	0.002
100	270	1.036	1.036	0.000
100	271	1.042	1.044	0.002
100	273	1.048	1.042	-0.006
105	274	1.038	1.034	-0.004
105	275	1.042	1.038	-0.004
105	276	1.044	1.040	-0.004
105	277	1.034	1.032	-0.002
105	278	1.042	1.038	-0.004
	Max	1.054	1.054	0.004
	Average	1.042	1.041	-0.001
	Min	1.034	1.032	-0.006
	Std Dev	0.004	0.004	0.002

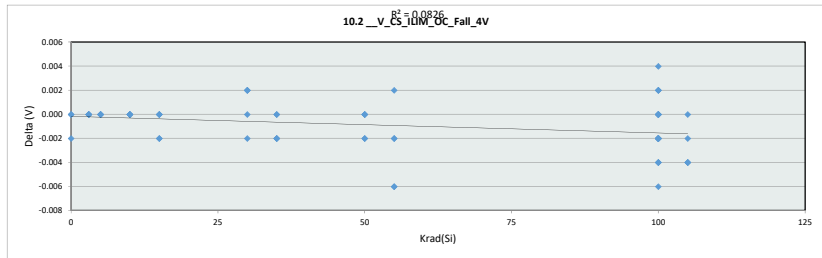


10.1_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
LL											
Min	1.042	1.040	1.024	1.036	1.038	1.038	1.034	1.036	1.038	1.032	1.032
Average	1.045	1.044	1.040	1.042	1.042	1.044	1.041	1.042	1.043	1.040	1.036
Max	1.046	1.050	1.044	1.054	1.048	1.046	1.044	1.048	1.048	1.046	1.040
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

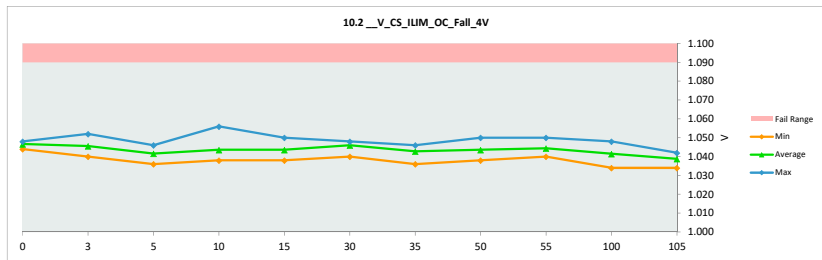


HDR TID Report
TPS7H5002-SP

10.2_V_CS_ILIM_OC_Fall_4V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.048	1.048	0.000
0	190	1.048	1.048	0.000
0	191	1.046	1.044	-0.002
3	300	1.042	1.042	0.000
3	305	1.052	1.052	0.000
3	309	1.046	1.046	0.000
3	311	1.048	1.048	0.000
3	313	1.040	1.040	0.000
5	319	1.046	1.046	0.000
5	321	1.046	1.046	0.000
5	324	1.036	1.036	0.000
5	327	1.040	1.040	0.000
5	331	1.040	1.040	0.000
10	234	1.038	1.038	0.000
10	235	1.042	1.042	0.000
10	236	1.056	1.056	0.000
10	237	1.042	1.042	0.000
10	238	1.040	1.040	0.000
15	239	1.040	1.038	-0.002
15	240	1.048	1.048	0.000
15	241	1.044	1.042	-0.002
15	243	1.040	1.040	0.000
15	245	1.050	1.050	0.000
30	222	1.046	1.048	0.002
30	223	1.046	1.046	0.000
30	225	1.046	1.048	0.002
30	226	1.046	1.048	0.002
30	227	1.042	1.040	-0.002
35	228	1.046	1.044	-0.002
35	229	1.038	1.036	-0.002
35	231	1.044	1.044	0.000
35	232	1.046	1.046	0.000
35	233	1.046	1.044	-0.002
50	210	1.038	1.038	0.000
50	211	1.046	1.044	-0.002
50	212	1.042	1.042	0.000
50	213	1.046	1.044	-0.002
50	214	1.050	1.050	0.000
55	215	1.040	1.042	0.002
55	216	1.046	1.040	-0.006
55	217	1.050	1.044	-0.006
55	218	1.052	1.050	-0.002
55	220	1.048	1.046	-0.002
100	247	1.048	1.048	0.000
100	249	1.038	1.034	-0.004
100	250	1.044	1.042	-0.002
100	251	1.048	1.044	-0.004
100	253	1.044	1.042	-0.002
100	255	1.038	1.038	0.000
100	256	1.044	1.044	0.000
100	257	1.042	1.044	0.002
100	258	1.040	1.036	-0.004
100	259	1.046	1.044	-0.002
100	260	1.042	1.040	-0.002
100	261	1.040	1.040	0.000
100	262	1.038	1.038	0.000
100	263	1.046	1.044	-0.002
100	264	1.044	1.048	0.004
100	265	1.040	1.038	-0.002
100	266	1.044	1.042	-0.002
100	267	1.038	1.036	-0.002
100	268	1.040	1.040	0.000
100	269	1.044	1.046	0.002
100	270	1.038	1.036	-0.002
100	271	1.044	1.046	0.002
100	273	1.050	1.044	-0.006
105	274	1.040	1.036	-0.004
105	275	1.044	1.042	-0.002
105	276	1.046	1.042	-0.004
105	277	1.034	1.034	0.000
105	278	1.044	1.040	-0.004
	Max	1.056	1.056	0.004
	Average	1.044	1.043	-0.001
	Min	1.034	1.034	-0.006
	Std Dev	0.004	0.005	0.002

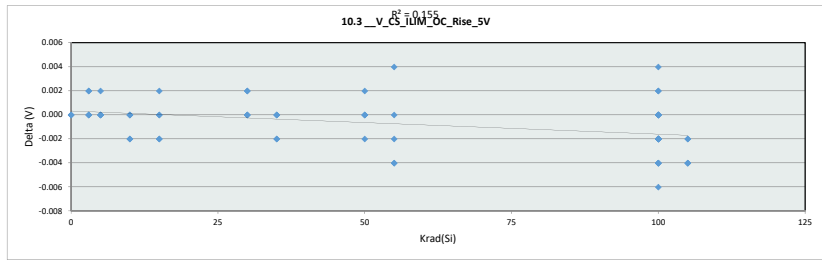


10.2_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.044	1.040	1.036	1.038	1.038	1.040	1.036	1.038	1.040	1.034	1.034
Min	1.047	1.046	1.042	1.044	1.044	1.046	1.043	1.044	1.044	1.041	1.039
Average	1.048	1.052	1.046	1.056	1.050	1.048	1.046	1.050	1.050	1.048	1.042
Max	1.048	1.052	1.046	1.056	1.050	1.048	1.046	1.050	1.050	1.048	1.042
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

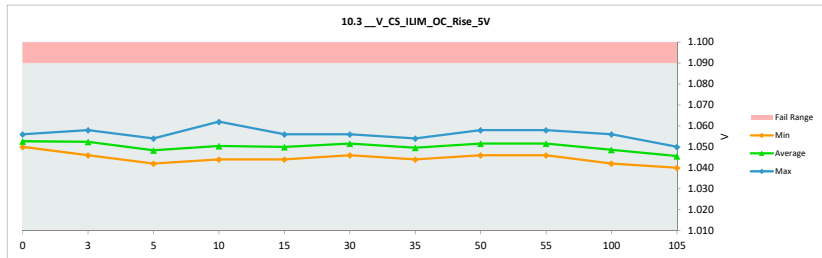


HDR TID Report TPS7H5002-SP

10.3_V_CS_ILIM_OC_Rise_SV				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.056	1.056	0.000
0	190	1.052	1.052	0.000
0	191	1.050	1.050	0.000
3	300	1.046	1.048	0.002
3	305	1.058	1.058	0.000
3	309	1.054	1.054	0.000
3	311	1.056	1.056	0.000
3	313	1.044	1.046	0.002
5	319	1.052	1.052	0.000
5	321	1.054	1.054	0.000
5	324	1.040	1.042	0.002
5	327	1.048	1.048	0.000
5	331	1.046	1.046	0.000
10	234	1.044	1.044	0.000
10	235	1.050	1.048	-0.002
10	236	1.052	1.062	0.000
10	237	1.050	1.050	0.000
10	238	1.050	1.048	-0.002
15	239	1.046	1.044	-0.002
15	240	1.054	1.056	0.002
15	241	1.050	1.048	-0.002
15	243	1.048	1.048	0.000
15	245	1.054	1.054	0.000
30	222	1.054	1.056	0.002
30	223	1.052	1.052	0.000
30	225	1.050	1.052	0.002
30	226	1.052	1.052	0.000
30	227	1.046	1.046	0.000
35	228	1.052	1.050	-0.002
35	229	1.044	1.044	0.000
35	231	1.050	1.050	0.000
35	232	1.054	1.054	0.000
35	233	1.052	1.050	-0.002
50	210	1.046	1.046	0.000
50	211	1.054	1.054	0.000
50	212	1.048	1.050	0.002
50	213	1.052	1.050	-0.002
50	214	1.058	1.058	0.000
55	215	1.046	1.050	0.004
55	216	1.050	1.046	-0.004
55	217	1.056	1.052	-0.004
55	218	1.058	1.058	0.000
55	220	1.054	1.052	-0.002
100	247	1.056	1.056	0.000
100	249	1.048	1.044	-0.004
100	250	1.050	1.048	-0.002
100	251	1.054	1.052	-0.002
100	253	1.048	1.048	0.000
100	255	1.046	1.046	0.000
100	256	1.052	1.050	-0.002
100	257	1.050	1.050	0.000
100	258	1.048	1.042	-0.006
100	259	1.052	1.050	-0.002
100	260	1.048	1.048	0.000
100	261	1.048	1.046	-0.002
100	262	1.044	1.044	0.000
100	263	1.052	1.050	-0.002
100	264	1.052	1.056	0.004
100	265	1.046	1.044	-0.002
100	266	1.054	1.050	-0.004
100	267	1.046	1.042	-0.004
100	268	1.048	1.048	0.000
100	269	1.052	1.054	0.002
100	270	1.046	1.044	-0.002
100	271	1.052	1.054	0.002
100	273	1.056	1.052	-0.004
105	274	1.044	1.042	-0.002
105	275	1.052	1.048	-0.004
105	276	1.054	1.050	-0.004
105	277	1.042	1.040	-0.002
105	278	1.052	1.048	-0.004
	Max	1.062	1.062	0.004
	Average	1.050	1.050	-0.001
	Min	1.040	1.040	-0.006
	Std Dev	0.004	0.005	0.002

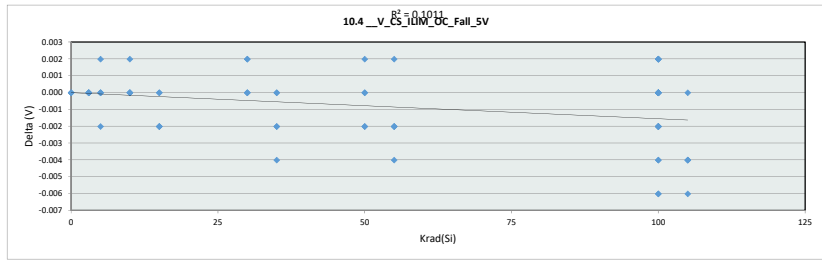


10.3_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.050	1.046	1.042	1.044	1.044	1.046	1.044	1.046	1.046	1.042	1.040
Min	1.053	1.052	1.048	1.050	1.050	1.052	1.050	1.052	1.052	1.049	1.046
Average	1.056	1.058	1.054	1.062	1.056	1.056	1.054	1.058	1.058	1.056	1.050
Max	1.056	1.058	1.054	1.062	1.056	1.056	1.054	1.058	1.058	1.056	1.050
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

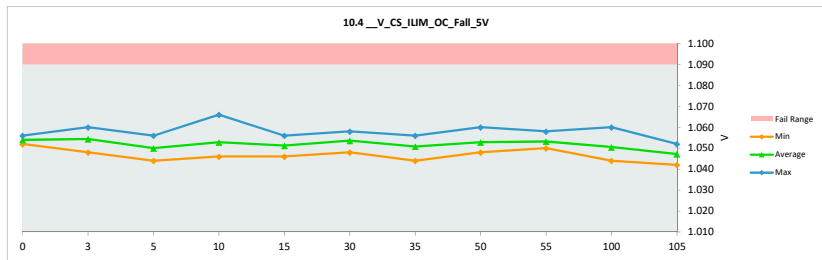


HDR TID Report
TPS7H5002-SP

10.4_V_CS_ILIM_OC_Fall_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.056	1.056	0.000
0	190	1.054	1.054	0.000
0	191	1.052	1.052	0.000
3	300	1.050	1.050	0.000
3	305	1.060	1.060	0.000
3	309	1.056	1.056	0.000
3	311	1.058	1.058	0.000
3	313	1.048	1.048	0.000
5	319	1.054	1.054	0.000
5	321	1.056	1.056	0.000
5	324	1.042	1.044	0.002
5	327	1.050	1.048	-0.002
5	331	1.048	1.048	0.000
10	234	1.046	1.046	0.000
10	235	1.050	1.050	0.000
10	236	1.064	1.066	0.002
10	237	1.052	1.052	0.000
10	238	1.050	1.050	0.000
15	239	1.048	1.046	-0.002
15	240	1.056	1.056	0.000
15	241	1.052	1.050	-0.002
15	243	1.050	1.048	-0.002
15	245	1.056	1.056	0.000
30	222	1.056	1.058	0.002
30	223	1.054	1.054	0.000
30	225	1.052	1.054	0.002
30	226	1.054	1.054	0.000
30	227	1.048	1.048	0.000
35	228	1.054	1.052	-0.002
35	229	1.046	1.044	-0.002
35	231	1.052	1.052	0.000
35	232	1.056	1.056	0.000
35	233	1.054	1.050	-0.004
50	210	1.048	1.048	0.000
50	211	1.056	1.054	-0.002
50	212	1.050	1.050	0.000
50	213	1.054	1.052	-0.002
50	214	1.058	1.060	0.002
55	215	1.048	1.050	0.002
55	216	1.052	1.050	-0.002
55	217	1.058	1.054	-0.004
55	218	1.060	1.058	-0.002
55	220	1.056	1.054	-0.002
100	247	1.060	1.060	0.000
100	249	1.048	1.044	-0.004
100	250	1.052	1.050	-0.002
100	251	1.056	1.054	-0.002
100	253	1.050	1.050	0.000
100	255	1.048	1.046	-0.002
100	256	1.054	1.054	0.000
100	257	1.050	1.052	0.002
100	258	1.050	1.044	-0.006
100	259	1.054	1.054	0.000
100	260	1.050	1.050	0.000
100	261	1.050	1.048	-0.002
100	262	1.046	1.046	0.000
100	263	1.052	1.052	0.000
100	264	1.054	1.056	0.002
100	265	1.048	1.046	-0.002
100	266	1.054	1.052	-0.002
100	267	1.048	1.044	-0.004
100	268	1.050	1.050	0.000
100	269	1.054	1.056	0.002
100	270	1.046	1.046	0.000
100	271	1.054	1.056	0.002
100	273	1.058	1.052	-0.006
105	274	1.048	1.044	-0.004
105	275	1.054	1.050	-0.004
105	276	1.056	1.052	-0.004
105	277	1.042	1.042	0.000
105	278	1.054	1.048	-0.006
Max		1.064	1.066	0.002
Average		1.052	1.051	-0.001
Min		1.042	1.042	-0.006
Std Dev		0.004	0.005	0.002

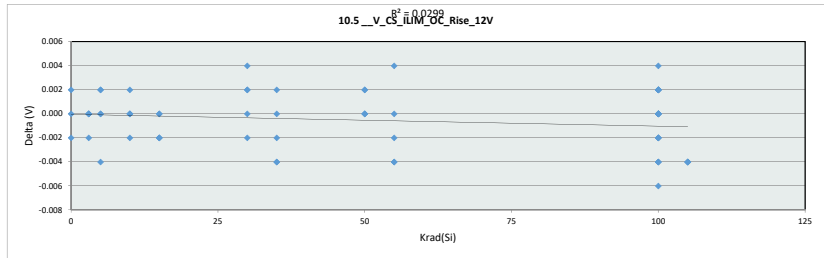


10.4_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.052	1.048	1.044	1.046	1.046	1.048	1.044	1.048	1.050	1.044	1.042
Average	1.054	1.054	1.050	1.053	1.051	1.054	1.051	1.053	1.053	1.051	1.047
Max	1.056	1.060	1.056	1.066	1.056	1.058	1.056	1.060	1.058	1.060	1.052
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

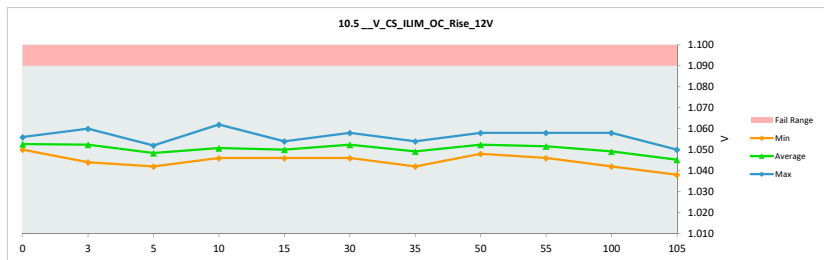


HDR TID Report
TPS7H5002-SP

10.5_V_CS_ILIM_OC_Rise_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.056	1.056	0.000
0	190	1.054	1.052	-0.002
0	191	1.048	1.050	0.002
3	300	1.048	1.048	0.000
3	305	1.060	1.060	0.000
3	309	1.054	1.054	0.000
3	311	1.056	1.056	0.000
3	313	1.046	1.044	-0.002
5	319	1.050	1.052	0.002
5	321	1.056	1.052	-0.004
5	324	1.042	1.042	0.000
5	327	1.048	1.048	0.000
5	331	1.046	1.048	0.002
10	234	1.044	1.046	0.002
10	235	1.048	1.048	0.000
10	236	1.052	1.062	0.000
10	237	1.050	1.050	0.000
10	238	1.050	1.048	-0.002
15	239	1.046	1.046	0.000
15	240	1.056	1.054	-0.002
15	241	1.050	1.048	-0.002
15	243	1.050	1.048	-0.002
15	245	1.054	1.054	0.000
30	222	1.054	1.058	0.004
30	223	1.052	1.052	0.000
30	225	1.050	1.052	0.002
30	226	1.052	1.054	0.002
30	227	1.048	1.046	-0.002
35	228	1.054	1.050	-0.004
35	229	1.044	1.042	-0.002
35	231	1.050	1.052	0.002
35	232	1.054	1.054	0.000
35	233	1.052	1.048	-0.004
50	210	1.046	1.048	0.002
50	211	1.054	1.054	0.000
50	212	1.048	1.050	0.002
50	213	1.052	1.052	0.000
50	214	1.058	1.058	0.000
55	215	1.046	1.050	0.004
55	216	1.050	1.052	-0.004
55	217	1.056	1.052	-0.004
55	218	1.058	1.058	0.000
55	220	1.054	1.052	-0.002
100	247	1.058	1.058	0.000
100	249	1.048	1.044	-0.004
100	250	1.050	1.048	-0.002
100	251	1.052	1.052	0.000
100	253	1.048	1.048	0.000
100	255	1.046	1.046	0.000
100	256	1.050	1.052	0.002
100	257	1.050	1.052	0.002
100	258	1.048	1.042	-0.006
100	259	1.052	1.052	0.000
100	260	1.048	1.048	0.000
100	261	1.046	1.048	0.002
100	262	1.044	1.046	0.002
100	263	1.052	1.050	-0.002
100	264	1.052	1.056	0.004
100	265	1.046	1.044	-0.002
100	266	1.054	1.050	-0.004
100	267	1.046	1.044	-0.002
100	268	1.046	1.048	0.002
100	269	1.052	1.052	0.000
100	270	1.044	1.044	0.000
100	271	1.052	1.054	0.002
100	273	1.056	1.052	-0.004
105	274	1.046	1.042	-0.004
105	275	1.052	1.048	-0.004
105	276	1.054	1.050	-0.004
105	277	1.042	1.038	-0.004
105	278	1.052	1.048	-0.004
	Max	1.062	1.062	0.004
	Average	1.051	1.050	-0.001
	Min	1.042	1.038	-0.006
	Std Dev	0.004	0.005	0.002

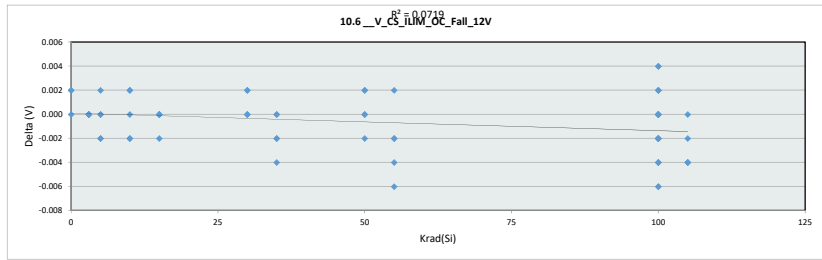


10.5_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.050	1.044	1.042	1.046	1.046	1.046	1.042	1.048	1.046	1.042	1.038
Min	1.053	1.052	1.048	1.051	1.050	1.052	1.049	1.052	1.052	1.049	1.045
Average	1.053	1.052	1.048	1.051	1.050	1.052	1.049	1.052	1.052	1.049	1.045
Max	1.056	1.060	1.052	1.062	1.054	1.058	1.054	1.058	1.058	1.058	1.050
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

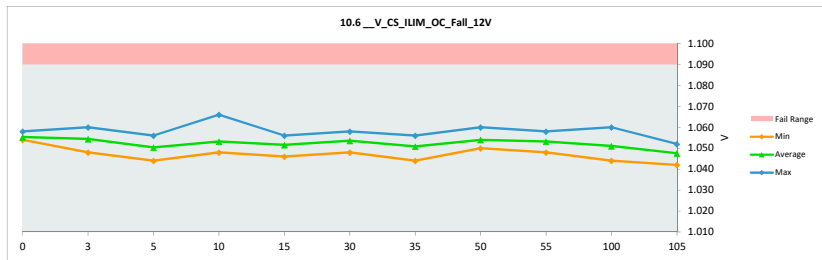


HDR TID Report TPS7H5002-SP

10.6_V_CS_ILIM_OC_Fall_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.056	1.058	0.002
0	190	1.054	1.054	0.000
0	191	1.052	1.054	0.002
3	300	1.050	1.050	0.000
3	305	1.060	1.060	0.000
3	309	1.056	1.056	0.000
3	311	1.058	1.058	0.000
3	313	1.048	1.048	0.000
5	319	1.054	1.054	0.000
5	321	1.058	1.056	-0.002
5	324	1.044	1.044	0.000
5	327	1.050	1.048	-0.002
5	331	1.048	1.050	0.002
10	234	1.046	1.048	0.002
10	235	1.052	1.050	-0.002
10	236	1.064	1.066	0.002
10	237	1.052	1.052	0.000
10	238	1.052	1.050	-0.002
15	239	1.048	1.046	-0.002
15	240	1.056	1.056	0.000
15	241	1.050	1.050	0.000
15	243	1.050	1.050	0.000
15	245	1.056	1.056	0.000
30	222	1.056	1.058	0.002
30	223	1.054	1.054	0.000
30	225	1.052	1.054	0.002
30	226	1.054	1.054	0.000
30	227	1.048	1.048	0.000
35	228	1.054	1.052	-0.002
35	229	1.046	1.044	-0.002
35	231	1.052	1.052	0.000
35	232	1.056	1.056	0.000
35	233	1.054	1.050	-0.004
50	210	1.048	1.050	0.002
50	211	1.056	1.054	-0.002
50	212	1.050	1.052	0.002
50	213	1.054	1.054	0.000
50	214	1.060	1.060	0.000
55	215	1.050	1.052	0.002
55	216	1.054	1.048	-0.006
55	217	1.058	1.054	-0.004
55	218	1.060	1.058	-0.002
55	220	1.056	1.054	-0.002
100	247	1.060	1.060	0.000
100	249	1.050	1.044	-0.006
100	250	1.052	1.050	-0.002
100	251	1.056	1.054	-0.002
100	253	1.050	1.050	0.000
100	255	1.048	1.048	0.000
100	256	1.054	1.054	0.000
100	257	1.050	1.054	0.004
100	258	1.050	1.044	-0.006
100	259	1.056	1.054	-0.002
100	260	1.050	1.050	0.000
100	261	1.050	1.050	0.000
100	262	1.046	1.048	0.002
100	263	1.054	1.052	-0.002
100	264	1.054	1.058	0.004
100	265	1.048	1.046	-0.002
100	266	1.056	1.052	-0.004
100	267	1.048	1.044	-0.004
100	268	1.050	1.050	0.000
100	269	1.054	1.056	0.002
100	270	1.048	1.046	-0.002
100	271	1.054	1.056	0.002
100	273	1.058	1.054	-0.004
105	274	1.048	1.044	-0.004
105	275	1.054	1.050	-0.004
105	276	1.056	1.052	-0.004
105	277	1.042	1.042	0.000
105	278	1.052	1.050	-0.002
	Max	1.064	1.066	0.004
	Average	1.053	1.052	-0.001
	Min	1.042	1.042	-0.006
	Std Dev	0.004	0.005	0.002

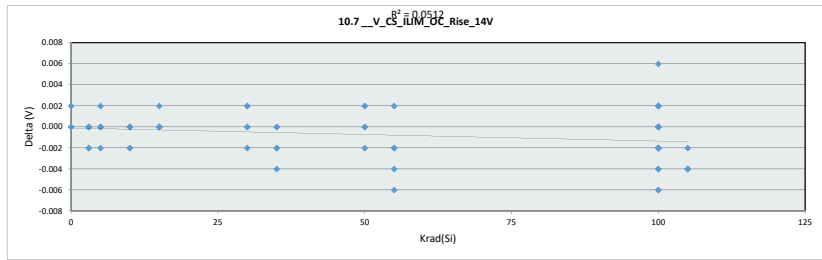


10.6_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	1.054	1.048	1.044	1.048	1.046	1.048	1.044	1.050	1.048	1.044	1.042
Average	1.055	1.054	1.050	1.053	1.052	1.054	1.051	1.054	1.053	1.051	1.048
Max	1.058	1.060	1.056	1.066	1.056	1.058	1.056	1.060	1.058	1.060	1.052
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

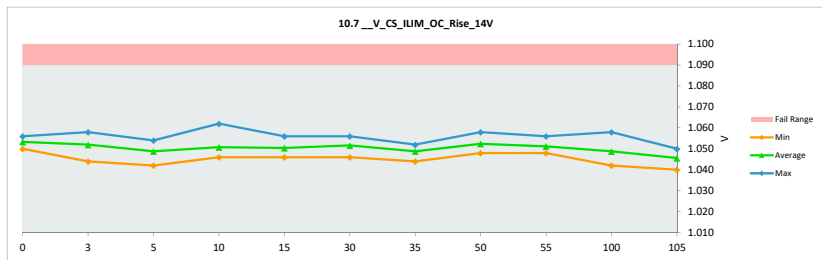


HDR TID Report TPS7H5002-SP

10.7_V_CS_ILIM_OC_Rise_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.056	1.056	0.000
0	190	1.052	1.054	0.002
0	191	1.050	1.050	0.000
3	300	1.048	1.048	0.000
3	305	1.060	1.058	-0.002
3	309	1.054	1.054	0.000
3	311	1.056	1.056	0.000
3	313	1.046	1.044	-0.002
5	319	1.052	1.052	0.000
5	321	1.054	1.054	0.000
5	324	1.042	1.042	0.000
5	327	1.050	1.048	-0.002
5	331	1.046	1.048	0.002
10	234	1.046	1.046	0.000
10	235	1.048	1.048	0.000
10	236	1.054	1.062	-0.002
10	237	1.050	1.050	0.000
10	238	1.050	1.048	-0.002
15	239	1.046	1.046	0.000
15	240	1.054	1.056	0.002
15	241	1.048	1.048	0.000
15	243	1.048	1.048	0.000
15	245	1.054	1.054	0.000
30	222	1.054	1.056	0.002
30	223	1.052	1.052	0.000
30	225	1.050	1.052	0.002
30	226	1.052	1.052	0.000
30	227	1.048	1.046	-0.002
35	228	1.052	1.050	-0.002
35	229	1.044	1.044	0.000
35	231	1.050	1.050	0.000
35	232	1.054	1.052	-0.002
35	233	1.052	1.048	-0.004
50	210	1.046	1.048	0.002
50	211	1.054	1.054	0.000
50	212	1.048	1.050	0.002
50	213	1.054	1.052	-0.002
50	214	1.058	1.058	0.000
55	215	1.048	1.048	0.000
55	216	1.052	1.048	-0.004
55	217	1.058	1.052	-0.006
55	218	1.058	1.056	-0.002
55	220	1.054	1.052	-0.002
100	247	1.056	1.058	0.002
100	249	1.048	1.042	-0.006
100	250	1.050	1.048	-0.002
100	251	1.054	1.050	-0.004
100	253	1.048	1.048	0.000
100	255	1.046	1.046	0.000
100	256	1.052	1.052	0.000
100	257	1.050	1.052	0.002
100	258	1.048	1.042	-0.006
100	259	1.054	1.052	-0.002
100	260	1.048	1.048	0.000
100	261	1.046	1.046	0.000
100	262	1.044	1.046	0.002
100	263	1.050	1.048	-0.002
100	264	1.050	1.056	0.006
100	265	1.046	1.044	-0.002
100	266	1.052	1.050	-0.002
100	267	1.044	1.042	-0.002
100	268	1.048	1.048	0.000
100	269	1.052	1.054	0.002
100	270	1.046	1.044	-0.002
100	271	1.052	1.054	0.002
100	273	1.056	1.052	-0.004
105	274	1.046	1.042	-0.004
105	275	1.052	1.048	-0.004
105	276	1.054	1.050	-0.004
105	277	1.042	1.040	-0.002
105	278	1.052	1.048	-0.004
Max		1.064	1.062	0.006
Average		1.051	1.050	-0.001
Min		1.042	1.040	-0.006
Std Dev		0.004	0.005	0.002

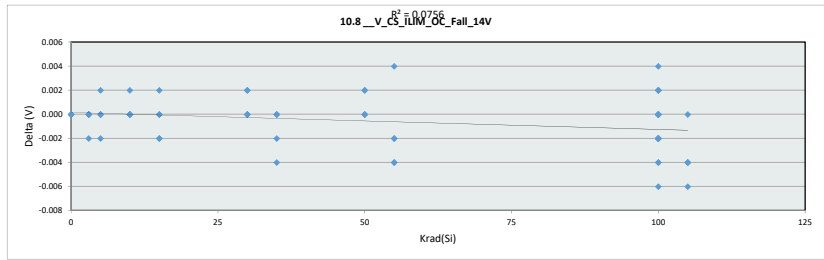


10.7_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.050	1.044	1.042	1.046	1.046	1.046	1.044	1.048	1.048	1.042	1.040
Average	1.053	1.052	1.049	1.051	1.050	1.052	1.049	1.052	1.051	1.049	1.046
Max	1.056	1.058	1.054	1.062	1.056	1.056	1.052	1.058	1.056	1.058	1.050
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

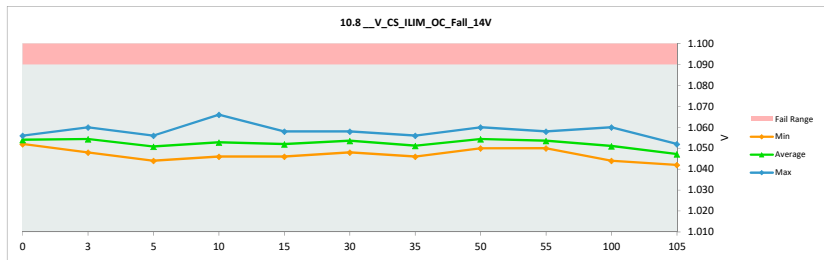


HDR TID Report
TPS7H5002-SP

10.8_V_CS_ILIM_OC_Fall_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	1.056	1.056	0.000
0	190	1.054	1.054	0.000
0	191	1.052	1.052	0.000
3	300	1.050	1.050	0.000
3	305	1.062	1.060	-0.002
3	309	1.056	1.056	0.000
3	311	1.058	1.058	0.000
3	313	1.048	1.048	0.000
5	319	1.054	1.054	0.000
5	321	1.058	1.056	-0.002
5	324	1.044	1.044	0.000
5	327	1.050	1.050	0.000
5	331	1.048	1.050	0.002
10	234	1.046	1.046	0.000
10	235	1.050	1.050	0.000
10	236	1.064	1.066	0.002
10	237	1.052	1.052	0.000
10	238	1.050	1.050	0.000
15	239	1.048	1.046	-0.002
15	240	1.056	1.058	0.002
15	241	1.052	1.050	-0.002
15	243	1.050	1.050	0.000
15	245	1.056	1.056	0.000
30	222	1.056	1.058	0.002
30	223	1.054	1.054	0.000
30	225	1.052	1.054	0.002
30	226	1.054	1.054	0.000
30	227	1.048	1.048	0.000
35	228	1.054	1.052	-0.002
35	229	1.046	1.046	0.000
35	231	1.052	1.052	0.000
35	232	1.056	1.056	0.000
35	233	1.054	1.050	-0.004
50	210	1.048	1.050	0.002
50	211	1.056	1.056	0.000
50	212	1.050	1.052	0.002
50	213	1.054	1.054	0.000
50	214	1.060	1.060	0.000
55	215	1.048	1.052	0.004
55	216	1.054	1.050	-0.004
55	217	1.058	1.054	-0.004
55	218	1.060	1.058	-0.002
55	220	1.056	1.054	-0.002
100	247	1.060	1.060	0.000
100	249	1.048	1.044	-0.004
100	250	1.052	1.050	-0.002
100	251	1.056	1.054	-0.002
100	253	1.052	1.050	-0.002
100	255	1.048	1.048	0.000
100	256	1.054	1.054	0.000
100	257	1.050	1.052	0.002
100	258	1.050	1.044	-0.006
100	259	1.054	1.054	0.000
100	260	1.050	1.050	0.000
100	261	1.050	1.050	0.000
100	262	1.046	1.048	0.002
100	263	1.054	1.052	-0.002
100	264	1.054	1.058	0.004
100	265	1.048	1.046	-0.002
100	266	1.054	1.052	-0.002
100	267	1.048	1.046	-0.002
100	268	1.050	1.050	0.000
100	269	1.054	1.056	0.002
100	270	1.048	1.046	-0.002
100	271	1.054	1.056	0.002
100	273	1.058	1.054	-0.004
105	274	1.048	1.044	-0.004
105	275	1.054	1.050	-0.004
105	276	1.056	1.052	-0.004
105	277	1.042	1.042	0.000
105	278	1.054	1.048	-0.006
Max		1.064	1.066	0.004
Average		1.053	1.052	-0.001
Min		1.042	1.042	-0.006
Std Dev		0.004	0.005	0.002

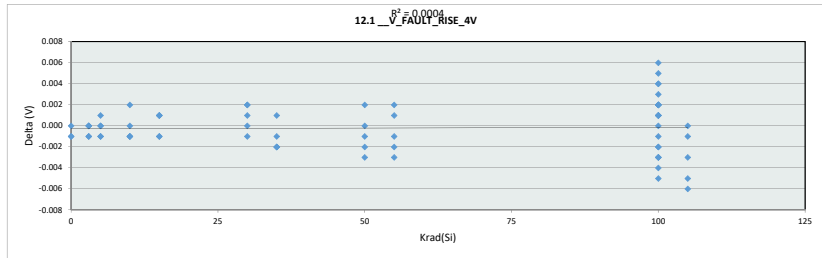


10.8_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.052	1.048	1.044	1.046	1.046	1.048	1.046	1.050	1.050	1.044	1.042
Average	1.054	1.054	1.051	1.053	1.052	1.054	1.051	1.054	1.054	1.051	1.047
Max	1.056	1.060	1.056	1.066	1.058	1.058	1.056	1.060	1.058	1.060	1.052
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

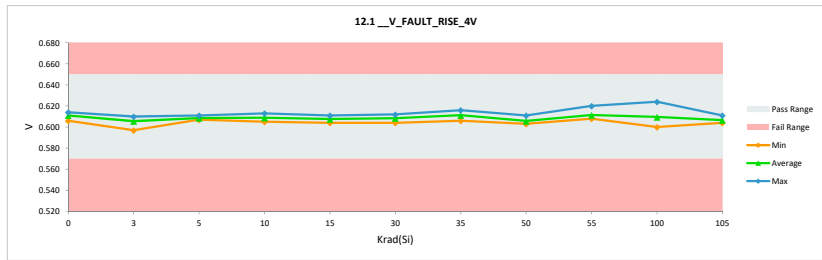


HDR TID Report
TPS7H5002-SP

12.1_V_FAULT_RISE_4V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	0.65	0.65		
Min Limit	0.57	0.57		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.615	0.614	-0.001
0	190	0.614	0.613	-0.001
0	191	0.606	0.606	0.000
3	300	0.608	0.607	-0.001
3	305	0.597	0.597	0.000
3	309	0.608	0.607	-0.001
3	311	0.610	0.610	0.000
3	313	0.607	0.607	0.000
5	319	0.610	0.609	-0.001
5	321	0.610	0.611	0.001
5	324	0.608	0.608	0.000
5	327	0.608	0.608	0.000
5	331	0.608	0.607	-0.001
10	234	0.611	0.611	0.000
10	235	0.614	0.613	-0.001
10	236	0.608	0.607	-0.001
10	237	0.606	0.608	0.002
10	238	0.606	0.605	-0.001
15	239	0.603	0.604	0.001
15	240	0.607	0.608	0.001
15	241	0.609	0.610	0.001
15	243	0.612	0.611	-0.001
15	245	0.606	0.605	-0.001
30	222	0.608	0.610	0.002
30	223	0.607	0.608	0.001
30	225	0.610	0.612	0.002
30	226	0.609	0.608	-0.001
30	227	0.604	0.604	0.000
35	228	0.608	0.606	-0.002
35	229	0.616	0.615	-0.001
35	231	0.608	0.606	-0.002
35	232	0.615	0.616	0.001
35	233	0.615	0.613	-0.002
50	210	0.606	0.603	-0.003
50	211	0.604	0.606	0.002
50	212	0.605	0.603	-0.002
50	213	0.611	0.611	0.000
50	214	0.607	0.606	-0.001
55	215	0.618	0.620	0.002
55	216	0.609	0.608	-0.001
55	217	0.611	0.609	-0.002
55	218	0.611	0.612	0.001
55	220	0.611	0.608	-0.003
100	247	0.613	0.609	-0.004
100	249	0.604	0.606	0.002
100	250	0.612	0.612	0.000
100	251	0.605	0.611	0.006
100	253	0.609	0.611	0.002
100	255	0.601	0.602	0.001
100	256	0.607	0.604	-0.003
100	257	0.611	0.613	0.002
100	258	0.608	0.610	0.002
100	259	0.607	0.609	0.002
100	260	0.612	0.609	-0.003
100	261	0.606	0.607	0.001
100	262	0.615	0.616	0.001
100	263	0.613	0.611	-0.002
100	264	0.602	0.600	-0.002
100	265	0.609	0.606	-0.003
100	266	0.607	0.606	-0.001
100	267	0.612	0.607	-0.005
100	268	0.610	0.614	0.004
100	269	0.621	0.624	0.003
100	270	0.610	0.614	0.004
100	271	0.610	0.611	0.001
100	273	0.603	0.608	0.005
105	274	0.611	0.605	-0.006
105	275	0.607	0.607	0.000
105	276	0.616	0.611	-0.005
105	277	0.607	0.606	-0.001
105	278	0.607	0.604	-0.003
	Max	0.621	0.624	0.006
	Average	0.609	0.609	0.000
	Min	0.597	0.597	-0.006
	Std Dev	0.004	0.004	0.002

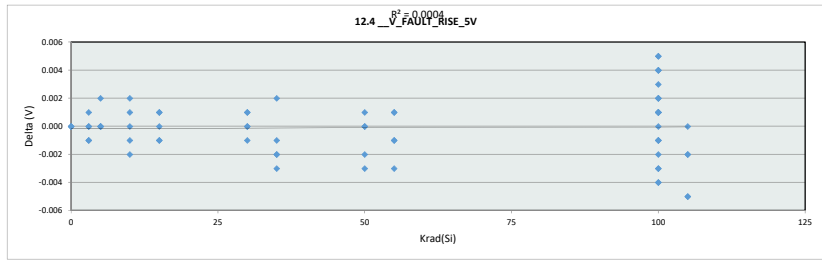


12.1_V_FAULT_RISE_4V											
Test Site											
Tester											
Test Number											
Max Limit	0.65	V									
Min Limit	0.57	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.606	0.597	0.607	0.605	0.604	0.604	0.606	0.603	0.608	0.600	0.604
Average	0.611	0.606	0.609	0.609	0.608	0.608	0.611	0.606	0.611	0.610	0.607
Max	0.614	0.610	0.611	0.613	0.611	0.612	0.616	0.611	0.620	0.624	0.611
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

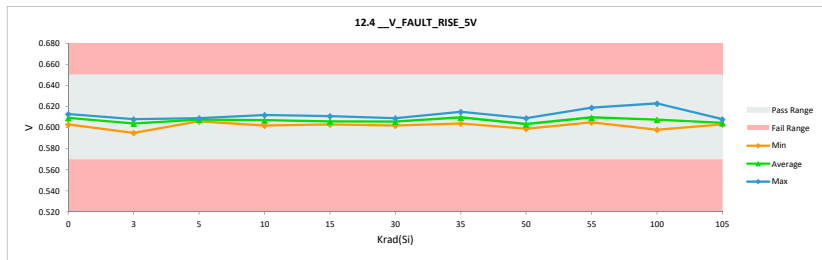


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12.4 __V_FAULT_RISE_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	0.65	0.65		
Min Limit	0.57	0.57		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.613	0.613	0.000
0	190	0.612	0.612	0.000
0	191	0.603	0.603	0.000
3	300	0.606	0.605	-0.001
3	305	0.595	0.595	0.000
3	309	0.606	0.605	-0.001
3	311	0.608	0.608	0.000
3	313	0.606	0.607	0.001
5	319	0.608	0.608	0.000
5	321	0.607	0.609	0.002
5	324	0.608	0.608	0.000
5	327	0.607	0.607	0.000
5	331	0.606	0.606	0.000
10	234	0.610	0.609	-0.001
10	235	0.612	0.612	0.000
10	236	0.606	0.607	0.001
10	237	0.604	0.606	0.002
10	238	0.604	0.602	-0.002
15	239	0.602	0.603	0.001
15	240	0.605	0.605	0.000
15	241	0.607	0.608	0.001
15	243	0.612	0.611	-0.001
15	245	0.604	0.603	-0.001
30	222	0.607	0.608	0.001
30	223	0.606	0.606	0.000
30	225	0.608	0.609	0.001
30	226	0.605	0.604	-0.001
30	227	0.602	0.602	0.000
35	228	0.607	0.605	-0.002
35	229	0.615	0.613	-0.002
35	231	0.607	0.604	-0.003
35	232	0.613	0.615	0.002
35	233	0.613	0.612	-0.001
50	210	0.604	0.601	-0.003
50	211	0.602	0.603	0.001
50	212	0.601	0.599	-0.002
50	213	0.609	0.609	0.000
50	214	0.605	0.605	0.000
55	215	0.618	0.619	0.001
55	216	0.608	0.607	-0.001
55	217	0.608	0.607	-0.001
55	218	0.610	0.611	0.001
55	220	0.608	0.605	-0.003
55	247	0.611	0.607	-0.004
100	249	0.601	0.603	0.002
100	250	0.610	0.611	0.001
100	251	0.603	0.608	0.005
100	253	0.607	0.610	0.003
100	255	0.596	0.598	0.002
100	256	0.606	0.603	-0.003
100	257	0.610	0.612	0.002
100	258	0.607	0.608	0.001
100	259	0.605	0.606	0.001
100	260	0.611	0.608	-0.003
100	261	0.604	0.605	0.001
100	262	0.613	0.614	0.001
100	263	0.610	0.609	-0.001
100	264	0.600	0.599	-0.001
100	265	0.607	0.605	-0.002
100	266	0.604	0.603	-0.001
100	267	0.611	0.607	-0.004
100	268	0.606	0.610	0.004
100	269	0.619	0.623	0.004
100	270	0.608	0.612	0.004
100	271	0.608	0.608	0.000
100	273	0.602	0.607	0.005
105	274	0.609	0.604	-0.005
105	275	0.604	0.604	0.000
105	276	0.613	0.608	-0.005
105	277	0.606	0.604	-0.002
105	278	0.605	0.603	-0.002
Max		0.619	0.623	0.005
Average		0.607	0.607	0.000
Min		0.595	0.595	-0.005
Std Dev		0.004	0.005	0.002

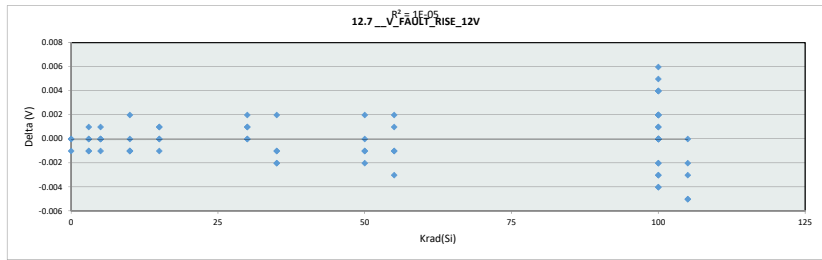


12.4 __V_FAULT_RISE_5V											
Test Site											
Tester											
Test Number											
Max Limit	0.65	V									
Min Limit	0.57	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.603	0.595	0.606	0.602	0.603	0.602	0.604	0.599	0.605	0.598	0.603
Average	0.609	0.604	0.608	0.607	0.606	0.606	0.610	0.603	0.610	0.608	0.605
Max	0.613	0.608	0.609	0.612	0.611	0.609	0.615	0.609	0.619	0.623	0.608
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

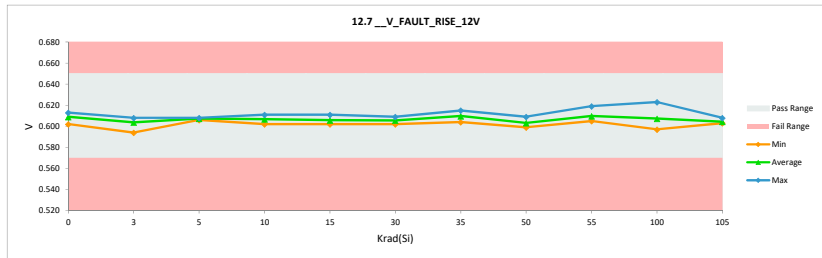


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12.7 __V_FAULT_RISE_12V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			0.65	0.65
Min Limit			0.57	0.57
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.613	0.613	0.000
0	190	0.612	0.612	0.000
0	191	0.603	0.602	-0.001
3	300	0.606	0.605	-0.001
3	305	0.595	0.594	-0.001
3	309	0.605	0.605	0.000
3	311	0.608	0.608	0.000
3	313	0.606	0.607	0.001
5	319	0.609	0.608	-0.001
5	321	0.607	0.608	0.001
5	324	0.608	0.608	0.000
5	327	0.607	0.607	0.000
5	331	0.606	0.606	0.000
10	234	0.610	0.609	-0.001
10	235	0.612	0.611	-0.001
10	236	0.606	0.606	0.000
10	237	0.604	0.606	0.002
10	238	0.603	0.602	-0.001
15	239	0.601	0.602	0.001
15	240	0.605	0.605	0.000
15	241	0.607	0.608	0.001
15	243	0.611	0.611	0.000
15	245	0.604	0.603	-0.001
30	222	0.607	0.607	0.000
30	223	0.605	0.606	0.001
30	225	0.607	0.609	0.002
30	226	0.604	0.604	0.000
30	227	0.601	0.602	0.001
35	228	0.607	0.605	-0.002
35	229	0.614	0.613	-0.001
35	231	0.606	0.604	-0.002
35	232	0.613	0.615	0.002
35	233	0.613	0.612	-0.001
50	210	0.603	0.601	-0.002
50	211	0.601	0.603	0.002
50	212	0.600	0.599	-0.001
50	213	0.609	0.609	0.000
50	214	0.605	0.604	-0.001
55	215	0.617	0.619	0.002
55	216	0.608	0.607	-0.001
55	217	0.608	0.607	-0.001
55	218	0.610	0.611	0.001
55	220	0.608	0.605	-0.003
100	247	0.610	0.606	-0.004
100	249	0.601	0.603	0.002
100	250	0.610	0.610	0.000
100	251	0.603	0.608	0.005
100	253	0.607	0.609	0.002
100	255	0.596	0.597	-0.001
100	256	0.606	0.604	-0.002
100	257	0.610	0.612	0.002
100	258	0.606	0.607	0.001
100	259	0.604	0.606	0.002
100	260	0.611	0.608	-0.003
100	261	0.604	0.604	0.000
100	262	0.613	0.613	0.000
100	263	0.609	0.609	0.000
100	264	0.600	0.598	-0.002
100	265	0.607	0.604	-0.003
100	266	0.603	0.603	0.000
100	267	0.611	0.607	-0.004
100	268	0.606	0.610	0.004
100	269	0.619	0.623	0.004
100	270	0.608	0.612	0.004
100	271	0.608	0.608	0.000
100	273	0.601	0.607	0.006
105	274	0.609	0.604	-0.005
105	275	0.604	0.604	0.000
105	276	0.613	0.608	-0.005
105	277	0.606	0.603	-0.003
105	278	0.605	0.603	-0.002
Max		0.619	0.623	0.006
Average		0.607	0.607	0.000
Min		0.595	0.594	-0.005
Std Dev		0.004	0.005	0.002

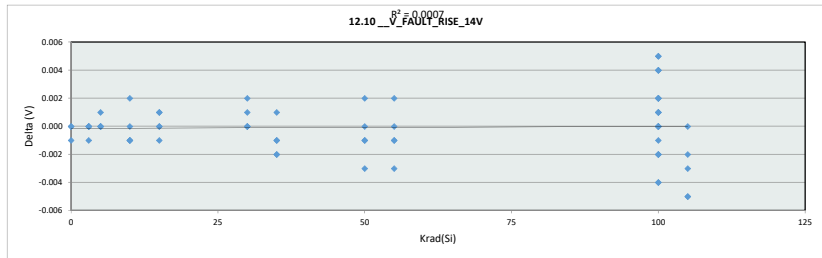


12.7 __V_FAULT_RISE_12V											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit			0.65	0.65							
Min Limit			0.57	0.57							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.602	0.594	0.606	0.602	0.602	0.602	0.604	0.599	0.605	0.597	0.603
Average	0.609	0.604	0.607	0.607	0.606	0.606	0.610	0.603	0.610	0.607	0.604
Max	0.613	0.608	0.608	0.611	0.611	0.609	0.615	0.609	0.619	0.623	0.608
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

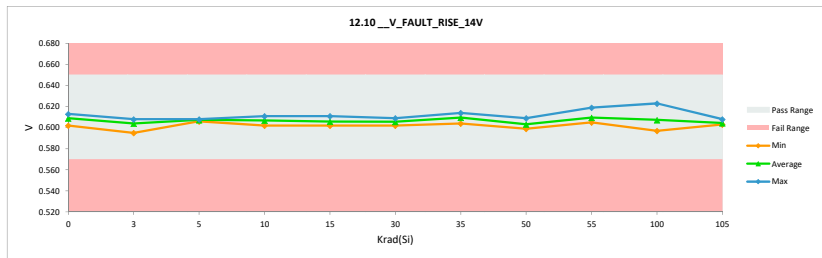


HDR TID Report TPS7H5002-SP

12.10 __V_FAULT_RISE_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	0.65	0.65		
Min Limit	0.57	0.57		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.613	0.613	0.000
0	190	0.612	0.612	0.000
0	191	0.603	0.602	-0.001
3	300	0.606	0.605	-0.001
3	305	0.595	0.595	0.000
3	309	0.605	0.605	0.000
3	311	0.608	0.608	0.000
3	313	0.607	0.607	0.000
5	319	0.608	0.608	0.000
5	321	0.607	0.608	0.001
5	324	0.608	0.608	0.000
5	327	0.607	0.607	0.000
5	331	0.606	0.606	0.000
10	234	0.610	0.609	-0.001
10	235	0.612	0.611	-0.001
10	236	0.606	0.606	0.000
10	237	0.604	0.606	0.002
10	238	0.603	0.602	-0.001
15	239	0.601	0.602	0.001
15	240	0.605	0.605	0.000
15	241	0.607	0.608	0.001
15	243	0.611	0.611	0.000
15	245	0.604	0.603	-0.001
30	222	0.607	0.607	0.000
30	223	0.606	0.606	0.000
30	225	0.607	0.609	0.002
30	226	0.604	0.604	0.000
30	227	0.601	0.602	0.001
35	228	0.607	0.605	-0.002
35	229	0.614	0.613	-0.001
35	231	0.606	0.604	-0.002
35	232	0.613	0.614	0.001
35	233	0.613	0.612	-0.001
50	210	0.604	0.601	-0.003
50	211	0.601	0.603	0.002
50	212	0.600	0.599	-0.001
50	213	0.609	0.609	0.000
50	214	0.605	0.604	-0.001
55	215	0.617	0.619	0.002
55	216	0.608	0.607	-0.001
55	217	0.608	0.607	-0.001
55	218	0.610	0.610	0.000
55	220	0.608	0.605	-0.003
55	247	0.610	0.606	-0.004
100	249	0.601	0.603	0.002
100	250	0.610	0.611	0.001
100	251	0.603	0.608	0.005
100	253	0.607	0.609	0.002
100	255	0.596	0.597	-0.001
100	256	0.606	0.604	-0.002
100	257	0.610	0.612	0.002
100	258	0.606	0.607	0.001
100	259	0.604	0.606	0.002
100	260	0.610	0.608	-0.002
100	261	0.604	0.604	0.000
100	262	0.613	0.613	0.000
100	263	0.609	0.609	0.000
100	264	0.600	0.599	-0.001
100	265	0.607	0.605	-0.002
100	266	0.603	0.603	0.000
100	267	0.611	0.607	-0.004
100	268	0.606	0.610	0.004
100	269	0.619	0.623	0.004
100	270	0.608	0.612	0.004
100	271	0.608	0.608	0.000
100	273	0.601	0.606	0.005
105	274	0.609	0.604	-0.005
105	275	0.604	0.604	0.000
105	276	0.613	0.608	-0.005
105	277	0.606	0.603	-0.003
105	278	0.605	0.603	-0.002
Max		0.619	0.623	0.005
Average		0.607	0.607	0.000
Min		0.595	0.595	-0.005
Std Dev		0.004	0.005	0.002

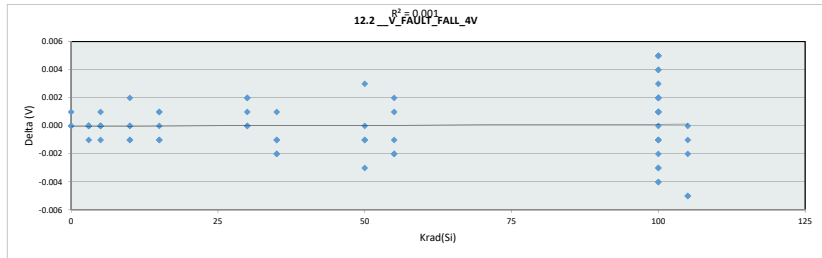


12.10 __V_FAULT											
Test Site											
Tester											
Test Number											
Max Limit	0.65	V									
Min Limit	0.57	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.602	0.595	0.606	0.602	0.602	0.602	0.604	0.599	0.605	0.597	0.603
Average	0.609	0.604	0.607	0.607	0.606	0.606	0.610	0.603	0.610	0.607	0.604
Max	0.613	0.608	0.608	0.611	0.611	0.609	0.614	0.609	0.619	0.623	0.608
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

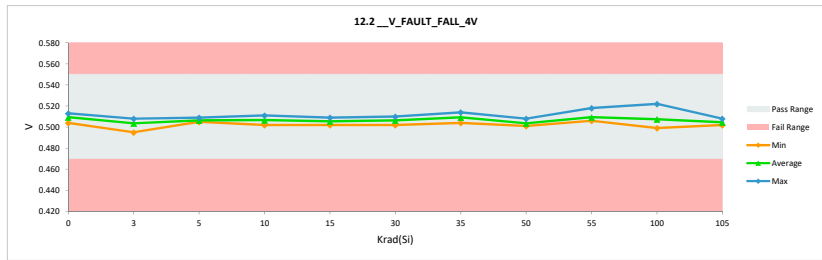


HDR TID Report
TPS7H5002-SP

12.2_V_FAULT_FALL_4V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	0.55	0.55		
Min Limit	0.47	0.47		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.512	0.513	0.001
0	190	0.511	0.511	0.000
0	191	0.504	0.504	0.000
3	300	0.505	0.505	0.000
3	305	0.495	0.495	0.000
3	309	0.506	0.505	-0.001
3	311	0.508	0.508	0.000
3	313	0.505	0.505	0.000
5	319	0.507	0.507	0.000
5	321	0.508	0.509	0.001
5	324	0.506	0.505	-0.001
5	327	0.506	0.506	0.000
5	331	0.505	0.505	0.000
10	234	0.509	0.509	0.000
10	235	0.511	0.511	0.000
10	236	0.506	0.505	-0.001
10	237	0.504	0.506	0.002
10	238	0.503	0.502	-0.001
15	239	0.501	0.502	0.001
15	240	0.505	0.505	0.000
15	241	0.507	0.508	0.001
15	243	0.510	0.509	-0.001
15	245	0.504	0.503	-0.001
30	222	0.506	0.508	0.002
30	223	0.505	0.506	0.001
30	225	0.508	0.510	0.002
30	226	0.506	0.506	0.000
30	227	0.502	0.502	0.000
35	228	0.506	0.504	-0.002
35	229	0.514	0.513	-0.001
35	231	0.506	0.504	-0.002
35	232	0.513	0.514	0.001
35	233	0.512	0.511	-0.001
50	210	0.504	0.501	-0.003
50	211	0.501	0.504	0.003
50	212	0.502	0.501	-0.001
50	213	0.508	0.508	0.000
50	214	0.505	0.504	-0.001
55	215	0.516	0.518	0.002
55	216	0.507	0.506	-0.001
55	217	0.509	0.507	-0.002
55	218	0.509	0.510	0.001
55	220	0.508	0.506	-0.002
100	247	0.511	0.507	-0.004
100	249	0.502	0.504	0.002
100	250	0.510	0.510	0.000
100	251	0.503	0.508	0.005
100	253	0.507	0.509	0.002
100	255	0.498	0.499	0.001
100	256	0.505	0.502	-0.003
100	257	0.508	0.511	0.003
100	258	0.506	0.507	0.001
100	259	0.505	0.507	0.002
100	260	0.510	0.507	-0.003
100	261	0.504	0.505	0.001
100	262	0.512	0.514	0.002
100	263	0.510	0.509	-0.001
100	264	0.500	0.499	-0.001
100	265	0.506	0.504	-0.002
100	266	0.505	0.504	-0.001
100	267	0.510	0.506	-0.004
100	268	0.507	0.512	0.005
100	269	0.518	0.522	0.004
100	270	0.507	0.511	0.004
100	271	0.508	0.509	0.001
100	273	0.501	0.506	0.005
105	274	0.509	0.504	-0.005
105	275	0.505	0.505	0.000
105	276	0.513	0.508	-0.005
105	277	0.505	0.504	-0.001
105	278	0.504	0.502	-0.002
Max		0.518	0.522	0.005
Average		0.507	0.507	0.000
Min		0.495	0.495	-0.005
Std Dev		0.004	0.004	0.002

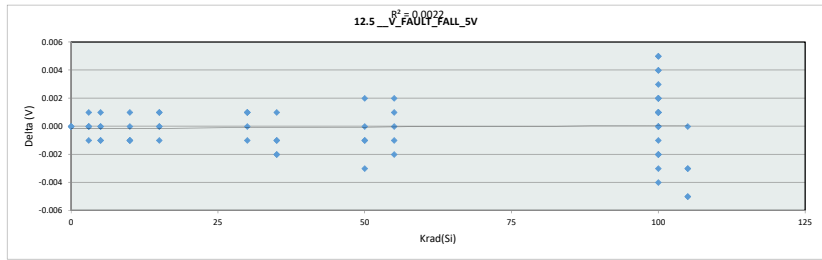


12.2_V_FAULT_FALL_4V											
Test Site											
Tester											
Test Number											
Max Limit	0.55	V									
Min Limit	0.47	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.504	0.495	0.505	0.502	0.502	0.502	0.504	0.501	0.506	0.499	0.502
Average	0.509	0.504	0.506	0.507	0.505	0.506	0.509	0.504	0.509	0.507	0.505
Max	0.513	0.508	0.509	0.511	0.509	0.510	0.514	0.508	0.518	0.522	0.508
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

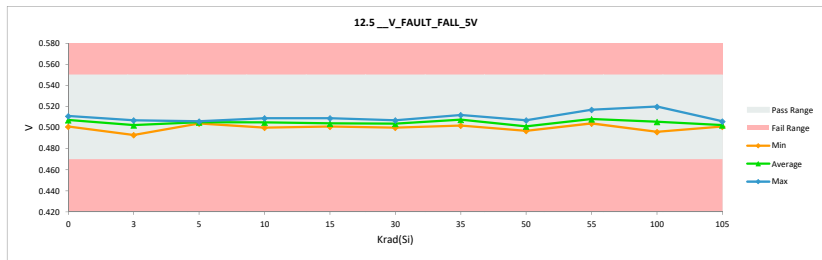


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12.5_V_FAULT_FALL_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	0.55	0.55		
Min Limit	0.47	0.47		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.511	0.511	0.000
0	190	0.510	0.510	0.000
0	191	0.501	0.501	0.000
3	300	0.504	0.504	0.000
3	305	0.493	0.493	0.000
3	309	0.504	0.503	-0.001
3	311	0.507	0.507	0.000
3	313	0.504	0.505	0.001
5	319	0.507	0.506	-0.001
5	321	0.505	0.506	0.001
5	324	0.506	0.505	-0.001
5	327	0.505	0.505	0.000
5	331	0.504	0.504	0.000
10	234	0.509	0.508	-0.001
10	235	0.510	0.509	-0.001
10	236	0.504	0.504	0.000
10	237	0.503	0.504	0.001
10	238	0.501	0.500	-0.001
15	239	0.500	0.501	0.001
15	240	0.502	0.503	0.001
15	241	0.506	0.506	0.000
15	243	0.509	0.509	0.000
15	245	0.502	0.501	-0.001
30	222	0.505	0.506	0.001
30	223	0.503	0.504	0.001
30	225	0.506	0.507	0.001
30	226	0.503	0.502	-0.001
30	227	0.500	0.500	0.000
35	228	0.505	0.503	-0.002
35	229	0.512	0.511	-0.001
35	231	0.504	0.502	-0.002
35	232	0.511	0.512	0.001
35	233	0.511	0.510	-0.001
50	210	0.502	0.499	-0.003
50	211	0.499	0.501	0.002
50	212	0.498	0.497	-0.001
50	213	0.507	0.507	0.000
50	214	0.503	0.502	-0.001
55	215	0.515	0.517	0.002
55	216	0.506	0.505	-0.001
55	217	0.506	0.506	0.000
55	218	0.508	0.509	0.001
55	220	0.506	0.504	-0.002
100	247	0.508	0.505	-0.003
100	249	0.499	0.501	0.002
100	250	0.508	0.509	0.001
100	251	0.501	0.506	0.005
100	253	0.505	0.507	0.002
100	255	0.495	0.496	0.001
100	256	0.504	0.502	-0.002
100	257	0.508	0.510	0.002
100	258	0.504	0.505	0.001
100	259	0.503	0.505	0.002
100	260	0.509	0.507	-0.002
100	261	0.502	0.503	0.001
100	262	0.510	0.511	0.001
100	263	0.507	0.507	0.000
100	264	0.498	0.497	-0.001
100	265	0.505	0.503	-0.002
100	266	0.501	0.501	0.000
100	267	0.509	0.505	-0.004
100	268	0.504	0.508	0.004
100	269	0.517	0.520	0.003
100	270	0.506	0.510	0.004
100	271	0.506	0.506	0.000
100	273	0.500	0.505	0.005
105	274	0.507	0.502	-0.005
105	275	0.502	0.502	0.000
105	276	0.511	0.506	-0.005
105	277	0.504	0.501	-0.003
105	278	0.504	0.501	-0.003
	Max	0.517	0.520	0.005
	Average	0.505	0.505	0.000
	Min	0.493	0.493	-0.005
	Std Dev	0.004	0.004	0.002

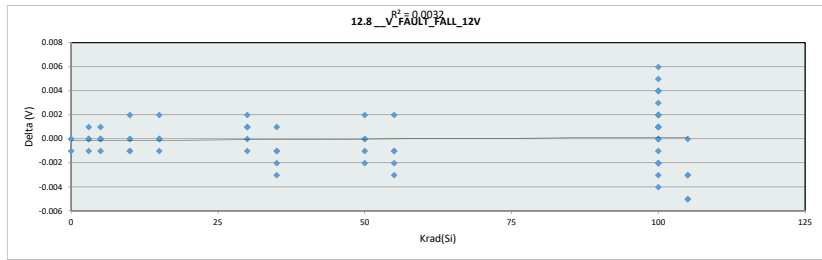


12.5_V_FAULT_FALL_5V											
Test Site											
Tester											
Test Number											
Max Limit	0.55	V									
Min Limit	0.47	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.501	0.493	0.504	0.500	0.501	0.500	0.502	0.497	0.504	0.496	0.501
Average	0.507	0.502	0.505	0.505	0.504	0.504	0.508	0.501	0.508	0.506	0.502
Max	0.511	0.507	0.506	0.509	0.509	0.509	0.507	0.512	0.507	0.517	0.506
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

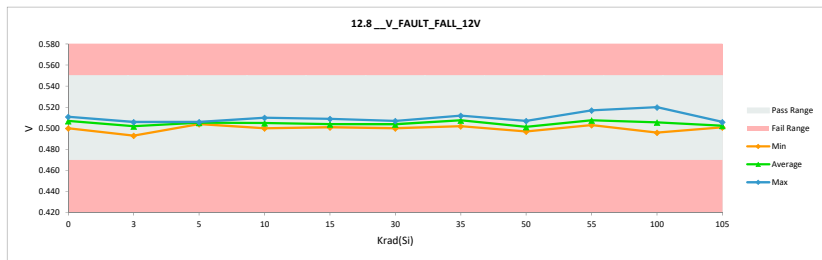


HDR TID Report
TPS7H5002-SP

12.8_V_FAULT_FALL_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	0.55	0.55		
Min Limit	0.47	0.47		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.511	0.511	0.000
0	190	0.510	0.510	0.000
0	191	0.501	0.500	-0.001
3	300	0.504	0.503	-0.001
3	305	0.492	0.493	0.001
3	309	0.503	0.503	0.000
3	311	0.506	0.506	0.000
3	313	0.505	0.505	0.000
5	319	0.507	0.506	-0.001
5	321	0.505	0.506	0.001
5	324	0.506	0.506	0.000
5	327	0.505	0.505	0.000
5	331	0.504	0.504	0.000
10	234	0.508	0.507	-0.001
10	235	0.510	0.510	0.000
10	236	0.504	0.504	0.000
10	237	0.502	0.504	0.002
10	238	0.501	0.500	-0.001
15	239	0.499	0.501	0.002
15	240	0.503	0.503	0.000
15	241	0.506	0.506	0.000
15	243	0.509	0.509	0.000
15	245	0.502	0.501	-0.001
30	222	0.505	0.506	0.001
30	223	0.503	0.504	0.001
30	225	0.505	0.507	0.002
30	226	0.503	0.502	-0.001
30	227	0.500	0.500	0.000
35	228	0.505	0.503	-0.002
35	229	0.512	0.511	-0.001
35	231	0.505	0.502	-0.003
35	232	0.511	0.512	0.001
35	233	0.511	0.510	-0.001
50	210	0.501	0.499	-0.002
50	211	0.499	0.501	0.002
50	212	0.498	0.497	-0.001
50	213	0.507	0.507	0.000
50	214	0.503	0.503	0.000
55	215	0.515	0.517	0.002
55	216	0.506	0.505	-0.001
55	217	0.507	0.505	-0.002
55	218	0.509	0.508	-0.001
55	220	0.506	0.503	-0.003
100	247	0.508	0.504	-0.004
100	249	0.499	0.501	0.002
100	250	0.508	0.509	0.001
100	251	0.501	0.506	0.005
100	253	0.505	0.508	0.003
100	255	0.494	0.496	0.002
100	256	0.504	0.502	-0.002
100	257	0.508	0.510	0.002
100	258	0.504	0.505	0.001
100	259	0.503	0.505	0.002
100	260	0.509	0.507	-0.002
100	261	0.502	0.503	0.001
100	262	0.510	0.511	0.001
100	263	0.507	0.507	0.000
100	264	0.498	0.497	-0.001
100	265	0.505	0.503	-0.002
100	266	0.501	0.501	0.000
100	267	0.508	0.505	-0.003
100	268	0.504	0.508	0.004
100	269	0.516	0.520	0.004
100	270	0.506	0.510	0.004
100	271	0.506	0.506	0.000
100	273	0.499	0.505	0.006
105	274	0.507	0.502	-0.005
105	275	0.502	0.502	0.000
105	276	0.511	0.506	-0.005
105	277	0.504	0.501	-0.003
105	278	0.504	0.501	-0.003
Max		0.516	0.520	0.006
Average		0.505	0.505	0.000
Min		0.492	0.493	-0.005
Std Dev		0.004	0.004	0.002

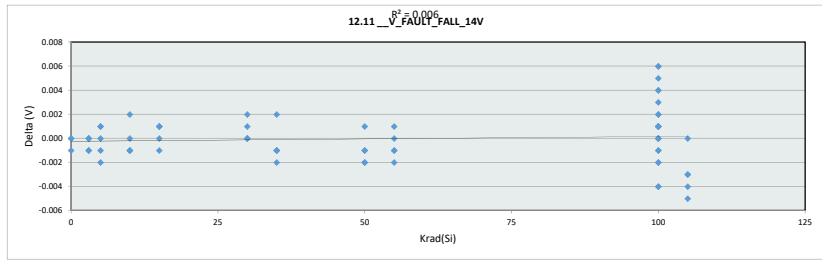


12.8_V_FAULT_FALL_12V											
Test Site											
Tester											
Test Number											
Max Limit	0.55	V									
Min Limit	0.47	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.500	0.493	0.504	0.500	0.501	0.500	0.502	0.497	0.503	0.496	0.501
Average	0.507	0.502	0.505	0.505	0.504	0.504	0.508	0.501	0.508	0.506	0.502
Max	0.511	0.506	0.506	0.510	0.509	0.507	0.512	0.507	0.517	0.520	0.506
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

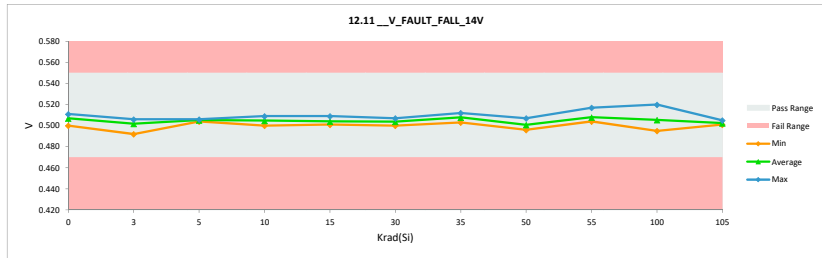


HDR TID Report
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12.11 __V_FAULT_FALL_14V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			0.55	0.55
Min Limit			0.47	0.47
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.511	0.511	0.000
0	190	0.510	0.510	0.000
0	191	0.501	0.500	-0.001
3	300	0.504	0.503	-0.001
3	305	0.492	0.492	0.000
3	309	0.504	0.503	-0.001
3	311	0.506	0.506	0.000
3	313	0.505	0.505	0.000
5	319	0.507	0.506	-0.001
5	321	0.505	0.506	0.001
5	324	0.507	0.505	-0.002
5	327	0.505	0.505	0.000
5	331	0.503	0.504	0.001
10	234	0.508	0.507	-0.001
10	235	0.510	0.509	-0.001
10	236	0.504	0.504	0.000
10	237	0.502	0.504	0.002
10	238	0.501	0.500	-0.001
15	239	0.500	0.501	0.001
15	240	0.502	0.503	0.001
15	241	0.505	0.506	0.001
15	243	0.509	0.509	0.000
15	245	0.502	0.501	-0.001
30	222	0.505	0.506	0.001
30	223	0.504	0.504	0.000
30	225	0.505	0.507	0.002
30	226	0.502	0.502	0.000
30	227	0.500	0.500	0.000
35	228	0.505	0.503	-0.002
35	229	0.512	0.511	-0.001
35	231	0.504	0.503	-0.001
35	232	0.510	0.512	0.002
35	233	0.511	0.510	-0.001
50	210	0.501	0.499	-0.002
50	211	0.499	0.500	0.001
50	212	0.498	0.496	-0.002
50	213	0.508	0.507	-0.001
50	214	0.503	0.502	-0.001
55	215	0.516	0.517	0.001
55	216	0.506	0.505	-0.001
55	217	0.506	0.505	-0.001
55	218	0.509	0.509	0.000
55	220	0.506	0.504	-0.002
100	247	0.508	0.504	-0.004
100	249	0.499	0.501	0.002
100	250	0.508	0.509	0.001
100	251	0.500	0.506	0.006
100	253	0.504	0.508	0.004
100	255	0.494	0.495	0.001
100	256	0.504	0.502	-0.002
100	257	0.508	0.509	0.001
100	258	0.504	0.505	0.001
100	259	0.503	0.505	0.002
100	260	0.508	0.507	-0.001
100	261	0.501	0.502	0.001
100	262	0.511	0.511	0.000
100	263	0.507	0.507	0.000
100	264	0.498	0.497	-0.001
100	265	0.505	0.503	-0.002
100	266	0.501	0.501	0.000
100	267	0.508	0.504	-0.004
100	268	0.504	0.508	0.004
100	269	0.517	0.520	0.003
100	270	0.506	0.511	0.005
100	271	0.506	0.506	0.000
100	273	0.499	0.505	0.006
105	274	0.507	0.503	-0.004
105	275	0.502	0.502	0.000
105	276	0.510	0.505	-0.005
105	277	0.504	0.501	-0.003
105	278	0.504	0.501	-0.003
Max		0.517	0.520	0.006
Average		0.505	0.505	0.000
Min		0.492	0.492	-0.005
Std Dev		0.004	0.005	0.002

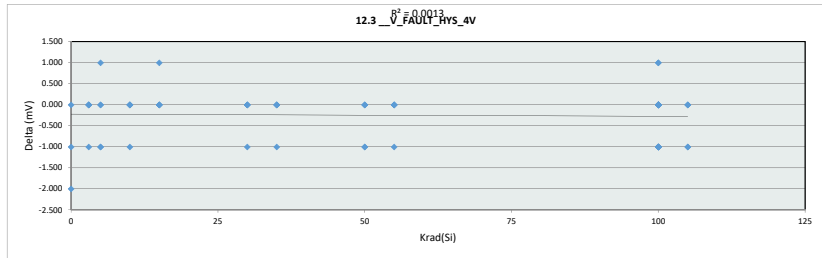


12.11 __V_FAULT											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit			0.55	0.55							
Min Limit			0.47	0.47							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.500	0.492	0.504	0.500	0.501	0.500	0.503	0.496	0.504	0.495	0.501
Average	0.507	0.502	0.505	0.505	0.504	0.504	0.508	0.501	0.508	0.505	0.502
Max	0.511	0.506	0.506	0.509	0.509	0.509	0.507	0.512	0.507	0.517	0.505
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

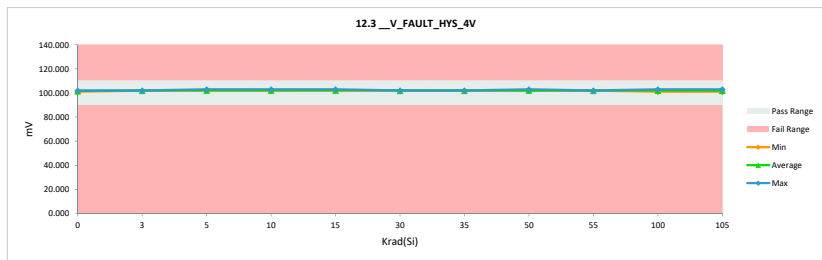


HDR TID Report
TPS7H5002-SP

12.3_V_FAULT_HYS_4V				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	103.000	101.000	-2.000
0	190	103.000	102.000	-1.000
0	191	102.000	102.000	0.000
3	300	103.000	102.000	-1.000
3	305	102.000	102.000	0.000
3	309	102.000	102.000	0.000
3	311	102.000	102.000	0.000
3	313	102.000	102.000	0.000
5	319	103.000	102.000	-1.000
5	321	102.000	102.000	0.000
5	324	102.000	103.000	1.000
5	327	102.000	102.000	0.000
5	331	103.000	102.000	-1.000
10	234	102.000	102.000	0.000
10	235	103.000	102.000	-1.000
10	236	102.000	102.000	0.000
10	237	102.000	102.000	0.000
10	238	103.000	103.000	0.000
15	239	102.000	102.000	0.000
15	240	102.000	103.000	1.000
15	241	102.000	102.000	0.000
15	243	102.000	102.000	0.000
15	245	102.000	102.000	0.000
30	222	102.000	102.000	0.000
30	223	102.000	102.000	0.000
30	225	102.000	102.000	0.000
30	226	103.000	102.000	-1.000
30	227	102.000	102.000	0.000
35	228	102.000	102.000	0.000
35	229	102.000	102.000	0.000
35	231	102.000	102.000	0.000
35	232	102.000	102.000	0.000
35	233	103.000	102.000	-1.000
50	210	102.000	102.000	0.000
50	211	103.000	102.000	-1.000
50	212	103.000	102.000	-1.000
50	213	103.000	103.000	0.000
50	214	102.000	102.000	0.000
55	215	102.000	102.000	0.000
55	216	102.000	102.000	0.000
55	217	102.000	102.000	0.000
55	218	102.000	102.000	0.000
55	220	103.000	102.000	-1.000
100	247	102.000	102.000	0.000
100	249	102.000	102.000	0.000
100	250	102.000	102.000	0.000
100	251	102.000	103.000	1.000
100	253	102.000	102.000	0.000
100	255	103.000	103.000	0.000
100	256	102.000	102.000	0.000
100	257	103.000	102.000	-1.000
100	258	102.000	103.000	1.000
100	259	102.000	102.000	0.000
100	260	102.000	102.000	0.000
100	261	102.000	102.000	0.000
100	262	103.000	102.000	-1.000
100	263	103.000	102.000	-1.000
100	264	102.000	101.000	-1.000
100	265	103.000	102.000	-1.000
100	266	102.000	102.000	0.000
100	267	102.000	101.000	-1.000
100	268	103.000	102.000	-1.000
100	269	103.000	102.000	-1.000
100	270	103.000	103.000	0.000
100	271	102.000	102.000	0.000
100	273	102.000	102.000	0.000
105	274	102.000	101.000	-1.000
105	275	102.000	102.000	0.000
105	276	103.000	103.000	0.000
105	277	102.000	102.000	0.000
105	278	103.000	102.000	-1.000
Max		103.000	103.000	1.000
Average		102.324	102.070	-0.254
Min		102.000	101.000	-2.000
Std Dev		0.471	0.425	0.579

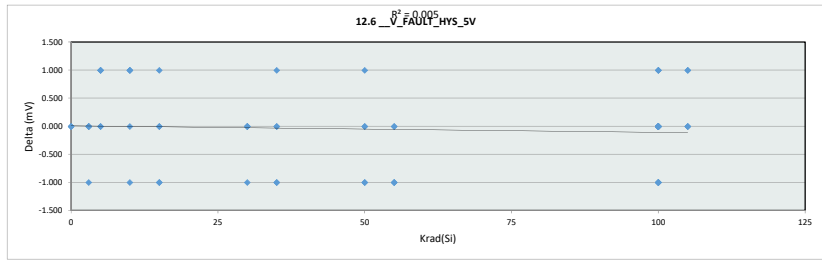


12.3_V_FAULT_HYS_4V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	101.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	101.000	101.000
Average	101.667	102.000	102.200	102.200	102.200	102.000	102.000	102.200	102.000	102.087	102.000
Max	102.000	102.000	103.000	103.000	103.000	102.000	102.000	103.000	102.000	103.000	103.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

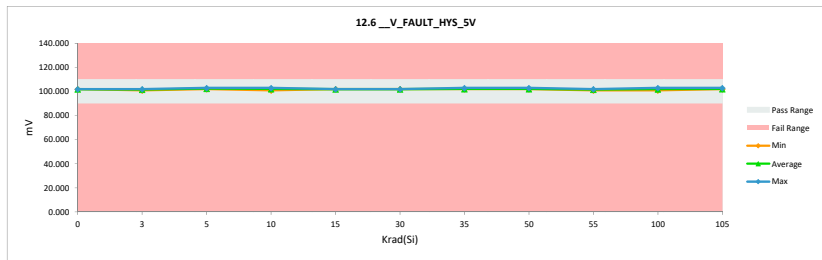


HDR TID Report
TPS7H5002-SP

12.6 __V_FAULT_HYS_5V				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	102.000	102.000	0.000
0	190	102.000	102.000	0.000
0	191	102.000	102.000	0.000
3	300	102.000	101.000	-1.000
3	305	102.000	102.000	0.000
3	309	102.000	102.000	0.000
3	311	101.000	101.000	0.000
3	313	102.000	102.000	0.000
5	319	101.000	102.000	1.000
5	321	102.000	103.000	1.000
5	324	102.000	103.000	1.000
5	327	102.000	102.000	0.000
5	331	102.000	102.000	0.000
10	234	101.000	101.000	0.000
10	235	102.000	103.000	1.000
10	236	102.000	103.000	1.000
10	237	101.000	102.000	1.000
10	238	103.000	102.000	-1.000
15	239	102.000	102.000	0.000
15	240	103.000	102.000	-1.000
15	241	101.000	102.000	1.000
15	243	103.000	102.000	-1.000
15	245	102.000	102.000	0.000
30	222	102.000	102.000	0.000
30	223	103.000	102.000	-1.000
30	225	102.000	102.000	0.000
30	226	102.000	102.000	0.000
30	227	102.000	102.000	0.000
35	228	102.000	102.000	0.000
35	229	103.000	102.000	-1.000
35	231	103.000	102.000	-1.000
35	232	102.000	103.000	1.000
35	233	102.000	102.000	0.000
50	210	102.000	102.000	0.000
50	211	103.000	102.000	-1.000
50	212	103.000	102.000	-1.000
50	213	102.000	102.000	0.000
50	214	102.000	103.000	1.000
55	215	103.000	102.000	-1.000
55	216	102.000	102.000	0.000
55	217	102.000	101.000	-1.000
55	218	102.000	102.000	0.000
55	220	102.000	101.000	-1.000
100	247	103.000	102.000	-1.000
100	249	102.000	102.000	0.000
100	250	102.000	102.000	0.000
100	251	102.000	102.000	0.000
100	253	102.000	103.000	1.000
100	255	101.000	102.000	1.000
100	256	102.000	101.000	-1.000
100	257	102.000	102.000	0.000
100	258	103.000	103.000	0.000
100	259	102.000	101.000	-1.000
100	260	102.000	101.000	-1.000
100	261	102.000	102.000	0.000
100	262	103.000	103.000	0.000
100	263	103.000	102.000	-1.000
100	264	102.000	102.000	0.000
100	265	102.000	102.000	0.000
100	266	103.000	102.000	-1.000
100	267	102.000	102.000	0.000
100	268	102.000	102.000	0.000
100	269	102.000	103.000	1.000
100	270	102.000	102.000	0.000
100	271	102.000	102.000	0.000
100	273	102.000	102.000	0.000
105	274	102.000	102.000	0.000
105	275	102.000	102.000	0.000
105	276	102.000	102.000	0.000
105	277	102.000	103.000	1.000
105	278	101.000	102.000	1.000
Max		103.000	103.000	1.000
Average		102.099	102.042	-0.056
Min		101.000	101.000	-1.000
Std Dev		0.539	0.519	0.674

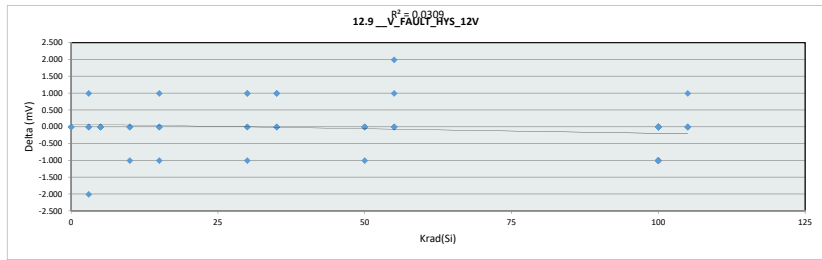


12.6 __V_FAULT_HYS_5V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	101.000	102.000	101.000	102.000	102.000	102.000	102.000	101.000	101.000	102.000
Average	102.000	101.600	102.400	102.200	102.000	102.000	102.200	102.200	101.600	102.043	102.200
Max	102.000	102.000	103.000	103.000	102.000	102.000	103.000	103.000	102.000	103.000	103.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

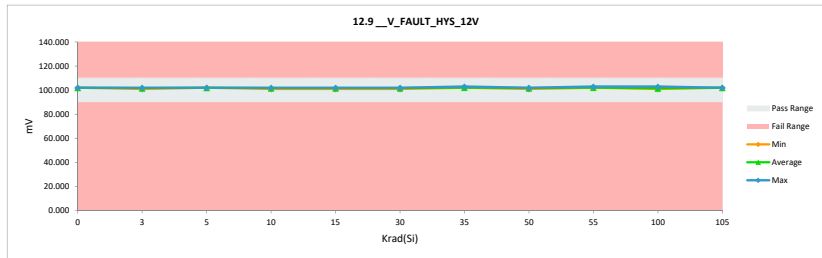


HDR TID Report
TPS7H5002-SP

12.9_V_FAULT_HYS_12V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	110	110		
Min Limit	90	90		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	102.000	102.000	0.000
0	190	102.000	102.000	0.000
0	191	102.000	102.000	0.000
3	300	102.000	102.000	0.000
3	305	103.000	101.000	-2.000
3	309	102.000	102.000	0.000
3	311	102.000	102.000	0.000
3	313	101.000	102.000	1.000
5	319	102.000	102.000	0.000
5	321	102.000	102.000	0.000
5	324	102.000	102.000	0.000
5	327	102.000	102.000	0.000
5	331	102.000	102.000	0.000
10	234	102.000	102.000	0.000
10	235	102.000	101.000	-1.000
10	236	102.000	102.000	0.000
10	237	102.000	102.000	0.000
10	238	102.000	102.000	0.000
15	239	102.000	101.000	-1.000
15	240	102.000	102.000	0.000
15	241	101.000	102.000	1.000
15	243	102.000	102.000	0.000
15	245	102.000	102.000	0.000
30	222	102.000	101.000	-1.000
30	223	102.000	102.000	0.000
30	225	102.000	102.000	0.000
30	226	101.000	102.000	1.000
30	227	101.000	102.000	1.000
35	228	102.000	102.000	0.000
35	229	102.000	102.000	0.000
35	231	101.000	102.000	1.000
35	232	102.000	103.000	1.000
35	233	102.000	102.000	0.000
50	210	102.000	102.000	0.000
50	211	102.000	102.000	0.000
50	212	102.000	102.000	0.000
50	213	102.000	102.000	0.000
50	214	102.000	101.000	-1.000
55	215	102.000	102.000	0.000
55	216	102.000	102.000	0.000
55	217	101.000	102.000	1.000
55	218	101.000	103.000	2.000
55	220	102.000	102.000	0.000
100	247	102.000	102.000	0.000
100	249	102.000	102.000	0.000
100	250	102.000	101.000	-1.000
100	251	102.000	102.000	0.000
100	253	102.000	101.000	-1.000
100	255	102.000	101.000	-1.000
100	256	102.000	102.000	0.000
100	257	102.000	102.000	0.000
100	258	102.000	102.000	0.000
100	259	101.000	101.000	0.000
100	260	102.000	101.000	-1.000
100	261	102.000	101.000	-1.000
100	262	103.000	102.000	-1.000
100	263	102.000	102.000	0.000
100	264	102.000	101.000	-1.000
100	265	102.000	101.000	-1.000
100	266	102.000	102.000	0.000
100	267	103.000	102.000	-1.000
100	268	102.000	102.000	0.000
100	269	103.000	103.000	0.000
100	270	102.000	102.000	0.000
100	271	102.000	102.000	0.000
100	273	102.000	102.000	0.000
105	274	102.000	102.000	0.000
105	275	102.000	102.000	0.000
105	276	102.000	102.000	0.000
105	277	102.000	102.000	0.000
105	278	101.000	102.000	1.000
Max		103.000	103.000	2.000
Average		101.930	101.859	-0.070
Min		101.000	101.000	-2.000
Std Dev		0.425	0.457	0.640

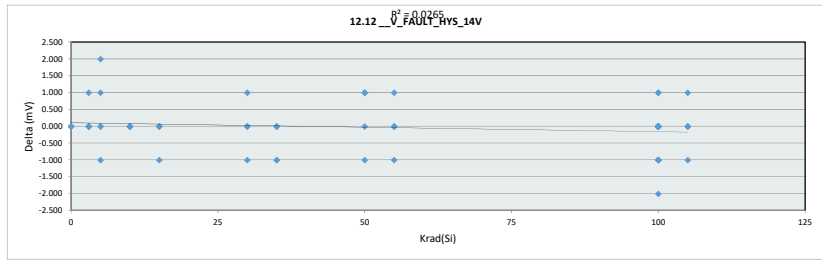


12.9_V_FAULT_HYS_12V											
Test Site											
Tester											
Test Number											
Max Limit	110	mV									
Min Limit	90	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	101.000	102.000	101.000	101.000	101.000	102.000	101.000	102.000	101.000	102.000
Average	102.000	101.800	102.000	101.800	101.800	101.800	102.200	101.800	102.200	101.696	102.000
Max	102.000	102.000	102.000	102.000	102.000	102.000	103.000	102.000	103.000	103.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

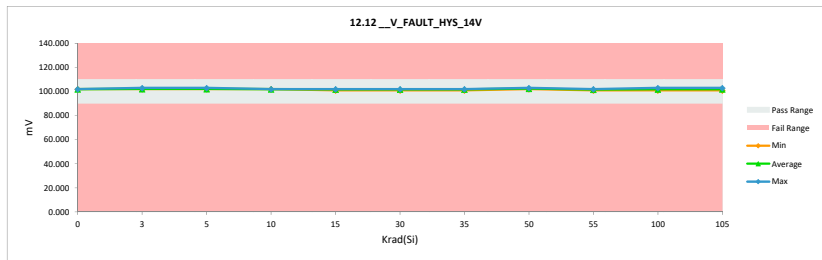


HDR TID Report
TPS7H5002-SP

12.12_V_FAULT_HYS_14V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	110	110		
Min Limit	90	90		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	102.000	102.000	0.000
0	190	102.000	102.000	0.000
0	191	102.000	102.000	0.000
3	300	102.000	102.000	0.000
3	305	103.000	103.000	0.000
3	309	101.000	102.000	1.000
3	311	102.000	102.000	0.000
3	313	102.000	102.000	0.000
5	319	101.000	102.000	1.000
5	321	102.000	102.000	0.000
5	324	101.000	103.000	2.000
5	327	102.000	102.000	0.000
5	331	103.000	102.000	-1.000
10	234	102.000	102.000	0.000
10	235	102.000	102.000	0.000
10	236	102.000	102.000	0.000
10	237	102.000	102.000	0.000
10	238	102.000	102.000	0.000
15	239	101.000	101.000	0.000
15	240	103.000	102.000	-1.000
15	241	102.000	102.000	0.000
15	243	102.000	102.000	0.000
15	245	102.000	102.000	0.000
30	222	102.000	101.000	-1.000
30	223	102.000	102.000	0.000
30	225	102.000	102.000	0.000
30	226	102.000	102.000	0.000
30	227	101.000	102.000	1.000
35	228	102.000	102.000	0.000
35	229	102.000	102.000	0.000
35	231	102.000	101.000	-1.000
35	232	103.000	102.000	-1.000
35	233	102.000	102.000	0.000
50	210	103.000	102.000	-1.000
50	211	102.000	103.000	1.000
50	212	102.000	103.000	1.000
50	213	101.000	102.000	1.000
50	214	102.000	102.000	0.000
55	215	101.000	102.000	1.000
55	216	102.000	102.000	0.000
55	217	102.000	102.000	0.000
55	218	101.000	101.000	0.000
55	220	102.000	101.000	-1.000
100	247	102.000	102.000	0.000
100	249	102.000	102.000	0.000
100	250	102.000	102.000	0.000
100	251	103.000	102.000	-1.000
100	253	103.000	101.000	-2.000
100	255	102.000	102.000	0.000
100	256	102.000	102.000	0.000
100	257	102.000	103.000	1.000
100	258	102.000	102.000	0.000
100	259	101.000	101.000	0.000
100	260	102.000	101.000	-1.000
100	261	103.000	102.000	-1.000
100	262	102.000	102.000	0.000
100	263	102.000	102.000	0.000
100	264	102.000	102.000	0.000
100	265	102.000	102.000	0.000
100	266	102.000	102.000	0.000
100	267	103.000	103.000	0.000
100	268	102.000	102.000	0.000
100	269	102.000	103.000	1.000
100	270	102.000	101.000	-1.000
100	271	102.000	102.000	0.000
100	273	102.000	101.000	-1.000
105	274	102.000	101.000	-1.000
105	275	102.000	102.000	0.000
105	276	103.000	103.000	0.000
105	277	102.000	102.000	0.000
105	278	101.000	102.000	1.000
Max		103.000	103.000	2.000
Average		102.000	101.958	-0.042
Min		101.000	101.000	-2.000
Std Dev		0.535	0.519	0.664

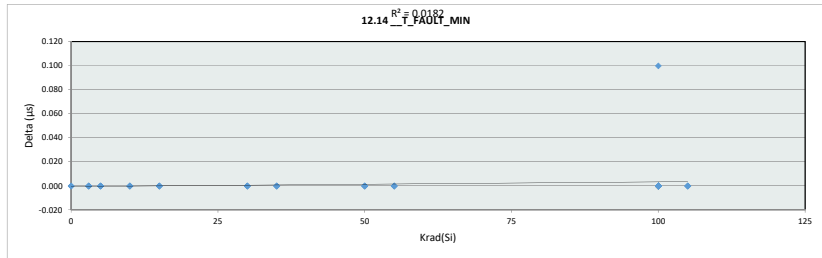


12.12_V_FAULT											
Test Site											
Tester											
Test Number											
Max Limit	110	mV									
Min Limit	90	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	102.000	102.000	102.000	101.000	101.000	101.000	102.000	101.000	101.000	101.000
Average	102.000	102.200	102.200	102.000	101.800	101.800	101.800	102.400	101.600	101.913	102.000
Max	102.000	103.000	103.000	102.000	102.000	102.000	102.000	103.000	102.000	103.000	103.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

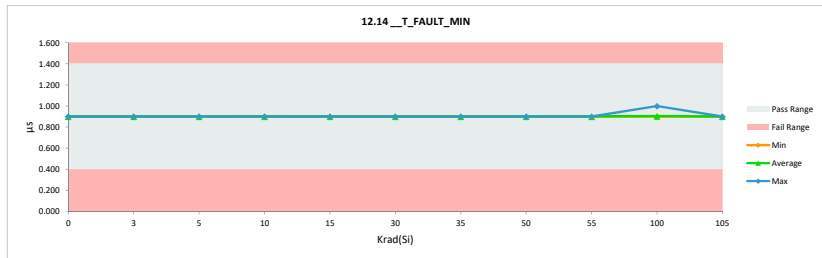


HDR TID Report
TPS7H5002-SP

12.14 __ T_FAULT_MIN				
Test Site	Tester	Test Number	Unit	
			µs	µs
Max Limit			1.4	1.4
Min Limit			0.4	0.4
Krad(Si)	Serial #	PRE	POST	Delta
0	189	0.900	0.900	0.000
0	190	0.900	0.900	0.000
0	191	0.900	0.900	0.000
3	300	0.900	0.900	0.000
3	305	0.900	0.900	0.000
3	309	0.900	0.900	0.000
3	311	0.900	0.900	0.000
3	313	0.900	0.900	0.000
5	319	0.900	0.900	0.000
5	321	0.900	0.900	0.000
5	324	0.900	0.900	0.000
5	327	0.900	0.900	0.000
5	331	0.900	0.900	0.000
10	234	0.900	0.900	0.000
10	235	0.900	0.900	0.000
10	236	0.900	0.900	0.000
10	237	0.900	0.900	0.000
10	238	0.900	0.900	0.000
15	239	0.900	0.900	0.000
15	240	0.900	0.900	0.000
15	241	0.900	0.900	0.000
15	243	0.900	0.900	0.000
15	245	0.900	0.900	0.000
30	222	0.900	0.900	0.000
30	223	0.900	0.900	0.000
30	225	0.900	0.900	0.000
30	226	0.900	0.900	0.000
30	227	0.900	0.900	0.000
35	228	0.900	0.900	0.000
35	229	0.900	0.900	0.000
35	231	0.900	0.900	0.000
35	232	0.900	0.900	0.000
35	233	0.900	0.900	0.000
50	210	0.900	0.900	0.000
50	211	0.900	0.900	0.000
50	212	0.900	0.900	0.000
50	213	0.900	0.900	0.000
50	214	0.900	0.900	0.000
55	215	0.900	0.900	0.000
55	216	0.900	0.900	0.000
55	217	0.900	0.900	0.000
55	218	0.900	0.900	0.000
55	220	0.900	0.900	0.000
100	247	0.900	0.900	0.000
100	249	0.900	0.900	0.000
100	250	0.900	0.900	0.000
100	251	0.900	0.900	0.000
100	253	0.900	0.900	0.000
100	255	0.900	0.900	0.000
100	256	0.900	0.900	0.000
100	257	0.900	1.000	0.100
100	258	0.900	0.900	0.000
100	259	0.900	0.900	0.000
100	260	0.900	0.900	0.000
100	261	0.900	0.900	0.000
100	262	0.900	0.900	0.000
100	263	0.900	0.900	0.000
100	264	0.900	0.900	0.000
100	265	0.900	0.900	0.000
100	266	0.900	0.900	0.000
100	267	0.900	0.900	0.000
100	268	0.900	0.900	0.000
100	269	0.900	0.900	0.000
100	270	0.900	0.900	0.000
100	271	0.900	0.900	0.000
100	273	0.900	0.900	0.000
105	274	0.900	0.900	0.000
105	275	0.900	0.900	0.000
105	276	0.900	0.900	0.000
105	277	0.900	0.900	0.000
105	278	0.900	0.900	0.000
Max		0.900	1.000	0.100
Average		0.900	0.901	0.001
Min		0.900	0.900	0.000
Std Dev		0.000	0.012	0.012



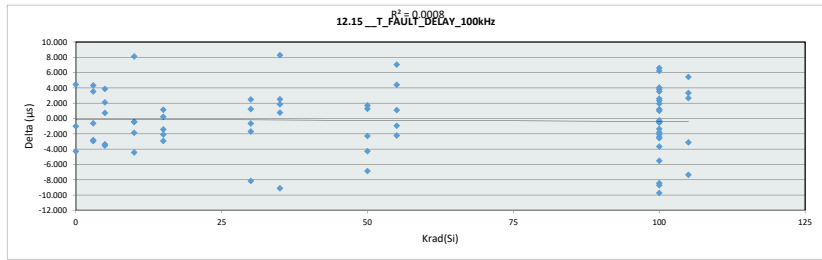
12.14 T_FAULT											
Test Site	Tester	Test Number	Unit								
			µs								
Max Limit			1.4								
Min Limit			0.4								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400
Min	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900
Average	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.904	0.900
Max	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	1.000	0.900
UL	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400



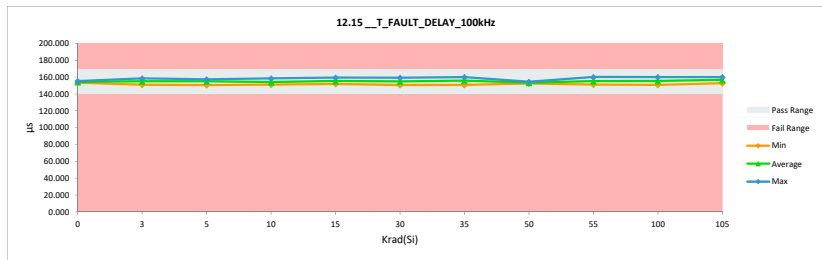
HDR TID Report

TPS7H5002-SP

		12.15 _T_FAULT_DELAY_100kHz		
Test Site	Tester			
Test Number	Unit	µs	µs	
Max Limit	169		169	
Min Limit	140		140	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	157.580	153.341	-4.239
0	190	150.790	155.287	4.497
0	191	155.463	154.505	-0.958
3	300	153.736	150.954	-2.782
3	305	155.492	152.576	-2.916
3	309	154.934	158.514	3.580
3	311	154.155	158.527	4.372
3	313	155.514	154.929	-0.585
5	319	155.313	157.470	2.157
5	321	158.693	155.203	-3.490
5	324	156.647	157.419	0.772
5	327	150.712	154.630	3.918
5	331	153.905	150.586	-3.319
10	234	153.061	151.231	-1.830
10	235	156.409	152.008	-4.401
10	236	151.499	151.085	-0.414
10	237	150.466	158.610	8.144
10	238	158.460	158.082	-0.378
15	239	156.930	155.547	-1.383
15	240	158.889	155.998	-2.891
15	241	156.978	154.922	-2.056
15	243	158.242	159.441	1.199
15	245	151.644	151.911	0.267
30	222	155.100	156.373	1.273
30	223	154.722	153.078	-1.644
30	225	156.663	159.197	2.534
30	226	158.680	150.558	-8.122
30	227	155.819	155.204	-0.615
35	228	152.270	153.092	0.822
35	229	157.389	159.295	1.906
35	231	159.839	150.768	-9.071
35	232	151.739	160.067	8.328
35	233	153.687	156.246	2.559
50	210	159.250	152.442	-6.808
50	211	152.850	154.173	1.323
50	212	156.845	152.627	-4.218
50	213	155.428	153.186	-2.242
50	214	152.605	154.364	1.759
55	215	153.052	160.128	7.076
55	216	155.448	156.598	1.150
55	217	154.376	152.195	-2.181
55	218	152.076	151.178	-0.898
55	220	151.572	156.030	4.458
100	247	160.291	151.621	-8.670
100	249	159.149	156.621	-2.528
100	250	151.266	155.142	3.876
100	251	159.897	157.859	-2.038
100	253	154.452	153.139	-1.313
100	255	157.908	157.423	-0.485
100	256	151.081	152.134	1.053
100	257	153.601	151.834	-1.767
100	258	150.668	151.926	1.258
100	259	152.290	158.567	6.277
100	260	159.747	159.282	-0.465
100	261	159.403	153.928	-5.475
100	262	160.362	150.684	-9.678
100	263	151.062	154.628	3.566
100	264	153.367	160.016	6.649
100	265	155.937	155.721	-0.216
100	266	160.028	151.624	-8.404
100	267	156.276	158.651	2.375
100	268	156.562	159.201	2.639
100	269	158.759	156.395	-2.364
100	270	155.917	160.013	4.096
100	271	156.024	152.415	-3.609
100	273	154.154	156.145	1.991
105	274	156.512	153.430	-3.082
105	275	155.308	158.687	3.379
105	276	160.094	152.765	-7.329
105	277	157.370	160.090	2.720
105	278	153.017	158.503	5.486
	Max	160.362	160.128	8.328
	Average	155.429	155.183	-0.245
	Min	150.466	150.558	-9.678
	Std Dev	2.871	2.982	4.117

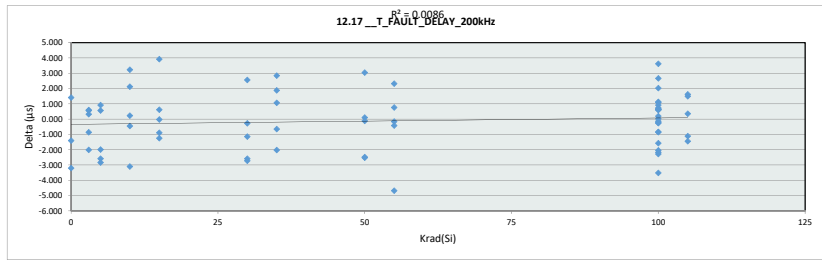


		12.15 T_FAULT											
Test Site	Tester												
Test Number	Unit	µs	µs										
Max Limit	169												
Min Limit	140												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000
Min	153.341	150.954	150.586	151.085	151.911	150.558	150.768	152.442	151.178	150.684	152.765		
Average	154.378	155.100	155.062	154.203	155.564	154.882	155.894	153.358	155.226	155.433	156.695		
Max	155.287	158.527	157.470	158.610	159.441	159.197	160.067	154.364	160.128	160.016	160.090		
UL	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000

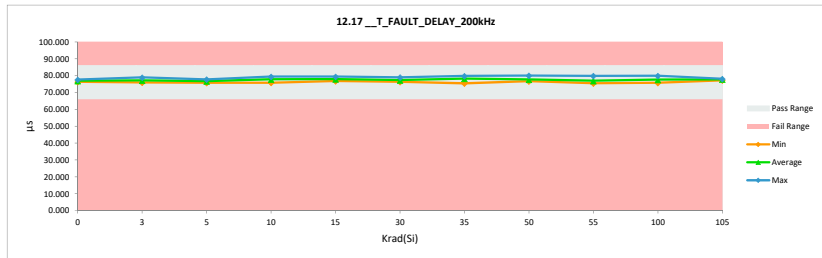


HDR TID Report TPS7H5002-SP

		12.17 __ T_FAULT_DELAY_200kHz		
Test Site	Tester			
Test Number	Unit	µs	µs	
Max Limit	Min Limit	86	66	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	79.534	76.344	-3.190
0	190	79.067	77.685	-1.382
0	191	75.495	76.921	1.426
3	300	78.676	79.015	0.339
3	305	75.716	76.293	0.577
3	309	76.440	77.053	0.613
3	311	79.716	77.721	-1.995
3	313	76.812	75.971	-0.841
5	319	77.253	77.830	0.577
5	321	76.348	77.280	0.932
5	324	78.293	76.321	-1.972
5	327	79.458	76.884	-2.574
5	331	78.597	75.774	-2.823
10	234	79.979	76.898	-3.081
10	235	76.218	79.467	3.249
10	236	75.764	77.908	2.144
10	237	78.984	79.232	0.248
10	238	76.248	75.816	-0.432
15	239	75.542	79.481	3.939
15	240	76.829	76.819	-0.010
15	241	78.462	77.229	-1.233
15	243	77.834	78.469	0.635
15	245	78.843	77.965	-0.878
30	222	78.608	78.347	-0.261
30	223	76.474	79.053	2.579
30	225	79.394	76.821	-2.573
30	226	77.644	76.514	-1.130
30	227	79.028	76.319	-2.709
35	228	76.128	75.496	-0.632
35	229	78.760	79.847	1.087
35	231	76.789	79.648	2.859
35	232	79.119	77.119	-2.000
35	233	77.408	79.308	1.900
50	210	77.013	80.077	3.064
50	211	79.198	76.693	-2.505
50	212	76.858	76.983	0.125
50	213	78.329	78.234	-0.095
50	214	79.184	76.719	-2.465
55	215	80.256	75.595	-4.661
55	216	76.458	77.235	0.777
55	217	77.550	79.884	2.334
55	218	75.649	75.506	-0.143
55	220	77.266	76.858	-0.408
100	247	75.894	76.828	0.934
100	249	76.210	78.251	2.041
100	250	77.056	76.938	-0.118
100	251	80.391	79.574	-0.817
100	253	78.060	78.271	0.211
100	255	80.281	76.781	-3.500
100	256	79.111	76.948	-2.163
100	257	76.089	75.869	-0.220
100	258	77.101	77.132	0.031
100	259	78.271	78.893	0.622
100	260	79.167	79.917	0.750
100	261	77.102	79.785	2.683
100	262	77.252	77.875	0.623
100	263	77.186	76.943	-0.243
100	264	78.993	78.158	-0.835
100	265	78.223	76.673	-1.550
100	266	76.335	79.971	3.636
100	267	75.800	76.480	0.680
100	268	78.263	76.243	-2.020
100	269	78.568	79.226	0.658
100	270	78.046	75.780	-2.266
100	271	75.799	76.898	1.099
100	273	75.655	76.804	1.149
105	274	78.447	77.353	-1.094
105	275	79.594	78.163	-1.431
105	276	76.482	78.124	1.642
105	277	76.925	77.297	0.372
105	278	76.513	78.029	1.516
	Max	80.391	80.077	3.939
	Average	77.691	77.575	-0.115
	Min	75.495	75.496	-4.661
	Std Dev	1.384	1.284	1.849

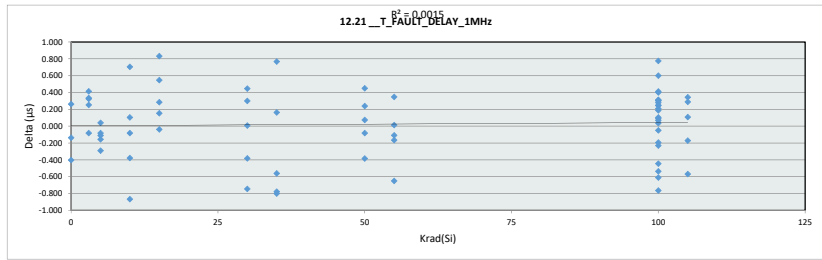


		12.17 __ T_FAULT													
Test Site	Tester														
Test Number	Unit	µs	µs												
Max Limit	Min Limit	86	66												
Krad(Si)	LL	Min	Average	Max	UL	LL	Min	Average	Max	UL	LL	Min	Average	Max	UL
0	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
3	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
5	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
10	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
15	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
30	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
35	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
50	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
55	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
100	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
105	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000

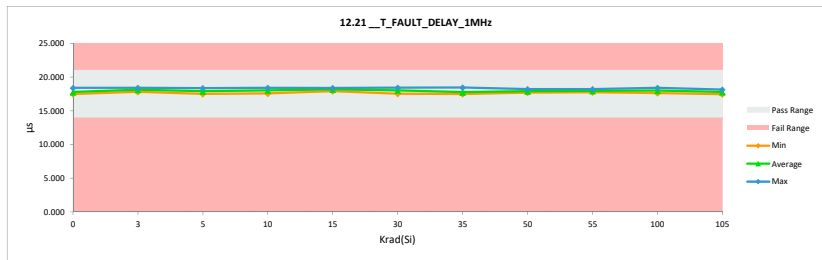


HDR TID Report TPS7H5002-SP

		12.21_T_FAULT_DELAY_1MHz		
Test Site	Tester			
Test Number	Unit	µs	µs	
Max Limit	21	21		
Min Limit	14	14		
Krad(Si)	Serial #	PRE	POST	Delta
0	189	17.881	17.481	-0.400
0	190	17.636	17.502	-0.134
0	191	18.136	18.402	0.266
3	300	18.127	18.049	-0.078
3	305	17.993	18.410	0.417
3	309	17.591	17.929	0.338
3	311	17.552	17.809	0.257
3	313	18.070	18.398	0.328
5	319	17.762	17.656	-0.106
5	321	18.313	18.160	-0.153
5	324	18.014	17.935	-0.079
5	327	17.788	17.501	-0.287
5	331	18.319	18.363	0.044
10	234	17.663	18.372	0.709
10	235	18.147	17.772	-0.375
10	236	18.133	18.055	-0.078
10	237	18.432	17.569	-0.863
10	238	18.306	18.414	0.108
15	239	17.548	18.384	0.836
15	240	17.948	17.913	-0.035
15	241	17.804	18.355	0.551
15	243	17.934	18.091	0.157
15	245	17.825	18.113	0.288
30	222	18.099	17.720	-0.379
30	223	17.895	18.199	0.304
30	225	17.782	18.231	0.449
30	226	18.284	17.543	-0.741
30	227	18.429	18.440	0.011
35	228	17.684	18.456	0.772
35	229	17.632	17.798	0.166
35	231	18.263	17.489	-0.774
35	232	18.038	17.480	-0.558
35	233	18.397	17.600	-0.797
50	210	17.641	17.719	0.078
50	211	18.166	17.785	-0.381
50	212	17.777	18.231	0.454
50	213	17.775	17.697	-0.078
50	214	17.937	18.180	0.243
55	215	18.155	17.992	-0.163
55	216	18.396	17.749	-0.647
55	217	18.319	18.214	-0.105
55	218	17.516	17.868	0.352
55	220	18.019	18.036	0.017
100	247	18.337	17.730	-0.607
100	249	17.678	17.931	0.253
100	250	18.257	18.349	0.092
100	251	17.480	18.259	0.779
100	253	17.906	17.860	-0.046
100	255	17.616	17.864	0.248
100	256	18.307	17.865	-0.442
100	257	17.593	17.806	0.213
100	258	18.339	18.148	-0.191
100	259	17.913	18.317	0.404
100	260	18.302	18.409	0.107
100	261	17.871	18.062	0.191
100	262	18.242	17.710	-0.532
100	263	17.565	18.168	0.603
100	264	17.946	18.255	0.309
100	265	17.831	17.870	0.039
100	266	17.564	17.881	0.317
100	267	17.723	18.028	0.305
100	268	17.562	17.845	0.283
100	269	18.389	17.629	-0.760
100	270	18.151	17.925	-0.226
100	271	17.484	17.899	0.415
100	273	18.048	18.119	0.071
105	274	17.661	17.772	0.111
105	275	18.295	17.730	-0.565
105	276	17.486	17.834	0.348
105	277	17.622	17.455	-0.167
105	278	17.852	18.145	0.293
Max		18.432	18.456	0.836
Average		17.946	17.971	0.025
Min		17.480	17.455	-0.863
Std Dev		0.291	0.289	0.405

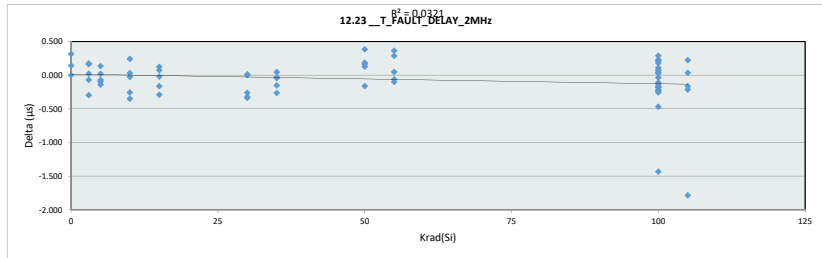


		12.21_T_FAULT										
Test Site	Tester											
Test Number	Unit	µs	µs									
Max Limit	21	21										
Min Limit	14	14										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	
Min	17.481	17.809	17.501	17.569	17.913	17.543	17.480	17.697	17.749	17.629	17.455	
Average	17.795	18.119	17.923	18.036	18.171	18.027	17.765	17.922	17.972	17.997	17.787	
Max	18.402	18.410	18.363	18.414	18.384	18.440	18.456	18.231	18.214	18.409	18.145	
UL	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	

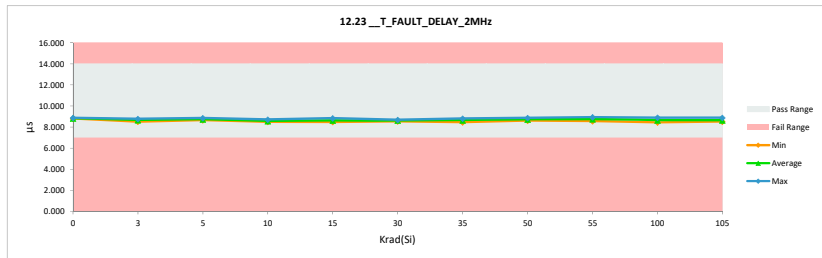


HDR TID Report
TPS7H5002-SP

12.23 __ T_FAULT_DELAY_2MHz				
Test Site				
Tester				
Test Number				
Unit		µs	µs	
Max Limit		14	14	
Min Limit		7	7	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.816	8.820	0.004
0	190	8.698	8.846	0.148
0	191	8.564	8.884	0.320
3	300	8.716	8.742	0.026
3	305	8.812	8.520	-0.292
3	309	8.590	8.767	0.177
3	311	8.645	8.581	-0.064
3	313	8.624	8.791	0.167
5	319	8.641	8.664	0.023
5	321	8.883	8.746	-0.137
5	324	8.922	8.828	-0.094
5	327	8.720	8.860	0.140
5	331	8.741	8.674	-0.067
10	234	8.482	8.728	0.246
10	235	8.765	8.512	-0.253
10	236	8.660	8.639	-0.021
10	237	8.883	8.539	-0.344
10	238	8.469	8.505	0.036
15	239	8.776	8.491	-0.285
15	240	8.868	8.708	-0.160
15	241	8.773	8.851	0.078
15	243	8.546	8.530	-0.016
15	245	8.499	8.628	0.129
30	222	8.955	8.699	-0.256
30	223	8.618	8.634	0.016
30	225	8.885	8.552	-0.333
30	226	8.941	8.623	-0.318
30	227	8.691	8.697	0.006
35	228	8.766	8.816	0.050
35	229	8.725	8.465	-0.260
35	231	8.925	8.777	-0.148
35	232	8.733	8.694	-0.039
35	233	8.600	8.577	-0.023
50	210	8.513	8.684	0.171
50	211	8.494	8.883	0.389
50	212	8.781	8.623	-0.158
50	213	8.555	8.688	0.133
50	214	8.623	8.814	0.191
55	215	8.806	8.711	-0.095
55	216	8.720	8.773	0.053
55	217	8.628	8.570	-0.058
55	218	8.582	8.950	0.368
55	220	8.524	8.816	0.292
100	247	8.469	8.698	0.229
100	249	8.466	8.516	0.050
100	250	8.793	8.870	0.077
100	251	8.896	8.679	-0.217
100	253	8.667	8.901	0.234
100	255	8.745	8.639	-0.106
100	256	8.616	8.446	-0.170
100	257	8.910	8.878	-0.032
100	258	8.491	8.782	0.291
100	259	8.514	8.627	0.113
100	260	8.944	8.721	-0.223
100	261	8.659	8.689	0.030
100	262	8.776	8.611	-0.165
100	263	8.739	8.487	-0.252
100	264	8.469	8.651	0.182
100	265	10.279	8.849	-1.430
100	266	8.777	8.548	-0.229
100	267	8.596	8.801	0.205
100	268	8.628	8.708	0.080
100	269	8.768	8.585	-0.183
100	270	8.629	8.467	-0.162
100	271	8.704	8.575	-0.129
100	273	8.930	8.466	-0.464
105	274	8.873	8.663	-0.210
105	275	10.357	8.576	-1.781
105	276	8.661	8.890	0.229
105	277	8.596	8.636	0.040
105	278	8.689	8.529	-0.160
	Max	10.357	8.950	0.389
	Average	8.744	8.682	-0.062
	Min	8.466	8.446	-1.781
	Std Dev	0.303	0.130	0.327

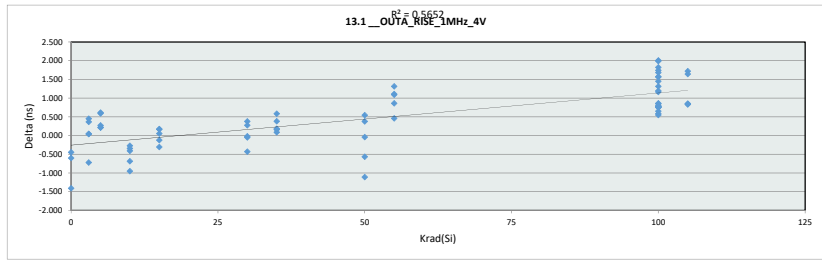


12.23 T_FAULT											
Test Site											
Tester											
Test Number											
Max Limit		14	µs								
Min Limit		7	µs								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000
Min	8.820	8.520	8.664	8.505	8.491	8.552	8.465	8.623	8.570	8.446	8.529
Average	8.850	8.680	8.754	8.585	8.642	8.641	8.666	8.738	8.764	8.661	8.659
Max	8.884	8.791	8.860	8.728	8.851	8.699	8.816	8.883	8.950	8.901	8.890
UL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000

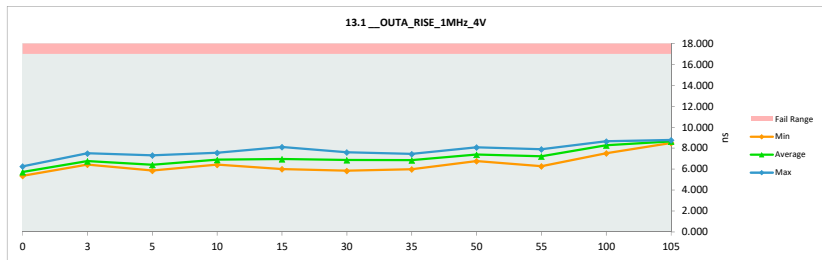


HDR TID Report
TPS7H5002-SP

13.1_OUTA_RISE_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.669	6.266	-1.403
0	190	6.158	5.566	-0.592
0	191	5.803	5.365	-0.438
3	300	7.181	6.466	-0.715
3	305	6.392	6.440	0.048
3	309	6.433	6.486	0.053
3	311	7.055	7.514	0.459
3	313	6.603	6.974	0.371
5	319	6.158	6.375	0.217
5	321	6.725	7.326	0.601
5	324	5.583	5.861	0.278
5	327	5.710	5.936	0.226
5	331	5.974	6.594	0.620
10	234	7.836	7.571	-0.265
10	235	7.488	7.088	-0.400
10	236	7.373	6.428	-0.945
10	237	6.839	6.501	-0.338
10	238	7.594	6.914	-0.680
15	239	7.935	8.111	0.176
15	240	7.202	7.384	0.182
15	241	6.097	6.156	0.059
15	243	7.518	7.220	-0.298
15	245	6.119	6.003	-0.116
30	222	7.277	6.854	-0.423
30	223	5.888	5.846	-0.042
30	225	7.228	7.214	-0.014
30	226	6.482	6.869	0.387
30	227	7.332	7.615	0.283
35	228	5.894	5.989	0.095
35	229	6.835	7.425	0.590
35	231	7.064	7.453	0.389
35	232	7.130	7.298	0.168
35	233	5.939	6.129	0.190
50	210	7.873	6.771	-1.102
50	211	7.618	7.057	-0.561
50	212	7.456	7.424	-0.032
50	213	7.702	8.087	0.385
50	214	7.080	7.634	0.554
55	215	5.821	6.287	0.466
55	216	5.871	6.969	1.098
55	217	6.066	7.183	1.117
55	218	6.506	7.828	1.322
55	220	7.033	7.902	0.869
100	247	6.683	7.884	1.201
100	249	6.864	8.037	1.173
100	250	6.681	8.003	1.322
100	251	7.365	8.232	0.867
100	253	7.553	8.353	0.800
100	255	6.547	8.123	1.576
100	256	7.806	8.568	0.762
100	257	5.776	7.524	1.748
100	258	6.598	8.177	1.579
100	259	7.660	8.518	0.858
100	260	6.674	8.363	1.689
100	261	7.781	8.544	0.763
100	262	6.188	8.205	2.017
100	263	5.958	7.956	1.998
100	264	7.766	8.535	0.769
100	265	7.841	8.430	0.589
100	266	8.101	8.659	0.558
100	267	6.257	8.088	1.831
100	268	7.704	8.476	0.772
100	269	6.852	8.313	1.461
100	270	7.957	8.617	0.660
100	271	7.723	8.515	0.792
100	273	7.705	8.479	0.774
105	274	6.901	8.553	1.652
105	275	7.877	8.717	0.840
105	276	7.856	8.715	0.859
105	277	7.925	8.777	0.852
105	278	6.769	8.498	1.729
Max		8.101	8.777	2.017
Average		6.942	7.440	0.498
Min		5.583	5.365	-1.403
Std Dev		0.725	0.935	0.760

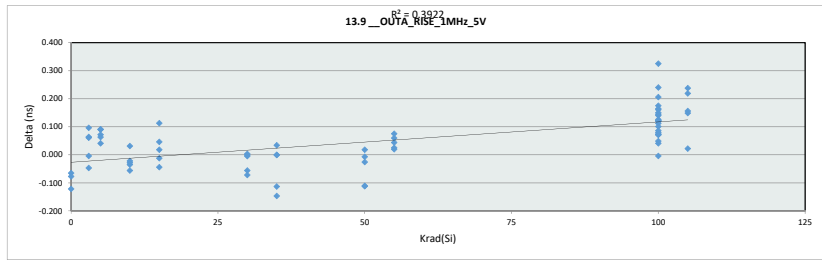


13.1_OUTA_RISE											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	5.365	6.440	5.861	6.428	6.003	5.846	5.989	6.771	6.287	7.524	8.498
Average	5.732	6.776	6.418	6.900	6.975	6.880	6.859	7.395	7.234	8.287	8.652
Max	6.266	7.514	7.326	7.571	8.111	7.615	7.453	8.087	7.902	8.659	8.777
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

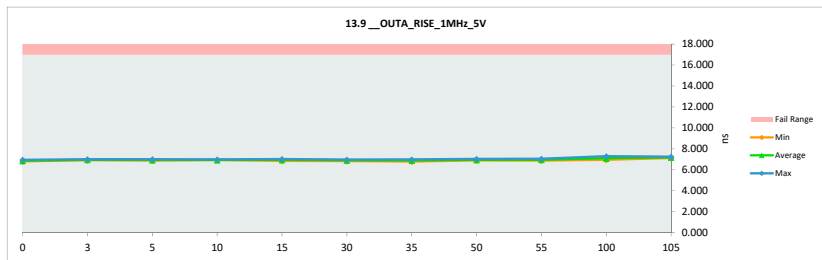


HDR TID Report
TPS7H5002-SP

13.9_OUTA_RISE_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.072	6.951	-0.121
0	190	6.883	6.807	-0.076
0	191	6.881	6.817	-0.064
3	300	6.995	6.949	-0.046
3	305	6.880	6.977	0.097
3	309	6.921	6.918	-0.003
3	311	6.937	6.999	0.062
3	313	6.917	6.982	0.065
5	319	6.916	6.958	0.042
5	321	6.918	7.010	0.092
5	324	6.785	6.876	0.091
5	327	6.862	6.926	0.064
5	331	6.890	6.963	0.073
10	234	7.037	6.982	-0.055
10	235	6.895	6.927	0.032
10	236	6.948	6.927	-0.021
10	237	6.949	6.922	-0.027
10	238	6.972	6.938	-0.034
15	239	7.032	7.021	-0.011
15	240	6.902	7.016	0.114
15	241	6.899	6.856	-0.043
15	243	6.975	6.994	0.019
15	245	6.887	6.934	0.047
30	222	6.967	6.912	-0.055
30	223	6.842	6.842	0.000
30	225	6.983	6.912	-0.071
30	226	6.940	6.936	-0.004
30	227	6.975	6.979	0.004
35	228	6.939	6.793	-0.146
35	229	6.931	6.931	0.000
35	231	7.033	6.921	-0.112
35	232	6.982	6.983	0.001
35	233	6.860	6.895	0.035
50	210	7.010	6.899	-0.111
50	211	7.041	6.931	-0.110
50	212	7.013	6.988	-0.025
50	213	7.037	7.031	-0.006
50	214	6.931	6.950	0.019
55	215	6.851	6.877	0.026
55	216	6.871	6.932	0.061
55	217	6.925	6.970	0.045
55	218	6.917	6.993	0.076
55	220	7.034	7.053	0.021
100	247	6.934	7.021	0.087
100	249	6.986	6.983	-0.003
100	250	6.977	7.027	0.050
100	251	7.049	7.091	0.042
100	253	7.021	7.167	0.146
100	255	6.986	7.059	0.073
100	256	6.971	7.297	0.326
100	257	6.840	6.953	0.113
100	258	6.901	7.042	0.141
100	259	7.024	7.145	0.121
100	260	6.928	7.104	0.176
100	261	7.018	7.169	0.151
100	262	6.966	7.046	0.080
100	263	6.879	7.006	0.127
100	264	6.985	7.150	0.165
100	265	7.092	7.164	0.072
100	266	7.130	7.205	0.075
100	267	6.861	7.023	0.162
100	268	7.027	7.150	0.123
100	269	6.967	7.089	0.122
100	270	7.056	7.263	0.207
100	271	6.948	7.189	0.241
100	273	7.027	7.129	0.102
105	274	6.971	7.128	0.157
105	275	7.027	7.247	0.220
105	276	7.047	7.197	0.150
105	277	7.125	7.148	0.023
105	278	6.938	7.177	0.239
Max		7.130	7.297	0.326
Average		6.959	7.011	0.051
Min		6.785	6.793	-0.146
Std Dev		0.071	0.114	0.094



13.9_OUTA_RISE											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
LL	0	3	5	10	15	30	35	50	55	100	105
Min	6.807	6.918	6.876	6.922	6.856	6.842	6.793	6.899	6.877	6.953	7.128
Average	6.858	6.965	6.947	6.939	6.964	6.916	6.905	6.960	6.965	7.107	7.179
Max	6.951	6.999	7.010	6.982	7.021	6.979	6.983	7.031	7.055	7.297	7.247
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

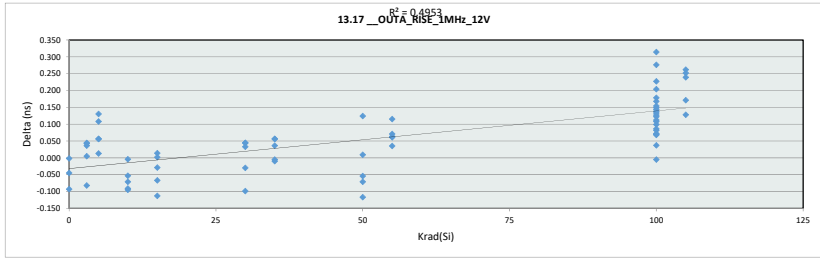


HDR TID Report TPS7H5002-SP

13.17 __OUTA_RISE_1MHz_12V

Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

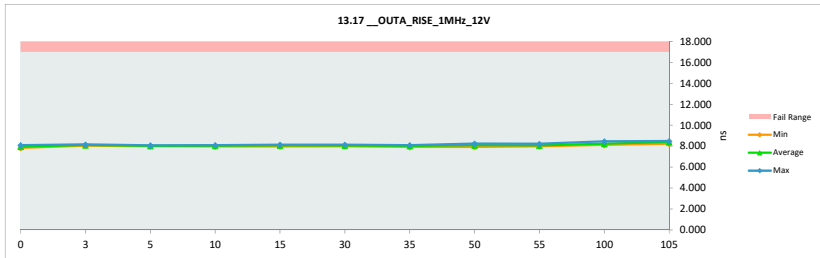
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.188	8.096	-0.092
0	190	7.844	7.843	-0.001
0	191	7.984	7.940	-0.044
3	300	8.176	8.095	-0.081
3	305	8.000	8.044	0.044
3	309	8.132	8.169	0.037
3	311	8.124	8.130	0.006
3	313	8.077	8.122	0.045
5	319	8.028	8.085	0.057
5	321	7.979	7.993	0.014
5	324	7.930	8.039	0.109
5	327	8.016	8.073	0.057
5	331	7.856	7.987	0.131
10	234	8.070	8.067	-0.003
10	235	8.068	7.978	-0.090
10	236	8.170	8.100	-0.070
10	237	8.121	8.069	-0.052
10	238	8.127	8.033	-0.094
15	239	8.250	8.138	-0.112
15	240	8.068	8.083	0.015
15	241	8.143	8.146	0.003
15	243	8.097	8.069	-0.028
15	245	8.041	7.975	-0.066
30	222	8.115	8.149	0.034
30	223	7.990	8.035	0.045
30	225	8.088	7.990	-0.098
30	226	8.039	8.084	0.045
30	227	8.113	8.084	-0.029
35	228	8.028	8.019	-0.009
35	229	7.930	7.926	-0.004
35	231	8.044	8.101	0.057
35	232	8.019	8.076	0.057
35	233	8.024	8.061	0.037
50	210	8.199	8.083	-0.116
50	211	8.221	8.168	-0.053
50	212	8.059	8.069	0.010
50	213	8.125	8.250	0.125
50	214	8.000	7.930	-0.070
55	215	7.932	7.994	0.062
55	216	7.973	8.037	0.064
55	217	7.974	8.090	0.116
55	218	8.000	8.036	0.036
55	220	8.154	8.226	0.072
100	247	7.970	8.125	0.155
100	249	8.072	8.155	0.083
100	250	8.065	8.165	0.100
100	251	8.200	8.334	0.134
100	253	8.203	8.241	0.038
100	255	8.089	8.258	0.169
100	256	8.056	8.184	0.128
100	257	8.130	8.126	-0.004
100	258	8.088	8.161	0.073
100	259	8.158	8.337	0.179
100	260	8.042	8.192	0.150
100	261	8.084	8.155	0.071
100	262	8.012	8.327	0.315
100	263	8.071	8.184	0.113
100	264	8.061	8.130	0.069
100	265	8.143	8.287	0.144
100	266	8.173	8.378	0.205
100	267	7.967	8.195	0.228
100	268	8.233	8.342	0.109
100	269	8.090	8.213	0.123
100	270	8.186	8.463	0.277
100	271	8.152	8.239	0.087
100	273	8.106	8.245	0.139
105	274	8.191	8.363	0.172
105	275	8.221	8.484	0.263
105	276	8.250	8.490	0.240
105	277	8.298	8.427	0.129
105	278	7.989	8.242	0.253
Max		8.298	8.490	0.315
Average		8.082	8.143	0.061
Min		7.844	7.843	-0.116
Std Dev		0.094	0.136	0.100



13.17 __OUTA_RISE

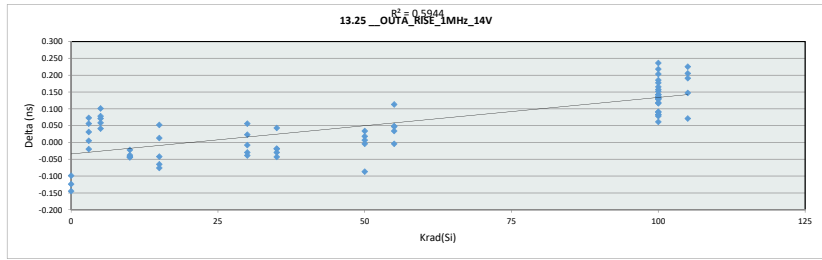
Test Site	
Tester	
Test Number	17
Max Limit	ns
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.843	8.044	7.987	7.978	7.975	7.990	7.926	7.930	7.994	8.125	8.242
Average	7.960	8.112	8.035	8.049	8.082	8.068	8.037	8.100	8.077	8.236	8.401
Max	8.096	8.169	8.085	8.100	8.146	8.149	8.101	8.250	8.226	8.463	8.490
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

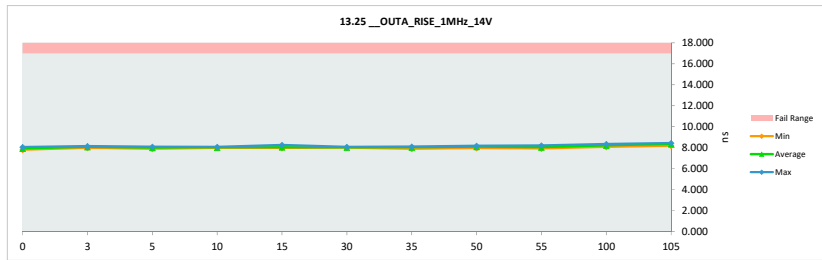


HDR TID Report TPS7H5002-SP

		13.25 _OUTA_RISE_1MHz_14V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	17	17	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.187	8.064	-0.123
0	190	7.902	7.804	-0.098
0	191	8.040	7.896	-0.144
3	300	8.081	8.062	-0.019
3	305	7.984	7.990	0.006
3	309	8.063	8.137	0.074
3	311	8.093	8.150	0.057
3	313	8.067	8.099	0.032
5	319	8.053	8.095	0.042
5	321	7.846	7.948	0.102
5	324	7.943	8.015	0.072
5	327	7.972	8.031	0.059
5	331	7.819	7.898	0.079
10	234	8.042	7.999	-0.043
10	235	8.030	7.987	-0.043
10	236	8.113	8.076	-0.037
10	237	8.109	8.068	-0.041
10	238	8.024	8.003	-0.021
15	239	8.208	8.261	0.053
15	240	8.078	8.037	-0.041
15	241	8.117	8.042	-0.075
15	243	8.045	7.981	-0.064
15	245	7.982	7.996	0.014
30	222	8.113	8.075	-0.038
30	223	7.998	8.022	0.024
30	225	8.027	7.998	-0.029
30	226	7.993	8.050	0.057
30	227	8.038	8.031	-0.007
35	228	8.044	8.088	0.044
35	229	7.924	7.895	-0.029
35	231	8.129	8.111	-0.018
35	232	8.039	8.021	-0.018
35	233	8.092	8.050	-0.042
50	210	8.201	8.115	-0.086
50	211	8.196	8.193	-0.003
50	212	8.013	8.032	0.019
50	213	8.117	8.152	0.035
50	214	7.962	7.970	0.008
55	215	7.927	7.924	-0.003
55	216	8.000	8.035	0.035
55	217	7.924	8.038	0.114
55	218	7.974	8.022	0.048
55	220	8.170	8.219	0.049
100	247	7.952	8.081	0.129
100	249	8.037	8.156	0.119
100	250	8.099	8.216	0.117
100	251	8.211	8.294	0.083
100	253	8.119	8.211	0.092
100	255	8.102	8.186	0.084
100	256	8.027	8.163	0.136
100	257	8.032	8.218	0.186
100	258	8.055	8.192	0.137
100	259	8.147	8.305	0.158
100	260	7.955	8.174	0.219
100	261	8.036	8.179	0.143
100	262	8.014	8.145	0.131
100	263	8.045	8.282	0.237
100	264	8.046	8.189	0.143
100	265	8.149	8.281	0.132
100	266	8.297	8.359	0.062
100	267	8.040	8.218	0.178
100	268	8.211	8.302	0.091
100	269	8.008	8.212	0.204
100	270	8.252	8.331	0.079
100	271	8.071	8.237	0.166
100	273	8.045	8.198	0.153
105	274	8.146	8.352	0.206
105	275	8.219	8.445	0.226
105	276	8.194	8.386	0.192
105	277	8.284	8.432	0.148
105	278	8.109	8.181	0.072
	Max	8.297	8.445	0.237
	Average	8.065	8.121	0.057
	Min	7.819	7.804	-0.144
	Std Dev	0.097	0.135	0.089

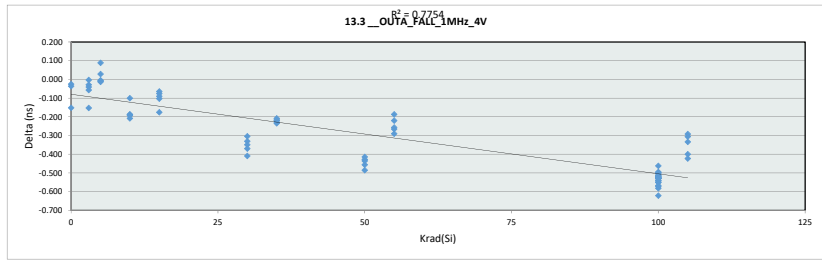


		13.25 _OUTA_RISE_1MHz_14V										
Test Site	Tester											
Test Number	Unit	ns	ns									
Max Limit	Min Limit	17	ns									
LL	Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
Min	7.804	7.990	7.898	7.987	7.981	7.998	7.895	7.970	7.924	8.081	8.181	
Average	7.921	8.088	7.997	8.027	8.063	8.035	8.033	8.092	8.048	8.223	8.359	
Max	8.064	8.150	8.095	8.076	8.261	8.075	8.111	8.193	8.219	8.359	8.445	
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	

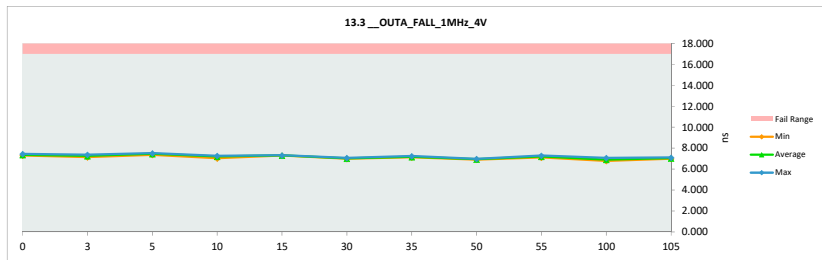


HDR TID Report
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13.3_OUTA_FALL_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.330	7.306	-0.024
0	190	7.491	7.456	-0.035
0	191	7.488	7.338	-0.150
3	300	7.291	7.140	-0.151
3	305	7.311	7.284	-0.027
3	309	7.354	7.299	-0.055
3	311	7.382	7.381	-0.001
3	313	7.260	7.221	-0.039
5	319	7.446	7.434	-0.012
5	321	7.367	7.364	-0.003
5	324	7.499	7.529	0.030
5	327	7.532	7.525	-0.007
5	331	7.392	7.482	0.090
10	234	7.370	7.271	-0.099
10	235	7.421	7.230	-0.191
10	236	7.240	7.033	-0.207
10	237	7.418	7.233	-0.185
10	238	7.458	7.271	-0.187
15	239	7.386	7.297	-0.089
15	240	7.405	7.331	-0.074
15	241	7.473	7.299	-0.174
15	243	7.378	7.315	-0.063
15	245	7.414	7.313	-0.101
30	222	7.400	7.032	-0.368
30	223	7.374	7.072	-0.302
30	225	7.334	6.986	-0.348
30	226	7.332	7.003	-0.329
30	227	7.437	7.029	-0.408
35	228	7.403	7.185	-0.218
35	229	7.339	7.124	-0.215
35	231	7.355	7.128	-0.227
35	232	7.404	7.197	-0.207
35	233	7.482	7.249	-0.233
50	210	7.407	6.923	-0.484
50	211	7.363	6.908	-0.455
50	212	7.365	6.952	-0.413
50	213	7.403	6.969	-0.434
50	214	7.413	6.987	-0.426
55	215	7.518	7.299	-0.219
55	216	7.477	7.188	-0.289
55	217	7.517	7.253	-0.264
55	218	7.346	7.161	-0.185
55	220	7.372	7.117	-0.255
100	247	7.607	7.077	-0.620
100	249	7.478	6.969	-0.509
100	250	7.393	6.853	-0.540
100	251	7.336	6.788	-0.548
100	253	7.445	6.918	-0.527
100	255	7.337	6.787	-0.550
100	256	7.407	6.889	-0.518
100	257	7.486	6.972	-0.514
100	258	7.455	6.962	-0.493
100	259	7.356	6.859	-0.497
100	260	7.449	6.923	-0.526
100	261	7.512	6.932	-0.580
100	262	7.492	6.963	-0.529
100	263	7.406	6.867	-0.539
100	264	7.398	6.883	-0.515
100	265	7.455	6.932	-0.523
100	266	7.473	6.973	-0.500
100	267	7.486	6.959	-0.527
100	268	7.379	6.856	-0.523
100	269	7.415	6.887	-0.528
100	270	7.451	6.885	-0.566
100	271	7.377	6.806	-0.571
100	273	7.372	6.911	-0.461
105	274	7.537	7.116	-0.421
105	275	7.330	7.040	-0.290
105	276	7.395	6.997	-0.398
105	277	7.335	7.031	-0.304
105	278	7.402	7.070	-0.332
	Max	7.697	7.529	0.090
	Average	7.411	7.102	-0.310
	Min	7.240	6.787	-0.620
	Std Dev	0.073	0.195	0.198

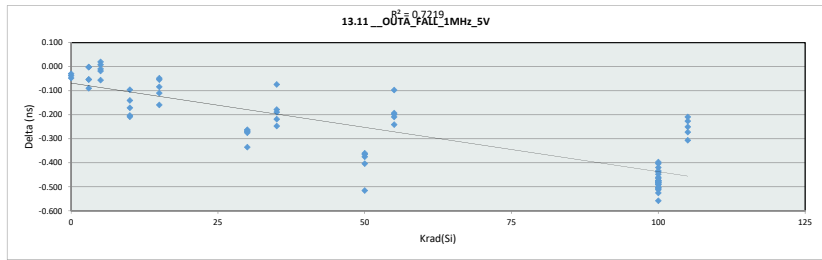


13.3_OUTA_FALL											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.306	7.140	7.364	7.033	7.297	6.986	7.124	6.908	7.117	6.787	6.997
Average	7.367	7.265	7.467	7.208	7.311	7.024	7.177	6.948	7.204	6.907	7.051
Max	7.456	7.381	7.529	7.271	7.331	7.072	7.249	6.987	7.299	7.077	7.116
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

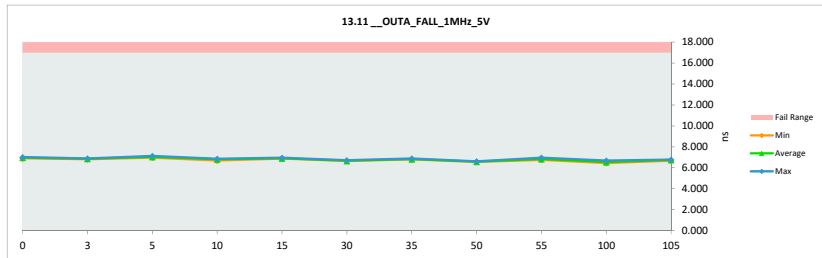


HDR TID Report
TPS7H5002-SP

13.11_OUTA_FALL_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	6.948	6.902	-0.046
0	190	7.051	7.023	-0.028
0	191	7.007	6.971	-0.036
3	300	6.908	6.819	-0.089
3	305	6.894	6.894	0.000
3	309	6.924	6.872	-0.052
3	311	6.938	6.886	-0.052
3	313	6.843	6.842	-0.001
5	319	7.027	7.036	0.009
5	321	6.979	6.971	-0.008
5	324	7.121	7.066	-0.055
5	327	7.152	7.135	-0.017
5	331	7.003	7.024	0.021
10	234	6.966	6.827	-0.139
10	235	6.948	6.853	-0.095
10	236	6.897	6.690	-0.207
10	237	7.011	6.841	-0.170
10	238	7.066	6.864	-0.202
15	239	6.957	6.910	-0.047
15	240	7.042	6.884	-0.158
15	241	7.047	6.964	-0.083
15	243	6.959	6.906	-0.053
15	245	7.016	6.907	-0.109
30	222	6.925	6.664	-0.261
30	223	6.989	6.720	-0.269
30	225	6.936	6.670	-0.266
30	226	6.990	6.656	-0.334
30	227	6.924	6.650	-0.274
35	228	7.024	6.836	-0.188
35	229	6.897	6.824	-0.073
35	231	7.047	6.801	-0.246
35	232	7.028	6.811	-0.217
35	233	7.069	6.892	-0.177
50	210	6.983	6.581	-0.402
50	211	6.945	6.586	-0.359
50	212	6.979	6.615	-0.364
50	213	7.067	6.553	-0.514
50	214	6.977	6.603	-0.374
55	215	7.161	6.969	-0.192
55	216	6.981	6.885	-0.096
55	217	7.076	6.879	-0.197
55	218	7.002	6.762	-0.240
55	220	6.966	6.758	-0.208
100	247	7.257	6.700	-0.557
100	249	7.095	6.571	-0.524
100	250	7.014	6.524	-0.490
100	251	6.961	6.454	-0.507
100	253	7.026	6.554	-0.472
100	255	6.981	6.518	-0.463
100	256	7.006	6.526	-0.480
100	257	7.076	6.598	-0.478
100	258	7.078	6.576	-0.502
100	259	7.005	6.494	-0.511
100	260	7.042	6.554	-0.488
100	261	7.072	6.586	-0.486
100	262	7.093	6.613	-0.480
100	263	6.964	6.562	-0.402
100	264	7.032	6.573	-0.459
100	265	7.032	6.558	-0.474
100	266	7.047	6.574	-0.473
100	267	7.060	6.628	-0.432
100	268	6.963	6.545	-0.418
100	269	6.956	6.560	-0.396
100	270	6.995	6.494	-0.501
100	271	6.997	6.560	-0.437
100	273	7.016	6.570	-0.446
105	274	7.063	6.792	-0.271
105	275	7.005	6.756	-0.249
105	276	6.990	6.685	-0.305
105	277	6.966	6.741	-0.225
105	278	6.988	6.780	-0.208
	Max	7.257	7.135	0.021
	Average	7.006	6.739	-0.268
	Min	6.843	6.454	-0.557
	Std Dev	0.068	0.170	0.178

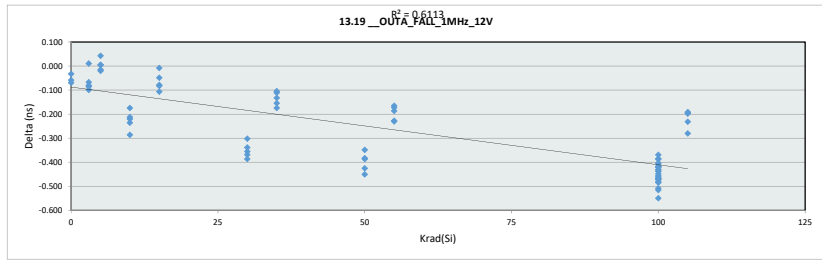


13.11_OUTA_FALL_1MHz_5V											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.902	6.819	6.971	6.690	6.884	6.650	6.801	6.553	6.758	6.454	6.685
Average	6.965	6.863	7.046	6.815	6.914	6.672	6.833	6.588	6.851	6.561	6.751
Max	7.023	6.894	7.135	6.864	6.964	6.720	6.892	6.615	6.969	6.700	6.792
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

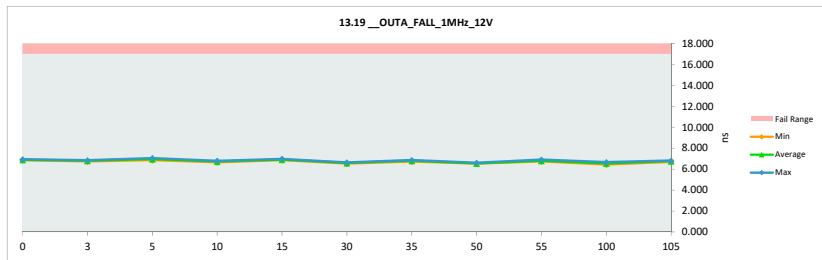


HDR TID Report TPS7H5002-SP

13.19_OUTA_FALL_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	6.916	6.859	-0.057
0	190	6.916	6.885	-0.031
0	191	7.006	6.938	-0.068
3	300	6.875	6.777	-0.098
3	305	6.833	6.749	-0.084
3	309	6.895	6.829	-0.066
3	311	6.935	6.856	-0.079
3	313	6.822	6.834	0.012
5	319	7.003	7.009	0.006
5	321	6.824	6.868	0.044
5	324	7.034	7.040	0.006
5	327	7.073	7.059	-0.014
5	331	6.906	6.887	-0.019
10	234	6.936	6.651	-0.285
10	235	6.916	6.743	-0.173
10	236	6.898	6.664	-0.234
10	237	7.010	6.799	-0.211
10	238	6.992	6.773	-0.219
15	239	6.957	6.950	-0.007
15	240	6.944	6.863	-0.081
15	241	7.059	6.982	-0.077
15	243	6.919	6.872	-0.047
15	245	6.984	6.879	-0.105
30	222	6.947	6.646	-0.301
30	223	7.017	6.632	-0.385
30	225	6.899	6.532	-0.367
30	226	6.920	6.566	-0.354
30	227	6.936	6.599	-0.337
35	228	6.997	6.866	-0.131
35	229	6.838	6.728	-0.110
35	231	6.899	6.796	-0.103
35	232	6.942	6.769	-0.173
35	233	7.023	6.870	-0.153
50	210	6.964	6.540	-0.424
50	211	6.993	6.611	-0.382
50	212	6.912	6.527	-0.385
50	213	6.982	6.533	-0.449
50	214	6.877	6.530	-0.347
55	215	7.137	6.911	-0.226
55	216	7.008	6.779	-0.229
55	217	6.957	6.772	-0.185
55	218	6.909	6.745	-0.164
55	220	6.988	6.817	-0.171
100	247	7.167	6.619	-0.548
100	249	7.075	6.615	-0.460
100	250	6.941	6.470	-0.471
100	251	6.929	6.527	-0.402
100	253	6.989	6.573	-0.416
100	255	6.918	6.481	-0.437
100	256	6.941	6.434	-0.507
100	257	7.033	6.665	-0.368
100	258	7.017	6.562	-0.455
100	259	6.979	6.499	-0.480
100	260	6.940	6.511	-0.429
100	261	6.997	6.483	-0.514
100	262	7.049	6.583	-0.466
100	263	6.979	6.561	-0.418
100	264	6.904	6.484	-0.420
100	265	7.008	6.524	-0.484
100	266	7.021	6.574	-0.447
100	267	7.002	6.618	-0.384
100	268	6.963	6.577	-0.386
100	269	6.956	6.490	-0.466
100	270	7.005	6.574	-0.431
100	271	6.874	6.469	-0.405
100	273	6.930	6.460	-0.470
105	274	7.047	6.817	-0.230
105	275	6.977	6.784	-0.193
105	276	6.966	6.770	-0.196
105	277	6.982	6.792	-0.190
105	278	6.985	6.706	-0.279
	Max	7.167	7.059	0.044
	Average	6.963	6.701	-0.262
	Min	6.822	6.434	-0.548
	Std Dev	0.066	0.164	0.169

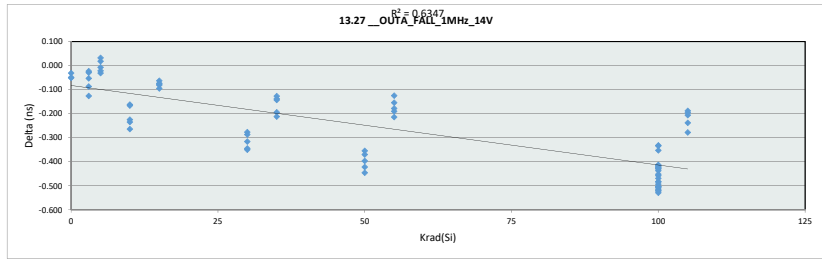


13.19_OUTA_FALL_1MHz_12V											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.859	6.749	6.868	6.651	6.863	6.532	6.728	6.527	6.745	6.434	6.706
Average	6.894	6.809	6.973	6.726	6.909	6.595	6.806	6.548	6.805	6.537	6.774
Max	6.938	6.856	7.059	6.799	6.982	6.646	6.870	6.611	6.911	6.665	6.817
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

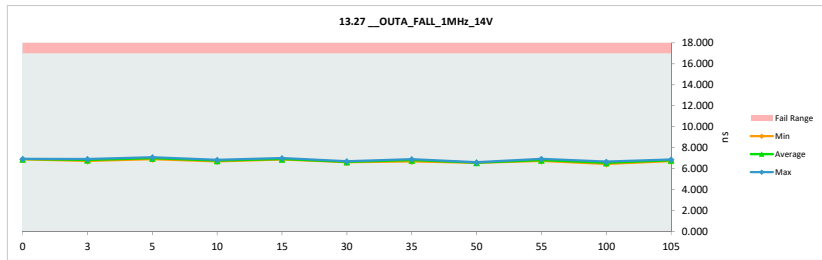


HDR TID Report
TPS7H5002-SP

13.27_OUTA_FALL_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	6.982	6.934	-0.048
0	190	6.911	6.881	-0.030
0	191	6.963	6.913	-0.050
3	300	6.850	6.725	-0.125
3	305	6.856	6.804	-0.052
3	309	6.944	6.858	-0.086
3	311	6.944	6.922	-0.022
3	313	6.863	6.835	-0.028
5	319	7.049	7.019	-0.030
5	321	6.869	6.902	0.033
5	324	7.063	7.082	0.019
5	327	7.059	7.053	-0.006
5	331	6.937	6.916	-0.021
10	234	6.902	6.741	-0.161
10	235	6.962	6.738	-0.224
10	236	6.922	6.698	-0.224
10	237	7.006	6.841	-0.165
10	238	7.045	6.782	-0.263
15	239	6.959	6.879	-0.080
15	240	6.952	6.890	-0.062
15	241	7.077	7.001	-0.076
15	243	6.969	6.875	-0.094
15	245	6.977	6.904	-0.073
30	222	7.001	6.657	-0.344
30	223	6.993	6.707	-0.286
30	225	6.907	6.631	-0.276
30	226	6.922	6.607	-0.315
30	227	6.961	6.612	-0.349
35	228	7.037	6.844	-0.193
35	229	6.903	6.692	-0.211
35	231	6.916	6.790	-0.126
35	232	6.926	6.784	-0.142
35	233	7.035	6.898	-0.137
50	210	7.006	6.561	-0.445
50	211	6.988	6.567	-0.421
50	212	6.905	6.551	-0.354
50	213	7.000	6.604	-0.396
50	214	6.918	6.549	-0.369
55	215	7.060	6.936	-0.124
55	216	6.966	6.753	-0.213
55	217	6.984	6.831	-0.153
55	218	6.917	6.728	-0.189
55	220	6.980	6.812	-0.177
100	247	7.149	6.643	-0.506
100	249	7.095	6.582	-0.513
100	250	6.956	6.535	-0.421
100	251	7.023	6.521	-0.502
100	253	6.996	6.575	-0.421
100	255	6.935	6.509	-0.426
100	256	6.923	6.511	-0.412
100	257	7.118	6.667	-0.451
100	258	7.039	6.557	-0.482
100	259	6.998	6.470	-0.528
100	260	6.909	6.576	-0.333
100	261	7.041	6.549	-0.492
100	262	7.088	6.586	-0.502
100	263	6.983	6.554	-0.429
100	264	6.967	6.449	-0.518
100	265	7.053	6.570	-0.483
100	266	6.977	6.563	-0.414
100	267	6.968	6.637	-0.331
100	268	7.006	6.549	-0.457
100	269	6.994	6.473	-0.521
100	270	7.057	6.589	-0.468
100	271	6.934	6.498	-0.436
100	273	6.892	6.540	-0.352
105	274	7.109	6.872	-0.237
105	275	6.996	6.791	-0.205
105	276	6.999	6.722	-0.277
105	277	6.978	6.791	-0.187
105	278	7.000	6.805	-0.195
Max		7.149	7.082	0.033
Average		6.980	6.718	-0.262
Min		6.850	6.449	-0.528
Std Dev		0.065	0.162	0.170

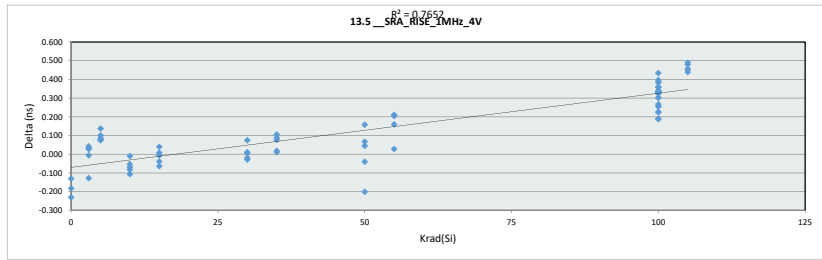


13.27_OUTA_FALL_1MHz_14V											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.881	6.725	6.902	6.688	6.875	6.607	6.692	6.549	6.728	6.449	6.722
Average	6.909	6.829	6.994	6.758	6.910	6.643	6.802	6.566	6.812	6.552	6.796
Max	6.934	6.922	7.082	6.841	7.001	6.707	6.898	6.604	6.936	6.667	6.872
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

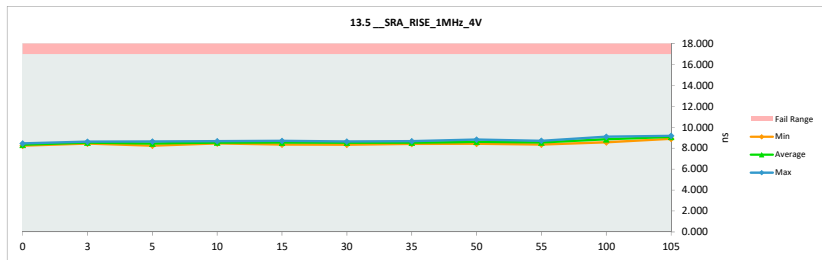


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13.5_SRA_RISE_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.637	8.455	-0.182
0	190	8.399	8.269	-0.130
0	191	8.476	8.246	-0.230
3	300	8.600	8.473	-0.127
3	305	8.447	8.442	-0.005
3	309	8.487	8.518	0.031
3	311	8.575	8.618	0.043
3	313	8.562	8.591	0.029
5	319	8.367	8.505	0.138
5	321	8.568	8.643	0.075
5	324	8.150	8.238	0.088
5	327	8.282	8.362	0.080
5	331	8.365	8.466	0.101
10	234	8.678	8.669	-0.009
10	235	8.678	8.572	-0.106
10	236	8.582	8.514	-0.068
10	237	8.554	8.501	-0.053
10	238	8.541	8.460	-0.081
15	239	8.653	8.693	0.040
15	240	8.561	8.570	0.009
15	241	8.386	8.348	-0.038
15	243	8.633	8.570	-0.063
15	245	8.390	8.385	-0.005
30	222	8.602	8.584	-0.018
30	223	8.367	8.339	-0.028
30	225	8.594	8.601	0.007
30	226	8.513	8.524	0.011
30	227	8.558	8.633	0.075
35	228	8.400	8.412	0.012
35	229	8.580	8.669	0.089
35	231	8.491	8.569	0.078
35	232	8.557	8.576	0.019
35	233	8.312	8.418	0.106
50	210	8.621	8.421	-0.200
50	211	8.571	8.532	-0.039
50	212	8.598	8.643	0.045
50	213	8.663	8.821	0.158
50	214	8.569	8.637	0.068
55	215	8.322	8.350	0.028
55	216	8.316	8.525	0.209
55	217	8.374	8.534	0.160
55	218	8.422	8.627	0.205
55	220	8.496	8.707	0.211
100	247	8.448	8.639	0.191
100	249	8.476	8.744	0.268
100	250	8.381	8.637	0.256
100	251	8.573	8.798	0.225
100	253	8.576	8.832	0.256
100	255	8.553	8.780	0.227
100	256	8.708	9.067	0.359
100	257	8.260	8.564	0.304
100	258	8.385	8.710	0.325
100	259	8.610	8.947	0.337
100	260	8.475	8.910	0.435
100	261	8.640	9.037	0.397
100	262	8.335	8.720	0.385
100	263	8.357	8.741	0.384
100	264	8.671	8.973	0.302
100	265	8.748	8.936	0.188
100	266	8.752	9.090	0.338
100	267	8.428	8.788	0.360
100	268	8.604	8.940	0.336
100	269	8.530	8.871	0.341
100	270	8.673	9.002	0.329
100	271	8.644	8.972	0.328
100	273	8.619	8.946	0.327
105	274	8.517	8.998	0.481
105	275	8.726	9.183	0.457
105	276	8.622	9.112	0.490
105	277	8.724	9.177	0.453
105	278	8.457	8.897	0.440
Max		8.752	9.183	0.490
Average		8.521	8.665	0.144
Min		8.150	8.238	-0.230
Std Dev		0.131	0.234	0.186

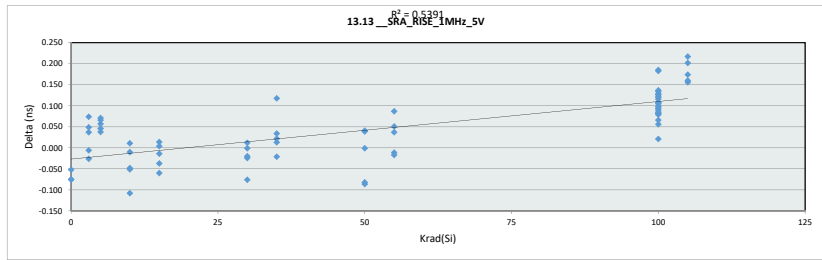


13.5_SRA_RISE											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.246	8.442	8.228	8.460	8.348	8.339	8.412	8.421	8.350	8.564	8.897
Average	8.323	8.528	8.443	8.543	8.513	8.536	8.529	8.611	8.549	8.854	9.073
Max	8.455	8.618	8.643	8.669	8.693	8.633	8.669	8.821	8.707	9.090	9.183
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

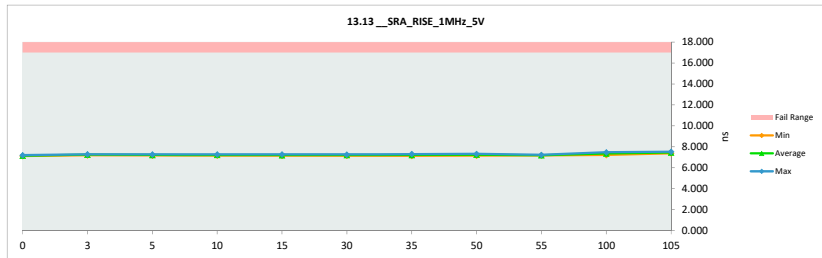


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13.13_SRA_RISE_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.278	7.203	-0.075
0	190	7.183	7.131	-0.052
0	191	7.201	7.126	-0.075
3	300	7.199	7.193	-0.006
3	305	7.229	7.278	0.049
3	309	7.246	7.283	0.037
3	311	7.147	7.221	0.074
3	313	7.312	7.286	-0.026
5	319	7.176	7.247	0.071
5	321	7.227	7.284	0.057
5	324	7.122	7.168	0.046
5	327	7.133	7.199	0.066
5	331	7.179	7.217	0.038
10	234	7.335	7.284	-0.051
10	235	7.228	7.218	-0.010
10	236	7.225	7.236	0.011
10	237	7.255	7.207	-0.048
10	238	7.286	7.178	-0.108
15	239	7.269	7.273	0.004
15	240	7.176	7.190	0.014
15	241	7.162	7.148	-0.014
15	243	7.298	7.238	-0.060
15	245	7.196	7.159	-0.037
30	222	7.281	7.205	-0.076
30	223	7.154	7.153	-0.001
30	225	7.284	7.264	-0.020
30	226	7.249	7.225	-0.024
30	227	7.244	7.256	0.012
35	228	7.103	7.137	0.034
35	229	7.234	7.213	-0.021
35	231	7.198	7.316	0.118
35	232	7.231	7.253	0.022
35	233	7.161	7.174	0.013
50	210	7.290	7.204	-0.086
50	211	7.239	7.157	-0.082
50	212	7.256	7.295	0.039
50	213	7.290	7.331	0.041
50	214	7.250	7.249	-0.001
55	215	7.108	7.195	0.087
55	216	7.146	7.183	0.037
55	217	7.147	7.198	0.051
55	218	7.192	7.181	-0.011
55	220	7.254	7.237	-0.017
100	247	7.216	7.237	0.021
100	249	7.176	7.361	0.185
100	250	7.183	7.288	0.105
100	251	7.292	7.374	0.082
100	253	7.257	7.391	0.134
100	255	7.261	7.317	0.056
100	256	7.320	7.457	0.137
100	257	7.129	7.219	0.090
100	258	7.236	7.315	0.079
100	259	7.323	7.446	0.123
100	260	7.248	7.346	0.098
100	261	7.300	7.394	0.094
100	262	7.166	7.282	0.116
100	263	7.210	7.295	0.085
100	264	7.286	7.469	0.183
100	265	7.349	7.432	0.083
100	266	7.348	7.446	0.098
100	267	7.208	7.315	0.107
100	268	7.276	7.384	0.108
100	269	7.261	7.389	0.128
100	270	7.352	7.480	0.128
100	271	7.347	7.413	0.066
100	273	7.287	7.407	0.120
105	274	7.240	7.414	0.174
105	275	7.319	7.536	0.217
105	276	7.328	7.488	0.160
105	277	7.324	7.480	0.156
105	278	7.186	7.388	0.202
	Max	7.352	7.536	0.217
	Average	7.237	7.284	0.047
	Min	7.103	7.126	-0.108
	Std Dev	0.064	0.103	0.077

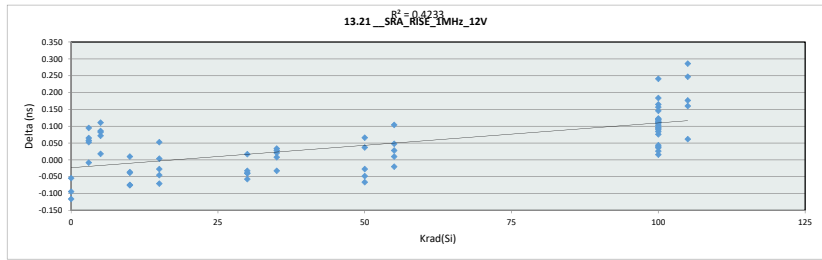


13.13_SRA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.126	7.193	7.168	7.178	7.148	7.153	7.137	7.157	7.181	7.219	7.388
Average	7.153	7.252	7.223	7.225	7.202	7.221	7.219	7.247	7.199	7.368	7.461
Max	7.203	7.286	7.284	7.284	7.273	7.264	7.316	7.331	7.237	7.480	7.536
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

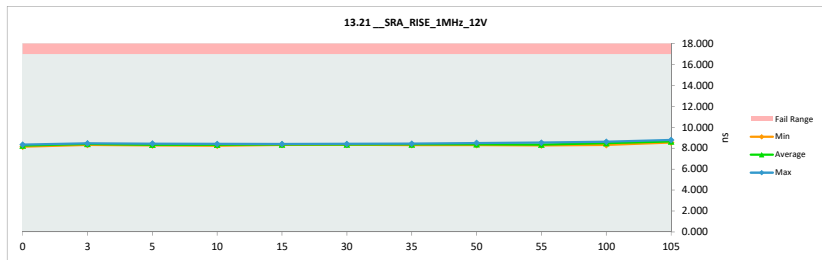


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13.21_SRA_RISE_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.459	8.343	-0.116
0	190	8.205	8.151	-0.054
0	191	8.345	8.251	-0.094
3	300	8.304	8.399	0.095
3	305	8.253	8.318	0.065
3	309	8.434	8.426	-0.008
3	311	8.350	8.403	0.053
3	313	8.411	8.469	0.058
5	319	8.380	8.452	0.072
5	321	8.275	8.293	0.018
5	324	8.239	8.325	0.086
5	327	8.262	8.345	0.083
5	331	8.153	8.264	0.111
10	234	8.355	8.317	-0.038
10	235	8.318	8.282	-0.036
10	236	8.482	8.408	-0.074
10	237	8.395	8.395	0.010
10	238	8.321	8.246	-0.075
15	239	8.471	8.401	-0.070
15	240	8.367	8.322	-0.045
15	241	8.391	8.395	0.004
15	243	8.315	8.368	0.053
15	245	8.338	8.311	-0.027
30	222	8.442	8.403	-0.039
30	223	8.409	8.352	-0.057
30	225	8.356	8.324	-0.032
30	226	8.355	8.372	0.017
30	227	8.394	8.354	-0.040
35	228	8.403	8.437	0.034
35	229	8.341	8.362	0.021
35	231	8.354	8.382	0.028
35	232	8.299	8.307	0.008
35	233	8.392	8.360	-0.032
50	210	8.402	8.354	-0.048
50	211	8.434	8.407	-0.027
50	212	8.362	8.296	-0.066
50	213	8.481	8.518	0.037
50	214	8.240	8.306	0.066
55	215	8.257	8.267	0.010
55	216	8.330	8.310	-0.020
55	217	8.273	8.301	0.028
55	218	8.271	8.319	0.048
55	220	8.428	8.533	0.104
100	247	8.283	8.309	0.026
100	249	8.348	8.494	0.146
100	250	8.415	8.454	0.039
100	251	8.472	8.629	0.157
100	253	8.410	8.500	0.090
100	255	8.417	8.514	0.097
100	256	8.341	8.460	0.119
100	257	8.370	8.406	0.036
100	258	8.330	8.432	0.102
100	259	8.378	8.562	0.184
100	260	8.338	8.449	0.111
100	261	8.352	8.436	0.084
100	262	8.323	8.435	0.112
100	263	8.351	8.474	0.123
100	264	8.328	8.372	0.044
100	265	8.443	8.564	0.121
100	266	8.443	8.608	0.165
100	267	8.276	8.517	0.241
100	268	8.483	8.559	0.076
100	269	8.366	8.481	0.115
100	270	8.574	8.590	0.016
100	271	8.416	8.510	0.094
100	273	8.374	8.481	0.107
105	274	8.457	8.634	0.177
105	275	8.537	8.784	0.247
105	276	8.492	8.652	0.160
105	277	8.580	8.642	0.062
105	278	8.263	8.549	0.286
Max		8.580	8.784	0.286
Average		8.369	8.418	0.049
Min		8.153	8.151	-0.116
Std Dev		0.083	0.117	0.084

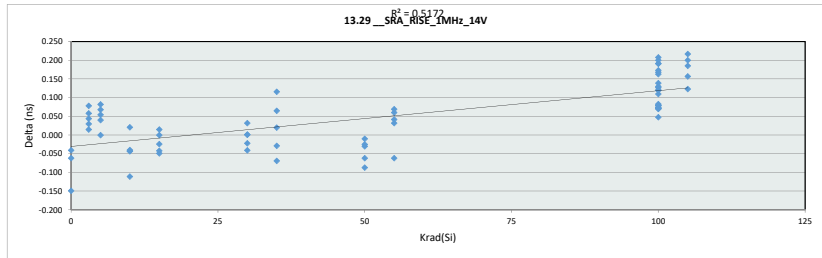


13.21_SRA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.151	8.318	8.264	8.246	8.311	8.324	8.307	8.296	8.267	8.309	8.549
Average	8.248	8.403	8.336	8.330	8.359	8.361	8.370	8.376	8.346	8.489	8.652
Max	8.343	8.469	8.452	8.408	8.401	8.403	8.437	8.518	8.533	8.629	8.784
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

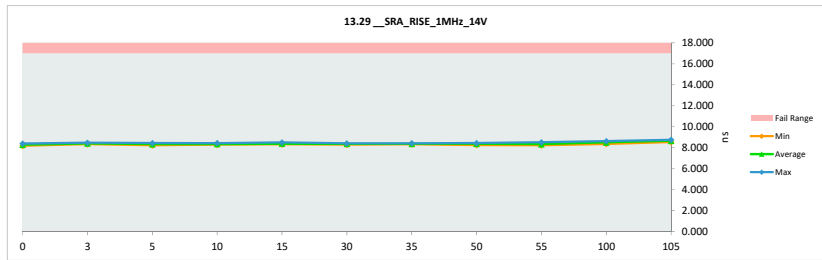


HDR TID Report
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13.29 _SRA_RISE_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	8.452	8.390	-0.062
0	190	8.219	8.178	-0.041
0	191	8.394	8.245	-0.149
3	300	8.336	8.366	0.030
3	305	8.270	8.328	0.058
3	309	8.395	8.439	0.044
3	311	8.384	8.399	0.015
3	313	8.385	8.463	0.078
5	319	8.378	8.432	0.054
5	321	8.232	8.314	0.082
5	324	8.264	8.332	0.068
5	327	8.298	8.338	0.040
5	331	8.221	8.221	0.000
10	234	8.322	8.279	-0.043
10	235	8.322	8.279	-0.043
10	236	8.539	8.428	-0.111
10	237	8.402	8.362	-0.040
10	238	8.298	8.319	0.021
15	239	8.518	8.494	-0.024
15	240	8.340	8.340	0.000
15	241	8.429	8.380	-0.049
15	243	8.352	8.310	-0.042
15	245	8.294	8.309	0.015
30	222	8.399	8.400	0.001
30	223	8.353	8.354	0.001
30	225	8.323	8.282	-0.041
30	226	8.323	8.355	0.032
30	227	8.376	8.354	-0.022
35	228	8.344	8.409	0.065
35	229	8.259	8.375	0.116
35	231	8.336	8.356	0.020
35	232	8.379	8.310	-0.069
35	233	8.378	8.349	-0.029
50	210	8.434	8.404	-0.030
50	211	8.441	8.431	-0.010
50	212	8.387	8.300	-0.087
50	213	8.470	8.408	-0.062
50	214	8.267	8.242	-0.025
55	215	8.286	8.224	-0.062
55	216	8.266	8.326	0.060
55	217	8.235	8.267	0.032
55	218	8.262	8.304	0.042
55	220	8.446	8.515	0.069
100	247	8.305	8.353	0.048
100	249	8.384	8.462	0.078
100	250	8.362	8.482	0.120
100	251	8.420	8.588	0.168
100	253	8.407	8.517	0.110
100	255	8.431	8.504	0.073
100	256	8.317	8.400	0.083
100	257	8.340	8.413	0.073
100	258	8.338	8.417	0.079
100	259	8.415	8.542	0.127
100	260	8.334	8.525	0.191
100	261	8.369	8.449	0.080
100	262	8.253	8.383	0.130
100	263	8.352	8.560	0.208
100	264	8.278	8.469	0.191
100	265	8.497	8.568	0.071
100	266	8.480	8.600	0.120
100	267	8.395	8.558	0.163
100	268	8.513	8.583	0.070
100	269	8.361	8.432	0.071
100	270	8.442	8.615	0.173
100	271	8.364	8.564	0.200
100	273	8.375	8.514	0.139
105	274	8.481	8.638	0.157
105	275	8.539	8.746	0.217
105	276	8.496	8.696	0.200
105	277	8.522	8.645	0.123
105	278	8.347	8.532	0.185
	Max	8.539	8.746	0.217
	Average	8.368	8.418	0.050
	Min	8.219	8.178	-0.149
	Std Dev	0.086	0.121	0.085

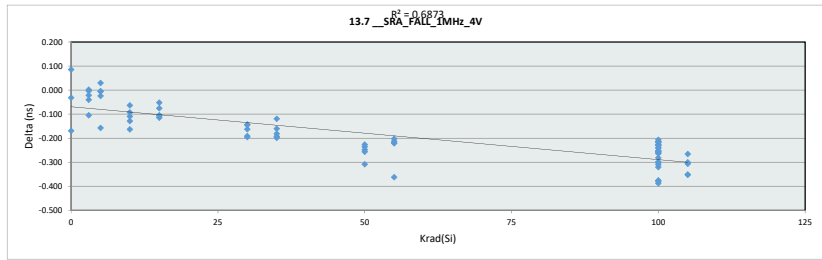


13.29 _SRA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.178	8.328	8.221	8.279	8.309	8.282	8.310	8.242	8.224	8.353	8.532
Average	8.271	8.399	8.327	8.333	8.367	8.349	8.360	8.357	8.327	8.500	8.651
Max	8.390	8.463	8.432	8.428	8.494	8.400	8.409	8.431	8.515	8.615	8.746
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

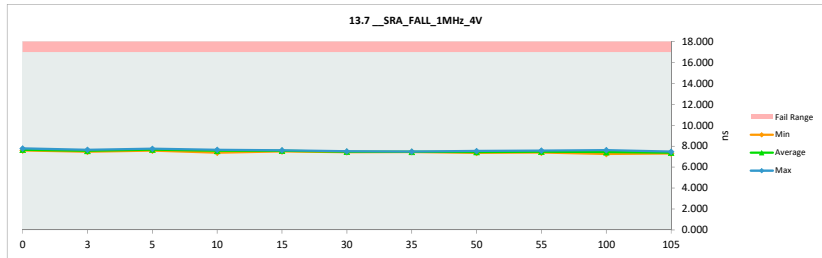


HDR TID Report
TPS7H5002-SP

13.7_SRA_FALL_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.640	7.609	-0.031
0	190	7.697	7.783	0.086
0	191	7.765	7.596	-0.169
3	300	7.580	7.476	-0.104
3	305	7.632	7.592	-0.040
3	309	7.576	7.573	-0.003
3	311	7.679	7.658	-0.021
3	313	7.488	7.490	0.002
5	319	7.734	7.577	-0.157
5	321	7.722	7.698	-0.024
5	324	7.761	7.756	-0.005
5	327	7.733	7.729	-0.004
5	331	7.594	7.624	0.030
10	234	7.695	7.632	-0.063
10	235	7.668	7.559	-0.109
10	236	7.547	7.384	-0.163
10	237	7.748	7.656	-0.092
10	238	7.692	7.564	-0.128
15	239	7.670	7.618	-0.052
15	240	7.613	7.509	-0.104
15	241	7.674	7.599	-0.075
15	243	7.658	7.544	-0.114
15	245	7.672	7.566	-0.106
30	222	7.674	7.512	-0.162
30	223	7.595	7.452	-0.143
30	225	7.623	7.428	-0.195
30	226	7.688	7.497	-0.191
30	227	7.607	7.463	-0.144
35	228	7.660	7.462	-0.198
35	229	7.611	7.492	-0.119
35	231	7.605	7.445	-0.160
35	232	7.648	7.467	-0.181
35	233	7.666	7.474	-0.192
50	210	7.686	7.378	-0.308
50	211	7.609	7.362	-0.247
50	212	7.777	7.542	-0.235
50	213	7.687	7.431	-0.256
50	214	7.710	7.484	-0.226
55	215	7.948	7.586	-0.362
55	216	7.612	7.392	-0.220
55	217	7.768	7.555	-0.213
55	218	7.651	7.431	-0.220
55	220	7.595	7.393	-0.202
100	247	7.998	7.623	-0.375
100	249	7.868	7.481	-0.387
100	250	7.639	7.260	-0.379
100	251	7.582	7.331	-0.251
100	253	7.644	7.430	-0.214
100	255	7.659	7.432	-0.227
100	256	7.646	7.431	-0.215
100	257	7.674	7.418	-0.256
100	258	7.671	7.441	-0.230
100	259	7.679	7.426	-0.253
100	260	7.581	7.363	-0.218
100	261	7.819	7.518	-0.301
100	262	7.817	7.611	-0.206
100	263	7.651	7.370	-0.281
100	264	7.672	7.433	-0.239
100	265	7.734	7.494	-0.240
100	266	7.746	7.437	-0.309
100	267	7.792	7.529	-0.263
100	268	7.655	7.441	-0.214
100	269	7.689	7.429	-0.260
100	270	7.680	7.360	-0.320
100	271	7.615	7.388	-0.227
100	273	7.685	7.387	-0.298
105	274	7.789	7.483	-0.306
105	275	7.690	7.339	-0.351
105	276	7.705	7.354	-0.351
105	277	7.573	7.308	-0.265
105	278	7.750	7.450	-0.300
Max		7.998	7.783	0.086
Average		7.681	7.493	-0.188
Min		7.488	7.260	-0.387
Std Dev		0.085	0.109	0.109

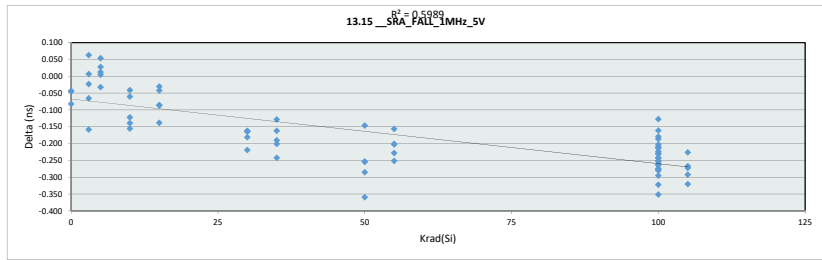


13.7_SRA_FALL											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.596	7.476	7.577	7.384	7.509	7.428	7.445	7.362	7.392	7.260	7.308
Average	7.663	7.558	7.677	7.559	7.567	7.470	7.468	7.439	7.471	7.436	7.387
Max	7.783	7.658	7.756	7.656	7.618	7.512	7.492	7.542	7.586	7.623	7.483
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

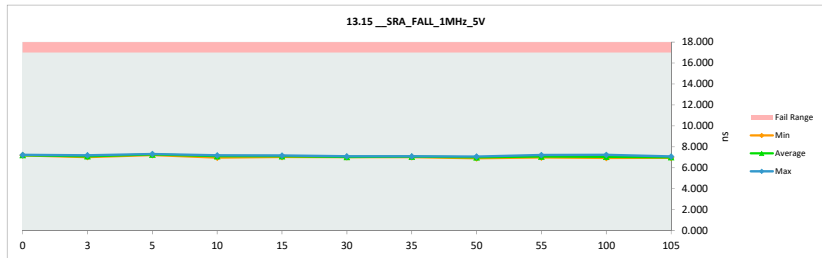


HDR TID Report
TPS7H5002-SP

13.15_SRA_FALL_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.240	7.195	-0.045
0	190	7.248	7.205	-0.043
0	191	7.316	7.234	-0.082
3	300	7.184	7.026	-0.158
3	305	7.180	7.157	-0.023
3	309	7.159	7.094	-0.065
3	311	7.134	7.197	0.063
3	313	7.034	7.041	0.007
5	319	7.223	7.228	0.005
5	321	7.226	7.280	0.054
5	324	7.348	7.316	-0.032
5	327	7.293	7.306	0.013
5	331	7.189	7.217	0.028
10	234	7.227	7.105	-0.122
10	235	7.174	7.133	-0.041
10	236	7.117	6.978	-0.139
10	237	7.334	7.179	-0.155
10	238	7.246	7.186	-0.060
15	239	7.221	7.135	-0.086
15	240	7.065	7.035	-0.030
15	241	7.267	7.129	-0.138
15	243	7.212	7.170	-0.042
15	245	7.193	7.108	-0.085
30	222	7.243	7.024	-0.219
30	223	7.198	7.017	-0.181
30	225	7.189	7.026	-0.163
30	226	7.259	7.095	-0.164
30	227	7.194	7.032	-0.162
35	228	7.333	7.091	-0.242
35	229	7.239	7.077	-0.162
35	231	7.223	7.022	-0.201
35	232	7.186	7.058	-0.128
35	233	7.290	7.100	-0.190
50	210	7.272	6.913	-0.359
50	211	7.191	6.906	-0.285
50	212	7.329	7.075	-0.254
50	213	7.214	7.068	-0.146
50	214	7.294	7.041	-0.253
55	215	7.466	7.215	-0.251
55	216	7.192	6.990	-0.202
55	217	7.315	7.114	-0.201
55	218	7.223	7.067	-0.156
55	220	7.231	7.003	-0.228
100	247	7.583	7.232	-0.351
100	249	7.408	7.131	-0.277
100	250	7.246	6.972	-0.274
100	251	7.176	6.945	-0.231
100	253	7.227	6.984	-0.243
100	255	7.259	6.995	-0.264
100	256	7.209	7.082	-0.127
100	257	7.249	6.998	-0.251
100	258	7.326	7.031	-0.295
100	259	7.262	7.049	-0.213
100	260	7.176	6.974	-0.202
100	261	7.386	7.174	-0.212
100	262	7.449	7.169	-0.280
100	263	7.228	6.986	-0.242
100	264	7.303	7.050	-0.253
100	265	7.304	7.043	-0.261
100	266	7.220	7.034	-0.186
100	267	7.326	7.117	-0.209
100	268	7.266	7.043	-0.223
100	269	7.287	7.059	-0.228
100	270	7.204	7.025	-0.179
100	271	7.266	6.944	-0.322
100	273	7.221	7.060	-0.161
105	274	7.370	7.078	-0.292
105	275	7.227	7.001	-0.226
105	276	7.287	6.967	-0.320
105	277	7.249	6.977	-0.272
105	278	7.300	7.033	-0.267
	Max	7.583	7.316	0.063
	Average	7.252	7.081	-0.172
	Min	7.034	6.906	-0.359
	Std Dev	0.085	0.093	0.102

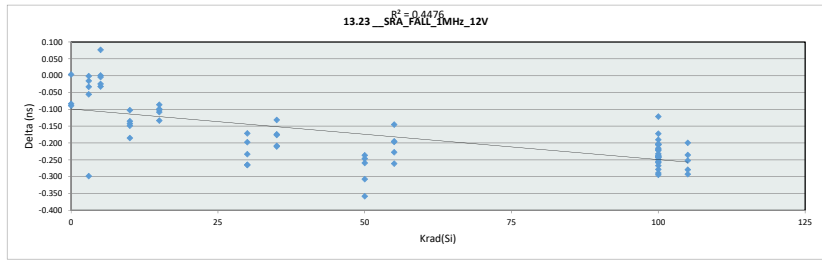


13.15_SRA_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.195	7.026	7.217	6.978	7.035	7.017	7.022	6.906	6.990	6.944	6.967
Average	7.211	7.103	7.269	7.116	7.115	7.039	7.070	7.001	7.078	7.048	7.011
Max	7.234	7.197	7.316	7.186	7.170	7.095	7.100	7.075	7.215	7.232	7.078
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

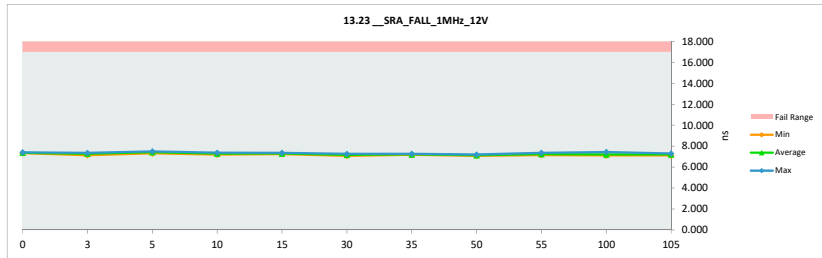


HDR TID Report TPS7H5002-SP

13.23_SRA_FALL_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.440	7.351	-0.089
0	190	7.416	7.333	-0.083
0	191	7.412	7.416	0.004
3	300	7.434	7.136	-0.298
3	305	7.354	7.299	-0.055
3	309	7.333	7.318	-0.015
3	311	7.360	7.359	-0.001
3	313	7.279	7.246	-0.033
5	319	7.327	7.404	0.077
5	321	7.437	7.413	-0.024
5	324	7.501	7.502	0.001
5	327	7.496	7.492	-0.004
5	331	7.347	7.315	-0.032
10	234	7.419	7.277	-0.142
10	235	7.485	7.300	-0.185
10	236	7.336	7.201	-0.135
10	237	7.477	7.375	-0.102
10	238	7.429	7.281	-0.148
15	239	7.374	7.288	-0.086
15	240	7.382	7.249	-0.133
15	241	7.463	7.362	-0.101
15	243	7.399	7.291	-0.108
15	245	7.387	7.288	-0.099
30	222	7.426	7.193	-0.233
30	223	7.399	7.135	-0.264
30	225	7.271	7.074	-0.197
30	226	7.441	7.270	-0.171
30	227	7.423	7.158	-0.265
35	228	7.448	7.274	-0.174
35	229	7.383	7.207	-0.176
35	231	7.382	7.174	-0.208
35	232	7.394	7.184	-0.210
35	233	7.401	7.270	-0.131
50	210	7.447	7.089	-0.358
50	211	7.388	7.081	-0.307
50	212	7.440	7.204	-0.236
50	213	7.418	7.172	-0.246
50	214	7.397	7.138	-0.259
55	215	7.596	7.369	-0.227
55	216	7.411	7.150	-0.261
55	217	7.448	7.303	-0.145
55	218	7.407	7.212	-0.195
55	220	7.403	7.207	-0.196
100	247	7.685	7.445	-0.240
100	249	7.525	7.320	-0.205
100	250	7.375	7.117	-0.258
100	251	7.396	7.153	-0.243
100	253	7.418	7.129	-0.289
100	255	7.446	7.152	-0.294
100	256	7.408	7.186	-0.222
100	257	7.431	7.259	-0.172
100	258	7.445	7.209	-0.236
100	259	7.422	7.167	-0.255
100	260	7.351	7.148	-0.203
100	261	7.503	7.271	-0.232
100	262	7.516	7.326	-0.190
100	263	7.396	7.177	-0.219
100	264	7.441	7.192	-0.249
100	265	7.464	7.197	-0.267
100	266	7.472	7.194	-0.278
100	267	7.486	7.271	-0.215
100	268	7.421	7.216	-0.205
100	269	7.410	7.167	-0.243
100	270	7.350	7.147	-0.203
100	271	7.389	7.150	-0.239
100	273	7.344	7.223	-0.121
105	274	7.533	7.281	-0.252
105	275	7.411	7.212	-0.199
105	276	7.462	7.170	-0.292
105	277	7.349	7.114	-0.235
105	278	7.586	7.307	-0.279
	Max	7.685	7.502	0.077
	Average	7.423	7.243	-0.180
	Min	7.271	7.074	-0.358
	Std Dev	0.068	0.097	0.092

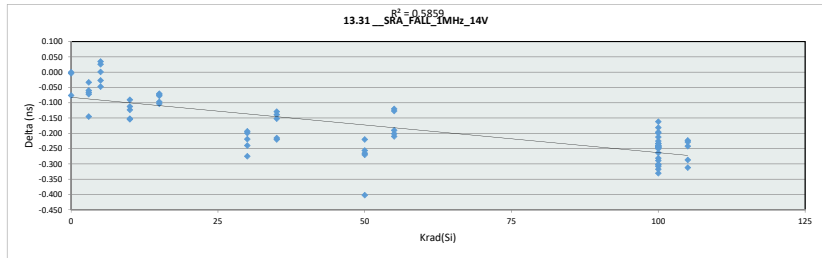


13.23_SRA_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.333	7.136	7.315	7.201	7.249	7.074	7.174	7.081	7.150	7.117	7.114
Average	7.367	7.272	7.425	7.287	7.296	7.166	7.222	7.137	7.248	7.209	7.217
Max	7.416	7.359	7.502	7.375	7.362	7.270	7.274	7.204	7.369	7.445	7.307
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

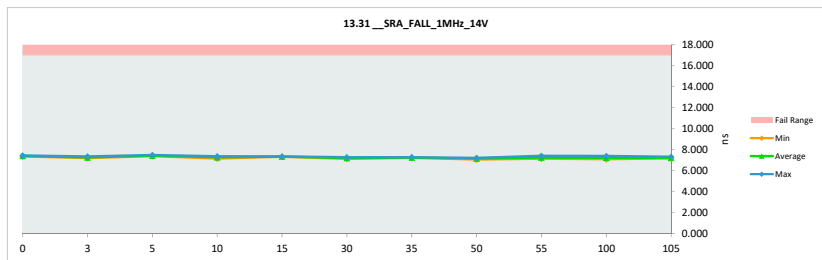


HDR TID Report TPS7H5002-SP

13.31_SRA_FALL_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	189	7.432	7.356	-0.076
0	190	7.374	7.374	0.000
0	191	7.443	7.439	-0.004
3	300	7.338	7.193	-0.145
3	305	7.352	7.280	-0.072
3	309	7.346	7.280	-0.066
3	311	7.376	7.343	-0.033
3	313	7.274	7.214	-0.060
5	319	7.433	7.386	-0.047
5	321	7.396	7.422	0.026
5	324	7.491	7.492	0.001
5	327	7.466	7.439	-0.027
5	331	7.340	7.375	0.035
10	234	7.383	7.293	-0.090
10	235	7.441	7.287	-0.154
10	236	7.266	7.154	-0.112
10	237	7.484	7.361	-0.123
10	238	7.433	7.281	-0.152
15	239	7.441	7.338	-0.103
15	240	7.416	7.319	-0.097
15	241	7.421	7.351	-0.070
15	243	7.393	7.319	-0.074
15	245	7.388	7.311	-0.077
30	222	7.471	7.272	-0.199
30	223	7.417	7.142	-0.275
30	225	7.346	7.127	-0.219
30	226	7.460	7.267	-0.193
30	227	7.417	7.177	-0.240
35	228	7.434	7.214	-0.220
35	229	7.397	7.245	-0.152
35	231	7.366	7.227	-0.139
35	232	7.353	7.224	-0.129
35	233	7.495	7.280	-0.215
50	210	7.461	7.059	-0.402
50	211	7.399	7.134	-0.265
50	212	7.432	7.212	-0.220
50	213	7.451	7.195	-0.256
50	214	7.412	7.142	-0.270
55	215	7.526	7.406	-0.120
55	216	7.341	7.131	-0.210
55	217	7.424	7.297	-0.127
55	218	7.398	7.208	-0.190
55	220	7.396	7.195	-0.201
100	247	7.718	7.388	-0.330
100	249	7.620	7.303	-0.317
100	250	7.391	7.150	-0.241
100	251	7.432	7.131	-0.301
100	253	7.409	7.163	-0.246
100	255	7.427	7.185	-0.242
100	256	7.427	7.183	-0.244
100	257	7.459	7.263	-0.196
100	258	7.423	7.183	-0.240
100	259	7.406	7.181	-0.225
100	260	7.401	7.093	-0.308
100	261	7.501	7.251	-0.250
100	262	7.655	7.352	-0.303
100	263	7.395	7.149	-0.246
100	264	7.395	7.196	-0.199
100	265	7.432	7.199	-0.233
100	266	7.439	7.227	-0.212
100	267	7.547	7.283	-0.264
100	268	7.463	7.182	-0.281
100	269	7.433	7.199	-0.234
100	270	7.468	7.179	-0.289
100	271	7.319	7.157	-0.162
100	273	7.362	7.181	-0.181
105	274	7.542	7.314	-0.228
105	275	7.406	7.183	-0.223
105	276	7.457	7.170	-0.287
105	277	7.403	7.161	-0.242
105	278	7.531	7.219	-0.312
	Max	7.718	7.492	0.035
	Average	7.428	7.248	-0.180
	Min	7.266	7.059	-0.402
	Std Dev	0.075	0.094	0.097

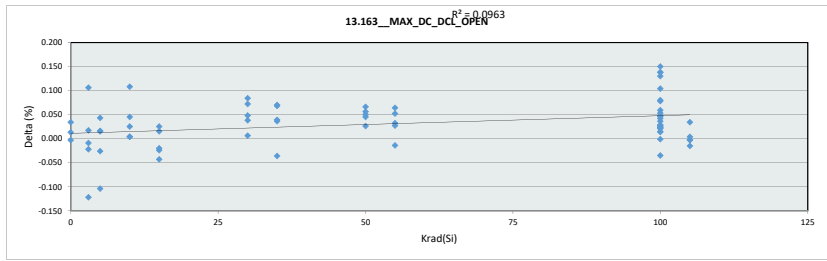


13.31_SRA_FALL											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.356	7.193	7.375	7.154	7.311	7.127	7.214	7.059	7.131	7.093	7.161
Average	7.390	7.262	7.423	7.275	7.328	7.197	7.238	7.148	7.247	7.208	7.209
Max	7.439	7.343	7.492	7.361	7.351	7.272	7.280	7.212	7.406	7.388	7.314
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

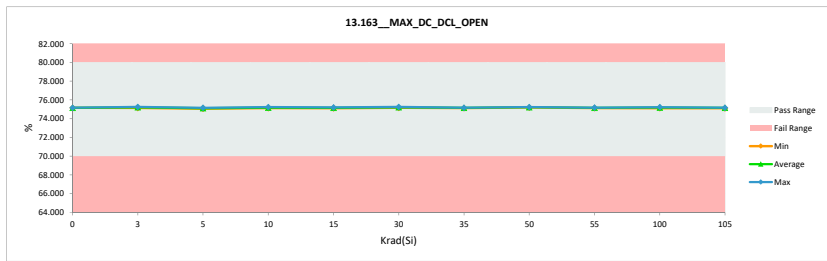


HDR TID Report
TPS7H5002-SP

13.163_MAX_DC_DCL_OPEN				
Test Site				
Tester				
Test Number				
Unit		%	%	
Max Limit		80	80	
Min Limit		70	70	
Krad(Si)	Serial #	PRE	POST	Delta
0	189	75.149	75.146	-0.003
0	190	75.158	75.171	0.013
0	191	75.122	75.156	0.034
3	300	75.245	75.236	-0.009
3	305	75.233	75.250	0.017
3	309	75.142	75.248	0.106
3	311	75.255	75.133	-0.122
3	313	75.216	75.194	-0.022
5	319	75.183	75.079	-0.104
5	321	75.161	75.135	-0.026
5	324	75.023	75.066	0.043
5	327	75.118	75.134	0.016
5	331	75.148	75.163	0.015
10	234	75.167	75.171	0.004
10	235	75.135	75.160	0.025
10	236	75.189	75.234	0.045
10	237	75.086	75.194	0.108
10	238	75.114	75.118	0.004
15	239	75.149	75.129	-0.020
15	240	75.181	75.138	-0.043
15	241	75.083	75.098	0.015
15	243	75.180	75.156	-0.024
15	245	75.185	75.210	0.025
30	222	75.204	75.252	0.048
30	223	75.177	75.249	0.072
30	225	75.166	75.204	0.038
30	226	75.144	75.150	0.006
30	227	75.161	75.245	0.084
35	228	75.070	75.138	0.068
35	229	75.105	75.175	0.070
35	231	75.208	75.172	-0.036
35	232	75.122	75.161	0.039
35	233	75.077	75.113	0.036
50	210	75.191	75.241	0.050
50	211	75.141	75.207	0.066
50	212	75.157	75.213	0.056
50	213	75.185	75.230	0.045
50	214	75.163	75.189	0.026
55	215	75.096	75.123	0.027
55	216	75.117	75.149	0.032
55	217	75.114	75.178	0.064
55	218	75.183	75.169	-0.014
55	220	75.083	75.135	0.052
100	247	75.026	75.164	0.138
100	249	75.086	75.122	0.036
100	250	75.171	75.170	-0.001
100	251	75.121	75.225	0.104
100	253	75.089	75.227	0.138
100	255	75.131	75.190	0.059
100	256	75.163	75.191	0.028
100	257	75.157	75.171	0.014
100	258	75.079	75.229	0.150
100	259	75.138	75.180	0.042
100	260	75.137	75.215	0.078
100	261	75.139	75.219	0.080
100	262	75.084	75.109	0.025
100	263	75.121	75.149	0.028
100	264	75.163	75.211	0.048
100	265	75.147	75.168	0.021
100	266	75.163	75.128	-0.035
100	267	75.084	75.214	0.130
100	268	75.203	75.226	0.023
100	269	75.137	75.190	0.053
100	270	75.148	75.162	0.014
100	271	75.163	75.185	0.022
100	273	75.161	75.207	0.046
105	274	75.130	75.164	0.034
105	275	75.100	75.104	0.004
105	276	75.162	75.159	-0.003
105	277	75.181	75.166	-0.015
105	278	75.171	75.169	-0.002
	Max	75.255	75.252	0.150
	Average	75.144	75.175	0.031
	Min	75.023	75.066	-0.122
	Std Dev	0.046	0.044	0.049



13.163_MAX_DC											
Test Site											
Tester											
Test Number											
Max Limit		80	%								
Min Limit		70	%								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000
Min	75.146	75.133	75.066	75.118	75.098	75.150	75.113	75.189	75.123	75.109	75.104
Average	75.158	75.212	75.115	75.175	75.146	75.220	75.152	75.216	75.151	75.185	75.152
Max	75.171	75.250	75.163	75.234	75.210	75.252	75.175	75.241	75.178	75.229	75.169
UL	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000

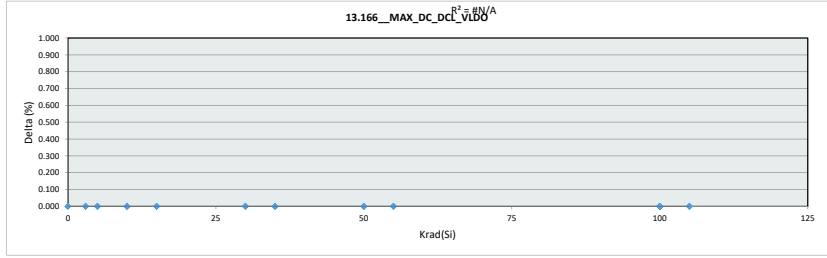


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13.166_MAX_DC_DCL_VLDO

Test Site	
Tester	
Test Number	
Unit	% %
Max Limit	100 100
Min Limit	

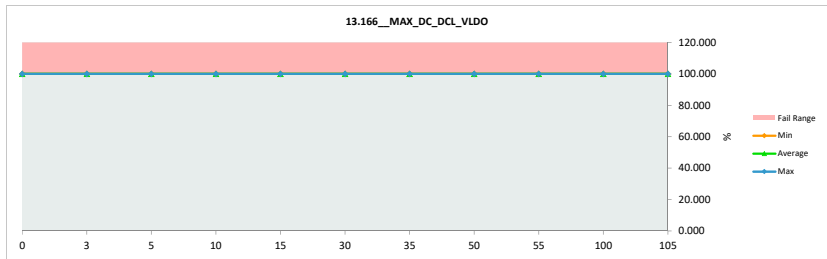
Krad(Si)	Serial #	PRE	POST	Delta
0	189	100.000	100.000	0.000
0	190	100.000	100.000	0.000
0	191	100.000	100.000	0.000
3	300	100.000	100.000	0.000
3	305	100.000	100.000	0.000
3	309	100.000	100.000	0.000
3	311	100.000	100.000	0.000
3	313	100.000	100.000	0.000
5	319	100.000	100.000	0.000
5	321	100.000	100.000	0.000
5	324	100.000	100.000	0.000
5	327	100.000	100.000	0.000
5	331	100.000	100.000	0.000
10	234	100.000	100.000	0.000
10	235	100.000	100.000	0.000
10	236	100.000	100.000	0.000
10	237	100.000	100.000	0.000
10	238	100.000	100.000	0.000
15	239	100.000	100.000	0.000
15	240	100.000	100.000	0.000
15	241	100.000	100.000	0.000
15	243	100.000	100.000	0.000
15	245	100.000	100.000	0.000
30	222	100.000	100.000	0.000
30	223	100.000	100.000	0.000
30	225	100.000	100.000	0.000
30	226	100.000	100.000	0.000
30	227	100.000	100.000	0.000
35	228	100.000	100.000	0.000
35	229	100.000	100.000	0.000
35	231	100.000	100.000	0.000
35	232	100.000	100.000	0.000
35	233	100.000	100.000	0.000
50	210	100.000	100.000	0.000
50	211	100.000	100.000	0.000
50	212	100.000	100.000	0.000
50	213	100.000	100.000	0.000
50	214	100.000	100.000	0.000
55	215	100.000	100.000	0.000
55	216	100.000	100.000	0.000
55	217	100.000	100.000	0.000
55	218	100.000	100.000	0.000
55	220	100.000	100.000	0.000
100	247	100.000	100.000	0.000
100	249	100.000	100.000	0.000
100	250	100.000	100.000	0.000
100	251	100.000	100.000	0.000
100	253	100.000	100.000	0.000
100	255	100.000	100.000	0.000
100	256	100.000	100.000	0.000
100	257	100.000	100.000	0.000
100	258	100.000	100.000	0.000
100	259	100.000	100.000	0.000
100	260	100.000	100.000	0.000
100	261	100.000	100.000	0.000
100	262	100.000	100.000	0.000
100	263	100.000	100.000	0.000
100	264	100.000	100.000	0.000
100	265	100.000	100.000	0.000
100	266	100.000	100.000	0.000
100	267	100.000	100.000	0.000
100	268	100.000	100.000	0.000
100	269	100.000	100.000	0.000
100	270	100.000	100.000	0.000
100	271	100.000	100.000	0.000
100	273	100.000	100.000	0.000
105	274	100.000	100.000	0.000
105	275	100.000	100.000	0.000
105	276	100.000	100.000	0.000
105	277	100.000	100.000	0.000
105	278	100.000	100.000	0.000
	Max	100.000	100.000	0.000
	Average	100.000	100.000	0.000
	Min	100.000	100.000	0.000
	Std Dev	0.000	0.000	0.000



13.166_MAX_DC

Test Site	
Tester	
Test Number	
Max Limit	100 %
Min Limit	%

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Average	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Max	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
UL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000



C Appendix: LDR TID Report Data

Description of identifiers for LDR TID report

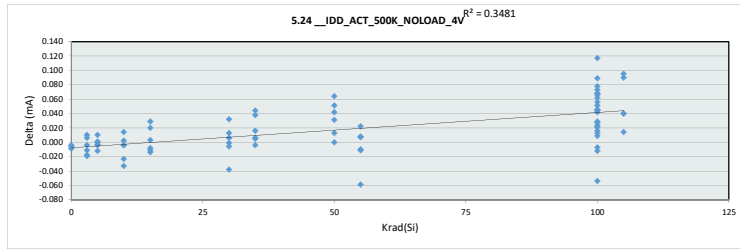
IDENTIFIER - Krad(Si)	DESCRIPTION
0	Control Unit
3	3 krad(Si) biased
5	3 krad(Si) unbiased
10	10 krad(Si) biased
15	10 krad(Si) unbiased
30	30 krad(Si) biased
35	30 krad(Si) unbiased
50	50 krad(Si) biased
55	50 krad(Si) unbiased
100	100 krad(Si) biased
105	100 krad(Si) unbiased

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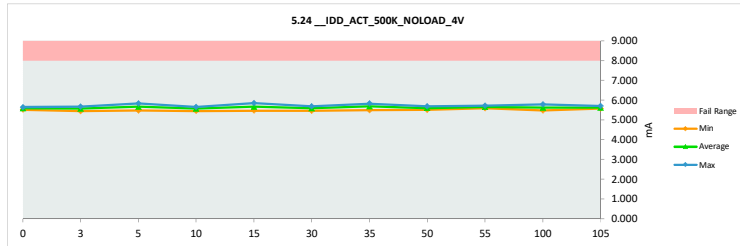
LDR TID Report
TPS7H5001-SP

5.24 IDD_ACT_500K_NOLOAD_4V			
Test Site			
Tester			
Test Number			
Unit	mA	mA	
Max Limit	8	8	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.527	5.521	-0.006
0	118	5.630	5.626	-0.004
0	122	5.659	5.650	-0.009
3	61	5.652	5.633	-0.019
3	62	5.685	5.681	-0.004
3	63	5.604	5.610	0.006
3	64	5.652	5.641	-0.011
3	65	5.460	5.442	-0.018
3	66	5.478	5.488	0.010
5	67	5.562	5.562	0.000
5	68	5.709	5.705	-0.004
5	69	5.487	5.488	0.001
5	70	5.834	5.844	0.010
5	71	5.678	5.666	-0.012
5	72	5.758	5.755	-0.003
10	73	5.652	5.619	-0.033
10	74	5.685	5.662	-0.023
10	75	5.604	5.618	0.014
10	76	5.652	5.648	-0.004
10	77	5.460	5.456	-0.004
10	78	5.478	5.480	0.002
15	79	5.562	5.591	0.029
15	80	5.709	5.701	-0.008
15	81	5.487	5.473	-0.014
15	82	5.834	5.854	0.020
15	83	5.678	5.667	-0.011
15	84	5.758	5.761	0.003
30	86	5.652	5.665	0.013
30	89	5.685	5.691	0.006
30	91	5.604	5.636	0.032
30	92	5.652	5.614	-0.038
30	94	5.460	5.459	-0.001
30	95	5.478	5.472	-0.006
35	100	5.562	5.606	0.044
35	101	5.709	5.747	0.038
35	102	5.487	5.493	0.006
35	103	5.834	5.830	-0.004
35	104	5.678	5.683	0.005
35	105	5.758	5.774	0.016
50	106	5.656	5.687	0.031
50	107	5.560	5.602	0.042
50	108	5.451	5.515	0.064
50	109	5.608	5.659	0.051
50	110	5.628	5.641	0.013
50	111	5.510	5.510	0.000
55	113	5.675	5.682	0.007
55	114	5.611	5.600	-0.011
55	115	5.662	5.684	0.022
55	116	5.619	5.627	0.008
55	126	5.741	5.731	-0.010
55	128	5.668	5.609	-0.059
100	129	5.669	5.747	0.078
100	131	5.550	5.496	-0.054
100	132	5.672	5.665	-0.007
100	133	5.670	5.714	0.044
100	134	5.559	5.604	0.045
100	135	5.598	5.586	-0.012
100	138	5.614	5.680	0.066
100	139	5.476	5.544	0.068
100	140	5.587	5.633	0.046
100	141	5.500	5.521	0.021
100	142	5.650	5.723	0.073
100	143	5.625	5.693	0.068
100	144	5.556	5.598	0.042
100	220	5.664	5.677	0.013
100	221	5.540	5.556	0.016
100	224	5.578	5.634	0.056
100	225	5.453	5.480	0.027
100	226	5.736	5.787	0.051
100	228	5.544	5.661	0.117
100	232	5.603	5.692	0.089
100	233	5.611	5.634	0.023
100	236	5.469	5.498	0.029
100	238	5.659	5.721	0.062
100	243	5.520	5.529	0.009
105	244	5.594	5.608	0.014
105	246	5.494	5.589	0.095
105	248	5.642	5.682	0.040
105	374	5.614	5.704	0.090
105	376	5.532	5.572	0.040
105	Max	5.834	5.854	0.020
105	Average	5.611	5.629	0.018
105	Min	5.451	5.442	-0.009
105	Std Dev	0.094	0.095	0.034



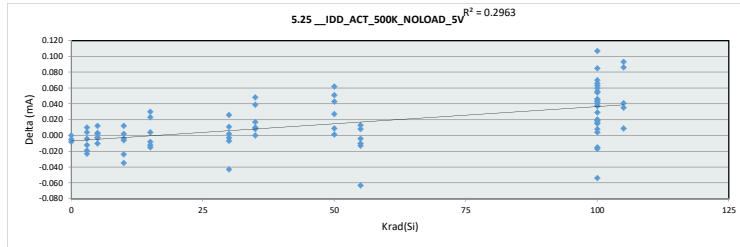
5.24 IDD_ACT_500K_NOLOAD_4V											
Test Site											
Tester											
Test Number											
Max Limit	8	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	5.521	5.442	5.488	5.456	5.473	5.459	5.493	5.510	5.600	5.480	5.572
Average	5.599	5.583	5.670	5.581	5.675	5.590	5.689	5.602	5.656	5.628	5.631
Max	5.650	5.681	5.844	5.662	5.854	5.691	5.830	5.687	5.731	5.787	5.704
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000



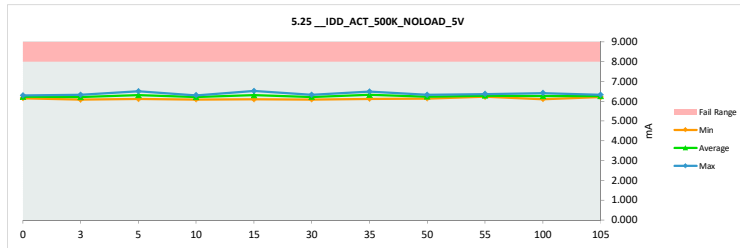
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5.25 IDD_ACT_500K_NOLOAD_5V	
Test Site	
Tester	
Test Number	
Unit	mA mA
Max Limit	8 8
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.164	6.159	-0.005
0	118	6.264	6.264	0.000
0	122	6.300	6.292	-0.008
3	61	6.297	6.274	-0.023
3	62	6.329	6.325	-0.004
3	63	6.238	6.242	0.004
3	64	6.295	6.283	-0.012
3	65	6.098	6.079	-0.019
3	66	6.117	6.127	0.010
5	67	6.196	6.199	0.003
5	68	6.344	6.341	-0.003
5	69	6.117	6.119	0.002
5	70	6.492	6.504	0.012
5	71	6.316	6.306	-0.010
5	72	6.394	6.391	-0.003
10	73	6.297	6.262	-0.035
10	74	6.329	6.305	-0.024
10	75	6.238	6.250	0.012
10	76	6.295	6.289	-0.006
10	77	6.098	6.094	-0.004
10	78	6.117	6.119	0.002
15	79	6.196	6.226	0.030
15	80	6.344	6.336	-0.008
15	81	6.117	6.102	-0.015
15	82	6.492	6.515	0.023
15	83	6.316	6.304	-0.012
15	84	6.394	6.398	0.004
30	86	6.297	6.308	0.011
30	89	6.329	6.331	0.002
30	91	6.238	6.264	0.026
30	92	6.295	6.252	-0.043
30	94	6.098	6.095	-0.003
30	95	6.117	6.110	-0.007
35	100	6.196	6.244	0.048
35	101	6.344	6.383	0.039
35	102	6.117	6.127	0.010
35	103	6.492	6.492	0.000
35	104	6.316	6.324	0.008
35	105	6.394	6.411	0.017
50	106	6.299	6.326	0.027
50	107	6.201	6.244	0.043
50	108	6.088	6.150	0.062
50	109	6.248	6.299	0.051
50	110	6.266	6.275	0.009
50	111	6.146	6.147	0.001
55	113	6.312	6.308	-0.004
55	114	6.244	6.231	-0.013
55	115	6.296	6.309	0.013
55	116	6.256	6.264	0.008
55	126	6.382	6.372	-0.010
55	128	6.312	6.249	-0.063
100	129	6.307	6.377	0.070
100	131	6.184	6.130	-0.054
100	132	6.312	6.297	-0.015
100	133	6.309	6.350	0.041
100	134	6.198	6.241	0.043
100	135	6.239	6.222	-0.017
100	138	6.249	6.315	0.066
100	139	6.107	6.167	0.060
100	140	6.221	6.266	0.045
100	141	6.138	6.153	0.015
100	142	6.285	6.341	0.056
100	143	6.260	6.323	0.063
100	144	6.190	6.227	0.037
100	220	6.301	6.309	0.008
100	221	6.177	6.194	0.017
100	224	6.213	6.259	0.046
100	225	6.093	6.112	0.019
100	226	6.382	6.420	0.038
100	228	6.179	6.286	0.107
100	232	6.242	6.327	0.085
100	233	6.267	6.287	0.020
100	236	6.105	6.134	0.029
100	238	6.299	6.353	0.054
100	243	6.154	6.158	0.004
105	244	6.229	6.238	0.009
105	246	6.127	6.220	0.093
105	248	6.279	6.314	0.035
105	374	6.248	6.334	0.086
105	376	6.172	6.213	0.041
105	Max	6.492	6.515	0.107
105	Average	6.249	6.265	0.016
105	Min	6.088	6.079	-0.063
105	Std Dev	0.097	0.097	0.032



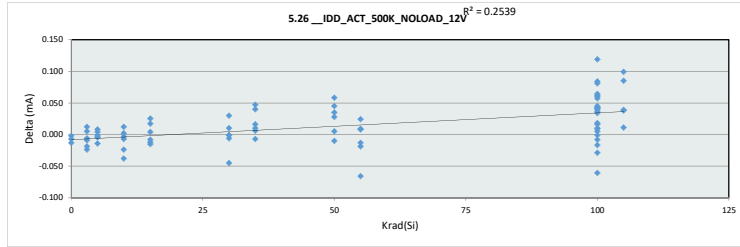
5.25 IDD_ACT_5	
Test Site	
Tester	
Test Number	
Max Limit	8 mA
Min Limit	
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	6.159 6.079 6.119 6.094 6.102 6.095 6.127 6.147 6.231 6.112 6.213
Average	6.238 6.222 6.310 6.220 6.314 6.227 6.330 6.240 6.289 6.260 6.264
Max	6.292 6.325 6.504 6.305 6.515 6.331 6.492 6.326 6.372 6.420 6.334
UL	8.000 8.000 8.000 8.000 8.000 8.000 8.000 8.000 8.000 8.000 8.000



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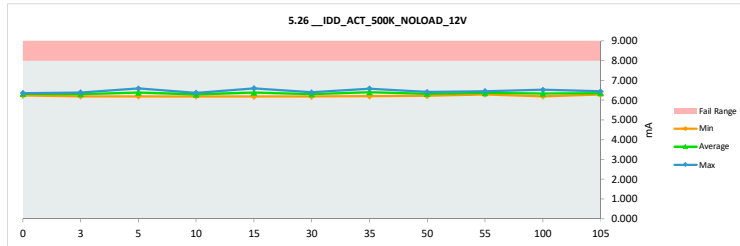
5.26 IDD_ACT_500K_NOLOAD_12V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	8
Min Limit	8

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.237	6.230	-0.007
0	118	6.359	6.357	-0.002
0	122	6.374	6.381	-0.013
3	61	6.383	6.359	-0.024
3	62	6.393	6.387	-0.006
3	63	6.298	6.303	0.005
3	64	6.376	6.367	-0.009
3	65	6.206	6.187	-0.019
3	66	6.188	6.200	0.012
5	67	6.270	6.269	-0.001
5	68	6.415	6.411	-0.004
5	69	6.185	6.189	0.004
5	70	6.582	6.590	0.008
5	71	6.394	6.380	-0.014
5	72	6.469	6.465	-0.004
10	73	6.383	6.345	-0.038
10	74	6.393	6.369	-0.024
10	75	6.298	6.310	0.012
10	76	6.376	6.369	-0.007
10	77	6.206	6.203	-0.003
10	78	6.188	6.190	0.002
15	79	6.270	6.295	0.025
15	80	6.415	6.407	-0.008
15	81	6.185	6.173	-0.012
15	82	6.582	6.599	0.017
15	83	6.394	6.379	-0.015
15	84	6.469	6.473	0.004
30	86	6.383	6.393	0.010
30	89	6.393	6.392	-0.001
30	91	6.298	6.328	0.030
30	92	6.376	6.331	-0.045
30	94	6.206	6.205	-0.001
30	95	6.188	6.182	-0.006
35	100	6.270	6.317	0.047
35	101	6.415	6.455	0.040
35	102	6.185	6.195	0.010
35	103	6.582	6.575	-0.007
35	104	6.394	6.400	0.006
35	105	6.469	6.485	0.016
50	106	6.381	6.409	0.028
50	107	6.290	6.325	0.035
50	108	6.179	6.237	0.058
50	109	6.309	6.354	0.045
50	110	6.361	6.366	0.005
50	111	6.222	6.212	-0.010
55	113	6.408	6.416	0.008
55	114	6.308	6.289	-0.019
55	115	6.357	6.381	0.024
55	116	6.323	6.332	0.009
55	126	6.462	6.449	-0.013
55	128	6.367	6.301	-0.066
100	129	6.404	6.488	0.084
100	131	6.255	6.194	-0.061
100	132	6.384	6.367	-0.017
100	133	6.411	6.448	0.037
100	134	6.274	6.314	0.040
100	135	6.308	6.279	-0.029
100	138	6.326	6.383	0.057
100	139	6.186	6.246	0.060
100	140	6.305	6.347	0.042
100	141	6.207	6.216	0.009
100	142	6.360	6.422	0.062
100	143	6.332	6.396	0.064
100	144	6.266	6.300	0.034
100	220	6.376	6.375	-0.001
100	221	6.246	6.251	0.005
100	224	6.294	6.339	0.045
100	225	6.205	6.215	0.010
100	226	6.481	6.524	0.043
100	228	6.263	6.382	0.119
100	232	6.324	6.405	0.081
100	233	6.358	6.374	0.016
100	236	6.192	6.210	0.018
100	238	6.367	6.409	0.042
100	243	6.241	6.233	-0.008
105	244	6.313	6.324	0.011
105	246	6.193	6.278	0.085
105	248	6.370	6.408	0.038
105	374	6.345	6.444	0.099
105	376	6.245	6.287	0.039
	Max	6.582	6.590	0.119
	Average	6.328	6.342	0.014
	Min	6.179	6.173	-0.066
	Std Dev	0.097	0.099	0.034



5.26 IDD_ACT_500K_NOLOAD_12V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	8
Min Limit	8

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.230	6.187	6.189	6.190	6.173	6.182	6.195	6.212	6.289	6.194	6.278
Average	6.316	6.301	6.384	6.298	6.388	6.305	6.405	6.317	6.361	6.338	6.348
Max	6.361	6.387	6.590	6.369	6.599	6.393	6.575	6.409	6.449	6.524	6.444
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

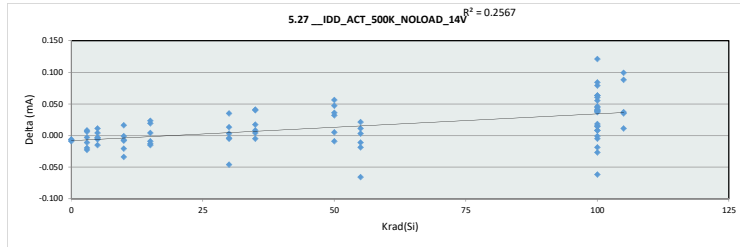


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5.27 IDD_ACT_500K_NOLOAD_14V

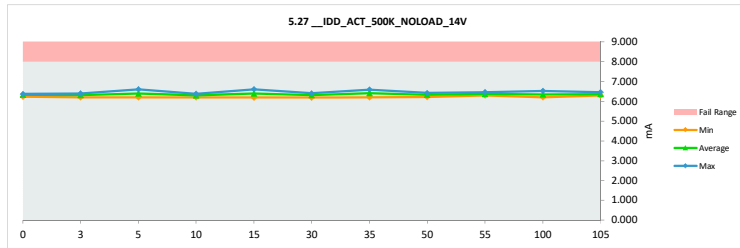
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	8	8
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.241	6.234	-0.007
0	118	6.365	6.359	-0.006
0	122	6.377	6.368	-0.009
3	61	6.385	6.362	-0.023
3	62	6.395	6.392	-0.003
3	63	6.301	6.307	0.006
3	64	6.381	6.370	-0.011
3	65	6.212	6.192	-0.020
3	66	6.194	6.202	0.008
5	67	6.276	6.272	-0.004
5	68	6.420	6.414	-0.006
5	69	6.191	6.195	0.004
5	70	6.585	6.596	0.011
5	71	6.396	6.381	-0.015
5	72	6.472	6.469	-0.003
10	73	6.385	6.351	-0.034
10	74	6.395	6.374	-0.021
10	75	6.301	6.317	0.016
10	76	6.381	6.373	-0.008
10	77	6.212	6.205	-0.007
10	78	6.194	6.193	-0.001
15	79	6.276	6.299	0.023
15	80	6.420	6.411	-0.009
15	81	6.191	6.178	-0.013
15	82	6.585	6.604	0.019
15	83	6.396	6.381	-0.015
15	84	6.472	6.476	0.004
30	86	6.385	6.398	0.013
30	89	6.395	6.398	0.003
30	91	6.301	6.336	0.035
30	92	6.381	6.335	-0.046
30	94	6.212	6.208	-0.004
30	95	6.194	6.189	-0.005
35	100	6.276	6.316	0.040
35	101	6.420	6.461	0.041
35	102	6.191	6.199	0.008
35	103	6.585	6.580	-0.005
35	104	6.396	6.400	0.004
35	105	6.472	6.489	0.017
50	106	6.384	6.416	0.032
50	107	6.294	6.330	0.036
50	108	6.186	6.242	0.056
50	109	6.313	6.360	0.047
50	110	6.365	6.370	0.005
50	111	6.226	6.217	-0.009
55	113	6.416	6.419	0.003
55	114	6.312	6.293	-0.019
55	115	6.364	6.385	0.021
55	116	6.326	6.337	0.011
55	126	6.466	6.455	-0.011
55	128	6.373	6.307	-0.066
100	129	6.408	6.492	0.084
100	131	6.257	6.195	-0.062
100	132	6.389	6.370	-0.019
100	133	6.413	6.452	0.039
100	134	6.279	6.317	0.038
100	135	6.310	6.283	-0.027
100	138	6.330	6.390	0.060
100	139	6.194	6.249	0.055
100	140	6.309	6.349	0.040
100	141	6.211	6.219	0.008
100	142	6.366	6.429	0.063
100	143	6.337	6.400	0.063
100	144	6.267	6.304	0.037
100	220	6.380	6.379	-0.001
100	221	6.246	6.254	0.008
100	224	6.298	6.342	0.044
100	225	6.207	6.221	0.014
100	226	6.485	6.526	0.041
100	228	6.264	6.385	0.121
100	232	6.329	6.408	0.079
100	233	6.363	6.378	0.015
100	236	6.196	6.214	0.018
100	238	6.370	6.416	0.046
100	243	6.243	6.238	-0.005
105	244	6.318	6.329	0.011
105	246	6.196	6.284	0.088
105	248	6.374	6.409	0.035
105	374	6.350	6.449	0.099
105	376	6.253	6.290	0.037
Max		6.585	6.604	0.019
Average		6.332	6.346	0.014
Min		6.186	6.178	-0.008
Std Dev		0.097	0.099	0.034



Test Site		
Tester		
Test Number		
Max Limit	8	mA
Min Limit		

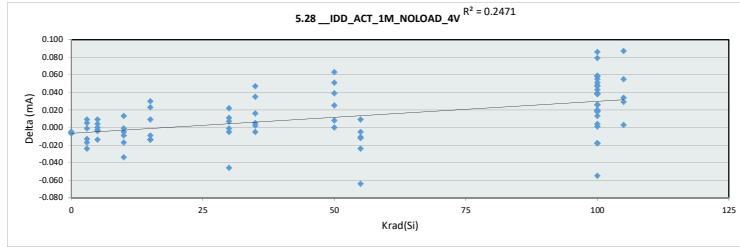
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.234	6.192	6.195	6.193	6.178	6.189	6.199	6.217	6.293	6.195	6.284
Average	6.320	6.304	6.388	6.302	6.392	6.311	6.408	6.323	6.366	6.342	6.352
Max	6.368	6.392	6.596	6.374	6.604	6.398	6.580	6.416	6.455	6.526	6.449
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000



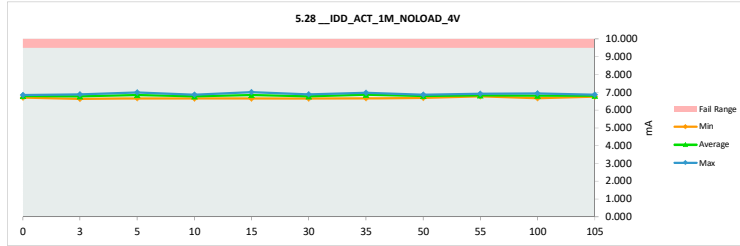
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5.28 IDD_ACT_1M_NOLOAD_4V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.713	6.706	-0.007
0	118	6.814	6.809	-0.005
0	122	6.853	6.848	-0.005
3	61	6.844	6.820	-0.024
3	62	6.880	6.879	-0.001
3	63	6.780	6.785	0.005
3	64	6.842	6.829	-0.013
3	65	6.650	6.633	-0.017
3	66	6.674	6.683	0.009
5	67	6.738	6.738	0.000
5	68	6.898	6.894	-0.004
5	69	6.662	6.666	0.004
5	70	6.985	6.994	0.009
5	71	6.861	6.847	-0.014
5	72	6.940	6.937	-0.003
10	73	6.844	6.810	-0.034
10	74	6.880	6.863	-0.017
10	75	6.780	6.793	0.013
10	76	6.842	6.833	-0.009
10	77	6.650	6.645	-0.005
10	78	6.674	6.673	-0.001
15	79	6.738	6.768	0.030
15	80	6.898	6.889	-0.009
15	81	6.662	6.648	-0.014
15	82	6.985	7.008	0.023
15	83	6.861	6.847	-0.014
15	84	6.940	6.949	0.009
30	86	6.844	6.855	0.011
30	89	6.880	6.887	0.007
30	91	6.780	6.802	0.022
30	92	6.842	6.796	-0.046
30	94	6.650	6.649	-0.001
30	95	6.674	6.669	-0.005
35	100	6.738	6.785	0.047
35	101	6.898	6.933	0.035
35	102	6.662	6.667	0.005
35	103	6.985	6.980	-0.005
35	104	6.861	6.863	0.002
35	105	6.940	6.956	0.016
50	106	6.842	6.867	0.025
50	107	6.747	6.786	0.039
50	108	6.636	6.699	0.063
50	109	6.796	6.847	0.051
50	110	6.814	6.822	0.008
50	111	6.692	6.692	0.000
55	113	6.858	6.834	-0.024
55	114	6.784	6.772	-0.012
55	115	6.842	6.837	-0.005
55	116	6.799	6.808	0.009
55	126	6.925	6.914	-0.011
55	128	6.864	6.800	-0.064
100	129	6.857	6.908	0.051
100	131	6.741	6.686	-0.055
100	132	6.865	6.847	-0.018
100	133	6.864	6.902	0.038
100	134	6.753	6.792	0.039
100	135	6.786	6.768	-0.018
100	138	6.801	6.860	0.059
100	139	6.662	6.717	0.055
100	140	6.771	6.810	0.039
100	141	6.685	6.698	0.013
100	142	6.840	6.887	0.047
100	143	6.803	6.861	0.058
100	144	6.747	6.773	0.026
100	220	6.847	6.848	0.001
100	221	6.720	6.738	0.018
100	224	6.757	6.800	0.043
100	225	6.649	6.668	0.019
100	226	6.923	6.942	0.019
100	228	6.730	6.816	0.086
100	232	6.791	6.870	0.079
100	233	6.795	6.815	0.020
100	236	6.646	6.672	0.026
100	238	6.851	6.899	0.048
100	243	6.700	6.704	0.004
105	244	6.779	6.782	0.003
105	246	6.681	6.768	0.087
105	248	6.831	6.860	0.029
105	374	6.789	6.844	0.055
105	376	6.720	6.754	0.034
	Max	6.985	7.008	0.027
	Average	6.795	6.808	0.012
	Min	6.636	6.633	-0.064
	Std Dev	0.091	0.091	0.030



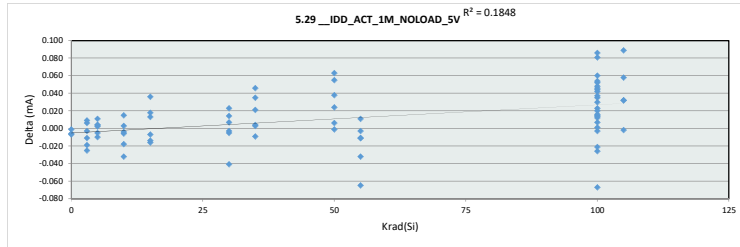
5.28 IDD_ACT											
Test Site											
Tester											
Test Number											
Max Limit	9.5 mA										
Min Limit	mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.706	6.633	6.666	6.645	6.648	6.649	6.667	6.692	6.772	6.668	6.754
Average	6.788	6.772	6.846	6.770	6.852	6.776	6.864	6.786	6.828	6.803	6.802
Max	6.848	6.879	6.994	6.863	7.008	6.887	6.980	6.867	6.914	6.942	6.860
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



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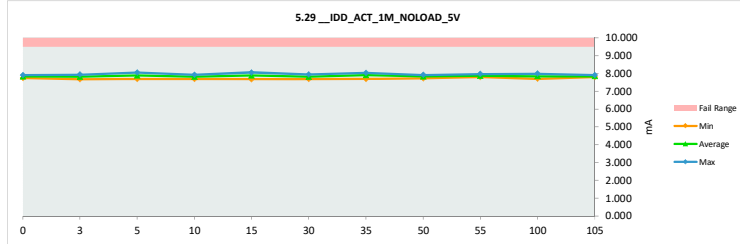
5.29 IDD_ACT_1M_NOLOAD_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.759	7.752	-0.007
0	118	7.857	7.856	-0.001
0	122	7.910	7.904	-0.006
3	61	7.901	7.876	-0.025
3	62	7.943	7.940	-0.003
3	63	7.821	7.827	0.006
3	64	7.894	7.883	-0.011
3	65	7.698	7.679	-0.019
3	66	7.724	7.733	0.009
5	67	7.783	7.787	0.004
5	68	7.945	7.940	-0.005
5	69	7.699	7.703	0.004
5	70	8.045	8.056	0.011
5	71	7.908	7.898	-0.010
5	72	7.986	7.898	-0.088
10	73	7.901	7.869	-0.032
10	74	7.943	7.925	-0.018
10	75	7.821	7.836	0.015
10	76	7.894	7.888	-0.006
10	77	7.698	7.694	-0.004
10	78	7.724	7.727	0.003
15	79	7.783	7.819	0.036
15	80	7.945	7.938	-0.007
15	81	7.699	7.683	-0.016
15	82	8.045	8.063	0.018
15	83	7.908	7.894	-0.014
15	84	7.986	7.999	0.013
30	86	7.901	7.915	0.014
30	89	7.943	7.950	0.007
30	91	7.821	7.844	0.023
30	92	7.894	7.853	-0.041
30	94	7.698	7.695	-0.003
30	95	7.724	7.719	-0.005
35	100	7.783	7.829	0.046
35	101	7.945	7.980	0.035
35	102	7.699	7.702	0.003
35	103	8.045	8.036	-0.009
35	104	7.908	7.912	0.004
35	105	7.986	8.007	0.021
50	106	7.899	7.923	0.024
50	107	7.804	7.842	0.038
50	108	7.681	7.744	0.063
50	109	7.846	7.901	0.055
50	110	7.865	7.871	0.006
50	111	7.739	7.738	-0.001
55	113	7.910	7.878	-0.032
55	114	7.822	7.811	-0.011
55	115	7.887	7.884	-0.003
55	116	7.845	7.856	0.011
55	126	7.986	7.975	-0.011
55	128	7.922	7.857	-0.065
100	129	7.906	7.954	0.048
100	131	7.791	7.724	-0.067
100	132	7.915	7.894	-0.021
100	133	7.915	7.945	0.030
100	134	7.806	7.843	0.037
100	135	7.839	7.813	-0.026
100	138	7.849	7.909	0.060
100	139	7.701	7.753	0.052
100	140	7.813	7.848	0.035
100	141	7.736	7.743	0.007
100	142	7.890	7.936	0.046
100	143	7.848	7.902	0.054
100	144	7.797	7.820	0.023
100	220	7.895	7.892	-0.003
100	221	7.772	7.784	0.012
100	224	7.796	7.838	0.042
100	225	7.699	7.713	0.014
100	226	7.981	7.995	0.014
100	228	7.775	7.861	0.086
100	232	7.840	7.921	0.081
100	233	7.864	7.880	0.016
100	236	7.688	7.708	0.020
100	238	7.906	7.950	0.044
100	243	7.744	7.745	0.001
105	244	7.824	7.822	-0.002
105	246	7.724	7.813	0.089
105	248	7.882	7.914	0.032
105	374	7.831	7.889	0.058
105	376	7.772	7.894	0.032
105	Max	8.045	8.063	0.089
105	Average	7.845	7.856	0.011
105	Min	7.681	7.679	-0.067
105	Std Dev	0.095	0.094	0.030



5.29 IDD_ACT	
Test Site	
Tester	
Test Number	
Max Limit	9.5 mA
Min Limit	9.5 mA

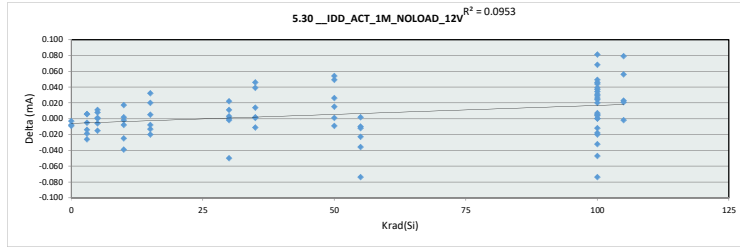
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.752	7.679	7.703	7.694	7.683	7.695	7.702	7.738	7.811	7.708	7.804
Average	7.837	7.823	7.895	7.823	7.899	7.829	7.911	7.837	7.877	7.849	7.848
Max	7.904	7.940	8.056	7.925	8.063	7.950	8.036	7.923	7.975	7.995	7.914
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



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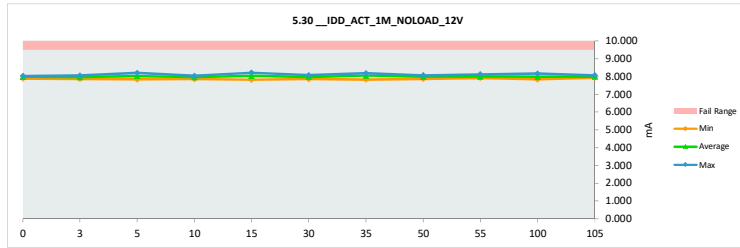
5.30 IDD_ACT_1M_NOLOAD_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	9.5	9.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.895	7.886	-0.009
0	118	8.028	8.025	-0.003
0	122	8.047	8.039	-0.008
3	61	8.061	8.035	-0.026
3	62	8.065	8.060	-0.005
3	63	7.930	7.936	0.006
3	64	8.045	8.031	-0.014
3	65	7.890	7.871	-0.019
3	66	7.860	7.866	0.006
5	67	7.915	7.915	0.000
5	68	8.078	8.072	-0.006
5	69	7.829	7.837	0.008
5	70	8.193	8.204	0.011
5	71	8.050	8.035	-0.015
5	72	8.125	8.126	0.001
10	73	8.061	8.022	-0.039
10	74	8.065	8.040	-0.025
10	75	7.930	7.947	0.017
10	76	8.045	8.037	-0.008
10	77	7.890	7.888	-0.002
10	78	7.860	7.862	0.002
15	79	7.915	7.947	0.032
15	80	8.078	8.070	-0.008
15	81	7.829	7.816	-0.013
15	82	8.193	8.213	0.020
15	83	8.050	8.030	-0.020
15	84	8.125	8.130	0.005
30	86	8.061	8.072	0.011
30	89	8.065	8.068	0.003
30	91	7.930	7.952	0.022
30	92	8.045	7.995	-0.050
30	94	7.890	7.890	0.000
30	95	7.860	7.858	-0.002
35	100	7.915	7.961	0.046
35	101	8.078	8.117	0.039
35	102	7.829	7.831	0.002
35	103	8.193	8.182	-0.011
35	104	8.050	8.051	0.001
35	105	8.125	8.139	0.014
50	106	8.051	8.066	0.015
50	107	7.965	7.991	0.026
50	108	7.846	7.900	0.054
50	109	7.961	8.010	0.049
50	110	8.039	8.040	0.001
50	111	7.876	7.867	-0.009
55	113	8.083	8.047	-0.036
55	114	7.943	7.920	-0.023
55	115	8.003	7.993	-0.010
55	116	7.974	7.976	0.002
55	126	8.130	8.118	-0.012
55	128	8.026	7.952	-0.074
100	129	8.083	8.122	0.039
100	131	7.918	7.844	-0.074
100	132	8.048	8.016	-0.032
100	133	8.100	8.124	0.024
100	134	7.943	7.974	0.031
100	135	7.967	7.920	-0.047
100	138	7.989	8.038	0.049
100	139	7.851	7.895	0.044
100	140	7.965	7.994	0.029
100	141	7.869	7.857	-0.012
100	142	8.029	8.066	0.037
100	143	7.986	8.032	0.046
100	144	7.929	7.949	0.020
100	220	8.031	8.011	-0.020
100	221	7.896	7.896	0.000
100	224	7.944	7.978	0.034
100	225	7.897	7.902	0.005
100	226	8.156	8.160	0.004
100	228	7.930	8.011	0.081
100	232	7.990	8.058	0.068
100	233	8.027	8.034	0.007
100	236	7.848	7.848	0.000
100	238	8.031	8.057	0.026
100	243	7.901	7.883	-0.018
105	244	7.976	7.974	-0.002
105	246	7.846	7.925	0.079
105	248	8.047	8.068	0.021
105	374	8.005	8.061	0.056
105	376	7.918	7.941	0.023
105	Max	8.193	8.213	0.021
105	Average	7.989	7.995	0.006
105	Min	7.829	7.816	-0.013
105	Std Dev	0.095	0.095	0.030



5.30 IDD_ACT		
Test Site		
Tester		
Test Number		
Max Limit	9.5	mA
Min Limit		mA
Krad(Si)	0	3

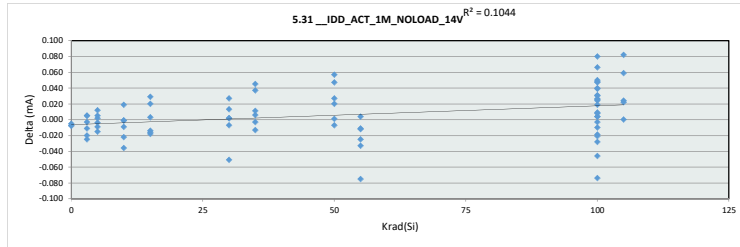
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.886	7.866	7.837	7.862	7.816	7.858	7.831	7.867	7.920	7.844	7.925
Average	7.983	7.967	8.032	7.966	8.034	7.973	8.047	7.979	8.001	7.986	7.994
Max	8.039	8.060	8.204	8.040	8.213	8.072	8.182	8.066	8.118	8.160	8.068
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



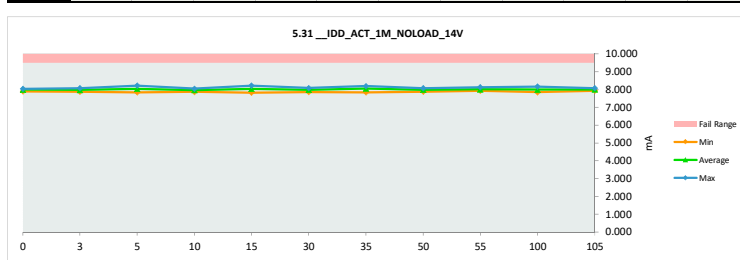
LDR TID Report
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5.31 IDD_ACT_1M_NOLOAD_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.899	7.892	-0.007
0	118	8.031	8.026	-0.005
0	122	8.050	8.042	-0.008
3	61	8.064	8.039	-0.025
3	62	8.068	8.065	-0.003
3	63	7.933	7.938	0.005
3	64	8.048	8.037	-0.011
3	65	7.892	7.872	-0.020
3	66	7.867	7.872	0.005
5	67	7.922	7.918	-0.004
5	68	8.084	8.075	-0.009
5	69	7.833	7.838	0.005
5	70	8.198	8.210	0.012
5	71	8.054	8.039	-0.015
5	72	8.130	8.132	0.002
10	73	8.064	8.028	-0.036
10	74	8.068	8.046	-0.022
10	75	7.933	7.952	0.019
10	76	8.048	8.039	-0.009
10	77	7.892	7.891	-0.001
10	78	7.867	7.866	-0.001
15	79	7.922	7.951	0.029
15	80	8.084	8.070	-0.014
15	81	7.833	7.817	-0.016
15	82	8.198	8.218	0.020
15	83	8.054	8.036	-0.018
15	84	8.130	8.133	0.003
30	86	8.064	8.077	0.013
30	89	8.068	8.070	0.002
30	91	7.933	7.960	0.027
30	92	8.048	7.997	-0.051
30	94	7.892	7.894	0.002
30	95	7.867	7.860	-0.007
35	100	7.922	7.967	0.045
35	101	8.084	8.121	0.037
35	102	7.833	7.839	0.006
35	103	8.198	8.185	-0.013
35	104	8.054	8.051	-0.003
35	105	8.130	8.141	0.011
50	106	8.052	8.072	0.020
50	107	7.968	7.995	0.027
50	108	7.846	7.903	0.057
50	109	7.959	8.016	0.047
50	110	8.044	8.045	0.001
50	111	7.879	7.872	-0.007
55	113	8.085	8.052	-0.033
55	114	7.950	7.925	-0.025
55	115	8.008	7.996	-0.012
55	116	7.979	7.983	0.004
55	126	8.134	8.123	-0.011
55	128	8.031	7.956	-0.075
100	129	8.085	8.124	0.039
100	131	7.921	7.847	-0.074
100	132	8.050	8.022	-0.028
100	133	8.103	8.129	0.026
100	134	7.951	7.977	0.026
100	135	7.971	7.925	-0.046
100	138	7.993	8.043	0.050
100	139	7.853	7.900	0.047
100	140	7.966	7.996	0.030
100	141	7.870	7.860	-0.010
100	142	8.030	8.070	0.040
100	143	7.990	8.038	0.048
100	144	7.934	7.953	0.019
100	220	8.033	8.014	-0.019
100	221	7.901	7.898	-0.003
100	224	7.949	7.980	0.031
100	225	7.900	7.908	0.008
100	226	8.159	8.163	0.004
100	228	7.933	8.013	0.080
100	232	7.996	8.062	0.066
100	233	8.030	8.039	0.009
100	236	7.852	7.856	0.004
100	238	8.036	8.060	0.024
100	243	7.908	7.887	-0.021
105	244	7.978	7.978	0.000
105	246	7.848	7.930	0.082
105	248	8.050	8.072	0.022
105	374	8.006	8.065	0.059
105	376	7.921	7.945	0.024
	Max	8.198	8.218	0.020
	Average	7.993	7.999	0.006
	Min	7.833	7.817	-0.016
	Std Dev	0.095	0.095	0.030



5.31 IDD_ACT	
Test Site	
Tester	
Test Number	
Max Limit	9.5 mA
Min Limit	9.5 mA

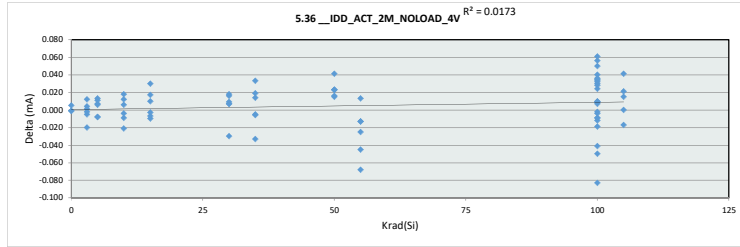


Krad(Si)	LL	Min	Average	Max
0	7.892	7.872	7.838	7.866
3	7.897	7.971	8.035	7.970
5	8.042	8.065	8.210	8.218
10	9.500	9.500	9.500	9.500
15	9.500	9.500	9.500	9.500
30	9.500	9.500	9.500	9.500
35	9.500	9.500	9.500	9.500
50	9.500	9.500	9.500	9.500
55	9.500	9.500	9.500	9.500
100	9.500	9.500	9.500	9.500
105	9.500	9.500	9.500	9.500

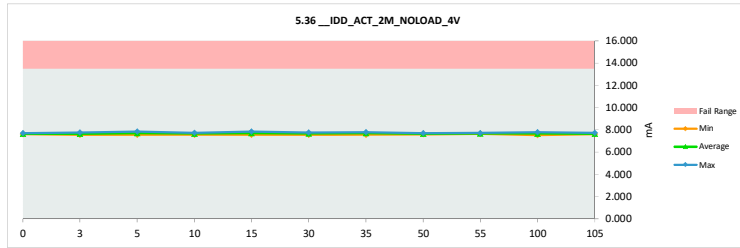
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5.36 IDD_ACT_2M_NOLOAD_4V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	13.5	13.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.611	7.610	-0.001
0	118	7.639	7.638	-0.001
0	122	7.699	7.704	0.005
3	61	7.675	7.655	-0.020
3	62	7.741	7.745	0.004
3	63	7.672	7.684	0.012
3	64	7.717	7.715	-0.002
3	65	7.547	7.542	-0.005
3	66	7.570	7.571	0.001
5	67	7.560	7.566	0.006
5	68	7.738	7.730	-0.008
5	69	7.561	7.568	0.007
5	70	7.820	7.833	0.013
5	71	7.711	7.703	-0.008
5	72	7.761	7.772	0.011
10	73	7.675	7.654	-0.021
10	74	7.741	7.732	-0.009
10	75	7.672	7.690	0.018
10	76	7.717	7.713	-0.004
10	77	7.547	7.553	0.006
10	78	7.570	7.582	0.012
15	79	7.560	7.590	0.030
15	80	7.738	7.735	-0.003
15	81	7.561	7.551	-0.010
15	82	7.820	7.837	0.017
15	83	7.711	7.704	-0.007
15	84	7.761	7.771	0.010
30	86	7.675	7.691	0.016
30	89	7.741	7.748	0.007
30	91	7.672	7.690	0.018
30	92	7.717	7.687	-0.030
30	94	7.547	7.556	0.009
30	95	7.570	7.577	0.007
35	100	7.560	7.593	0.033
35	101	7.738	7.757	0.019
35	102	7.561	7.556	-0.005
35	103	7.820	7.787	-0.033
35	104	7.711	7.705	-0.006
35	105	7.761	7.775	0.014
50	106	7.674	7.697	0.023
50	107	7.574	7.597	0.023
50	108	7.599	7.614	0.015
50	109	7.612	7.653	0.041
50	110	7.628	7.651	0.023
50	111	7.608	7.624	0.016
55	113	7.748	7.703	-0.045
55	114	7.650	7.637	-0.013
55	115	7.663	7.650	-0.013
55	116	7.620	7.633	0.013
55	126	7.735	7.710	-0.025
55	128	7.701	7.633	-0.068
100	129	7.675	7.715	0.040
100	131	7.618	7.568	-0.050
100	132	7.725	7.684	-0.041
100	133	7.688	7.718	0.030
100	134	7.594	7.628	0.034
100	135	7.779	7.696	-0.083
100	138	7.630	7.665	0.035
100	139	7.573	7.605	0.032
100	140	7.622	7.672	0.050
100	141	7.571	7.562	-0.009
100	142	7.680	7.704	0.024
100	143	7.644	7.680	0.036
100	144	7.602	7.609	0.007
100	220	7.705	7.701	-0.004
100	221	7.583	7.581	-0.002
100	224	7.609	7.637	0.028
100	225	7.594	7.585	-0.009
100	226	7.764	7.773	0.009
100	228	7.589	7.650	0.061
100	232	7.657	7.713	0.056
100	233	7.687	7.697	0.010
100	236	7.581	7.569	-0.012
100	238	7.749	7.758	0.009
100	243	7.549	7.530	-0.019
105	244	7.622	7.605	-0.017
105	246	7.567	7.608	0.041
105	248	7.712	7.712	0.000
105	374	7.640	7.655	0.015
105	376	7.606	7.627	0.021
105	Max	7.820	7.837	0.061
105	Average	7.657	7.662	0.005
105	Min	7.547	7.530	-0.083
105	Std Dev	0.075	0.072	0.025



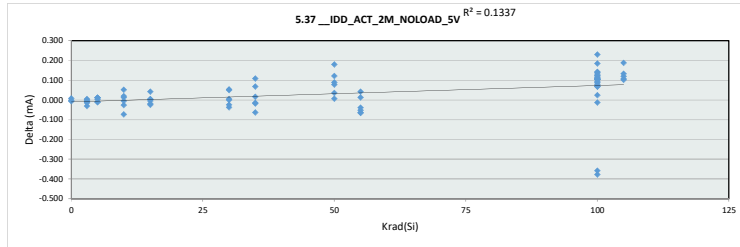
5.36 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.610	7.542	7.566	7.553	7.551	7.556	7.556	7.597	7.633	7.530	7.605
Average	7.651	7.652	7.695	7.654	7.698	7.658	7.696	7.639	7.661	7.654	7.641
Max	7.704	7.745	7.833	7.732	7.837	7.748	7.787	7.697	7.710	7.773	7.712
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



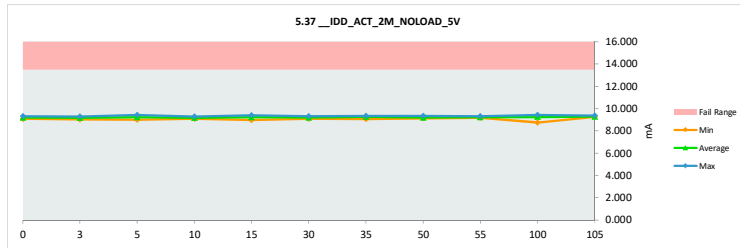
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5.37 IDD_ACT_2M_NOLOAD_5V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	13.5	13.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.103	9.096	-0.007
0	118	9.228	9.226	-0.002
0	122	9.320	9.328	0.008
3	61	9.283	9.251	-0.032
3	62	9.313	9.303	-0.010
3	63	9.154	9.159	0.005
3	64	9.265	9.260	-0.005
3	65	9.044	9.029	-0.015
3	66	9.104	9.104	0.000
5	67	9.101	9.112	0.011
5	68	9.314	9.303	-0.011
5	69	8.992	9.003	0.011
5	70	9.426	9.428	0.002
5	71	9.278	9.266	-0.012
5	72	9.372	9.383	0.011
10	73	9.283	9.209	-0.074
10	74	9.313	9.286	-0.027
10	75	9.154	9.173	0.019
10	76	9.265	9.260	-0.005
10	77	9.044	9.096	0.052
10	78	9.104	9.116	0.012
15	79	9.101	9.143	0.042
15	80	9.314	9.316	0.002
15	81	8.992	8.987	-0.005
15	82	9.426	9.406	-0.020
15	83	9.278	9.254	-0.024
15	84	9.372	9.377	0.005
30	86	9.283	9.258	-0.025
30	89	9.313	9.313	0.000
30	91	9.154	9.204	0.050
30	92	9.265	9.228	-0.037
30	94	9.044	9.097	0.053
30	95	9.104	9.109	0.005
35	100	9.101	9.209	0.108
35	101	9.314	9.329	0.015
35	102	8.992	9.060	0.068
35	103	9.426	9.363	-0.063
35	104	9.278	9.263	-0.015
35	105	9.372	9.353	-0.019
50	106	9.228	9.349	0.121
50	107	9.108	9.143	0.035
50	108	8.983	9.163	0.180
50	109	9.204	9.210	0.006
50	110	9.218	9.296	0.078
50	111	9.049	9.137	0.088
55	113	9.302	9.315	0.013
55	114	9.183	9.224	0.041
55	115	9.229	9.177	-0.052
55	116	9.227	9.188	-0.039
55	126	9.318	9.252	-0.066
55	128	9.292	9.227	-0.065
100	129	9.300	9.405	0.105
100	131	9.204	9.191	-0.013
100	132	9.325	9.349	0.024
100	133	9.296	9.419	0.123
100	134	9.216	9.323	0.107
100	135	9.137	8.759	-0.378
100	138	9.220	9.362	0.142
100	139	9.058	9.179	0.121
100	140	9.188	9.292	0.104
100	141	9.084	9.154	0.070
100	142	9.282	8.924	-0.358
100	143	9.187	9.325	0.138
100	144	9.160	9.272	0.112
100	220	9.268	9.335	0.067
100	221	9.121	9.212	0.091
100	224	9.143	9.230	0.087
100	225	9.060	9.162	0.102
100	226	9.310	9.441	0.131
100	228	9.128	9.358	0.230
100	232	9.228	9.412	0.184
100	233	9.251	9.318	0.067
100	236	8.978	9.083	0.105
100	238	9.312	9.423	0.111
100	243	9.090	9.169	0.079
105	244	9.160	9.261	0.101
105	246	9.090	9.277	0.187
105	248	9.259	9.378	0.119
105	374	9.192	9.325	0.133
105	376	9.151	9.258	0.107
105	Max	9.426	9.441	0.015
105	Average	9.204	9.237	0.033
105	Min	8.978	8.759	-0.378
105	Std Dev	0.113	0.124	0.093



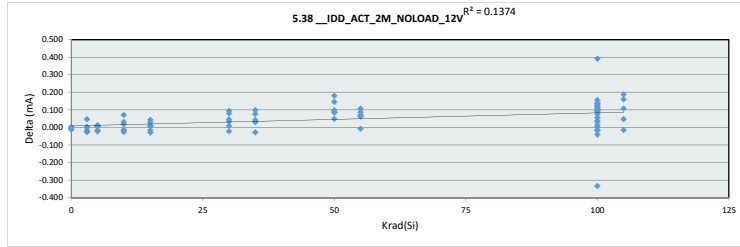
5.37 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	13.5 mA										
Min Limit	mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.096	9.029	9.003	9.096	8.987	9.097	9.060	9.137	9.177	8.759	9.258
Average	9.217	9.184	9.249	9.190	9.247	9.202	9.263	9.216	9.231	9.254	9.300
Max	9.328	9.303	9.428	9.286	9.406	9.313	9.363	9.349	9.315	9.441	9.378
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



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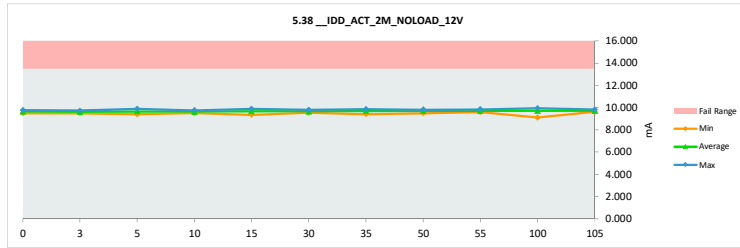
5.38 IDD_ACT_2M_NOLOAD_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	13.5	13.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.515	9.503	-0.012
0	118	9.709	9.704	-0.005
0	122	9.765	9.769	0.004
3	61	9.753	9.728	-0.025
3	62	9.727	9.727	0.000
3	63	9.504	9.508	0.004
3	64	9.692	9.674	-0.018
3	65	9.573	9.549	-0.024
3	66	9.489	9.536	0.047
5	67	9.553	9.559	0.006
5	68	9.746	9.724	-0.022
5	69	9.388	9.394	0.006
5	70	9.885	9.897	0.012
5	71	9.701	9.686	-0.015
5	72	9.781	9.791	0.010
10	73	9.753	9.728	-0.025
10	74	9.727	9.709	-0.018
10	75	9.504	9.522	0.018
10	76	9.692	9.763	0.071
10	77	9.573	9.560	-0.013
10	78	9.489	9.519	0.030
15	79	9.553	9.596	0.043
15	80	9.746	9.729	-0.017
15	81	9.388	9.359	-0.029
15	82	9.885	9.893	0.008
15	83	9.701	9.730	0.029
15	84	9.781	9.792	0.011
30	86	9.753	9.798	0.045
30	89	9.727	9.736	0.009
30	91	9.504	9.598	0.094
30	92	9.692	9.727	0.035
30	94	9.573	9.551	-0.022
30	95	9.489	9.569	0.080
35	100	9.553	9.652	0.099
35	101	9.746	9.777	0.031
35	102	9.388	9.420	0.032
35	103	9.885	9.857	-0.028
35	104	9.701	9.743	0.042
35	105	9.781	9.857	0.076
50	106	9.731	9.819	0.088
50	107	9.633	9.731	0.098
50	108	9.450	9.631	0.181
50	109	9.565	9.708	0.145
50	110	9.731	9.816	0.085
50	111	9.456	9.505	0.049
55	113	9.775	9.837	0.062
55	114	9.544	9.611	0.067
55	115	9.600	9.671	0.071
55	116	9.617	9.724	0.107
55	126	9.760	9.848	0.088
55	128	9.632	9.624	-0.008
100	129	9.794	9.908	0.114
100	131	9.603	9.588	-0.015
100	132	9.745	9.758	0.013
100	133	9.836	9.937	0.101
100	134	9.644	9.743	0.099
100	135	9.521	9.913	0.392
100	138	9.626	9.782	0.156
100	139	9.493	9.620	0.127
100	140	9.631	9.746	0.115
100	141	9.469	9.508	0.039
100	142	9.691	9.828	0.137
100	143	9.655	9.730	0.075
100	144	9.620	9.677	0.057
100	220	9.752	9.711	-0.041
100	221	9.561	9.563	0.002
100	224	9.580	9.677	0.097
100	225	9.610	9.699	0.089
100	226	9.873	9.903	0.030
100	228	9.655	9.796	0.141
100	232	9.744	9.842	0.098
100	233	9.676	9.777	0.101
100	236	9.431	9.098	-0.333
100	238	9.677	9.801	0.124
100	243	9.604	9.585	-0.019
105	244	9.654	9.702	0.048
105	246	9.456	9.643	0.187
105	248	9.723	9.831	0.108
105	374	9.665	9.826	0.161
105	376	9.638	9.624	-0.014
105	Max	9.885	9.937	0.392
105	Average	9.644	9.691	0.047
105	Min	9.388	9.098	-0.333
105	Std Dev	0.123	0.144	0.081



5.38 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105

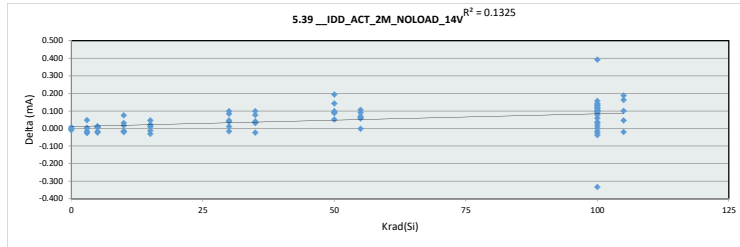
LL											
Min	9.503	9.508	9.394	9.519	9.359	9.551	9.420	9.505	9.611	9.098	9.624
Average	9.659	9.620	9.675	9.634	9.683	9.663	9.718	9.702	9.719	9.716	9.725
Max	9.769	9.728	9.897	9.763	9.893	9.798	9.857	9.819	9.848	9.937	9.831
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



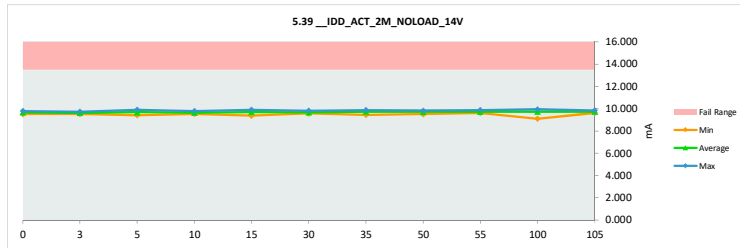
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5.39 IDD_ACT_2M_NOLOAD_14V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	13.5	13.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.518	9.509	-0.009
0	118	9.710	9.706	-0.004
0	122	9.766	9.774	0.008
3	61	9.754	9.731	-0.023
3	62	9.728	9.728	0.000
3	63	9.508	9.511	0.003
3	64	9.696	9.680	-0.016
3	65	9.578	9.552	-0.026
3	66	9.496	9.543	0.047
5	67	9.557	9.566	0.009
5	68	9.747	9.724	-0.023
5	69	9.393	9.399	0.006
5	70	9.885	9.897	0.012
5	71	9.708	9.691	-0.017
5	72	9.785	9.794	0.009
10	73	9.754	9.733	-0.021
10	74	9.728	9.710	-0.018
10	75	9.508	9.524	0.016
10	76	9.696	9.771	0.075
10	77	9.578	9.564	-0.014
10	78	9.496	9.526	0.030
15	79	9.557	9.602	0.045
15	80	9.747	9.734	-0.013
15	81	9.393	9.362	-0.031
15	82	9.885	9.895	0.010
15	83	9.708	9.730	0.022
15	84	9.785	9.795	0.010
30	86	9.754	9.799	0.045
30	89	9.728	9.738	0.010
30	91	9.508	9.607	0.099
30	92	9.696	9.735	0.039
30	94	9.578	9.561	-0.017
30	95	9.496	9.579	0.083
35	100	9.557	9.656	0.099
35	101	9.747	9.782	0.035
35	102	9.393	9.424	0.031
35	103	9.885	9.861	-0.024
35	104	9.708	9.748	0.040
35	105	9.785	9.861	0.076
50	106	9.732	9.823	0.091
50	107	9.638	9.737	0.099
50	108	9.451	9.644	0.193
50	109	9.569	9.711	0.142
50	110	9.734	9.821	0.087
50	111	9.456	9.507	0.051
55	113	9.783	9.840	0.057
55	114	9.550	9.612	0.062
55	115	9.606	9.675	0.069
55	116	9.624	9.729	0.105
55	126	9.766	9.855	0.089
55	128	9.635	9.633	-0.002
100	129	9.797	9.911	0.114
100	131	9.604	9.590	-0.014
100	132	9.747	9.764	0.017
100	133	9.838	9.939	0.101
100	134	9.645	9.746	0.101
100	135	9.528	9.919	0.391
100	138	9.629	9.786	0.157
100	139	9.493	9.624	0.131
100	140	9.634	9.748	0.114
100	141	9.476	9.512	0.036
100	142	9.693	9.827	0.134
100	143	9.658	9.735	0.077
100	144	9.621	9.680	0.059
100	220	9.751	9.713	-0.038
100	221	9.564	9.565	0.001
100	224	9.583	9.682	0.099
100	225	9.612	9.702	0.090
100	226	9.875	9.905	0.030
100	228	9.658	9.800	0.142
100	232	9.745	9.845	0.100
100	233	9.680	9.780	0.100
100	236	9.434	9.100	-0.334
100	238	9.681	9.803	0.122
100	243	9.614	9.589	-0.025
105	244	9.659	9.705	0.046
105	246	9.461	9.648	0.187
105	248	9.732	9.832	0.100
105	374	9.668	9.830	0.162
105	376	9.643	9.623	-0.020
105		9.885	9.939	0.391
	Max	9.647	9.695	0.048
	Average	9.647	9.695	0.048
	Min	9.393	9.100	-0.334
	Std Dev	0.122	0.143	0.081



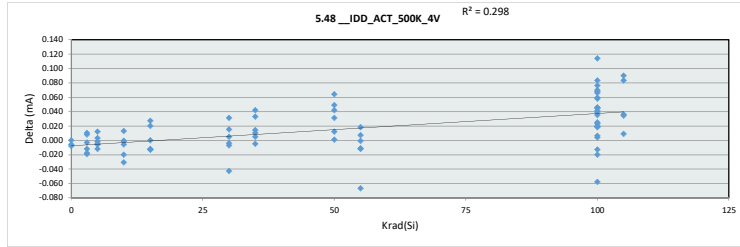
5.39 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	13.5 mA										
Min Limit	3 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.509	9.511	9.399	9.524	9.362	9.561	9.424	9.507	9.612	9.100	9.623
Average	9.663	9.624	9.679	9.638	9.686	9.670	9.722	9.707	9.724	9.719	9.728
Max	9.774	9.731	9.897	9.771	9.895	9.799	9.861	9.823	9.855	9.939	9.832
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



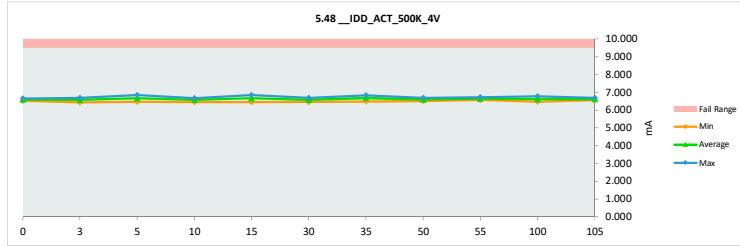
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5.48 IDD_ACT_500K_4V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.519	6.513	-0.006
0	118	6.617	6.617	0.000
0	122	6.649	6.641	-0.008
3	61	6.643	6.624	-0.019
3	62	6.681	6.678	-0.003
3	63	6.584	6.592	0.008
3	64	6.646	6.634	-0.012
3	65	6.455	6.437	-0.018
3	66	6.472	6.482	0.010
5	67	6.550	6.548	-0.002
5	68	6.696	6.691	-0.005
5	69	6.467	6.470	0.003
5	70	6.831	6.843	0.012
5	71	6.666	6.654	-0.012
5	72	6.739	6.733	-0.006
10	73	6.643	6.612	-0.031
10	74	6.681	6.661	-0.020
10	75	6.584	6.597	0.013
10	76	6.646	6.640	-0.006
10	77	6.455	6.454	-0.001
10	78	6.472	6.471	-0.001
15	79	6.550	6.577	0.027
15	80	6.696	6.683	-0.013
15	81	6.467	6.455	-0.012
15	82	6.831	6.851	0.020
15	83	6.666	6.653	-0.013
15	84	6.739	6.739	0.000
30	86	6.643	6.658	0.015
30	89	6.681	6.686	0.005
30	91	6.584	6.615	0.031
30	92	6.646	6.603	-0.043
30	94	6.455	6.451	-0.004
30	95	6.472	6.465	-0.007
35	100	6.550	6.592	0.042
35	101	6.696	6.729	0.033
35	102	6.467	6.477	0.010
35	103	6.831	6.826	-0.005
35	104	6.666	6.671	0.005
35	105	6.739	6.753	0.014
50	106	6.650	6.681	0.031
50	107	6.550	6.592	0.042
50	108	6.447	6.511	0.064
50	109	6.606	6.655	0.049
50	110	6.614	6.626	0.012
50	111	6.495	6.496	0.001
55	113	6.656	6.655	-0.001
55	114	6.590	6.579	-0.011
55	115	6.647	6.665	0.018
55	116	6.594	6.601	0.007
55	126	6.737	6.725	-0.012
55	128	6.662	6.595	-0.067
100	129	6.653	6.729	0.076
100	131	6.543	6.485	-0.058
100	132	6.670	6.657	-0.013
100	133	6.654	6.695	0.041
100	134	6.549	6.594	0.045
100	135	6.588	6.568	-0.020
100	138	6.598	6.664	0.066
100	139	6.464	6.522	0.058
100	140	6.577	6.616	0.039
100	141	6.491	6.509	0.018
100	142	6.639	6.709	0.070
100	143	6.605	6.673	0.068
100	144	6.547	6.582	0.035
100	220	6.654	6.658	0.004
100	221	6.530	6.549	0.019
100	224	6.567	6.613	0.046
100	225	6.449	6.469	0.020
100	226	6.731	6.776	0.045
100	228	6.529	6.643	0.114
100	232	6.589	6.672	0.083
100	233	6.595	6.618	0.023
100	236	6.455	6.480	0.025
100	238	6.651	6.710	0.059
100	243	6.505	6.511	0.006
105	244	6.574	6.583	0.009
105	246	6.479	6.569	0.090
105	248	6.634	6.668	0.034
105	374	6.593	6.676	0.083
105	376	6.527	6.558	0.036
105	Max	6.831	6.851	0.020
105	Average	6.599	6.615	0.016
105	Min	6.447	6.437	-0.010
105	Std Dev	0.094	0.095	0.033



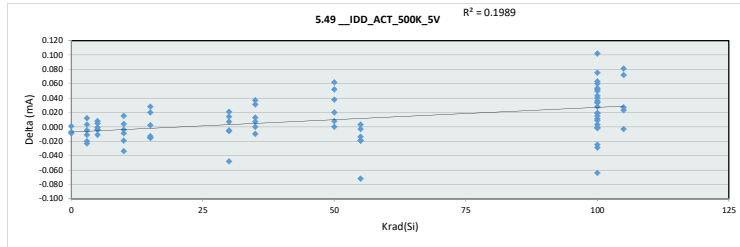
5.48 IDD_ACT_5											
Test Site											
Tester											
Test Number											
Max Limit	9.5 mA										
Min Limit	9.5 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.513	6.437	6.470	6.454	6.455	6.451	6.477	6.496	6.579	6.469	6.558
Average	6.590	6.575	6.657	6.573	6.660	6.580	6.675	6.594	6.637	6.613	6.611
Max	6.641	6.678	6.843	6.661	6.851	6.686	6.826	6.681	6.725	6.776	6.676
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



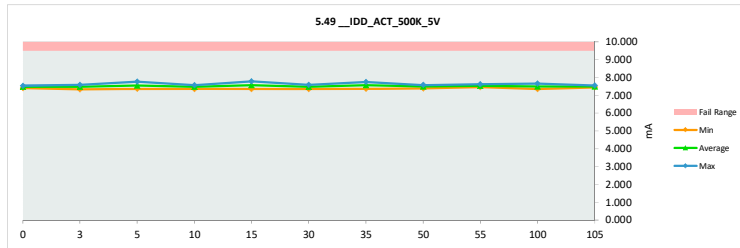
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5.49 IDD_ACT_500K_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.420	7.411	-0.009
0	118	7.507	7.508	0.001
0	122	7.548	7.541	-0.007
3	61	7.548	7.525	-0.023
3	62	7.586	7.581	-0.005
3	63	7.479	7.482	0.003
3	64	7.553	7.542	-0.011
3	65	7.359	7.339	-0.020
3	66	7.368	7.380	0.012
5	67	7.450	7.449	-0.001
5	68	7.586	7.582	-0.004
5	69	7.366	7.371	0.005
5	70	7.760	7.768	0.008
5	71	7.568	7.557	-0.011
5	72	7.640	7.636	-0.004
10	73	7.548	7.514	-0.034
10	74	7.586	7.567	-0.019
10	75	7.479	7.494	0.015
10	76	7.553	7.544	-0.009
10	77	7.359	7.355	-0.004
10	78	7.368	7.372	0.004
15	79	7.450	7.478	0.028
15	80	7.586	7.573	-0.013
15	81	7.366	7.351	-0.015
15	82	7.760	7.780	0.020
15	83	7.568	7.553	-0.015
15	84	7.640	7.642	0.002
30	86	7.548	7.562	0.014
30	89	7.586	7.593	0.007
30	91	7.479	7.500	0.021
30	92	7.553	7.505	-0.048
30	94	7.359	7.354	-0.005
30	95	7.368	7.362	-0.006
35	100	7.450	7.487	0.037
35	101	7.586	7.617	0.031
35	102	7.366	7.373	0.007
35	103	7.760	7.750	-0.010
35	104	7.568	7.568	0.000
35	105	7.640	7.653	0.013
50	106	7.559	7.579	0.020
50	107	7.461	7.499	0.038
50	108	7.345	7.407	0.062
50	109	7.500	7.552	0.052
50	110	7.518	7.526	0.008
50	111	7.395	7.395	0.000
55	113	7.559	7.540	-0.019
55	114	7.486	7.467	-0.019
55	115	7.552	7.549	-0.003
55	116	7.484	7.487	0.003
55	126	7.636	7.622	-0.014
55	128	7.567	7.495	-0.072
100	129	7.556	7.619	0.063
100	131	7.437	7.373	-0.064
100	132	7.578	7.553	-0.025
100	133	7.552	7.586	0.034
100	134	7.462	7.498	0.036
100	135	7.491	7.462	-0.029
100	138	7.502	7.562	0.060
100	139	7.352	7.402	0.050
100	140	7.472	7.506	0.034
100	141	7.396	7.399	0.003
100	142	7.537	7.589	0.052
100	143	7.497	7.551	0.054
100	144	7.445	7.464	0.019
100	220	7.549	7.548	-0.001
100	221	7.428	7.437	0.009
100	224	7.462	7.502	0.040
100	225	7.355	7.366	0.011
100	226	7.633	7.661	0.028
100	228	7.420	7.522	0.102
100	232	7.497	7.572	0.075
100	233	7.510	7.525	0.015
100	236	7.342	7.361	0.019
100	238	7.551	7.594	0.043
100	243	7.397	7.395	-0.002
105	244	7.468	7.465	-0.003
105	246	7.372	7.453	0.081
105	248	7.534	7.557	0.023
105	374	7.477	7.549	0.072
105	376	7.428	7.455	0.027
105	Max	7.760	7.780	0.102
105	Average	7.500	7.511	0.010
105	Min	7.342	7.339	-0.072
105	Std Dev	0.098	0.097	0.031



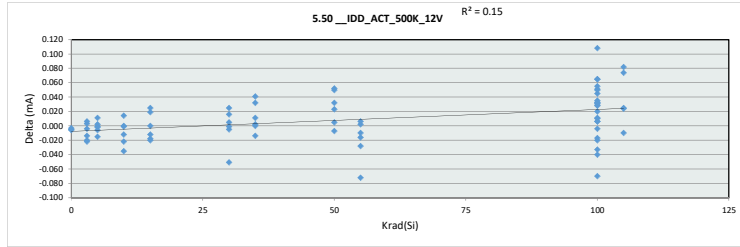
5.49 IDD_ACT_5											
Test Site											
Tester											
Test Number											
Max Limit	9.5 mA										
Min Limit	mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.411	7.339	7.371	7.355	7.351	7.354	7.373	7.395	7.467	7.361	7.453
Average	7.487	7.475	7.561	7.474	7.563	7.479	7.575	7.493	7.527	7.502	7.496
Max	7.541	7.581	7.768	7.567	7.780	7.593	7.750	7.579	7.622	7.661	7.557
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



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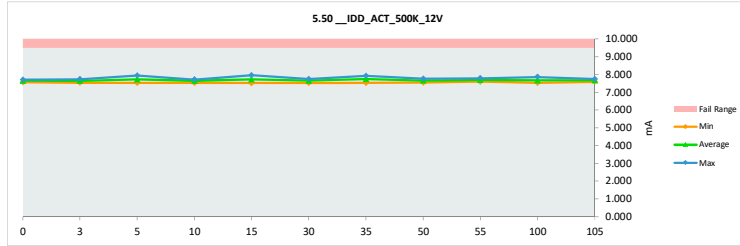
5.50 IDD_ACT_500K_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	9.5	9.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.579	7.574	-0.005
0	118	7.698	7.695	-0.003
0	122	7.709	7.703	-0.006
3	61	7.730	7.708	-0.022
3	62	7.742	7.738	-0.004
3	63	7.620	7.623	0.003
3	64	7.732	7.718	-0.014
3	65	7.572	7.552	-0.020
3	66	7.535	7.541	0.006
5	67	7.611	7.610	-0.001
5	68	7.745	7.739	-0.006
5	69	7.535	7.537	0.002
5	70	7.941	7.952	0.011
5	71	7.739	7.724	-0.015
5	72	7.802	7.800	-0.002
10	73	7.730	7.695	-0.035
10	74	7.742	7.720	-0.022
10	75	7.620	7.634	0.014
10	76	7.732	7.720	-0.012
10	77	7.572	7.571	-0.001
10	78	7.535	7.535	0.000
15	79	7.611	7.636	0.025
15	80	7.745	7.733	-0.012
15	81	7.535	7.515	-0.020
15	82	7.941	7.960	0.019
15	83	7.739	7.721	-0.018
15	84	7.802	7.802	0.000
30	86	7.730	7.746	0.016
30	89	7.742	7.747	0.005
30	91	7.620	7.645	0.025
30	92	7.732	7.681	-0.051
30	94	7.572	7.571	-0.001
30	95	7.535	7.530	-0.005
35	100	7.611	7.652	0.041
35	101	7.745	7.777	0.032
35	102	7.535	7.537	0.002
35	103	7.941	7.927	-0.014
35	104	7.739	7.739	0.000
35	105	7.802	7.813	0.011
50	106	7.742	7.765	0.023
50	107	7.645	7.677	0.032
50	108	7.532	7.584	0.052
50	109	7.643	7.693	0.050
50	110	7.722	7.727	0.005
50	111	7.561	7.554	-0.007
55	113	7.757	7.747	-0.010
55	114	7.638	7.610	-0.028
55	115	7.695	7.701	0.006
55	116	7.639	7.641	0.002
55	126	7.809	7.793	-0.016
55	128	7.709	7.637	-0.072
100	129	7.764	7.829	0.065
100	131	7.599	7.529	-0.070
100	132	7.743	7.710	-0.033
100	133	7.765	7.785	0.020
100	134	7.629	7.661	0.032
100	135	7.643	7.603	-0.040
100	138	7.668	7.723	0.055
100	139	7.520	7.565	0.045
100	140	7.648	7.683	0.035
100	141	7.557	7.553	-0.004
100	142	7.704	7.754	0.050
100	143	7.661	7.712	0.051
100	144	7.610	7.638	0.028
100	220	7.722	7.705	-0.017
100	221	7.577	7.583	0.006
100	224	7.636	7.667	0.031
100	225	7.569	7.575	0.006
100	226	7.835	7.864	0.029
100	228	7.601	7.709	0.108
100	232	7.674	7.739	0.065
100	233	7.694	7.705	0.011
100	236	7.516	7.526	0.010
100	238	7.706	7.739	0.033
100	243	7.580	7.560	-0.020
105	244	7.646	7.636	-0.010
105	246	7.519	7.593	0.074
105	248	7.722	7.746	0.024
105	374	7.670	7.752	0.082
105	376	7.607	7.632	0.025
105	Max	7.941	7.960	0.108
105	Average	7.672	7.680	0.008
105	Min	7.516	7.515	-0.072
105	Std Dev	0.099	0.100	0.032



5.50 IDD_ACT_5		
Test Site		
Tester		
Test Number		
Min Limit	9.5	mA
Max Limit		
Krad(Si)	0	3
LL		
Min	7.574	7.541
Average	7.657	7.647
Max	7.703	7.738
UL	9.500	9.500

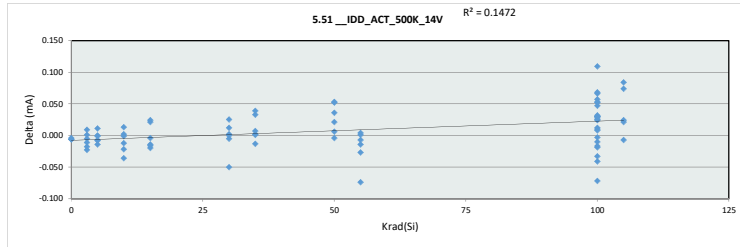
	5	10	15	30	35	50	55	100	105
Min	7.537	7.535	7.515	7.530	7.537	7.554	7.610	7.526	7.593
Average	7.646	7.646	7.628	7.653	7.741	7.667	7.688	7.672	7.672
Max	7.952	7.952	7.960	7.947	7.927	7.765	7.793	7.864	7.752
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



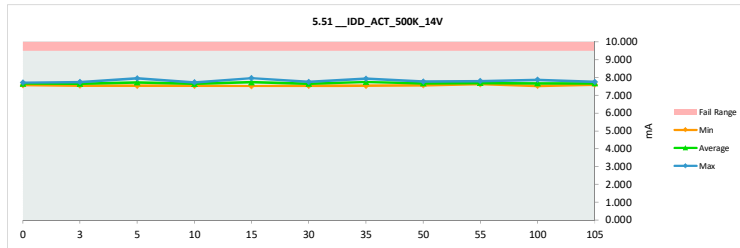
LDR TID Report
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5.51 IDD_ACT_500K_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.582	7.576	-0.006
0	118	7.703	7.699	-0.004
0	122	7.714	7.708	-0.006
3	61	7.737	7.714	-0.023
3	62	7.748	7.743	-0.005
3	63	7.625	7.626	0.001
3	64	7.734	7.723	-0.011
3	65	7.576	7.558	-0.018
3	66	7.540	7.549	0.009
5	67	7.614	7.614	0.000
5	68	7.749	7.742	-0.007
5	69	7.540	7.539	-0.001
5	70	7.944	7.955	0.011
5	71	7.742	7.728	-0.014
5	72	7.810	7.802	-0.008
10	73	7.737	7.701	-0.036
10	74	7.748	7.726	-0.022
10	75	7.625	7.638	0.013
10	76	7.734	7.722	-0.012
10	77	7.576	7.578	0.002
10	78	7.540	7.539	-0.001
15	79	7.614	7.638	0.024
15	80	7.749	7.735	-0.014
15	81	7.540	7.520	-0.020
15	82	7.944	7.965	0.021
15	83	7.742	7.726	-0.016
15	84	7.810	7.806	-0.004
30	86	7.737	7.749	0.012
30	89	7.748	7.750	0.002
30	91	7.625	7.650	0.025
30	92	7.734	7.684	-0.050
30	94	7.576	7.576	0.000
30	95	7.540	7.535	-0.005
35	100	7.614	7.653	0.039
35	101	7.749	7.782	0.033
35	102	7.540	7.542	0.002
35	103	7.944	7.931	-0.013
35	104	7.742	7.743	0.001
35	105	7.810	7.817	0.007
50	106	7.747	7.768	0.021
50	107	7.648	7.684	0.036
50	108	7.536	7.588	0.052
50	109	7.647	7.700	0.053
50	110	7.726	7.732	0.006
50	111	7.566	7.562	-0.004
55	113	7.762	7.755	-0.007
55	114	7.645	7.618	-0.027
55	115	7.698	7.702	0.004
55	116	7.646	7.647	0.001
55	126	7.812	7.798	-0.014
55	128	7.715	7.641	-0.074
100	129	7.766	7.834	0.068
100	131	7.606	7.534	-0.072
100	132	7.748	7.715	-0.033
100	133	7.768	7.792	0.024
100	134	7.636	7.666	0.030
100	135	7.647	7.606	-0.041
100	138	7.673	7.730	0.057
100	139	7.526	7.573	0.047
100	140	7.654	7.684	0.030
100	141	7.565	7.555	-0.010
100	142	7.708	7.760	0.052
100	143	7.665	7.718	0.053
100	144	7.614	7.639	0.025
100	220	7.725	7.708	-0.017
100	221	7.586	7.583	-0.003
100	224	7.641	7.671	0.030
100	225	7.573	7.583	0.010
100	226	7.840	7.868	0.028
100	228	7.607	7.716	0.109
100	232	7.677	7.743	0.066
100	233	7.698	7.710	0.012
100	236	7.520	7.528	0.008
100	238	7.713	7.745	0.032
100	243	7.583	7.564	-0.019
105	244	7.649	7.642	-0.007
105	246	7.523	7.597	0.074
105	248	7.727	7.751	0.024
105	374	7.674	7.758	0.084
105	376	7.611	7.632	0.021
105	Max	7.944	7.965	0.109
105	Average	7.677	7.685	0.008
105	Min	7.520	7.520	-0.074
105	Std Dev	0.098	0.100	0.032



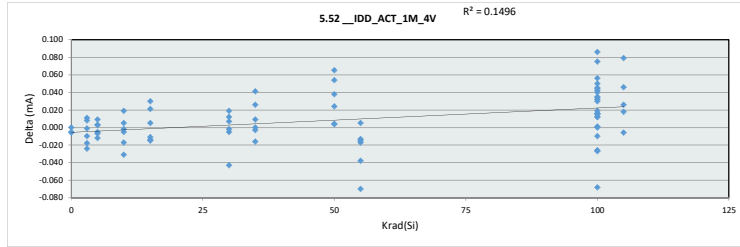
5.51 IDD_ACT_5											
Test Site											
Tester											
Test Number											
Max Limit	9.5 mA										
Min Limit	9.5 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.576	7.549	7.539	7.539	7.520	7.535	7.542	7.562	7.618	7.528	7.597
Average	7.661	7.652	7.730	7.651	7.732	7.657	7.745	7.672	7.694	7.676	7.676
Max	7.708	7.743	7.955	7.726	7.965	7.750	7.931	7.768	7.798	7.868	7.758
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



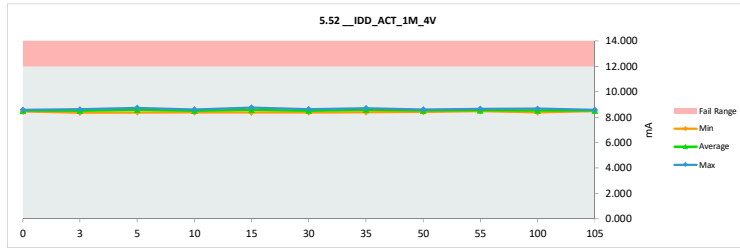
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5.52 IDD_ACT_1M_4V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	12
Min Limit	12

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.436	8.431	-0.005
0	118	8.532	8.532	0.000
0	122	8.576	8.570	-0.006
3	61	8.566	8.542	-0.024
3	62	8.614	8.613	-0.001
3	63	8.486	8.494	0.008
3	64	8.566	8.556	-0.010
3	65	8.374	8.356	-0.018
3	66	8.399	8.410	0.011
5	67	8.455	8.458	0.003
5	68	8.617	8.610	-0.007
5	69	8.370	8.373	0.003
5	70	8.720	8.729	0.009
5	71	8.579	8.567	-0.012
5	72	8.654	8.649	-0.005
10	73	8.566	8.535	-0.031
10	74	8.614	8.597	-0.017
10	75	8.486	8.505	0.019
10	76	8.566	8.561	-0.005
10	77	8.374	8.372	-0.002
10	78	8.399	8.404	0.005
15	79	8.455	8.485	0.030
15	80	8.617	8.606	-0.011
15	81	8.370	8.356	-0.014
15	82	8.720	8.741	0.021
15	83	8.579	8.564	-0.015
15	84	8.654	8.659	0.005
30	86	8.566	8.578	0.012
30	89	8.614	8.621	0.007
30	91	8.486	8.505	0.019
30	92	8.566	8.523	-0.043
30	94	8.374	8.372	-0.002
30	95	8.399	8.394	-0.005
35	100	8.455	8.496	0.041
35	101	8.617	8.643	0.026
35	102	8.370	8.370	0.000
35	103	8.720	8.704	-0.016
35	104	8.579	8.576	-0.003
35	105	8.654	8.663	0.009
50	106	8.567	8.591	0.024
50	107	8.467	8.505	0.038
50	108	8.359	8.424	0.065
50	109	8.525	8.579	0.054
50	110	8.539	8.543	0.004
50	111	8.405	8.409	0.004
55	113	8.572	8.534	-0.038
55	114	8.488	8.471	-0.017
55	115	8.561	8.548	-0.013
55	116	8.502	8.507	0.005
55	126	8.655	8.640	-0.015
55	128	8.588	8.518	-0.070
100	129	8.575	8.619	0.044
100	131	8.470	8.402	-0.068
100	132	8.597	8.571	-0.026
100	133	8.579	8.609	0.030
100	134	8.476	8.510	0.034
100	135	8.504	8.477	-0.027
100	138	8.522	8.578	0.056
100	139	8.381	8.426	0.045
100	140	8.489	8.521	0.032
100	141	8.408	8.409	0.001
100	142	8.562	8.604	0.042
100	143	8.512	8.562	0.050
100	144	8.475	8.487	0.012
100	220	8.569	8.559	-0.010
100	221	8.442	8.457	0.015
100	224	8.473	8.508	0.035
100	225	8.376	8.388	0.012
100	226	8.643	8.659	0.016
100	228	8.447	8.533	0.086
100	232	8.508	8.583	0.075
100	233	8.512	8.531	0.019
100	236	8.355	8.371	0.016
100	238	8.576	8.616	0.040
100	243	8.411	8.411	0.000
105	244	8.484	8.478	-0.006
105	246	8.398	8.477	0.079
105	248	8.558	8.576	0.018
105	374	8.493	8.539	0.046
105	376	8.447	8.473	0.026
105	Max	8.720	8.741	0.086
105	Average	8.515	8.524	0.009
105	Min	8.355	8.356	-0.070
105	Std Dev	0.094	0.092	0.029



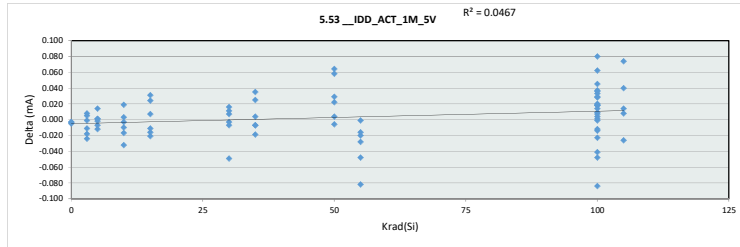
5.52 IDD_ACT	
Test Site	
Tester	
Test Number	
Max Limit	12 mA
Min Limit	12 mA
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	8.431 8.356 8.373 8.372 8.356 8.372 8.370 8.409 8.471 8.371 8.473
Average	8.511 8.495 8.564 8.496 8.569 8.499 8.575 8.509 8.536 8.516 8.509
Max	8.570 8.613 8.729 8.597 8.741 8.621 8.704 8.591 8.640 8.659 8.576
UL	12.000 12.000 12.000 12.000 12.000 12.000 12.000 12.000 12.000 12.000 12.000



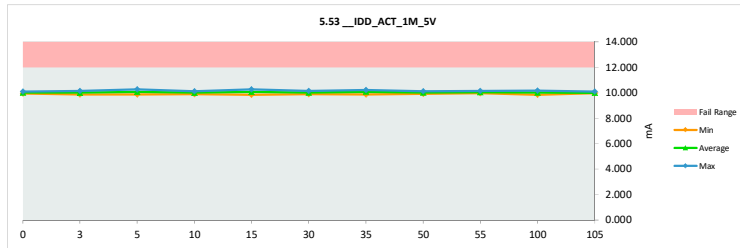
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5.53 IDD_ACT_1M_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	12
Min Limit	12

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.941	9.936	-0.005
0	118	10.029	10.026	-0.003
0	122	10.090	10.087	-0.003
3	61	10.084	10.060	-0.024
3	62	10.134	10.133	-0.001
3	63	9.981	9.986	0.005
3	64	10.084	10.073	-0.011
3	65	9.885	9.867	-0.018
3	66	9.908	9.916	0.008
5	67	9.967	9.968	0.001
5	68	10.116	10.109	-0.007
5	69	9.875	9.876	0.001
5	70	10.248	10.262	0.014
5	71	10.089	10.077	-0.012
5	72	10.170	10.168	-0.002
10	73	10.084	10.052	-0.032
10	74	10.134	10.117	-0.017
10	75	9.981	10.000	0.019
10	76	10.084	10.074	-0.010
10	77	9.885	9.882	-0.003
10	78	9.908	9.911	0.003
15	79	9.967	9.998	0.031
15	80	10.116	10.105	-0.011
15	81	9.875	9.854	-0.021
15	82	10.248	10.272	0.024
15	83	10.089	10.073	-0.016
15	84	10.170	10.177	0.007
30	86	10.084	10.095	0.011
30	89	10.134	10.141	0.007
30	91	9.981	9.997	0.016
30	92	10.084	10.035	-0.049
30	94	9.885	9.882	-0.003
30	95	9.908	9.901	-0.007
35	100	9.967	10.002	0.035
35	101	10.116	10.141	0.025
35	102	9.875	9.868	-0.007
35	103	10.248	10.229	-0.019
35	104	10.089	10.082	-0.007
35	105	10.170	10.174	0.004
50	106	10.090	10.112	0.022
50	107	10.006	10.035	0.029
50	108	9.861	9.925	0.064
50	109	10.032	10.090	0.058
50	110	10.059	10.053	-0.006
50	111	9.915	9.919	0.004
55	113	10.083	10.035	-0.048
55	114	9.988	9.960	-0.028
55	115	10.074	10.054	-0.020
55	116	9.998	9.997	-0.001
55	126	10.169	10.153	-0.016
55	128	10.111	10.029	-0.082
100	129	10.091	10.120	0.029
100	131	9.976	9.892	-0.084
100	132	10.126	10.085	-0.041
100	133	10.087	10.107	0.020
100	134	10.017	10.035	0.018
100	135	10.016	9.968	-0.048
100	138	10.036	10.081	0.045
100	139	9.875	9.911	0.036
100	140	9.997	10.016	0.019
100	141	9.923	9.909	-0.014
100	142	10.076	10.113	0.037
100	143	10.012	10.045	0.033
100	144	9.983	9.987	0.004
100	220	10.072	10.049	-0.023
100	221	9.950	9.951	0.001
100	224	9.980	9.998	0.018
100	225	9.891	9.890	-0.001
100	226	10.154	10.168	0.014
100	228	9.948	10.028	0.080
100	232	10.031	10.093	0.062
100	233	10.035	10.042	0.007
100	236	9.838	9.847	0.009
100	238	10.086	10.114	0.028
100	243	9.907	9.895	-0.012
105	244	9.986	9.960	-0.026
105	246	9.894	9.968	0.074
105	248	10.071	10.085	0.014
105	374	9.980	10.020	0.040
105	375	9.967	9.975	0.008
105	Max	10.248	10.272	0.024
105	Average	10.026	10.029	0.003
105	Min	9.838	9.847	-0.009
105	Std Dev	0.099	0.098	0.030



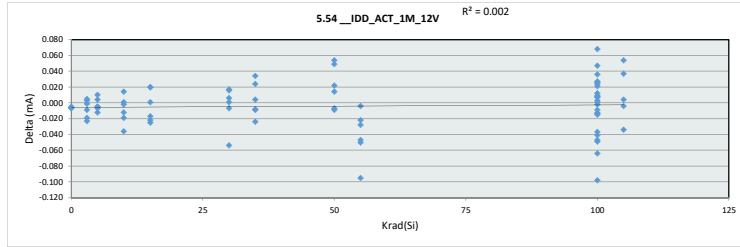
5.53 IDD_ACT											
Test Site											
Tester											
Test Number											
Max Limit	12 mA										
Min Limit	12 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.936	9.867	9.876	9.882	9.854	9.882	9.868	9.919	9.960	9.847	9.960
Average	10.016	10.006	10.077	10.006	10.080	10.009	10.083	10.022	10.038	10.014	10.002
Max	10.087	10.133	10.262	10.117	10.272	10.141	10.229	10.112	10.153	10.168	10.085
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000



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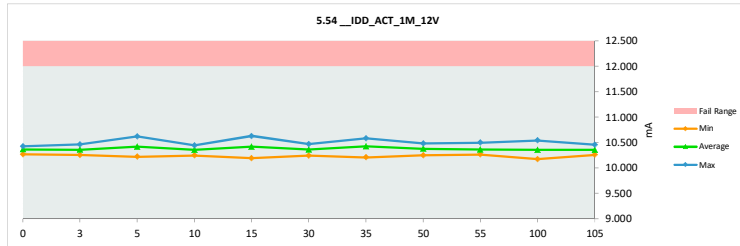
5.54 IDD_ACT_1M_12V			
Test Site			
Tester			
Test Number			
Unit	mA		mA
Max Limit	12		12
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.275	10.268	-0.007
0	118	10.413	10.407	-0.006
0	122	10.428	10.423	-0.005
3	61	10.454	10.431	-0.023
3	62	10.463	10.462	-0.001
3	63	10.279	10.282	0.003
3	64	10.449	10.440	-0.009
3	65	10.311	10.292	-0.019
3	66	10.249	10.254	0.005
5	67	10.305	10.300	-0.005
5	68	10.447	10.440	-0.007
5	69	10.217	10.221	0.004
5	70	10.610	10.620	0.010
5	71	10.439	10.427	-0.012
5	72	10.509	10.504	-0.005
10	73	10.454	10.418	-0.036
10	74	10.463	10.444	-0.019
10	75	10.279	10.293	0.014
10	76	10.449	10.437	-0.012
10	77	10.311	10.312	0.001
10	78	10.249	10.247	-0.002
15	79	10.305	10.325	0.020
15	80	10.447	10.430	-0.017
15	81	10.217	10.195	-0.022
15	82	10.610	10.630	0.020
15	83	10.439	10.414	-0.025
15	84	10.509	10.510	0.001
30	86	10.454	10.471	0.017
30	89	10.463	10.469	0.006
30	91	10.279	10.295	0.016
30	92	10.449	10.395	-0.054
30	94	10.311	10.312	0.001
30	95	10.249	10.242	-0.007
35	100	10.305	10.339	0.034
35	101	10.447	10.471	0.024
35	102	10.217	10.209	-0.008
35	103	10.610	10.586	-0.024
35	104	10.439	10.430	-0.009
35	105	10.509	10.513	0.004
50	106	10.470	10.484	0.014
50	107	10.380	10.402	0.022
50	108	10.235	10.289	0.054
50	109	10.339	10.388	0.049
50	110	10.472	10.463	-0.009
50	111	10.256	10.249	-0.007
55	113	10.484	10.434	-0.050
55	114	10.312	10.265	-0.047
55	115	10.381	10.353	-0.028
55	116	10.322	10.318	-0.004
55	126	10.521	10.499	-0.022
55	128	10.414	10.319	-0.095
100	129	10.505	10.529	0.024
100	131	10.316	10.218	-0.098
100	132	10.461	10.412	-0.049
100	133	10.513	10.514	0.001
100	134	10.369	10.378	0.009
100	135	10.327	10.263	-0.064
100	138	10.383	10.419	0.036
100	139	10.223	10.244	0.021
100	140	10.363	10.375	0.012
100	141	10.262	10.225	-0.037
100	142	10.422	10.448	0.026
100	143	10.349	10.376	0.027
100	144	10.334	10.337	0.003
100	220	10.421	10.374	-0.047
100	221	10.265	10.252	-0.013
100	224	10.337	10.345	0.008
100	225	10.323	10.310	-0.013
100	226	10.552	10.543	-0.009
100	228	10.314	10.382	0.068
100	232	10.394	10.441	0.047
100	233	10.410	10.408	-0.002
100	236	10.190	10.175	-0.015
100	238	10.410	10.417	0.007
100	243	10.276	10.235	-0.041
105	244	10.347	10.313	-0.034
105	246	10.205	10.259	0.054
105	248	10.455	10.459	0.004
105	374	10.365	10.402	0.037
105	376	10.338	10.334	-0.004
105	Max	10.610	10.630	0.068
105	Average	10.379	10.375	-0.004
105	Min	10.190	10.175	-0.098
105	Std Dev	0.102	0.103	0.030



5.54 IDD_ACT		
Test Site		
Tester		
Test Number		
Max Limit	12	mA
Min Limit		mA

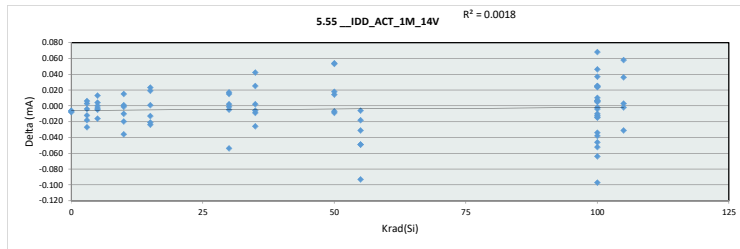
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.268	10.254	10.221	10.247	10.195	10.242	10.209	10.249	10.265	10.175	10.259
Average	10.366	10.360	10.419	10.359	10.417	10.364	10.425	10.379	10.365	10.359	10.353
Max	10.423	10.462	10.620	10.444	10.630	10.471	10.586	10.484	10.499	10.543	10.459
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000



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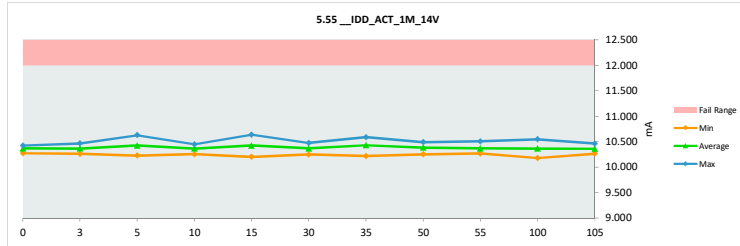
5.55 IDD_ACT_1M_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	12
Min Limit	12

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.279	10.272	-0.007
0	118	10.417	10.411	-0.006
0	122	10.433	10.425	-0.008
3	61	10.460	10.433	-0.027
3	62	10.469	10.465	-0.004
3	63	10.283	10.286	0.003
3	64	10.454	10.442	-0.012
3	65	10.316	10.298	-0.018
3	66	10.254	10.260	0.006
5	67	10.308	10.305	-0.003
5	68	10.451	10.446	-0.005
5	69	10.223	10.227	0.004
5	70	10.615	10.628	0.013
5	71	10.442	10.426	-0.016
5	72	10.513	10.513	0.000
10	73	10.460	10.424	-0.036
10	74	10.469	10.449	-0.020
10	75	10.283	10.298	0.015
10	76	10.454	10.444	-0.010
10	77	10.316	10.317	0.001
10	78	10.254	10.253	-0.001
15	79	10.308	10.331	0.023
15	80	10.451	10.438	-0.013
15	81	10.223	10.202	-0.021
15	82	10.615	10.634	0.019
15	83	10.442	10.418	-0.024
15	84	10.513	10.514	0.001
30	86	10.460	10.475	0.015
30	89	10.469	10.471	0.002
30	91	10.283	10.300	0.017
30	92	10.454	10.400	-0.054
30	94	10.316	10.315	-0.001
30	95	10.254	10.249	-0.005
35	100	10.308	10.350	0.042
35	101	10.451	10.476	0.025
35	102	10.223	10.217	-0.006
35	103	10.615	10.589	-0.026
35	104	10.442	10.433	-0.009
35	105	10.513	10.515	0.002
50	106	10.476	10.490	0.014
50	107	10.385	10.403	0.018
50	108	10.239	10.293	0.054
50	109	10.342	10.395	0.053
50	110	10.476	10.467	-0.009
50	111	10.259	10.252	-0.007
55	113	10.488	10.439	-0.049
55	114	10.317	10.268	-0.049
55	115	10.387	10.356	-0.031
55	116	10.327	10.321	-0.006
55	126	10.523	10.505	-0.018
55	128	10.419	10.326	-0.093
100	129	10.509	10.533	0.024
100	131	10.321	10.224	-0.097
100	132	10.468	10.416	-0.052
100	133	10.519	10.517	-0.002
100	134	10.375	10.381	0.006
100	135	10.332	10.268	-0.064
100	138	10.386	10.423	0.037
100	139	10.227	10.251	0.024
100	140	10.367	10.377	0.010
100	141	10.266	10.232	-0.034
100	142	10.427	10.452	0.025
100	143	10.353	10.378	0.025
100	144	10.338	10.343	0.005
100	220	10.425	10.379	-0.046
100	221	10.269	10.257	-0.012
100	224	10.342	10.349	0.007
100	225	10.327	10.313	-0.014
100	226	10.556	10.546	-0.010
100	228	10.318	10.386	0.068
100	232	10.398	10.444	0.046
100	233	10.413	10.409	-0.004
100	236	10.195	10.180	-0.015
100	238	10.414	10.420	0.006
100	243	10.278	10.240	-0.038
105	244	10.349	10.318	-0.031
105	246	10.207	10.265	0.058
105	248	10.459	10.462	0.003
105	374	10.368	10.404	0.036
105	376	10.341	10.339	-0.002
105	Max	10.615	10.634	0.068
105	Average	10.383	10.380	-0.004
105	Min	10.195	10.180	-0.097
105	Std Dev	0.102	0.102	0.030



5.55 IDD_ACT	
Test Site	
Tester	
Test Number	
Max Limit	12 mA
Min Limit	12 mA

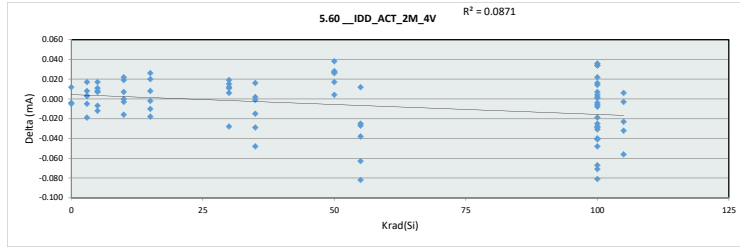
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.272	10.260	10.227	10.253	10.202	10.249	10.217	10.252	10.268	10.180	10.265
Average	10.369	10.364	10.424	10.364	10.423	10.368	10.430	10.383	10.369	10.363	10.358
Max	10.425	10.465	10.628	10.449	10.634	10.475	10.589	10.490	10.505	10.546	10.462
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000



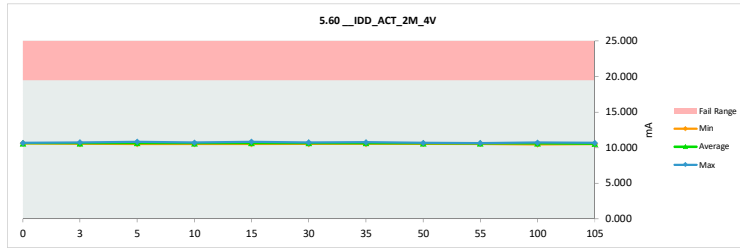
LDR TID Report
TPS7H5001-SP

5.60 IDD_ACT_2M_4V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	19.5	19.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.578	10.574	-0.004
0	118	10.578	10.573	-0.005
0	122	10.638	10.650	0.012
3	61	10.601	10.582	-0.019
3	62	10.692	10.700	0.008
3	63	10.614	10.631	0.017
3	64	10.679	10.682	0.003
3	65	10.512	10.507	-0.005
3	66	10.530	10.533	0.003
5	67	10.495	10.503	0.008
5	68	10.673	10.661	-0.012
5	69	10.515	10.522	0.007
5	70	10.777	10.794	0.017
5	71	10.649	10.642	-0.007
5	72	10.690	10.701	0.011
10	73	10.691	10.585	-0.106
10	74	10.692	10.689	-0.003
10	75	10.614	10.636	0.022
10	76	10.679	10.679	0.000
10	77	10.512	10.519	0.007
10	78	10.530	10.549	0.019
15	79	10.495	10.521	0.026
15	80	10.673	10.671	-0.002
15	81	10.515	10.497	-0.018
15	82	10.777	10.797	0.020
15	83	10.649	10.639	-0.010
15	84	10.690	10.698	0.008
30	86	10.601	10.620	0.019
30	89	10.692	10.703	0.011
30	91	10.614	10.626	0.012
30	92	10.679	10.651	-0.028
30	94	10.512	10.518	0.006
30	95	10.530	10.545	0.015
35	100	10.495	10.511	0.016
35	101	10.673	10.675	0.002
35	102	10.515	10.486	-0.029
35	103	10.777	10.729	-0.048
35	104	10.649	10.634	-0.015
35	105	10.690	10.689	-0.001
50	106	10.615	10.641	0.026
50	107	10.504	10.521	0.017
50	108	10.583	10.587	0.004
50	109	10.561	10.599	0.038
50	110	10.561	10.589	0.028
50	111	10.558	10.585	0.027
55	113	10.688	10.625	-0.063
55	114	10.576	10.549	-0.027
55	115	10.587	10.562	-0.025
55	116	10.523	10.535	0.012
55	126	10.673	10.635	-0.038
55	128	10.639	10.557	-0.082
100	129	10.605	10.627	0.022
100	131	10.576	10.509	-0.067
100	132	10.703	10.632	-0.071
100	133	10.617	10.624	0.007
100	134	10.543	10.559	0.016
100	135	10.750	10.669	-0.081
100	138	10.560	10.574	0.014
100	139	10.548	10.540	-0.008
100	140	10.569	10.603	0.034
100	141	10.521	10.481	-0.040
100	142	10.621	10.622	0.001
100	143	10.565	10.567	0.002
100	144	10.562	10.531	-0.031
100	220	10.657	10.629	-0.028
100	221	10.531	10.512	-0.019
100	224	10.355	10.359	0.004
100	225	10.571	10.542	-0.029
100	226	10.696	10.690	-0.006
100	228	10.529	10.565	0.036
100	232	10.606	10.640	0.034
100	233	10.646	10.642	-0.004
100	236	10.535	10.494	-0.041
100	238	10.711	10.686	-0.025
100	243	10.481	10.433	-0.048
105	244	10.538	10.482	-0.056
105	246	10.515	10.521	0.006
105	248	10.673	10.641	-0.032
105	374	10.556	10.533	-0.023
105	376	10.564	10.561	-0.003
105	Max	10.777	10.797	0.038
105	Average	10.603	10.597	-0.006
105	Min	10.481	10.433	-0.082
105	Std Dev	0.075	0.075	0.027



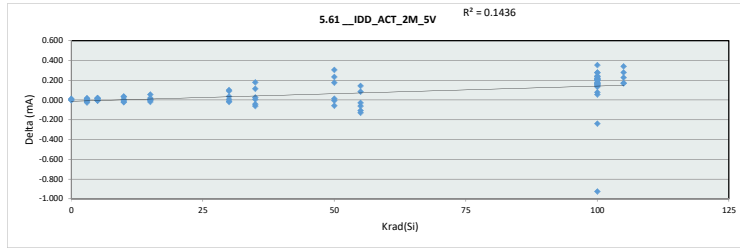
5.60 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.573	10.507	10.503	10.519	10.497	10.518	10.486	10.521	10.535	10.433	10.482
Average	10.599	10.606	10.637	10.610	10.637	10.611	10.621	10.587	10.577	10.580	10.548
Max	10.650	10.700	10.794	10.689	10.797	10.703	10.729	10.641	10.635	10.690	10.641
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500



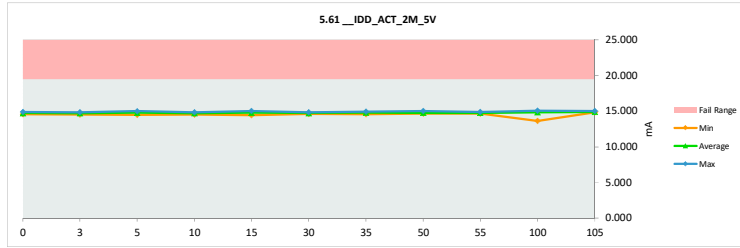
LDR TID Report
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5.61 IDD_ACT_2M_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	19.5
Min Limit	19.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	14.561	14.557	-0.004
0	118	14.725	14.727	0.002
0	122	14.863	14.875	0.012
3	61	14.778	14.750	-0.028
3	62	14.838	14.845	0.007
3	63	14.592	14.606	0.014
3	64	14.787	14.783	-0.004
3	65	14.525	14.509	-0.016
3	66	14.591	14.605	0.014
5	67	14.628	14.648	0.020
5	68	14.824	14.818	-0.006
5	69	14.447	14.463	0.016
5	70	14.975	14.992	0.017
5	71	14.768	14.759	-0.009
5	72	14.905	14.909	0.004
10	73	14.778	14.753	-0.025
10	74	14.838	14.819	-0.019
10	75	14.592	14.626	0.034
10	76	14.787	14.792	0.005
10	77	14.525	14.522	-0.003
10	78	14.591	14.621	0.030
15	79	14.628	14.683	0.055
15	80	14.824	14.825	0.001
15	81	14.447	14.425	-0.022
15	82	14.975	14.995	0.020
15	83	14.768	14.770	0.002
15	84	14.905	14.900	-0.005
30	86	14.778	14.782	0.004
30	89	14.838	14.824	-0.014
30	91	14.592	14.691	0.099
30	92	14.787	14.766	-0.021
30	94	14.525	14.616	0.091
30	95	14.591	14.627	0.036
35	100	14.628	14.805	0.177
35	101	14.824	14.830	0.006
35	102	14.447	14.560	0.113
35	103	14.975	14.911	-0.064
35	104	14.768	14.794	0.026
35	105	14.905	14.862	-0.043
50	106	14.766	14.999	0.233
50	107	14.657	14.670	0.013
50	108	14.447	14.749	0.302
50	109	14.712	14.706	-0.006
50	110	14.750	14.691	-0.059
50	111	14.509	14.683	0.174
55	113	14.798	14.882	0.084
55	114	14.633	14.775	0.142
55	115	14.749	14.688	-0.061
55	116	14.703	14.674	-0.029
55	126	14.839	14.731	-0.108
55	128	14.799	14.667	-0.132
100	129	14.832	14.886	0.054
100	131	14.721	14.798	0.077
100	132	14.904	15.037	0.133
100	133	14.818	15.051	0.233
100	134	14.816	14.993	0.177
100	135	14.569	13.641	-0.928
100	138	14.725	14.886	0.161
100	139	14.525	14.740	0.215
100	140	14.699	14.909	0.210
100	141	14.583	14.730	0.147
100	142	14.795	14.556	-0.239
100	143	14.678	14.912	0.234
100	144	14.707	14.921	0.214
100	220	14.794	14.934	0.140
100	221	14.623	14.819	0.196
100	224	14.641	14.834	0.193
100	225	14.534	14.740	0.206
100	226	14.819	15.060	0.241
100	228	14.637	14.988	0.351
100	232	14.793	15.069	0.276
100	233	14.749	14.906	0.157
100	236	14.381	14.548	0.167
100	238	14.779	15.053	0.274
100	243	14.574	14.746	0.172
105	244	14.662	14.833	0.171
105	246	14.539	14.878	0.339
105	248	14.776	15.001	0.225
105	374	14.618	14.896	0.278
105	376	14.678	14.846	0.168
105	375	14.975	15.069	0.351
Max				
Average		14.706	14.772	0.066
Min		14.381	13.641	-0.928
Std Dev		0.137	0.196	0.163



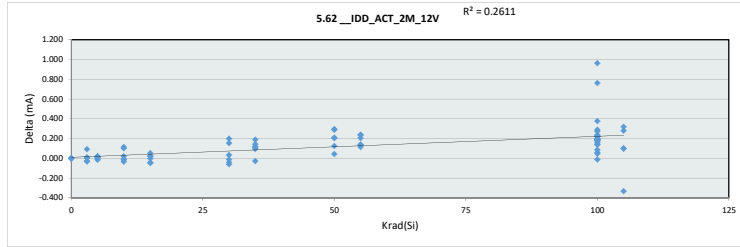
5.61 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5 mA										
Min Limit	19.5 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	14.557	14.509	14.463	14.522	14.425	14.616	14.560	14.670	14.667	13.641	14.833
Average	14.720	14.683	14.765	14.689	14.766	14.718	14.794	14.750	14.736	14.823	14.891
Max	14.875	14.845	14.992	14.819	14.995	14.824	14.911	14.999	14.882	15.069	15.001
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500



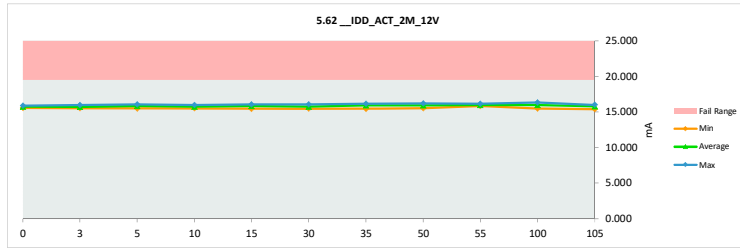
LDR TID Report
TPS7H5001-SP

5.62 IDD_ACT_2M_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	19.5	19.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	15.575	15.568	-0.007
0	118	15.846	15.845	-0.001
0	122	15.906	15.909	0.003
3	61	15.887	15.979	0.092
3	62	15.851	15.858	0.007
3	63	15.516	15.520	0.004
3	64	15.837	15.805	-0.032
3	65	15.735	15.705	-0.030
3	66	15.553	15.561	0.008
5	67	15.829	15.841	0.012
5	68	15.844	15.837	-0.007
5	69	15.487	15.493	0.006
5	70	16.069	16.087	0.018
5	71	15.834	15.819	-0.015
5	72	16.042	16.063	0.021
10	73	15.887	16.000	0.113
10	74	15.851	15.952	0.101
10	75	15.516	15.537	0.021
10	76	15.837	15.800	-0.037
10	77	15.735	15.718	-0.017
10	78	15.553	15.543	-0.010
15	79	15.829	15.881	0.052
15	80	15.844	15.840	-0.004
15	81	15.487	15.437	-0.050
15	82	16.069	16.091	0.022
15	83	15.834	15.791	-0.043
15	84	16.042	16.063	0.021
30	86	15.887	16.085	0.198
30	89	15.851	16.004	0.153
30	91	15.516	15.503	-0.013
30	92	15.837	15.777	-0.060
30	94	15.735	15.694	-0.041
30	95	15.553	15.585	0.032
35	100	15.829	15.940	0.111
35	101	15.844	15.987	0.143
35	102	15.487	15.459	-0.028
35	103	16.069	16.185	0.116
35	104	15.834	16.022	0.188
35	105	16.042	16.135	0.093
50	106	15.903	16.192	0.289
50	107	15.772	16.067	0.295
50	108	15.517	15.725	0.208
50	109	15.778	15.982	0.204
50	110	16.086	16.210	0.124
50	111	15.515	15.557	0.042
55	113	15.920	16.155	0.235
55	114	15.612	15.850	0.238
55	115	15.711	15.914	0.203
55	116	15.808	15.942	0.134
55	126	15.984	16.123	0.139
55	128	15.742	15.854	0.112
100	129	16.064	16.255	0.191
100	131	15.770	15.829	0.059
100	132	15.962	16.103	0.141
100	133	16.079	16.240	0.161
100	134	15.909	16.096	0.187
100	135	15.512	16.274	0.762
100	138	15.762	16.049	0.287
100	139	15.571	15.750	0.179
100	140	15.812	16.035	0.223
100	141	15.505	15.493	-0.012
100	142	15.850	16.123	0.273
100	143	15.674	15.893	0.219
100	144	15.763	15.956	0.193
100	220	15.839	15.883	0.044
100	221	15.512	15.691	0.179
100	224	15.716	15.936	0.220
100	225	15.771	15.856	0.085
100	226	15.939	16.139	0.200
100	228	15.663	16.040	0.377
100	232	15.928	16.165	0.237
100	233	15.770	15.952	0.182
100	236	15.384	16.347	0.963
100	238	15.774	16.000	0.226
100	243	15.593	15.730	0.137
105	244	15.704	15.800	0.096
105	246	15.493	15.810	0.317
105	248	15.861	15.962	0.101
105	374	15.737	16.014	0.277
105	376	15.728	15.395	-0.333
105	Max	16.086	16.347	0.963
105	Average	15.772	15.891	0.119
105	Min	15.384	15.395	-0.333
105	Std Dev	0.176	0.223	0.168



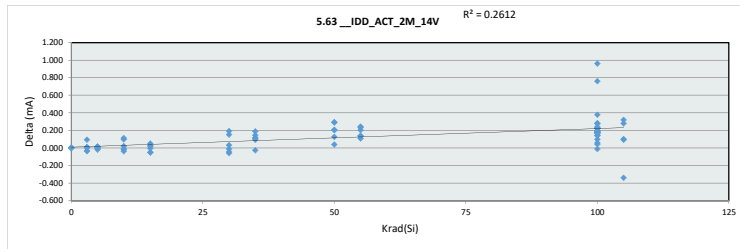
5.62 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	15.568	15.520	15.493	15.537	15.437	15.503	15.459	15.557	15.850	15.493	15.395
Average	15.774	15.738	15.857	15.758	15.851	15.775	15.955	15.956	15.973	15.993	15.796
Max	15.909	15.979	16.087	16.000	16.091	16.085	16.185	16.210	16.155	16.347	16.014
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500



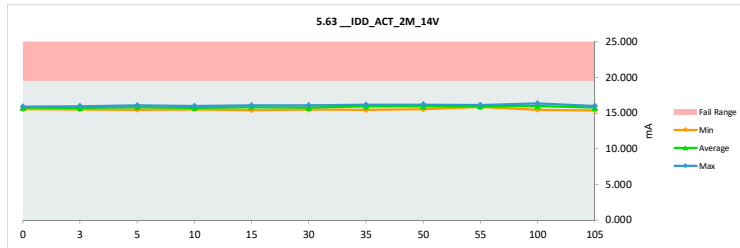
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5.63 IDD_ACT_2M_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	19.5
Min Limit	19.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	15.580	15.574	-0.006
0	118	15.854	15.856	0.002
0	122	15.908	15.916	0.008
3	61	15.893	15.986	0.093
3	62	15.857	15.861	0.004
3	63	15.521	15.527	0.006
3	64	15.843	15.809	-0.034
3	65	15.742	15.710	-0.032
3	66	15.560	15.570	0.010
5	67	15.836	15.845	0.009
5	68	15.848	15.842	-0.006
5	69	15.491	15.498	0.007
5	70	16.076	16.090	0.014
5	71	15.841	15.822	-0.019
5	72	16.047	16.065	0.018
10	73	15.893	16.007	0.114
10	74	15.857	15.955	0.098
10	75	15.521	15.542	0.021
10	76	15.843	15.806	-0.037
10	77	15.742	15.725	-0.017
10	78	15.560	15.549	-0.011
15	79	15.836	15.887	0.051
15	80	15.848	15.844	-0.004
15	81	15.491	15.441	-0.050
15	82	16.076	16.102	0.026
15	83	15.841	15.794	-0.047
15	84	16.047	16.069	0.022
30	86	15.893	16.086	0.193
30	89	15.857	16.010	0.153
30	91	15.521	15.510	-0.011
30	92	15.843	15.784	-0.059
30	94	15.742	15.699	-0.043
30	95	15.560	15.592	0.032
35	100	15.836	15.947	0.111
35	101	15.848	15.992	0.144
35	102	15.491	15.465	-0.026
35	103	16.076	16.191	0.115
35	104	15.841	16.030	0.189
35	105	16.047	16.141	0.094
50	106	15.909	16.200	0.291
50	107	15.778	16.072	0.294
50	108	15.535	15.729	0.204
50	109	15.783	15.990	0.207
50	110	16.092	16.219	0.127
50	111	15.521	15.559	0.038
55	113	15.926	16.160	0.234
55	114	15.616	15.858	0.242
55	115	15.717	15.920	0.203
55	116	15.815	15.951	0.136
55	126	15.991	16.128	0.137
55	128	15.748	15.858	0.110
100	129	16.072	16.259	0.187
100	131	15.776	15.834	0.058
100	132	15.968	16.110	0.142
100	133	16.082	16.246	0.164
100	134	15.915	16.105	0.190
100	135	15.520	16.282	0.762
100	138	15.768	16.052	0.284
100	139	15.577	15.752	0.175
100	140	15.818	16.042	0.224
100	141	15.512	15.500	-0.012
100	142	15.855	16.131	0.276
100	143	15.677	15.898	0.221
100	144	15.772	15.965	0.193
100	220	15.845	15.889	0.044
100	221	15.517	15.692	0.175
100	224	15.722	15.942	0.220
100	225	15.777	15.874	0.097
100	226	15.945	16.144	0.199
100	228	15.671	16.048	0.377
100	232	15.930	16.171	0.241
100	233	15.779	15.948	0.169
100	236	15.391	16.352	0.961
100	238	15.776	16.005	0.229
100	243	15.597	15.740	0.143
105	244	15.712	15.807	0.095
105	246	15.496	15.815	0.319
105	248	15.868	15.971	0.103
105	374	15.739	16.020	0.281
105	376	15.793	15.392	-0.341
105	Max	16.092	16.352	0.961
105	Average	15.778	15.897	0.119
105	Min	15.391	15.392	-0.341
105	Std Dev	0.176	0.224	0.169



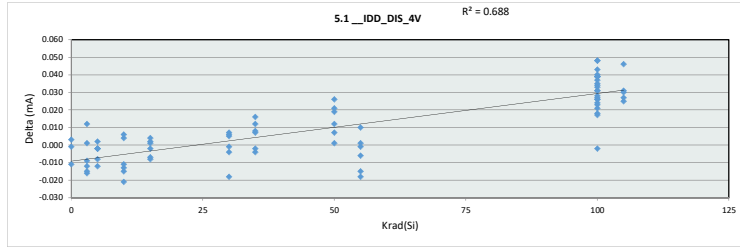
5.63 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5 mA										
Min Limit	19.5 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	15.574	15.527	15.498	15.542	15.441	15.510	15.465	15.559	15.858	15.500	15.392
Average	15.782	15.744	15.860	15.764	15.856	15.780	15.961	15.962	15.979	15.999	15.801
Max	15.916	15.986	16.090	16.007	16.102	16.086	16.191	16.219	16.160	16.352	16.020
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500



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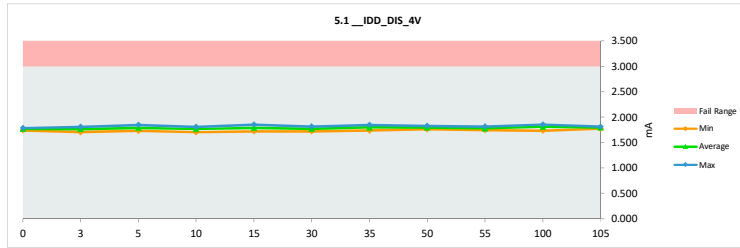
5.1 IDD_DIS_4V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	3	3
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.750	1.739	-0.011
0	118	1.782	1.781	-0.001
0	122	1.784	1.787	0.003
3	61	1.776	1.761	-0.015
3	62	1.819	1.807	-0.012
3	63	1.769	1.770	0.001
3	64	1.818	1.802	-0.016
3	65	1.714	1.705	-0.009
3	66	1.733	1.745	0.012
5	67	1.763	1.761	-0.002
5	68	1.822	1.820	-0.002
5	69	1.728	1.730	0.002
5	70	1.849	1.847	-0.002
5	71	1.791	1.783	-0.008
5	72	1.802	1.790	-0.012
10	73	1.776	1.755	-0.021
10	74	1.819	1.804	-0.015
10	75	1.769	1.773	0.004
10	76	1.818	1.807	-0.011
10	77	1.714	1.701	-0.013
10	78	1.733	1.739	0.006
15	79	1.763	1.767	0.004
15	80	1.822	1.820	-0.002
15	81	1.728	1.720	-0.008
15	82	1.849	1.851	0.002
15	83	1.791	1.792	0.001
15	84	1.802	1.795	-0.007
30	86	1.776	1.775	-0.001
30	89	1.819	1.815	-0.004
30	91	1.769	1.776	0.007
30	92	1.818	1.800	-0.018
30	94	1.714	1.720	0.006
30	95	1.733	1.738	0.005
35	100	1.763	1.775	0.012
35	101	1.822	1.838	0.016
35	102	1.728	1.736	0.008
35	103	1.849	1.847	-0.002
35	104	1.791	1.798	0.007
35	105	1.802	1.798	-0.004
50	106	1.816	1.823	0.007
50	107	1.800	1.826	0.026
50	108	1.750	1.771	0.021
50	109	1.796	1.815	0.019
50	110	1.784	1.785	0.001
50	111	1.748	1.760	0.012
55	113	1.788	1.773	-0.015
55	114	1.773	1.772	-0.001
55	115	1.765	1.775	0.010
55	116	1.790	1.791	0.001
55	126	1.822	1.816	-0.006
55	128	1.764	1.746	-0.018
100	129	1.795	1.834	0.039
100	131	1.762	1.760	-0.002
100	132	1.807	1.831	0.024
100	133	1.794	1.825	0.031
100	134	1.761	1.796	0.035
100	135	1.777	1.794	0.017
100	138	1.781	1.829	0.048
100	139	1.758	1.791	0.033
100	140	1.786	1.823	0.037
100	141	1.746	1.767	0.021
100	142	1.782	1.810	0.028
100	143	1.792	1.840	0.048
100	144	1.777	1.811	0.034
100	220	1.772	1.815	0.043
100	221	1.782	1.805	0.023
100	224	1.807	1.846	0.039
100	225	1.708	1.734	0.026
100	226	1.790	1.829	0.039
100	228	1.767	1.806	0.039
100	232	1.812	1.852	0.040
100	233	1.816	1.843	0.027
100	236	1.737	1.763	0.026
100	238	1.788	1.819	0.031
100	243	1.737	1.755	0.018
105	244	1.746	1.776	0.030
105	246	1.728	1.774	0.046
105	248	1.785	1.810	0.025
105	374	1.774	1.805	0.031
105	376	1.784	1.811	0.027
105	Max	1.849	1.852	0.048
105	Average	1.779	1.790	0.011
105	Min	1.708	1.701	-0.021
105	Std Dev	0.033	0.036	0.019



5.1 IDD DIS 4V		
Test Site		
Tester		
Test Number		
Max Limit	3	mA
Min Limit		

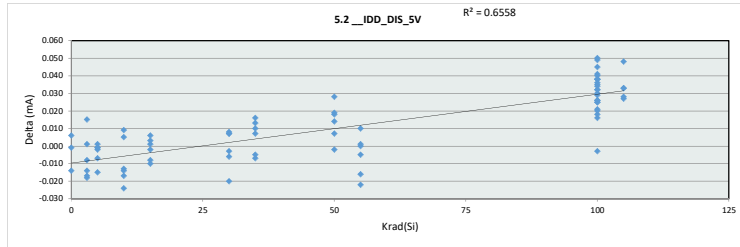
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.739	1.705	1.730	1.701	1.720	1.720	1.736	1.760	1.746	1.734	1.774
Average	1.769	1.765	1.789	1.763	1.791	1.771	1.799	1.797	1.779	1.807	1.795
Max	1.787	1.807	1.847	1.807	1.851	1.815	1.847	1.826	1.816	1.852	1.811
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000



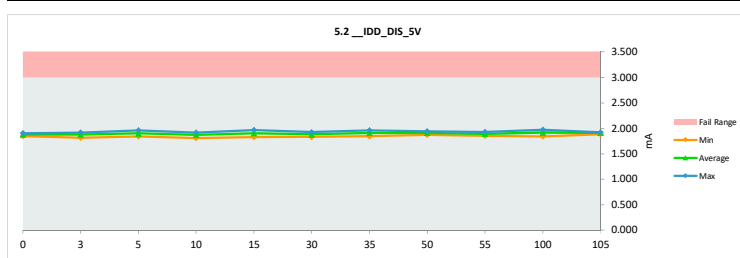
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5.2 IDD_DIS_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	3
Min Limit	3

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.858	1.844	-0.014
0	118	1.889	1.888	-0.001
0	122	1.892	1.898	0.006
3	61	1.886	1.869	-0.017
3	62	1.928	1.914	-0.014
3	63	1.876	1.877	0.001
3	64	1.928	1.910	-0.018
3	65	1.821	1.813	-0.008
3	66	1.838	1.853	0.015
5	67	1.870	1.869	-0.001
5	68	1.929	1.928	-0.001
5	69	1.834	1.835	0.001
5	70	1.961	1.959	-0.002
5	71	1.898	1.891	-0.007
5	72	1.912	1.897	-0.015
10	73	1.886	1.862	-0.024
10	74	1.928	1.911	-0.017
10	75	1.876	1.881	0.005
10	76	1.928	1.915	-0.013
10	77	1.821	1.807	-0.014
10	78	1.838	1.847	0.009
15	79	1.870	1.876	0.006
15	80	1.929	1.927	-0.002
15	81	1.834	1.826	-0.008
15	82	1.961	1.962	0.001
15	83	1.898	1.901	0.003
15	84	1.912	1.902	-0.010
30	86	1.886	1.883	-0.003
30	89	1.928	1.922	-0.006
30	91	1.876	1.883	0.007
30	92	1.928	1.908	-0.020
30	94	1.821	1.828	0.007
30	95	1.838	1.846	0.008
35	100	1.870	1.883	0.013
35	101	1.929	1.945	0.016
35	102	1.834	1.841	0.007
35	103	1.961	1.956	-0.005
35	104	1.898	1.908	0.010
35	105	1.912	1.905	-0.007
50	106	1.924	1.931	0.007
50	107	1.909	1.937	0.028
50	108	1.859	1.878	0.019
50	109	1.907	1.925	0.018
50	110	1.894	1.892	-0.002
50	111	1.854	1.868	0.014
55	113	1.897	1.881	-0.016
55	114	1.879	1.879	0.000
55	115	1.871	1.881	0.010
55	116	1.898	1.899	0.001
55	126	1.931	1.926	-0.005
55	128	1.873	1.851	-0.022
100	129	1.902	1.940	0.038
100	131	1.871	1.868	-0.003
100	132	1.915	1.941	0.026
100	133	1.901	1.933	0.032
100	134	1.868	1.902	0.034
100	135	1.886	1.902	0.016
100	138	1.887	1.936	0.049
100	139	1.866	1.896	0.030
100	140	1.896	1.932	0.036
100	141	1.852	1.872	0.020
100	142	1.889	1.918	0.029
100	143	1.900	1.950	0.050
100	144	1.885	1.920	0.035
100	220	1.879	1.924	0.045
100	221	1.892	1.913	0.021
100	224	1.916	1.954	0.038
100	225	1.813	1.838	0.025
100	226	1.899	1.939	0.040
100	228	1.873	1.914	0.041
100	232	1.923	1.961	0.038
100	233	1.945	1.970	0.025
100	236	1.846	1.872	0.026
100	238	1.896	1.928	0.032
100	243	1.844	1.862	0.018
105	244	1.854	1.887	0.033
105	246	1.834	1.882	0.048
105	248	1.892	1.920	0.028
105	374	1.882	1.915	0.033
105	376	1.894	1.921	0.027
105	Max	1.961	1.970	0.050
105	Average	1.887	1.898	0.011
105	Min	1.813	1.807	-0.024
105	Std Dev	0.035	0.037	0.019



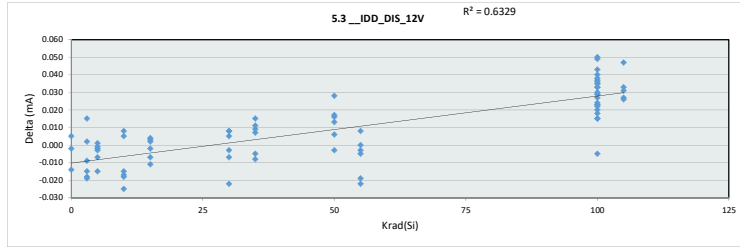
5.2 IDD DIS 5V	
Test Site	
Tester	
Test Number	
Max Limit	3 mA
Min Limit	3 mA



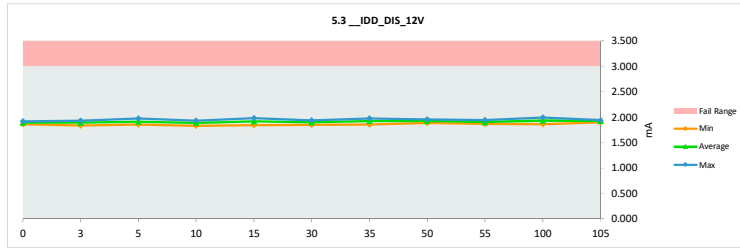
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5.3 IDD_DIS_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	3	3
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.874	1.860	-0.014
0	118	1.910	1.908	-0.002
0	122	1.909	1.914	0.005
3	61	1.906	1.888	-0.018
3	62	1.943	1.928	-0.015
3	63	1.889	1.891	0.002
3	64	1.947	1.928	-0.019
3	65	1.845	1.836	-0.009
3	66	1.855	1.870	0.015
5	67	1.888	1.885	-0.003
5	68	1.945	1.944	-0.001
5	69	1.850	1.851	0.001
5	70	1.978	1.976	-0.002
5	71	1.915	1.908	-0.007
5	72	1.928	1.913	-0.015
10	73	1.906	1.881	-0.025
10	74	1.943	1.925	-0.018
10	75	1.889	1.894	0.005
10	76	1.947	1.932	-0.015
10	77	1.845	1.828	-0.017
10	78	1.855	1.863	0.008
15	79	1.888	1.892	0.004
15	80	1.945	1.943	-0.002
15	81	1.850	1.843	-0.007
15	82	1.978	1.980	0.002
15	83	1.915	1.918	0.003
15	84	1.928	1.917	-0.011
30	86	1.906	1.903	-0.003
30	89	1.943	1.936	-0.007
30	91	1.889	1.897	0.008
30	92	1.947	1.925	-0.022
30	94	1.845	1.850	0.005
30	95	1.855	1.863	0.008
35	100	1.888	1.899	0.011
35	101	1.945	1.960	0.015
35	102	1.850	1.857	0.007
35	103	1.978	1.973	-0.005
35	104	1.915	1.924	0.009
35	105	1.928	1.920	-0.008
50	106	1.943	1.949	0.006
50	107	1.928	1.956	0.028
50	108	1.879	1.896	0.017
50	109	1.922	1.938	0.016
50	110	1.914	1.911	-0.003
50	111	1.871	1.884	0.013
55	113	1.919	1.900	-0.019
55	114	1.895	1.892	-0.003
55	115	1.886	1.894	0.008
55	116	1.914	1.914	0.000
55	126	1.948	1.943	-0.005
55	128	1.886	1.864	-0.022
100	129	1.923	1.959	0.036
100	131	1.887	1.882	-0.005
100	132	1.932	1.955	0.023
100	133	1.923	1.952	0.029
100	134	1.885	1.918	0.033
100	135	1.901	1.916	0.015
100	138	1.904	1.953	0.049
100	139	1.883	1.913	0.030
100	140	1.915	1.950	0.035
100	141	1.869	1.887	0.018
100	142	1.907	1.934	0.027
100	143	1.917	1.967	0.050
100	144	1.902	1.935	0.033
100	220	1.896	1.939	0.043
100	221	1.907	1.927	0.020
100	224	1.935	1.972	0.037
100	225	1.837	1.859	0.022
100	226	1.920	1.958	0.038
100	228	1.892	1.932	0.040
100	232	1.942	1.977	0.035
100	233	1.967	1.990	0.023
100	236	1.866	1.890	0.024
100	238	1.912	1.941	0.029
100	243	1.863	1.878	0.015
105	244	1.873	1.904	0.031
105	246	1.849	1.896	0.047
105	248	1.912	1.938	0.026
105	374	1.902	1.935	0.033
105	376	1.912	1.939	0.027
105	Max	1.978	1.990	0.050
105	Average	1.905	1.914	0.010
105	Min	1.837	1.828	-0.025
105	Std Dev	0.034	0.037	0.019



5.3 IDD DIS 12V											
Test Site											
Tester											
Test Number											
Max Limit	3	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.860	1.836	1.851	1.828	1.843	1.850	1.857	1.884	1.864	1.859	1.896
Average	1.894	1.890	1.913	1.887	1.916	1.896	1.922	1.922	1.901	1.933	1.922
Max	1.914	1.928	1.976	1.932	1.980	1.936	1.973	1.956	1.943	1.990	1.939
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

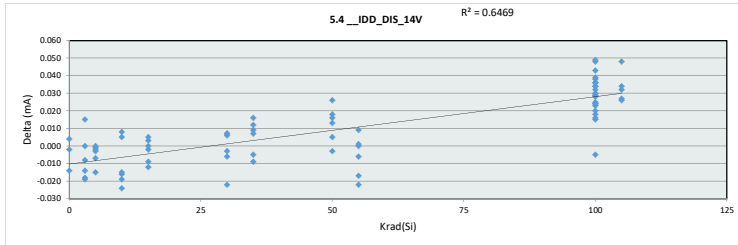


LDR TID Report
TPS7H5001-SP

5.4 IDD_DIS_14V

Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	3	
Min Limit	3	

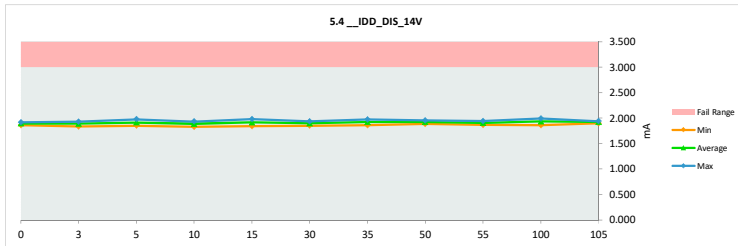
Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.879	1.865	-0.014
0	118	1.914	1.912	-0.002
0	122	1.914	1.918	0.004
3	61	1.910	1.892	-0.018
3	62	1.947	1.933	-0.014
3	63	1.894	1.894	0.000
3	64	1.951	1.932	-0.019
3	65	1.848	1.840	-0.008
3	66	1.859	1.874	0.015
5	67	1.891	1.889	-0.002
5	68	1.949	1.948	-0.001
5	69	1.855	1.855	0.000
5	70	1.983	1.980	-0.003
5	71	1.919	1.912	-0.007
5	72	1.933	1.918	-0.015
10	73	1.910	1.886	-0.024
10	74	1.947	1.928	-0.019
10	75	1.894	1.899	0.005
10	76	1.951	1.936	-0.015
10	77	1.848	1.832	-0.016
10	78	1.859	1.867	0.008
15	79	1.891	1.896	0.005
15	80	1.949	1.947	-0.002
15	81	1.855	1.846	-0.009
15	82	1.983	1.983	0.000
15	83	1.919	1.922	0.003
15	84	1.933	1.921	-0.012
30	86	1.910	1.907	-0.003
30	89	1.947	1.941	-0.006
30	91	1.894	1.901	0.007
30	92	1.951	1.929	-0.022
30	94	1.848	1.854	0.006
30	95	1.859	1.866	0.007
35	100	1.891	1.903	0.012
35	101	1.949	1.965	0.016
35	102	1.855	1.862	0.007
35	103	1.983	1.978	-0.005
35	104	1.919	1.928	0.009
35	105	1.933	1.924	-0.009
50	106	1.948	1.953	0.005
50	107	1.933	1.959	0.026
50	108	1.883	1.901	0.018
50	109	1.926	1.942	0.016
50	110	1.919	1.916	-0.003
50	111	1.875	1.888	0.013
55	113	1.922	1.905	-0.017
55	114	1.898	1.898	0.000
55	115	1.889	1.898	0.009
55	116	1.918	1.919	0.001
55	126	1.953	1.947	-0.006
55	128	1.890	1.868	-0.022
100	129	1.927	1.963	0.036
100	131	1.891	1.886	-0.005
100	132	1.936	1.961	0.025
100	133	1.927	1.957	0.030
100	134	1.889	1.921	0.032
100	135	1.905	1.920	0.015
100	138	1.909	1.957	0.048
100	139	1.888	1.917	0.029
100	140	1.919	1.953	0.034
100	141	1.872	1.890	0.018
100	142	1.910	1.938	0.028
100	143	1.921	1.970	0.049
100	144	1.906	1.940	0.034
100	220	1.900	1.943	0.043
100	221	1.912	1.932	0.020
100	224	1.940	1.976	0.036
100	225	1.841	1.864	0.023
100	226	1.924	1.962	0.038
100	228	1.897	1.936	0.039
100	232	1.946	1.982	0.036
100	233	1.971	1.995	0.024
100	236	1.870	1.894	0.024
100	238	1.916	1.945	0.029
100	243	1.867	1.883	0.016
105	244	1.877	1.909	0.032
105	246	1.853	1.901	0.048
105	248	1.916	1.943	0.027
105	374	1.907	1.941	0.034
105	376	1.916	1.942	0.026
105	Max	1.983	1.995	0.049
105	Average	1.909	1.919	0.010
105	Min	1.841	1.832	-0.024
105	Std Dev	0.034	0.037	0.019



5.4 IDD DIS 14

Test Site		
Tester		
Test Number		
Max Limit	3	mA
Min Limit	mA	

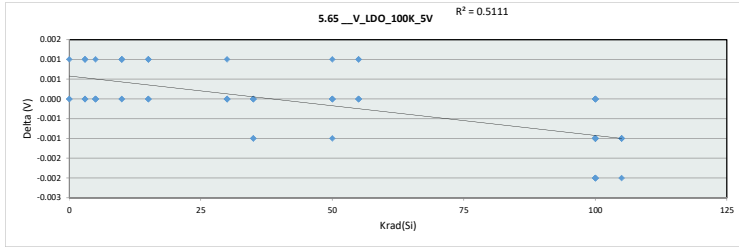
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.865	1.840	1.855	1.832	1.846	1.854	1.862	1.888	1.868	1.864	1.901
Average	1.898	1.894	1.917	1.891	1.919	1.900	1.927	1.927	1.906	1.937	1.927
Max	1.918	1.933	1.980	1.936	1.983	1.941	1.978	1.959	1.947	1.995	1.943
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000



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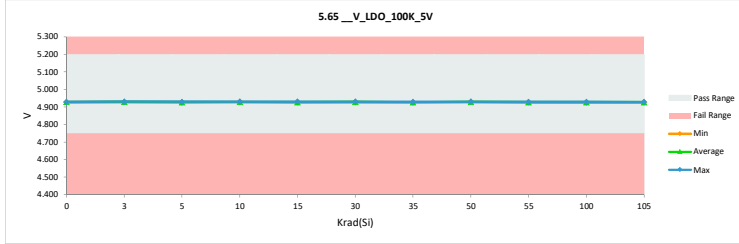
		5.65 V LDO 100K 5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	5.2	5.2	
Min Limit	4.75	4.75	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.929	4.929	0.000
0	118	4.927	4.927	0.000
0	122	4.927	4.928	0.001
3	61	4.927	4.927	0.000
3	62	4.927	4.928	0.001
3	63	4.927	4.928	0.001
3	64	4.928	4.928	0.000
3	65	4.930	4.931	0.001
3	66	4.930	4.930	0.000
5	67	4.929	4.929	0.000
5	68	4.926	4.926	0.000
5	69	4.929	4.929	0.000
5	70	4.925	4.925	0.000
5	71	4.926	4.927	0.001
5	72	4.926	4.926	0.000
10	73	4.927	4.928	0.001
10	74	4.927	4.928	0.001
10	75	4.927	4.928	0.001
10	76	4.928	4.928	0.000
10	77	4.930	4.930	0.000
10	78	4.930	4.931	0.001
15	79	4.929	4.929	0.000
15	80	4.926	4.926	0.000
15	81	4.929	4.930	0.001
15	82	4.925	4.926	0.001
15	83	4.926	4.927	0.001
15	84	4.926	4.926	0.000
30	86	4.927	4.927	0.000
30	89	4.927	4.927	0.000
30	91	4.927	4.927	0.000
30	92	4.928	4.928	0.000
30	94	4.930	4.930	0.000
30	95	4.930	4.931	0.001
35	100	4.929	4.928	-0.001
35	101	4.926	4.925	-0.001
35	102	4.929	4.929	0.000
35	103	4.925	4.925	0.000
35	104	4.926	4.926	0.000
35	105	4.926	4.926	0.000
50	106	4.927	4.928	0.001
50	107	4.929	4.929	0.000
50	108	4.930	4.929	-0.001
50	109	4.928	4.928	0.000
50	110	4.928	4.928	0.000
50	111	4.930	4.930	0.000
55	113	4.927	4.927	0.000
55	114	4.928	4.928	0.000
55	115	4.927	4.927	0.000
55	116	4.927	4.928	0.001
55	126	4.926	4.926	0.000
55	128	4.927	4.928	0.001
100	129	4.927	4.926	-0.001
100	131	4.929	4.929	0.000
100	132	4.927	4.927	0.000
100	133	4.927	4.926	-0.001
100	134	4.929	4.928	-0.001
100	135	4.929	4.929	0.000
100	138	4.928	4.927	-0.001
100	139	4.930	4.929	-0.001
100	140	4.928	4.927	-0.001
100	141	4.930	4.929	-0.001
100	142	4.927	4.926	-0.001
100	143	4.928	4.926	-0.002
100	144	4.929	4.928	-0.001
100	220	4.927	4.927	0.000
100	221	4.929	4.929	0.000
100	224	4.929	4.927	-0.002
100	225	4.931	4.929	-0.002
100	226	4.926	4.925	-0.001
100	228	4.929	4.927	-0.002
100	232	4.929	4.927	-0.002
100	233	4.928	4.927	-0.001
100	236	4.930	4.929	-0.001
100	238	4.927	4.926	-0.001
100	243	4.929	4.929	0.000
105	244	4.928	4.927	-0.001
105	246	4.930	4.928	-0.002
105	248	4.928	4.927	-0.001
105	374	4.927	4.926	-0.001
105	376	4.929	4.928	-0.001
	Max	4.931	4.931	0.001
	Average	4.928	4.928	0.000
	Min	4.925	4.925	-0.002
	Std Dev	0.001	0.001	0.001



		5.65 V LDO 10	
Test Site			
Tester			
Test Number			
Max Limit	5.2	V	
Min Limit	4.75	V	

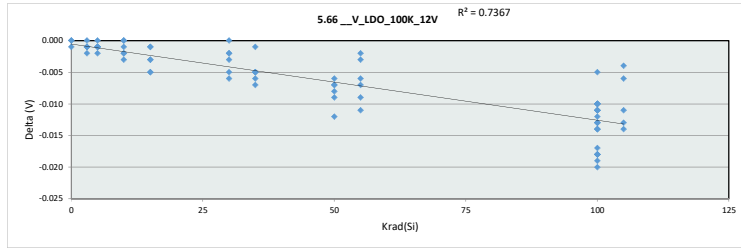
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.927	4.927	4.925	4.928	4.926	4.927	4.925	4.928	4.926	4.925
Average	4.928	4.929	4.927	4.929	4.927	4.928	4.927	4.929	4.927	4.927
Max	4.929	4.931	4.929	4.931	4.930	4.931	4.929	4.930	4.928	4.929
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



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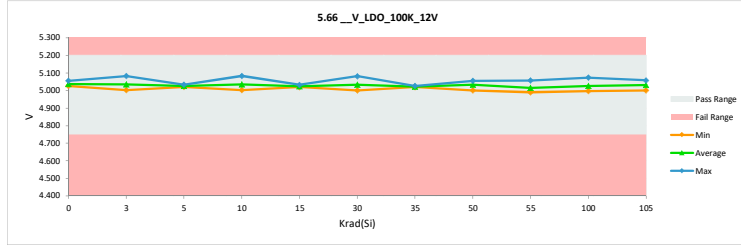
5.66 V_LDO_100K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.029	5.028	-0.001
0	118	5.055	5.055	0.000
0	122	5.026	5.026	0.000
3	61	5.047	5.046	-0.001
3	62	5.010	5.009	-0.001
3	63	5.003	5.002	-0.001
3	64	5.040	5.040	0.000
3	65	5.083	5.082	-0.001
3	66	5.029	5.027	-0.002
5	67	5.024	5.022	-0.002
5	68	5.021	5.020	-0.001
5	69	5.026	5.025	-0.001
5	70	5.033	5.033	0.000
5	71	5.029	5.028	-0.001
5	72	5.026	5.025	-0.001
10	73	5.047	5.045	-0.002
10	74	5.010	5.007	-0.003
10	75	5.003	5.002	-0.001
10	76	5.040	5.040	0.000
10	77	5.083	5.083	0.000
10	78	5.029	5.027	-0.002
15	79	5.024	5.019	-0.005
15	80	5.021	5.020	-0.001
15	81	5.026	5.023	-0.003
15	82	5.033	5.032	-0.001
15	83	5.029	5.026	-0.003
15	84	5.026	5.021	-0.005
30	86	5.047	5.047	0.000
30	89	5.010	5.005	-0.005
30	91	5.003	5.000	-0.003
30	92	5.040	5.034	-0.006
30	94	5.083	5.081	-0.002
30	95	5.029	5.027	-0.002
35	100	5.024	5.019	-0.005
35	101	5.021	5.020	-0.001
35	102	5.026	5.021	-0.005
35	103	5.033	5.026	-0.007
35	104	5.029	5.024	-0.005
35	105	5.026	5.020	-0.006
50	106	5.038	5.031	-0.007
50	107	5.053	5.041	-0.012
50	108	5.056	5.047	-0.009
50	109	5.008	5.000	-0.008
50	110	5.061	5.055	-0.006
50	111	5.029	5.022	-0.007
55	113	5.058	5.056	-0.002
55	114	5.013	5.002	-0.011
55	115	5.005	4.998	-0.007
55	116	5.019	5.013	-0.006
55	126	5.033	5.030	-0.003
55	128	4.999	4.990	-0.009
100	129	5.062	5.051	-0.011
100	131	5.021	5.011	-0.010
100	132	5.025	5.012	-0.013
100	133	5.074	5.061	-0.013
100	134	5.032	5.022	-0.010
100	135	5.019	5.000	-0.019
100	138	5.032	5.019	-0.013
100	139	5.040	5.030	-0.010
100	140	5.041	5.031	-0.010
100	141	5.025	5.007	-0.018
100	142	5.028	5.018	-0.010
100	143	5.029	5.018	-0.011
100	144	5.023	5.018	-0.005
100	220	5.025	5.008	-0.017
100	221	5.016	5.002	-0.014
100	224	5.039	5.027	-0.012
100	225	5.087	5.073	-0.014
100	226	5.059	5.045	-0.014
100	228	5.047	5.036	-0.011
100	232	5.043	5.025	-0.018
100	233	5.051	5.040	-0.011
100	236	5.051	5.033	-0.018
100	238	5.014	4.996	-0.018
100	243	5.048	5.028	-0.020
105	244	5.042	5.036	-0.006
105	246	5.014	5.000	-0.014
105	248	5.051	5.040	-0.011
105	374	5.061	5.057	-0.004
105	376	5.036	5.023	-0.013
	Max	5.087	5.083	0.000
	Average	5.034	5.027	-0.007
	Min	4.999	4.990	-0.020
	Std Dev	0.019	0.020	0.006



5.66 V_LDO_10	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.026	5.002	5.020	5.002	5.019	5.000	5.019	5.000	4.990	5.000
Average	5.036	5.034	5.026	5.034	5.024	5.032	5.022	5.033	5.015	5.025
Max	5.055	5.082	5.033	5.083	5.032	5.081	5.026	5.055	5.056	5.073
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

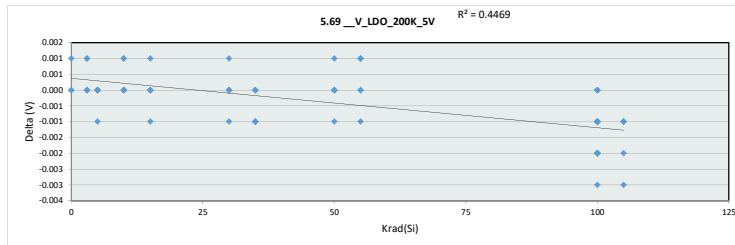


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5.69 V_LDO_200K_5V

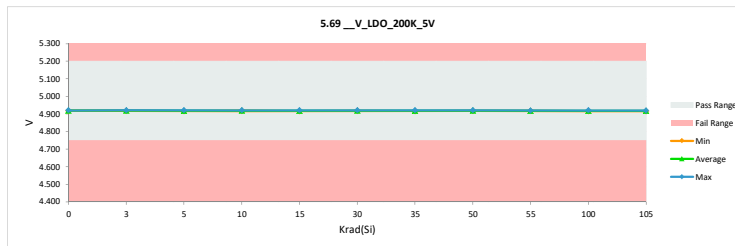
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	5.2	5.2
Min Limit	4.75	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.921	4.921	0.000
0	118	4.919	4.919	0.000
0	122	4.918	4.919	0.001
3	61	4.918	4.919	0.001
3	62	4.918	4.919	0.001
3	63	4.919	4.919	0.000
3	64	4.920	4.920	0.000
3	65	4.922	4.922	0.000
3	66	4.922	4.922	0.000
5	67	4.921	4.920	-0.001
5	68	4.917	4.917	0.000
5	69	4.921	4.921	0.000
5	70	4.916	4.916	0.000
5	71	4.918	4.918	0.000
5	72	4.917	4.917	0.000
10	73	4.918	4.919	0.001
10	74	4.918	4.919	0.001
10	75	4.919	4.919	0.000
10	76	4.920	4.920	0.000
10	77	4.922	4.922	0.000
10	78	4.922	4.922	0.000
15	79	4.921	4.920	-0.001
15	80	4.917	4.918	0.001
15	81	4.921	4.921	0.000
15	82	4.916	4.916	0.000
15	83	4.918	4.918	0.000
15	84	4.917	4.917	0.000
30	86	4.918	4.918	0.000
30	89	4.918	4.919	0.001
30	91	4.919	4.918	-0.001
30	92	4.920	4.920	0.000
30	94	4.922	4.922	0.000
30	95	4.922	4.922	0.000
35	100	4.921	4.920	-0.001
35	101	4.917	4.916	-0.001
35	102	4.921	4.921	0.000
35	103	4.916	4.916	0.000
35	104	4.918	4.917	-0.001
35	105	4.917	4.917	0.000
50	106	4.918	4.918	0.000
50	107	4.921	4.920	-0.001
50	108	4.921	4.921	0.000
50	109	4.920	4.920	0.000
50	110	4.919	4.919	0.000
50	111	4.921	4.922	0.001
55	113	4.918	4.919	0.001
55	114	4.919	4.920	0.001
55	115	4.918	4.918	0.000
55	116	4.919	4.918	-0.001
55	126	4.918	4.918	0.000
55	128	4.918	4.919	0.001
100	129	4.919	4.917	-0.002
100	131	4.921	4.921	0.000
100	132	4.919	4.918	-0.001
100	133	4.919	4.918	-0.001
100	134	4.921	4.920	-0.001
100	135	4.920	4.920	0.000
100	138	4.920	4.918	-0.002
100	139	4.922	4.920	-0.002
100	140	4.920	4.918	-0.002
100	141	4.921	4.921	0.000
100	142	4.918	4.917	-0.001
100	143	4.919	4.917	-0.002
100	144	4.921	4.920	-0.001
100	220	4.919	4.918	-0.001
100	221	4.921	4.920	-0.001
100	224	4.921	4.919	-0.002
100	225	4.922	4.921	-0.001
100	226	4.917	4.916	-0.001
100	228	4.920	4.918	-0.002
100	232	4.921	4.918	-0.003
100	233	4.920	4.919	-0.001
100	236	4.922	4.921	-0.001
100	238	4.918	4.917	-0.001
100	243	4.921	4.920	-0.001
105	244	4.919	4.918	-0.001
105	246	4.922	4.919	-0.003
105	248	4.919	4.918	-0.001
105	374	4.919	4.917	-0.002
105	376	4.921	4.920	-0.001
	Max	4.922	4.922	0.001
	Average	4.919	4.919	0.000
	Min	4.916	4.916	-0.003
	Std Dev	0.002	0.002	0.001



Test Site		
Tester		
Test Number		
Max Limit	5.2	V
Min Limit	4.75	V

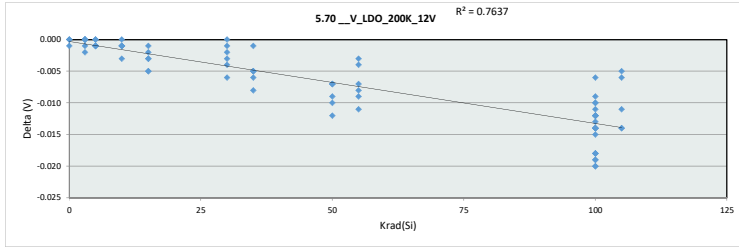
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.919	4.919	4.916	4.919	4.916	4.918	4.916	4.918	4.918	4.916	4.917
Average	4.920	4.920	4.918	4.920	4.918	4.920	4.918	4.920	4.919	4.919	4.918
Max	4.921	4.922	4.921	4.922	4.921	4.922	4.921	4.922	4.920	4.921	4.920
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



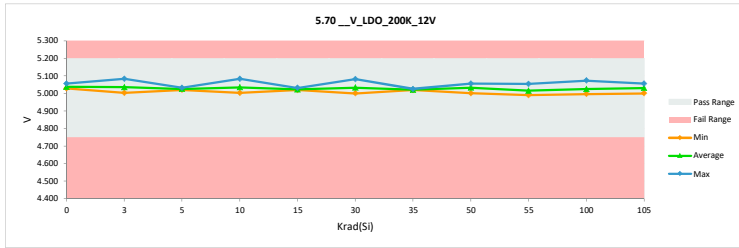
LDR TID Report

TPS7H5001-SP

5.70_V_LDO_200K_12V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	5.2	5.2			
Min Limit	4.75	4.75			
Krad(Si)	Serial #	PRE	POST	Delta	
0	117	5.030	5.030	0.000	
0	118	5.056	5.056	0.000	
0	122	5.029	5.028	-0.001	
3	61	5.048	5.047	-0.001	
3	62	5.011	5.011	0.000	
3	63	5.004	5.004	0.000	
3	64	5.042	5.042	0.000	
3	65	5.084	5.084	0.000	
3	66	5.030	5.028	-0.002	
5	67	5.025	5.024	-0.001	
5	68	5.022	5.021	-0.001	
5	69	5.027	5.026	-0.001	
5	70	5.034	5.033	-0.001	
5	71	5.030	5.030	0.000	
5	72	5.026	5.026	0.000	
10	73	5.048	5.047	-0.001	
10	74	5.011	5.008	-0.003	
10	75	5.004	5.003	-0.001	
10	76	5.042	5.041	-0.001	
10	77	5.084	5.084	0.000	
10	78	5.030	5.029	-0.001	
15	79	5.025	5.020	-0.005	
15	80	5.022	5.021	-0.001	
15	81	5.027	5.024	-0.003	
15	82	5.034	5.032	-0.002	
15	83	5.030	5.027	-0.003	
15	84	5.026	5.021	-0.005	
30	86	5.048	5.048	0.000	
30	89	5.011	5.007	-0.004	
30	91	5.001	5.001	0.000	
30	92	5.042	5.036	-0.006	
30	94	5.084	5.082	-0.002	
30	95	5.030	5.029	-0.001	
35	100	5.025	5.020	-0.005	
35	101	5.022	5.021	-0.001	
35	102	5.027	5.022	-0.005	
35	103	5.034	5.026	-0.008	
35	104	5.030	5.025	-0.005	
35	105	5.026	5.020	-0.006	
50	106	5.040	5.031	-0.009	
50	107	5.055	5.043	-0.012	
50	108	5.057	5.047	-0.010	
50	109	5.009	5.002	-0.007	
50	110	5.063	5.056	-0.007	
50	111	5.030	5.023	-0.007	
55	113	5.059	5.055	-0.004	
55	114	5.014	5.003	-0.011	
55	115	5.006	4.998	-0.008	
55	116	5.021	5.014	-0.007	
55	126	5.034	5.031	-0.003	
55	128	5.000	4.991	-0.009	
100	129	5.063	5.051	-0.012	
100	131	5.023	5.011	-0.012	
100	132	5.026	5.012	-0.014	
100	133	5.076	5.062	-0.014	
100	134	5.033	5.023	-0.010	
100	135	5.019	5.001	-0.018	
100	138	5.033	5.019	-0.014	
100	139	5.041	5.031	-0.010	
100	140	5.042	5.033	-0.009	
100	141	5.027	5.007	-0.020	
100	142	5.029	5.018	-0.011	
100	143	5.031	5.019	-0.012	
100	144	5.024	5.018	-0.006	
100	220	5.026	5.008	-0.018	
100	221	5.017	5.002	-0.015	
100	224	5.041	5.028	-0.013	
100	225	5.088	5.074	-0.014	
100	226	5.059	5.045	-0.014	
100	228	5.048	5.036	-0.012	
100	232	5.044	5.026	-0.018	
100	233	5.052	5.040	-0.012	
100	236	5.052	5.033	-0.019	
100	238	5.016	4.997	-0.019	
100	243	5.049	5.029	-0.020	
105	244	5.043	5.037	-0.006	
105	246	5.015	5.001	-0.014	
105	248	5.052	5.041	-0.011	
105	374	5.062	5.057	-0.005	
105	376	5.038	5.024	-0.014	
Max		5.088	5.084	0.000	
Average		5.035	5.028	-0.007	
Min		5.000	4.991	-0.020	
Std Dev		0.019	0.020	0.006	

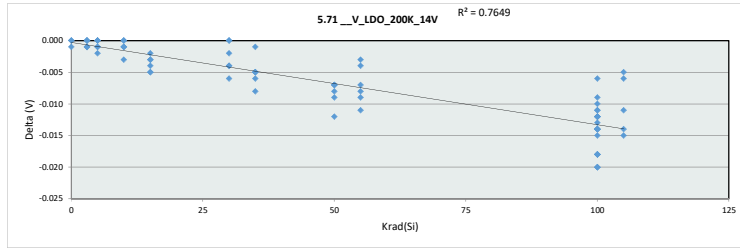


5.70_V_LDO_200											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.028	5.004	5.021	5.003	5.020	5.001	5.020	5.002	4.991	4.997	5.001
Average	5.038	5.036	5.027	5.035	5.024	5.034	5.022	5.034	5.015	5.026	5.032
Max	5.056	5.084	5.033	5.084	5.032	5.082	5.026	5.056	5.055	5.074	5.057
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

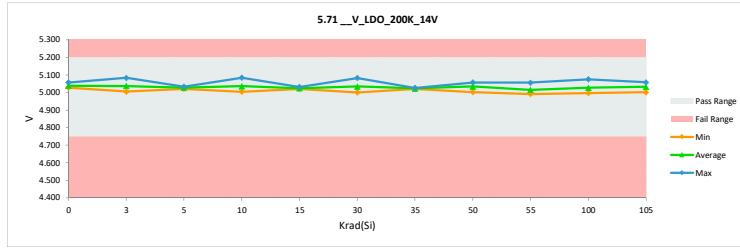


LDR TID Report TPS7H5001-SP

		5.71 V_LDO_200K_14V		
Test Site				
Tester				
Test Number				
Unit		V	V	
Max Limit		5.2	5.2	
Min Limit		4.75	4.75	
Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.030	5.029	-0.001
0	118	5.056	5.056	0.000
0	122	5.028	5.028	0.000
3	61	5.048	5.047	-0.001
3	62	5.011	5.011	0.000
3	63	5.004	5.004	0.000
3	64	5.042	5.042	0.000
3	65	5.084	5.083	-0.001
3	66	5.029	5.028	-0.001
5	67	5.025	5.024	-0.001
5	68	5.022	5.020	-0.002
5	69	5.027	5.027	0.000
5	70	5.034	5.033	-0.001
5	71	5.030	5.030	0.000
5	72	5.026	5.026	0.000
10	73	5.048	5.047	-0.001
10	74	5.011	5.008	-0.003
10	75	5.004	5.003	-0.001
10	76	5.042	5.041	-0.001
10	77	5.084	5.084	0.000
10	78	5.029	5.029	0.000
15	79	5.025	5.020	-0.005
15	80	5.022	5.020	-0.002
15	81	5.027	5.024	-0.003
15	82	5.034	5.031	-0.003
15	83	5.030	5.026	-0.004
15	84	5.026	5.021	-0.005
30	86	5.048	5.048	0.000
30	89	5.011	5.007	-0.004
30	91	5.004	5.000	-0.004
30	92	5.042	5.036	-0.006
30	94	5.084	5.082	-0.002
30	95	5.029	5.029	0.000
35	100	5.025	5.020	-0.005
35	101	5.022	5.021	-0.001
35	102	5.027	5.022	-0.005
35	103	5.034	5.026	-0.008
35	104	5.030	5.025	-0.005
35	105	5.026	5.020	-0.006
50	106	5.039	5.031	-0.008
50	107	5.055	5.043	-0.012
50	108	5.057	5.048	-0.009
50	109	5.009	5.002	-0.007
50	110	5.063	5.056	-0.007
50	111	5.030	5.023	-0.007
55	113	5.059	5.055	-0.004
55	114	5.014	5.003	-0.011
55	115	5.006	4.998	-0.008
55	116	5.021	5.014	-0.007
55	126	5.034	5.031	-0.003
55	128	5.000	4.991	-0.009
100	129	5.063	5.051	-0.012
100	131	5.023	5.011	-0.012
100	132	5.026	5.012	-0.014
100	133	5.076	5.062	-0.014
100	134	5.033	5.023	-0.010
100	135	5.019	5.001	-0.018
100	138	5.033	5.019	-0.014
100	139	5.041	5.030	-0.011
100	140	5.042	5.033	-0.009
100	141	5.027	5.007	-0.020
100	142	5.029	5.018	-0.011
100	143	5.030	5.018	-0.012
100	144	5.024	5.018	-0.006
100	220	5.026	5.008	-0.018
100	221	5.017	5.002	-0.015
100	224	5.041	5.028	-0.013
100	225	5.088	5.074	-0.014
100	226	5.059	5.045	-0.014
100	228	5.048	5.036	-0.012
100	232	5.044	5.026	-0.018
100	233	5.052	5.040	-0.012
100	236	5.051	5.033	-0.018
100	238	5.016	4.996	-0.020
100	243	5.049	5.029	-0.020
105	244	5.043	5.037	-0.006
105	246	5.016	5.001	-0.015
105	248	5.052	5.041	-0.011
105	374	5.062	5.057	-0.005
105	376	5.038	5.024	-0.014
Max		5.088	5.084	0.000
Average		5.035	5.028	-0.007
Min		5.000	4.991	-0.010
Std Dev		0.019	0.020	0.006

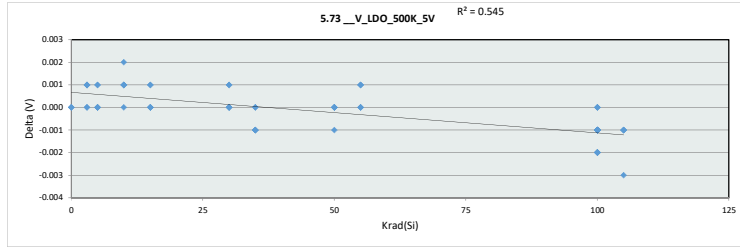


		5.71 V_LDO_20								
Test Site										
Tester										
Test Number										
Max Limit		5.2	V							
Min Limit		4.75	V							
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.028	5.004	5.020	5.003	5.020	5.000	5.020	5.002	4.991	4.996
Average	5.038	5.036	5.027	5.035	5.024	5.034	5.022	5.034	5.015	5.026
Max	5.056	5.083	5.033	5.084	5.031	5.082	5.026	5.056	5.055	5.074
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

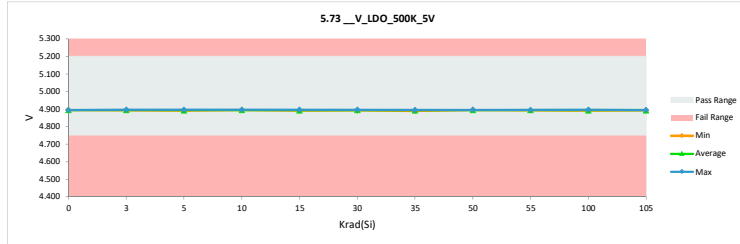


LDR TID Report
TPS7H5001-SP

5.73_V_LDO_500K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.894	4.894	0.000
0	118	4.893	4.893	0.000
0	122	4.893	4.893	0.000
3	61	4.891	4.892	0.001
3	62	4.892	4.893	0.001
3	63	4.893	4.893	0.000
3	64	4.893	4.894	0.001
3	65	4.895	4.896	0.001
3	66	4.895	4.895	0.000
5	67	4.894	4.895	0.001
5	68	4.891	4.891	0.000
5	69	4.895	4.895	0.000
5	70	4.890	4.890	0.000
5	71	4.891	4.892	0.001
5	72	4.892	4.892	0.000
10	73	4.891	4.893	0.002
10	74	4.892	4.893	0.001
10	75	4.893	4.893	0.000
10	76	4.893	4.894	0.001
10	77	4.895	4.896	0.001
10	78	4.895	4.896	0.001
15	79	4.894	4.894	0.000
15	80	4.891	4.892	0.001
15	81	4.895	4.895	0.000
15	82	4.890	4.890	0.000
15	83	4.891	4.892	0.001
15	84	4.892	4.892	0.000
30	86	4.891	4.891	0.000
30	89	4.892	4.893	0.001
30	91	4.893	4.893	0.000
30	92	4.893	4.894	0.001
30	94	4.895	4.895	0.000
30	95	4.895	4.895	0.000
35	100	4.894	4.894	0.000
35	101	4.891	4.890	-0.001
35	102	4.895	4.894	-0.001
35	103	4.890	4.889	-0.001
35	104	4.891	4.891	0.000
35	105	4.892	4.891	-0.001
50	106	4.893	4.893	0.000
50	107	4.895	4.894	-0.001
50	108	4.895	4.895	0.000
50	109	4.894	4.894	0.000
50	110	4.893	4.893	0.000
50	111	4.895	4.895	0.000
55	113	4.893	4.893	0.000
55	114	4.893	4.894	0.001
55	115	4.892	4.893	0.001
55	116	4.893	4.893	0.000
55	126	4.892	4.892	0.000
55	128	4.892	4.893	0.001
100	129	4.893	4.891	-0.002
100	131	4.894	4.894	0.000
100	132	4.893	4.892	-0.001
100	133	4.893	4.892	-0.001
100	134	4.894	4.893	-0.001
100	135	4.894	4.894	0.000
100	138	4.894	4.892	-0.002
100	139	4.895	4.894	-0.001
100	140	4.893	4.892	-0.001
100	141	4.895	4.894	-0.001
100	142	4.892	4.891	-0.001
100	143	4.893	4.891	-0.002
100	144	4.895	4.894	-0.001
100	220	4.893	4.892	-0.001
100	221	4.895	4.894	-0.001
100	224	4.895	4.893	-0.002
100	225	4.895	4.894	-0.001
100	226	4.891	4.890	-0.001
100	228	4.894	4.892	-0.002
100	232	4.894	4.892	-0.002
100	233	4.894	4.893	-0.001
100	236	4.895	4.895	0.000
100	238	4.893	4.891	-0.002
100	243	4.895	4.894	-0.001
105	244	4.893	4.892	-0.001
105	246	4.896	4.893	-0.003
105	248	4.893	4.892	-0.001
105	374	4.893	4.892	-0.001
105	376	4.895	4.894	-0.001
Max		4.896	4.896	0.002
Average		4.893	4.893	0.000
Min		4.890	4.889	-0.003
Std Dev		0.002	0.002	0.001



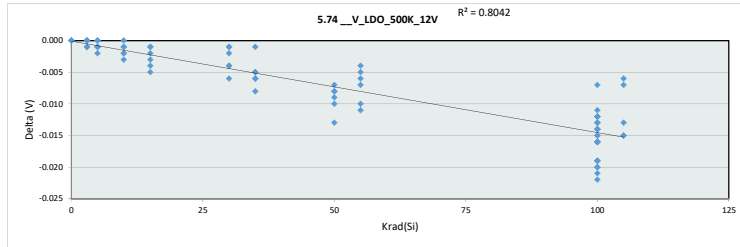
5.73_V_LDO_500												
Test Site												
Tester												
Test Number												
Max Limit	5.2	V										
Min Limit	4.75	V										
Krad(Si)	0	5	10	15	30	35	50	55	100	105		
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.893	4.894	4.890	4.893	4.890	4.891	4.889	4.893	4.892	4.890	4.892	4.892
Average	4.893	4.894	4.893	4.894	4.893	4.894	4.892	4.894	4.893	4.893	4.893	4.893
Max	4.894	4.896	4.895	4.896	4.895	4.895	4.894	4.895	4.894	4.895	4.894	4.894
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



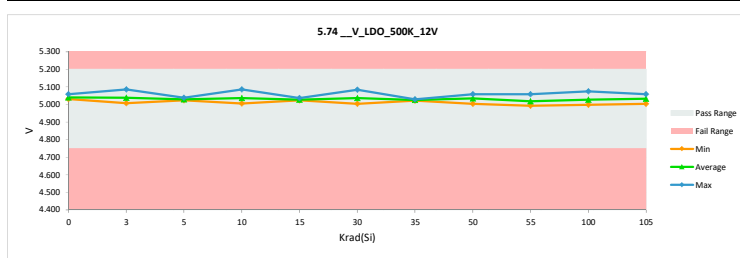
LDR TID Report
TPS7H5001-SP

5.74_V_LDO_500K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.032	5.032	0.000
0	118	5.058	5.058	0.000
0	122	5.031	5.031	0.000
3	61	5.051	5.050	-0.001
3	62	5.013	5.013	0.000
3	63	5.007	5.007	0.000
3	64	5.044	5.044	0.000
3	65	5.086	5.085	-0.001
3	66	5.032	5.031	-0.001
5	67	5.028	5.026	-0.002
5	68	5.025	5.024	-0.001
5	69	5.029	5.029	0.000
5	70	5.038	5.038	0.000
5	71	5.033	5.032	-0.001
5	72	5.030	5.029	-0.001
10	73	5.051	5.049	-0.002
10	74	5.013	5.010	-0.003
10	75	5.007	5.005	-0.002
10	76	5.044	5.044	0.000
10	77	5.086	5.085	-0.001
10	78	5.032	5.031	-0.001
15	79	5.028	5.023	-0.005
15	80	5.025	5.024	-0.001
15	81	5.029	5.027	-0.002
15	82	5.038	5.037	-0.001
15	83	5.033	5.030	-0.003
15	84	5.030	5.026	-0.004
30	86	5.051	5.050	-0.001
30	89	5.013	5.009	-0.004
30	91	5.007	5.003	-0.004
30	92	5.044	5.038	-0.006
30	94	5.086	5.084	-0.002
30	95	5.032	5.031	-0.001
35	100	5.028	5.022	-0.006
35	101	5.025	5.024	-0.001
35	102	5.029	5.024	-0.005
35	103	5.038	5.030	-0.008
35	104	5.033	5.028	-0.005
35	105	5.030	5.024	-0.006
50	106	5.043	5.035	-0.008
50	107	5.058	5.045	-0.013
50	108	5.059	5.049	-0.010
50	109	5.011	5.003	-0.008
50	110	5.065	5.058	-0.007
50	111	5.032	5.023	-0.009
55	113	5.064	5.059	-0.005
55	114	5.016	5.005	-0.011
55	115	5.008	5.001	-0.007
55	116	5.023	5.017	-0.006
55	126	5.037	5.033	-0.004
55	128	5.003	4.993	-0.010
100	129	5.068	5.054	-0.014
100	131	5.025	5.012	-0.013
100	132	5.029	5.013	-0.016
100	133	5.079	5.063	-0.016
100	134	5.036	5.024	-0.012
100	135	5.021	5.001	-0.020
100	138	5.034	5.020	-0.014
100	139	5.044	5.032	-0.012
100	140	5.045	5.034	-0.011
100	141	5.030	5.009	-0.021
100	142	5.032	5.020	-0.012
100	143	5.033	5.020	-0.013
100	144	5.026	5.019	-0.007
100	220	5.029	5.010	-0.019
100	221	5.019	5.003	-0.016
100	224	5.044	5.029	-0.015
100	225	5.090	5.075	-0.015
100	226	5.063	5.047	-0.016
100	228	5.051	5.038	-0.013
100	232	5.047	5.028	-0.019
100	233	5.055	5.041	-0.014
100	236	5.053	5.034	-0.019
100	238	5.018	4.998	-0.020
100	243	5.052	5.030	-0.022
105	244	5.045	5.038	-0.007
105	246	5.018	5.003	-0.015
105	248	5.055	5.042	-0.013
105	374	5.065	5.059	-0.006
105	376	5.040	5.025	-0.015
105	Max	5.090	5.085	0.000
105	Average	5.038	5.030	-0.008
105	Min	5.003	4.993	-0.022
105	Std Dev	0.020	0.020	0.006



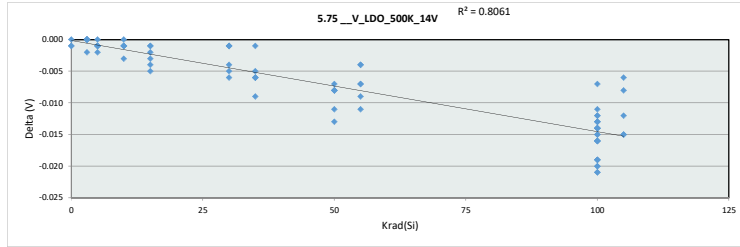
5.74_V_LDO_500	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75



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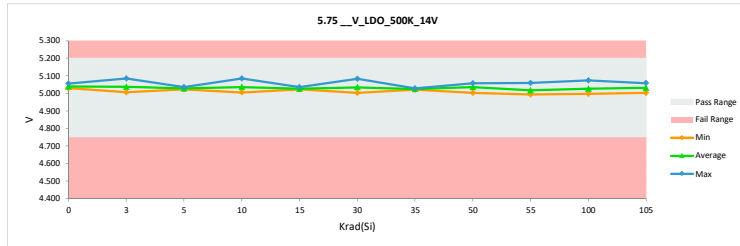
5.75 V_LDO_500K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.032	5.031	-0.001
0	118	5.058	5.057	-0.001
0	122	5.031	5.031	0.000
3	61	5.050	5.050	0.000
3	62	5.013	5.013	0.000
3	63	5.007	5.007	0.000
3	64	5.044	5.044	0.000
3	65	5.085	5.085	0.000
3	66	5.032	5.030	-0.002
5	67	5.028	5.026	-0.002
5	68	5.025	5.024	-0.001
5	69	5.029	5.029	0.000
5	70	5.038	5.037	-0.001
5	71	5.033	5.032	-0.001
5	72	5.030	5.029	-0.001
10	73	5.050	5.049	-0.001
10	74	5.013	5.010	-0.003
10	75	5.007	5.006	-0.001
10	76	5.044	5.043	-0.001
10	77	5.085	5.085	0.000
10	78	5.032	5.031	-0.001
15	79	5.028	5.023	-0.005
15	80	5.025	5.024	-0.001
15	81	5.029	5.027	-0.002
15	82	5.038	5.037	-0.001
15	83	5.033	5.030	-0.003
15	84	5.030	5.026	-0.004
30	86	5.050	5.049	-0.001
30	89	5.013	5.008	-0.005
30	91	5.007	5.003	-0.004
30	92	5.044	5.038	-0.006
30	94	5.085	5.084	-0.001
30	95	5.032	5.031	-0.001
35	100	5.028	5.022	-0.006
35	101	5.025	5.024	-0.001
35	102	5.029	5.023	-0.006
35	103	5.038	5.029	-0.009
35	104	5.033	5.028	-0.005
35	105	5.030	5.024	-0.006
50	106	5.043	5.035	-0.008
50	107	5.058	5.045	-0.013
50	108	5.060	5.049	-0.011
50	109	5.011	5.003	-0.008
50	110	5.065	5.058	-0.007
50	111	5.032	5.024	-0.008
55	113	5.064	5.060	-0.004
55	114	5.016	5.005	-0.011
55	115	5.008	5.001	-0.007
55	116	5.023	5.016	-0.007
55	126	5.037	5.033	-0.004
55	128	5.003	4.994	-0.009
100	129	5.068	5.053	-0.015
100	131	5.025	5.012	-0.013
100	132	5.029	5.013	-0.016
100	133	5.079	5.063	-0.016
100	134	5.036	5.024	-0.012
100	135	5.021	5.001	-0.020
100	138	5.034	5.020	-0.014
100	139	5.044	5.032	-0.012
100	140	5.045	5.034	-0.011
100	141	5.030	5.009	-0.021
100	142	5.032	5.019	-0.013
100	143	5.033	5.020	-0.013
100	144	5.026	5.019	-0.007
100	220	5.029	5.010	-0.019
100	221	5.019	5.003	-0.016
100	224	5.043	5.029	-0.014
100	225	5.090	5.075	-0.015
100	226	5.063	5.047	-0.016
100	228	5.051	5.037	-0.014
100	232	5.047	5.028	-0.019
100	233	5.055	5.041	-0.014
100	236	5.053	5.034	-0.019
100	238	5.018	4.998	-0.020
100	243	5.051	5.030	-0.021
105	244	5.045	5.037	-0.008
105	246	5.018	5.003	-0.015
105	248	5.054	5.042	-0.012
105	374	5.065	5.059	-0.006
105	376	5.040	5.025	-0.015
105	Max	5.090	5.085	0.000
105	Average	5.038	5.030	-0.008
105	Min	5.003	4.994	-0.021
105	Std Dev	0.019	0.020	0.006



5.75 V_LDO_500K_14V	
Test Site	
Tester	
Test Number	
Max Limit	5.2 V
Min Limit	4.75 V

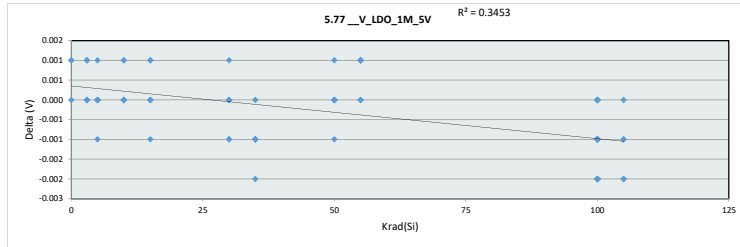
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.031	5.007	5.024	5.006	5.023	5.003	5.022	5.003	4.994	4.998	5.003
Average	5.040	5.038	5.030	5.037	5.028	5.036	5.025	5.036	5.018	5.027	5.033
Max	5.057	5.085	5.037	5.085	5.037	5.084	5.029	5.058	5.060	5.075	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



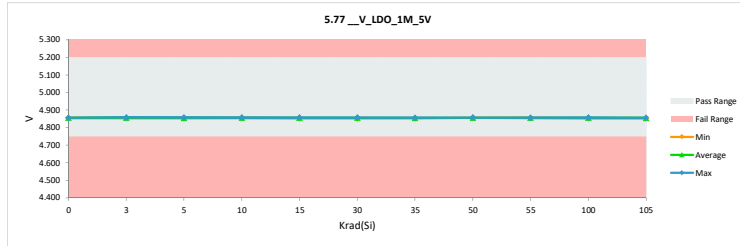
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		5.77_V_LDO_1M_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	5.2	5.2	
Min Limit	4.75	4.75	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.856	4.857	0.001
0	118	4.855	4.856	0.000
0	122	4.855	4.856	0.001
3	61	4.854	4.854	0.000
3	62	4.855	4.855	0.000
3	63	4.856	4.856	0.000
3	64	4.856	4.857	0.001
3	65	4.858	4.859	0.001
3	66	4.858	4.858	0.000
5	67	4.858	4.857	-0.001
5	68	4.854	4.854	0.000
5	69	4.858	4.858	0.000
5	70	4.853	4.853	0.000
5	71	4.854	4.855	0.001
5	72	4.855	4.855	0.000
10	73	4.854	4.855	0.001
10	74	4.855	4.855	0.000
10	75	4.856	4.856	0.000
10	76	4.856	4.857	0.001
10	77	4.858	4.858	0.000
10	78	4.858	4.858	0.000
15	79	4.858	4.857	-0.001
15	80	4.854	4.855	0.001
15	81	4.858	4.858	0.000
15	82	4.853	4.853	0.000
15	83	4.854	4.855	0.001
15	84	4.855	4.855	0.000
30	86	4.854	4.853	-0.001
30	89	4.855	4.855	0.000
30	91	4.856	4.856	0.000
30	92	4.856	4.857	0.001
30	94	4.858	4.858	0.000
30	95	4.858	4.858	0.000
35	100	4.858	4.856	-0.002
35	101	4.854	4.853	-0.001
35	102	4.858	4.857	-0.001
35	103	4.853	4.852	-0.001
35	104	4.854	4.854	0.000
35	105	4.855	4.854	-0.001
50	106	4.856	4.856	0.000
50	107	4.857	4.857	0.000
50	108	4.858	4.857	-0.001
50	109	4.857	4.857	0.000
50	110	4.855	4.856	0.001
50	111	4.858	4.858	0.000
55	113	4.856	4.856	0.000
55	114	4.856	4.857	0.001
55	115	4.855	4.856	0.001
55	116	4.856	4.856	0.000
55	126	4.854	4.855	0.001
55	128	4.855	4.856	0.001
100	129	4.856	4.855	-0.001
100	131	4.857	4.857	0.000
100	132	4.855	4.855	0.000
100	133	4.855	4.854	-0.001
100	134	4.857	4.856	-0.001
100	135	4.857	4.857	0.000
100	138	4.857	4.855	-0.002
100	139	4.858	4.857	-0.001
100	140	4.856	4.855	-0.001
100	141	4.858	4.857	-0.001
100	142	4.855	4.854	-0.001
100	143	4.856	4.854	-0.002
100	144	4.858	4.856	-0.002
100	220	4.855	4.855	0.000
100	221	4.857	4.857	0.000
100	224	4.857	4.856	-0.001
100	225	4.858	4.857	-0.001
100	226	4.854	4.853	-0.001
100	228	4.857	4.855	-0.002
100	232	4.857	4.855	-0.002
100	233	4.856	4.855	-0.001
100	236	4.859	4.858	-0.001
100	238	4.855	4.853	-0.002
100	243	4.858	4.857	-0.001
105	244	4.855	4.855	0.000
105	246	4.858	4.856	-0.002
105	248	4.856	4.854	-0.002
105	374	4.856	4.855	-0.001
105	376	4.858	4.857	-0.001
	Max	4.859	4.859	0.001
	Average	4.856	4.856	0.000
	Min	4.853	4.852	-0.002
	Std Dev	0.002	0.002	0.001



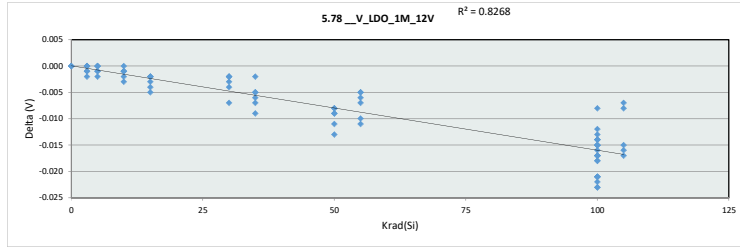
		5.77_V_LDO_1M								
Test Site										
Tester										
Test Number										
Max Limit	5.2	V								
Min Limit	4.75	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.855	4.854	4.853	4.855	4.853	4.853	4.852	4.856	4.855	4.854
Average	4.856	4.857	4.855	4.857	4.856	4.856	4.854	4.857	4.856	4.855
Max	4.857	4.859	4.858	4.858	4.858	4.858	4.857	4.858	4.857	4.858
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



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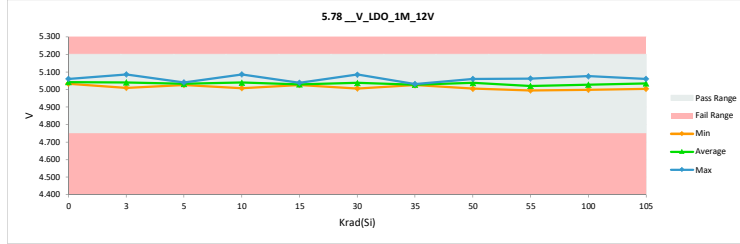
5.78 V_LDO_1M_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.033	5.033	0.000
0	118	5.059	5.059	0.000
0	122	5.033	5.033	0.000
3	61	5.052	5.051	-0.001
3	62	5.014	5.014	0.000
3	63	5.008	5.008	0.000
3	64	5.046	5.046	0.000
3	65	5.086	5.085	-0.001
3	66	5.034	5.032	-0.002
5	67	5.030	5.029	-0.001
5	68	5.027	5.025	-0.002
5	69	5.030	5.030	0.000
5	70	5.040	5.040	0.000
5	71	5.034	5.034	0.000
5	72	5.031	5.030	-0.001
10	73	5.052	5.050	-0.002
10	74	5.014	5.011	-0.003
10	75	5.008	5.007	-0.001
10	76	5.046	5.045	-0.001
10	77	5.086	5.086	0.000
10	78	5.034	5.033	-0.001
15	79	5.030	5.025	-0.005
15	80	5.027	5.025	-0.002
15	81	5.030	5.028	-0.002
15	82	5.040	5.038	-0.002
15	83	5.034	5.031	-0.003
15	84	5.031	5.027	-0.004
30	86	5.052	5.050	-0.002
30	89	5.014	5.010	-0.004
30	91	5.008	5.005	-0.003
30	92	5.046	5.039	-0.007
30	94	5.086	5.084	-0.002
30	95	5.034	5.032	-0.002
35	100	5.030	5.024	-0.006
35	101	5.027	5.025	-0.002
35	102	5.030	5.025	-0.005
35	103	5.040	5.031	-0.009
35	104	5.034	5.029	-0.005
35	105	5.031	5.024	-0.007
50	106	5.045	5.037	-0.008
50	107	5.060	5.047	-0.013
50	108	5.061	5.050	-0.011
50	109	5.013	5.004	-0.009
50	110	5.067	5.059	-0.008
50	111	5.032	5.023	-0.009
55	113	5.066	5.061	-0.005
55	114	5.018	5.007	-0.011
55	115	5.009	5.002	-0.007
55	116	5.024	5.018	-0.006
55	126	5.040	5.035	-0.005
55	128	5.004	4.994	-0.010
100	129	5.070	5.054	-0.016
100	131	5.027	5.012	-0.015
100	132	5.030	5.013	-0.017
100	133	5.082	5.064	-0.018
100	134	5.038	5.024	-0.014
100	135	5.022	5.001	-0.021
100	138	5.035	5.018	-0.017
100	139	5.046	5.032	-0.014
100	140	5.047	5.035	-0.012
100	141	5.032	5.009	-0.023
100	142	5.032	5.019	-0.013
100	143	5.034	5.020	-0.014
100	144	5.027	5.019	-0.008
100	220	5.031	5.010	-0.021
100	221	5.021	5.003	-0.018
100	224	5.045	5.030	-0.015
100	225	5.092	5.075	-0.017
100	226	5.064	5.047	-0.017
100	228	5.052	5.037	-0.015
100	232	5.050	5.029	-0.021
100	233	5.057	5.042	-0.015
100	236	5.054	5.033	-0.021
100	238	5.020	4.998	-0.022
100	243	5.053	5.030	-0.023
105	244	5.046	5.038	-0.008
105	246	5.020	5.003	-0.017
105	248	5.057	5.042	-0.015
105	374	5.066	5.059	-0.007
105	376	5.042	5.026	-0.016
105	Max	5.092	5.086	0.000
105	Average	5.039	5.031	-0.008
105	Min	5.004	4.994	-0.023
105	Std Dev	0.020	0.020	0.007



5.78 V_LDO_1M	
Test Site	
Tester	
Test Number	
Max Limit	5.2 V
Min Limit	4.75 V

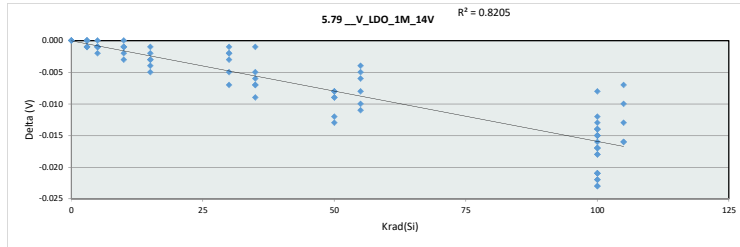
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.033	5.008	5.025	5.007	5.025	5.005	5.024	5.004	4.994	4.998	5.003
Average	5.042	5.039	5.031	5.039	5.029	5.037	5.026	5.037	5.020	5.027	5.034
Max	5.059	5.085	5.040	5.086	5.038	5.084	5.031	5.059	5.061	5.075	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



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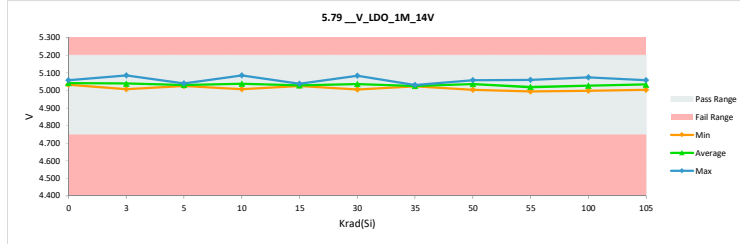
5.79_V_LDO_1M_14V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	5.2 5.2
Min Limit	4.75 4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.033	5.033	0.000
0	118	5.059	5.059	0.000
0	122	5.033	5.033	0.000
3	61	5.052	5.051	-0.001
3	62	5.014	5.014	0.000
3	63	5.008	5.008	0.000
3	64	5.046	5.046	0.000
3	65	5.086	5.085	-0.001
3	66	5.033	5.032	-0.001
5	67	5.030	5.028	-0.002
5	68	5.026	5.025	-0.001
5	69	5.031	5.030	-0.001
5	70	5.040	5.040	0.000
5	71	5.034	5.033	-0.001
5	72	5.031	5.030	-0.001
10	73	5.052	5.050	-0.002
10	74	5.014	5.011	-0.003
10	75	5.008	5.007	-0.001
10	76	5.046	5.045	-0.001
10	77	5.086	5.086	0.000
10	78	5.033	5.032	-0.001
15	79	5.030	5.025	-0.005
15	80	5.026	5.025	-0.001
15	81	5.031	5.028	-0.003
15	82	5.040	5.038	-0.002
15	83	5.034	5.031	-0.003
15	84	5.031	5.027	-0.004
30	86	5.052	5.050	-0.002
30	89	5.014	5.009	-0.005
30	91	5.008	5.005	-0.003
30	92	5.046	5.039	-0.007
30	94	5.086	5.084	-0.002
30	95	5.033	5.032	-0.001
35	100	5.030	5.024	-0.006
35	101	5.026	5.025	-0.001
35	102	5.031	5.024	-0.007
35	103	5.040	5.031	-0.009
35	104	5.034	5.029	-0.005
35	105	5.031	5.024	-0.007
50	106	5.045	5.037	-0.008
50	107	5.060	5.047	-0.013
50	108	5.061	5.049	-0.012
50	109	5.013	5.004	-0.009
50	110	5.067	5.059	-0.008
50	111	5.032	5.023	-0.009
55	113	5.066	5.061	-0.005
55	114	5.018	5.007	-0.011
55	115	5.009	5.001	-0.008
55	116	5.024	5.018	-0.006
55	126	5.039	5.035	-0.004
55	128	5.004	4.994	-0.010
100	129	5.070	5.054	-0.016
100	131	5.027	5.012	-0.015
100	132	5.030	5.013	-0.017
100	133	5.081	5.064	-0.017
100	134	5.038	5.024	-0.014
100	135	5.022	5.000	-0.022
100	138	5.034	5.019	-0.015
100	139	5.046	5.033	-0.013
100	140	5.047	5.035	-0.012
100	141	5.032	5.009	-0.023
100	142	5.032	5.018	-0.014
100	143	5.034	5.020	-0.014
100	144	5.027	5.019	-0.008
100	220	5.031	5.010	-0.021
100	221	5.021	5.003	-0.018
100	224	5.045	5.030	-0.015
100	225	5.092	5.075	-0.017
100	226	5.064	5.046	-0.018
100	228	5.052	5.037	-0.015
100	232	5.050	5.029	-0.021
100	233	5.057	5.042	-0.015
100	236	5.054	5.033	-0.021
100	238	5.020	4.998	-0.022
100	243	5.053	5.030	-0.023
105	244	5.047	5.037	-0.010
105	246	5.020	5.004	-0.016
105	248	5.056	5.043	-0.013
105	374	5.066	5.059	-0.007
105	376	5.042	5.026	-0.016
105	Max	5.092	5.086	0.000
105	Average	5.039	5.031	-0.008
105	Min	5.004	4.994	-0.023
105	Std Dev	0.020	0.020	0.007



5.79_V_LDO_1M	
Test Site	
Tester	
Test Number	
Max Limit	5.2 V
Min Limit	4.75 V

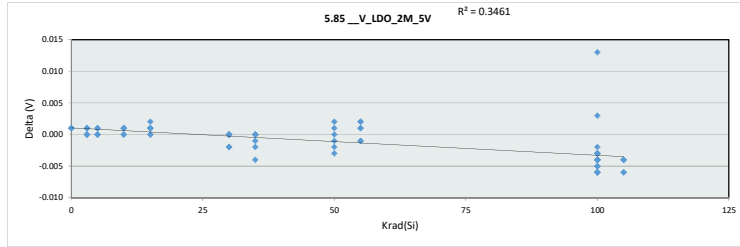
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.033	5.008	5.025	5.007	5.025	5.005	5.024	5.004	4.994	4.998	5.004
Average	5.042	5.039	5.031	5.039	5.029	5.037	5.026	5.037	5.019	5.027	5.034
Max	5.059	5.085	5.040	5.086	5.038	5.084	5.031	5.059	5.061	5.075	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



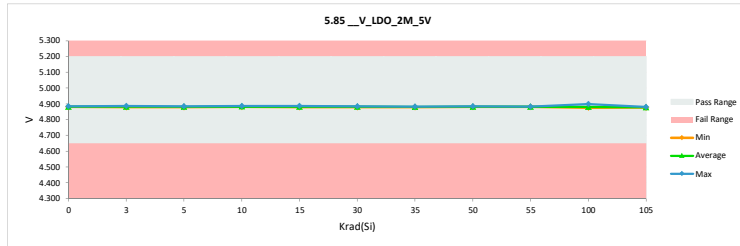
LDR TID Report
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		5.85 V_LDO_2M_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	5.2	5.2	
Min Limit	4.65	4.65	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.884	4.885	0.001
0	118	4.882	4.883	0.001
0	122	4.880	4.881	0.001
3	61	4.880	4.880	0.000
3	62	4.882	4.883	0.001
3	63	4.884	4.884	0.000
3	64	4.884	4.885	0.001
3	65	4.886	4.887	0.001
3	66	4.885	4.885	0.000
5	67	4.885	4.885	0.000
5	68	4.880	4.880	0.000
5	69	4.886	4.886	0.000
5	70	4.880	4.880	0.000
5	71	4.880	4.881	0.001
5	72	4.881	4.882	0.001
10	73	4.880	4.881	0.001
10	74	4.882	4.883	0.001
10	75	4.884	4.884	0.000
10	76	4.884	4.885	0.001
10	77	4.886	4.887	0.001
10	78	4.885	4.885	0.000
15	79	4.885	4.885	0.000
15	80	4.880	4.881	0.001
15	81	4.886	4.887	0.001
15	82	4.880	4.880	0.000
15	83	4.880	4.881	0.001
15	84	4.881	4.883	0.002
30	86	4.880	4.880	0.000
30	89	4.882	4.882	0.000
30	91	4.884	4.884	0.000
30	92	4.884	4.884	0.000
30	94	4.886	4.884	-0.002
30	95	4.885	4.885	0.000
35	100	4.885	4.881	-0.004
35	101	4.880	4.880	0.000
35	102	4.886	4.884	-0.002
35	103	4.880	4.879	-0.001
35	104	4.880	4.880	0.000
35	105	4.881	4.881	0.000
50	106	4.883	4.881	-0.002
50	107	4.885	4.886	0.001
50	108	4.886	4.883	-0.003
50	109	4.885	4.885	0.000
50	110	4.882	4.884	0.002
50	111	4.886	4.885	-0.001
55	113	4.882	4.881	-0.001
55	114	4.883	4.882	-0.001
55	115	4.882	4.884	0.002
55	116	4.882	4.883	0.001
55	126	4.882	4.883	0.001
55	128	4.882	4.884	0.002
100	129	4.883	4.881	-0.002
100	131	4.883	4.880	-0.003
100	132	4.881	4.878	-0.003
100	133	4.882	4.877	-0.005
100	134	4.883	4.879	-0.004
100	135	4.886	4.899	0.013
100	138	4.884	4.880	-0.004
100	139	4.886	4.881	-0.005
100	140	4.882	4.878	-0.004
100	141	4.885	4.882	-0.003
100	142	4.881	4.884	0.003
100	143	4.883	4.877	-0.006
100	144	4.885	4.879	-0.006
100	220	4.882	4.879	-0.003
100	221	4.885	4.881	-0.004
100	224	4.886	4.880	-0.006
100	225	4.886	4.881	-0.005
100	226	4.881	4.877	-0.004
100	228	4.883	4.877	-0.006
100	232	4.884	4.878	-0.006
100	233	4.883	4.879	-0.004
100	236	4.888	4.885	-0.003
100	238	4.882	4.876	-0.006
100	243	4.885	4.882	-0.003
105	244	4.881	4.877	-0.004
105	246	4.886	4.880	-0.006
105	248	4.882	4.878	-0.004
105	374	4.884	4.878	-0.006
105	376	4.885	4.881	-0.004
	Max	4.888	4.899	0.013
	Average	4.883	4.882	-0.001
	Min	4.880	4.876	-0.006
	Std Dev	0.002	0.003	0.003



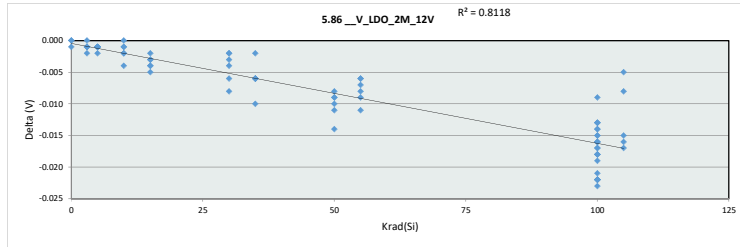
		5.85 V_LDO_2M								
Test Site										
Tester										
Test Number										
Max Limit	5.2	V								
Min Limit	4.65	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	4.881	4.880	4.880	4.881	4.880	4.880	4.879	4.881	4.881	4.876
Average	4.883	4.884	4.882	4.884	4.883	4.883	4.881	4.884	4.883	4.880
Max	4.885	4.887	4.886	4.887	4.887	4.885	4.884	4.886	4.884	4.899
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



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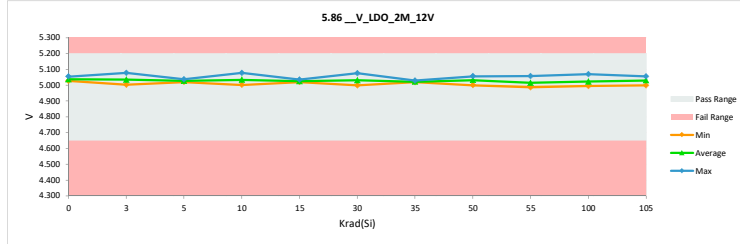
5.86_V_LDO_2M_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.65

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.029	5.028	-0.001
0	118	5.055	5.055	0.000
0	122	5.030	5.030	0.000
3	61	5.048	5.047	-0.001
3	62	5.013	5.012	-0.001
3	63	5.004	5.004	0.000
3	64	5.043	5.042	-0.001
3	65	5.079	5.078	-0.001
3	66	5.032	5.030	-0.002
5	67	5.028	5.027	-0.001
5	68	5.023	5.021	-0.002
5	69	5.026	5.025	-0.001
5	70	5.039	5.038	-0.001
5	71	5.031	5.030	-0.001
5	72	5.027	5.026	-0.001
10	73	5.048	5.046	-0.002
10	74	5.013	5.009	-0.004
10	75	5.004	5.003	-0.001
10	76	5.043	5.042	-0.001
10	77	5.079	5.079	0.000
10	78	5.032	5.030	-0.002
15	79	5.028	5.023	-0.005
15	80	5.023	5.021	-0.002
15	81	5.026	5.023	-0.003
15	82	5.039	5.036	-0.003
15	83	5.031	5.027	-0.004
15	84	5.027	5.023	-0.004
30	86	5.048	5.046	-0.002
30	89	5.013	5.007	-0.006
30	91	5.004	5.000	-0.004
30	92	5.043	5.035	-0.008
30	94	5.079	5.077	-0.002
30	95	5.032	5.029	-0.003
35	100	5.028	5.022	-0.006
35	101	5.023	5.021	-0.002
35	102	5.026	5.020	-0.006
35	103	5.039	5.029	-0.010
35	104	5.031	5.025	-0.006
35	105	5.027	5.021	-0.006
50	106	5.044	5.035	-0.009
50	107	5.059	5.045	-0.014
50	108	5.025	5.043	-0.011
50	109	5.010	5.001	-0.009
50	110	5.065	5.057	-0.008
50	111	5.028	5.018	-0.010
55	113	5.064	5.058	-0.006
55	114	5.014	5.003	-0.011
55	115	5.006	4.998	-0.008
55	116	5.021	5.014	-0.007
55	126	5.037	5.031	-0.006
55	128	4.997	4.988	-0.009
100	129	5.069	5.051	-0.018
100	131	5.023	5.008	-0.015
100	132	5.025	5.008	-0.017
100	133	5.080	5.063	-0.017
100	134	5.036	5.021	-0.015
100	135	5.017	4.995	-0.022
100	138	5.030	5.014	-0.016
100	139	5.042	5.028	-0.014
100	140	5.045	5.032	-0.013
100	141	5.029	5.006	-0.023
100	142	5.026	5.013	-0.013
100	143	5.030	5.017	-0.013
100	144	5.024	5.015	-0.009
100	220	5.030	5.008	-0.022
100	221	5.018	4.999	-0.019
100	224	5.043	5.027	-0.016
100	225	5.088	5.070	-0.018
100	226	5.062	5.044	-0.018
100	228	5.046	5.032	-0.014
100	232	5.050	5.028	-0.022
100	233	5.055	5.039	-0.016
100	236	5.047	5.026	-0.021
100	238	5.017	4.995	-0.022
100	243	5.048	5.026	-0.022
105	244	5.042	5.034	-0.008
105	246	5.016	5.000	-0.016
105	248	5.055	5.040	-0.015
105	374	5.061	5.056	-0.005
105	376	5.041	5.024	-0.017
	Max	5.088	5.079	0.009
	Average	5.036	5.027	-0.009
	Min	4.997	4.988	-0.023
	Std Dev	0.019	0.020	0.007



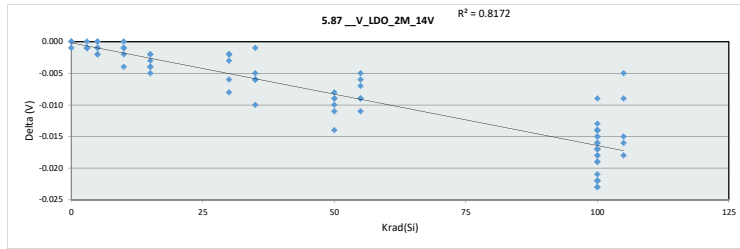
5.86_V_LDO_2M	
Test Site	
Tester	
Test Number	
Max Limit	5.2 V
Min Limit	4.65 V

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	5.028	5.004	5.021	5.003	5.021	5.000	5.020	5.001	4.988	4.995	5.000
Average	5.038	5.036	5.028	5.035	5.026	5.032	5.023	5.033	5.015	5.024	5.031
Max	5.055	5.078	5.038	5.079	5.036	5.077	5.029	5.057	5.058	5.070	5.056
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

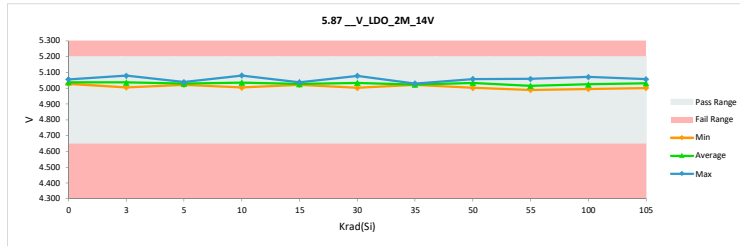


LDR TID Report
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5.87_V_LDO_2M_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.65	4.65		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.029	5.028	-0.001
0	118	5.055	5.055	0.000
0	122	5.030	5.030	0.000
3	61	5.048	5.047	-0.001
3	62	5.013	5.012	-0.001
3	63	5.004	5.004	0.000
3	64	5.043	5.042	-0.001
3	65	5.079	5.078	-0.001
3	66	5.031	5.030	-0.001
5	67	5.028	5.026	-0.002
5	68	5.023	5.021	-0.002
5	69	5.026	5.026	0.000
5	70	5.039	5.038	-0.001
5	71	5.030	5.030	0.000
5	72	5.027	5.026	-0.001
10	73	5.048	5.046	-0.002
10	74	5.013	5.009	-0.004
10	75	5.004	5.003	-0.001
10	76	5.043	5.042	-0.001
10	77	5.079	5.079	0.000
10	78	5.031	5.030	-0.001
15	79	5.028	5.023	-0.005
15	80	5.023	5.021	-0.002
15	81	5.026	5.022	-0.004
15	82	5.039	5.037	-0.002
15	83	5.030	5.027	-0.003
15	84	5.027	5.023	-0.004
30	86	5.048	5.046	-0.002
30	89	5.013	5.007	-0.006
30	91	5.004	5.001	-0.003
30	92	5.043	5.035	-0.008
30	94	5.079	5.077	-0.002
30	95	5.031	5.029	-0.002
35	100	5.028	5.022	-0.006
35	101	5.023	5.022	-0.001
35	102	5.026	5.020	-0.006
35	103	5.039	5.029	-0.010
35	104	5.030	5.025	-0.005
35	105	5.027	5.021	-0.006
50	106	5.044	5.035	-0.009
50	107	5.059	5.045	-0.014
50	108	5.054	5.043	-0.011
50	109	5.010	5.001	-0.009
50	110	5.065	5.057	-0.008
50	111	5.028	5.018	-0.010
55	113	5.063	5.058	-0.005
55	114	5.014	5.003	-0.011
55	115	5.007	4.998	-0.009
55	116	5.021	5.014	-0.007
55	126	5.037	5.031	-0.006
55	128	4.997	4.988	-0.009
100	129	5.069	5.052	-0.017
100	131	5.023	5.008	-0.015
100	132	5.025	5.007	-0.018
100	133	5.080	5.063	-0.017
100	134	5.036	5.020	-0.016
100	135	5.016	4.994	-0.022
100	138	5.030	5.013	-0.017
100	139	5.042	5.028	-0.014
100	140	5.045	5.032	-0.013
100	141	5.029	5.006	-0.023
100	142	5.027	5.013	-0.014
100	143	5.030	5.016	-0.014
100	144	5.024	5.015	-0.009
100	220	5.030	5.008	-0.022
100	221	5.018	4.999	-0.019
100	224	5.042	5.027	-0.015
100	225	5.088	5.070	-0.018
100	226	5.062	5.043	-0.019
100	228	5.046	5.032	-0.014
100	232	5.050	5.028	-0.022
100	233	5.055	5.039	-0.016
100	236	5.047	5.026	-0.021
100	238	5.017	4.995	-0.022
100	243	5.048	5.025	-0.023
105	244	5.042	5.033	-0.009
105	246	5.016	5.000	-0.016
105	248	5.055	5.040	-0.015
105	374	5.061	5.056	-0.005
105	376	5.041	5.023	-0.018
105	Max	5.088	5.079	0.000
105	Average	5.036	5.027	-0.009
105	Min	4.997	4.988	-0.023
105	Std Dev	0.019	0.020	0.007



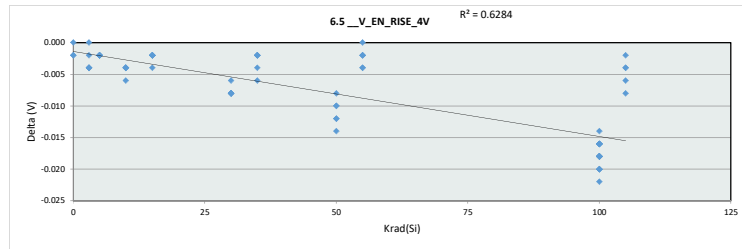
5.87_V_LDO_2M											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.65	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	5.028	5.004	5.021	5.003	5.021	5.001	5.020	5.001	4.988	4.994	5.000
Average	5.038	5.036	5.028	5.035	5.026	5.033	5.023	5.033	5.015	5.023	5.030
Max	5.055	5.078	5.038	5.079	5.037	5.077	5.029	5.057	5.058	5.070	5.056
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



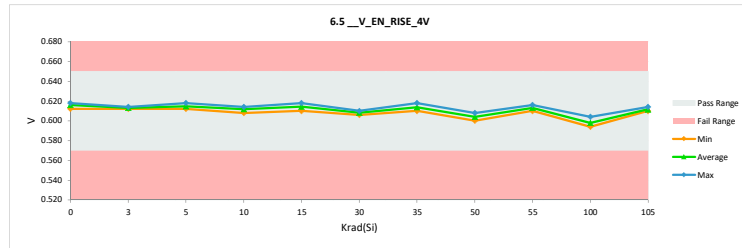
LDR TID Report
TPS7H5001-SP

6.5_V_EN_RISE_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.618	0.618	0.000
0	118	0.614	0.612	-0.002
0	122	0.620	0.618	-0.002
3	61	0.618	0.614	-0.004
3	62	0.616	0.612	-0.004
3	63	0.612	0.612	0.000
3	64	0.618	0.614	-0.004
3	65	0.616	0.614	-0.002
3	66	0.616	0.612	-0.004
5	67	0.620	0.618	-0.002
5	68	0.616	0.614	-0.002
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.612	-0.006
10	77	0.616	0.612	-0.004
10	78	0.616	0.612	-0.004
15	79	0.620	0.618	-0.002
15	80	0.616	0.614	-0.002
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.610	-0.004
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.608	-0.008
30	95	0.616	0.608	-0.008
35	100	0.620	0.618	-0.002
35	101	0.616	0.614	-0.002
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.612	-0.002
35	105	0.614	0.610	-0.004
50	106	0.614	0.606	-0.008
50	107	0.614	0.602	-0.012
50	108	0.612	0.600	-0.012
50	109	0.616	0.606	-0.010
50	110	0.618	0.608	-0.010
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.612	0.612	0.000
55	115	0.618	0.616	-0.002
55	116	0.616	0.612	-0.004
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.620	0.600	-0.020
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.604	-0.014
100	139	0.616	0.600	-0.016
100	140	0.612	0.594	-0.018
100	141	0.618	0.602	-0.016
100	142	0.612	0.594	-0.018
100	143	0.616	0.598	-0.018
100	144	0.622	0.600	-0.022
100	220	0.616	0.598	-0.018
100	221	0.616	0.598	-0.018
100	224	0.616	0.598	-0.018
100	225	0.614	0.596	-0.018
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.596	-0.020
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.620	0.612	-0.008
105	376	0.616	0.610	-0.006
	Max	0.622	0.618	0.000
	Average	0.616	0.608	-0.008
	Min	0.612	0.594	-0.022
	Std Dev	0.002	0.007	0.007



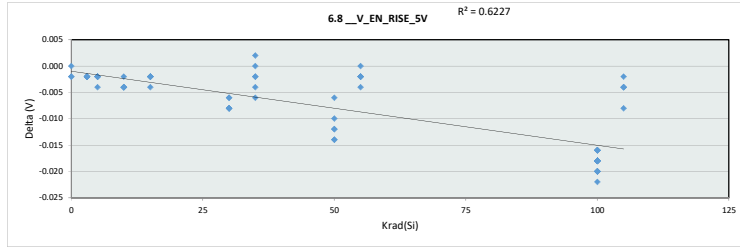
6.5_V_EN_RISE										
Test Site										
Tester										
Test Number										
Max Limit	0.65 V									
Min Limit	0.57 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.612	0.612	0.608	0.610	0.606	0.610	0.600	0.610	0.594
Average	0.616	0.613	0.615	0.612	0.614	0.608	0.614	0.604	0.613	0.598
Max	0.618	0.614	0.618	0.614	0.618	0.610	0.618	0.608	0.616	0.604
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



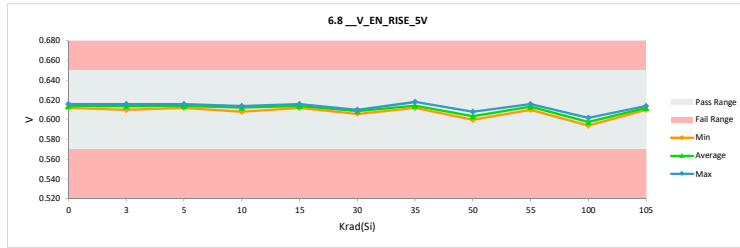
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6.8_V_EN_RISE_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.616	0.616	0.000
0	118	0.614	0.612	-0.002
0	122	0.616	0.614	-0.002
3	61	0.618	0.616	-0.002
3	62	0.616	0.614	-0.002
3	63	0.612	0.610	-0.002
3	64	0.618	0.616	-0.002
3	65	0.616	0.614	-0.002
3	66	0.616	0.614	-0.002
5	67	0.616	0.614	-0.002
5	68	0.618	0.614	-0.004
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.614	-0.004
10	77	0.616	0.614	-0.002
10	78	0.616	0.612	-0.004
15	79	0.616	0.614	-0.002
15	80	0.618	0.614	-0.004
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.612	-0.002
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.610	-0.006
30	95	0.616	0.608	-0.008
35	100	0.616	0.618	0.002
35	101	0.618	0.614	-0.004
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.614	0.000
35	105	0.614	0.612	-0.002
50	106	0.614	0.602	-0.012
50	107	0.616	0.602	-0.014
50	108	0.614	0.600	-0.012
50	109	0.618	0.608	-0.010
50	110	0.614	0.608	-0.006
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.614	0.612	-0.002
55	115	0.616	0.616	0.000
55	116	0.616	0.614	-0.002
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.618	0.600	-0.018
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.600	-0.018
100	139	0.616	0.594	-0.022
100	140	0.612	0.594	-0.018
100	141	0.618	0.600	-0.018
100	142	0.614	0.594	-0.020
100	143	0.616	0.598	-0.018
100	144	0.622	0.602	-0.020
100	220	0.616	0.600	-0.016
100	221	0.618	0.600	-0.018
100	224	0.614	0.596	-0.018
100	225	0.616	0.596	-0.020
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.598	-0.018
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.620	0.612	-0.008
105	376	0.616	0.612	-0.004
105	Max	0.622	0.618	0.002
105	Average	0.616	0.608	-0.008
105	Min	0.612	0.594	-0.022
105	Std Dev	0.002	0.007	0.007



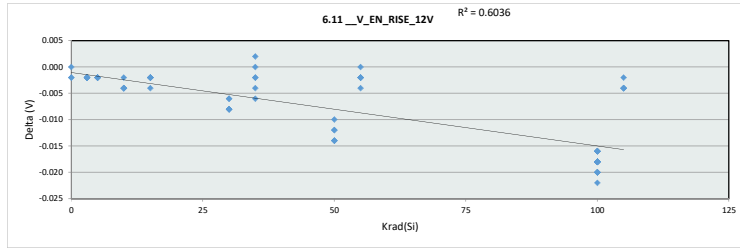
6.8_V_EN_RISE		0	5	10	15	30	35	50	55	100	105
LL		0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min		0.612	0.610	0.612	0.608	0.612	0.606	0.612	0.600	0.610	0.594
Average		0.614	0.614	0.614	0.612	0.614	0.609	0.614	0.604	0.613	0.598
Max		0.616	0.616	0.616	0.614	0.616	0.610	0.618	0.608	0.616	0.602
UL		0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



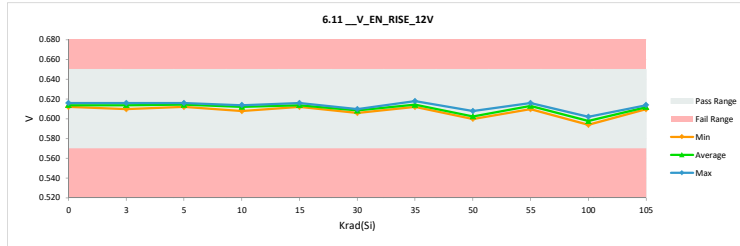
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6.11 V_EN_RISE_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.616	0.616	0.000
0	118	0.614	0.612	-0.002
0	122	0.616	0.614	-0.002
3	61	0.618	0.616	-0.002
3	62	0.616	0.614	-0.002
3	63	0.612	0.610	-0.002
3	64	0.618	0.616	-0.002
3	65	0.616	0.614	-0.002
3	66	0.616	0.614	-0.002
5	67	0.616	0.614	-0.002
5	68	0.618	0.616	-0.002
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.614	-0.004
10	77	0.616	0.614	-0.002
10	78	0.616	0.612	-0.004
15	79	0.616	0.614	-0.002
15	80	0.618	0.614	-0.004
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.612	-0.002
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.610	-0.006
30	95	0.616	0.608	-0.008
35	100	0.616	0.618	0.002
35	101	0.618	0.614	-0.004
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.614	0.000
35	105	0.614	0.612	-0.002
50	106	0.614	0.602	-0.012
50	107	0.616	0.602	-0.014
50	108	0.612	0.600	-0.012
50	109	0.618	0.608	-0.010
50	110	0.616	0.602	-0.014
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.614	0.612	-0.002
55	115	0.616	0.616	0.000
55	116	0.616	0.614	-0.002
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.618	0.600	-0.018
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.600	-0.018
100	139	0.616	0.596	-0.020
100	140	0.612	0.594	-0.018
100	141	0.618	0.600	-0.018
100	142	0.614	0.594	-0.020
100	143	0.616	0.598	-0.018
100	144	0.624	0.602	-0.022
100	220	0.616	0.600	-0.016
100	221	0.618	0.600	-0.018
100	224	0.614	0.596	-0.018
100	225	0.616	0.596	-0.020
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.598	-0.018
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.616	0.612	-0.004
105	376	0.616	0.612	-0.004
	Max	0.624	0.618	0.002
	Average	0.616	0.608	-0.008
	Min	0.612	0.594	-0.022
	Std Dev	0.002	0.007	0.007

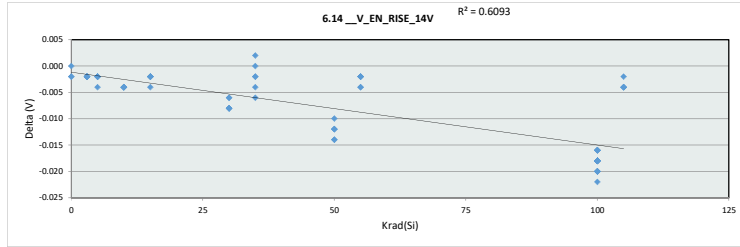


6.11 V_EN_RISE		0	5	10	15	30	35	50	55	100	105
LL		0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min		0.612	0.610	0.612	0.608	0.612	0.606	0.612	0.600	0.610	0.594
Average		0.614	0.614	0.614	0.612	0.614	0.609	0.614	0.603	0.613	0.598
Max		0.616	0.616	0.616	0.614	0.616	0.610	0.618	0.608	0.616	0.602
UL		0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

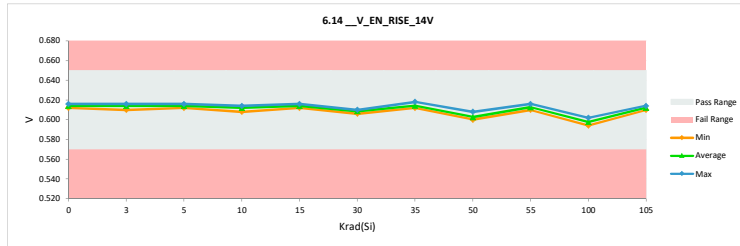


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6.14 V_EN_RISE_14V				
Test Site	Tester	Test Number	Unit	Delta
			V	
Max Limit	0.65		V	0.65
Min Limit	0.57		V	0.57
Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.616	0.616	0.000
0	118	0.614	0.612	-0.002
0	122	0.616	0.614	-0.002
3	61	0.618	0.616	-0.002
3	62	0.616	0.614	-0.002
3	63	0.612	0.610	-0.002
3	64	0.618	0.616	-0.002
3	65	0.616	0.614	-0.002
3	66	0.616	0.614	-0.002
5	67	0.616	0.614	-0.002
5	68	0.618	0.614	-0.004
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.614	-0.004
10	77	0.616	0.612	-0.004
10	78	0.616	0.612	-0.004
15	79	0.616	0.614	-0.002
15	80	0.618	0.614	-0.004
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.612	-0.002
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.610	-0.006
30	95	0.616	0.608	-0.008
35	100	0.616	0.618	0.002
35	101	0.618	0.614	-0.004
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.614	0.000
35	105	0.614	0.612	-0.002
50	106	0.614	0.602	-0.012
50	107	0.616	0.602	-0.014
50	108	0.614	0.600	-0.014
50	109	0.618	0.608	-0.010
50	110	0.614	0.602	-0.012
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.614	0.612	-0.002
55	115	0.616	0.612	-0.004
55	116	0.616	0.614	-0.002
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.618	0.600	-0.018
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.600	-0.018
100	139	0.616	0.594	-0.022
100	140	0.612	0.594	-0.018
100	141	0.618	0.600	-0.018
100	142	0.614	0.594	-0.020
100	143	0.616	0.598	-0.018
100	144	0.622	0.602	-0.020
100	220	0.616	0.600	-0.016
100	221	0.618	0.600	-0.018
100	224	0.614	0.596	-0.018
100	225	0.616	0.596	-0.020
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.598	-0.018
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.616	0.612	-0.004
105	376	0.616	0.612	-0.004
105	Max	0.622	0.618	0.002
105	Average	0.616	0.608	-0.008
105	Min	0.612	0.594	-0.022
105	Std Dev	0.002	0.007	0.007



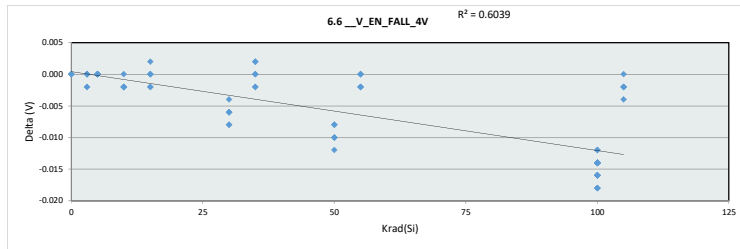
6.14 V_EN_RISE											
Test Site	Tester	Test Number	Unit	Delta	Delta	Delta	Delta	Delta	Delta	Delta	
			V								
Max Limit	0.65		V								
Min Limit	0.57		V								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.610	0.612	0.608	0.612	0.606	0.612	0.600	0.610	0.594	0.610
Average	0.614	0.614	0.614	0.612	0.614	0.609	0.614	0.603	0.613	0.598	0.612
Max	0.616	0.616	0.616	0.614	0.616	0.610	0.618	0.608	0.616	0.602	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



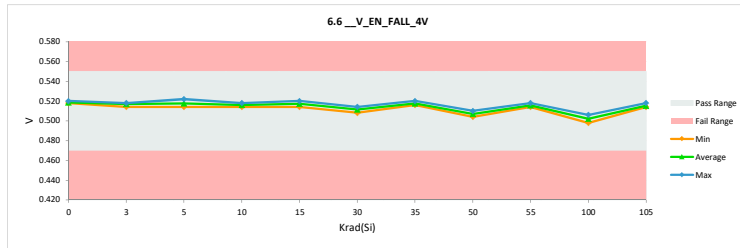
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6.6_V_EN_FALL_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.518	0.518	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.516	-0.002
3	63	0.516	0.514	-0.002
3	64	0.518	0.518	0.000
3	65	0.518	0.518	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.516	0.516	0.000
5	69	0.520	0.520	0.000
5	70	0.518	0.518	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	-0.002
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.518	0.518	0.000
10	77	0.518	0.516	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.520	-0.002
15	80	0.516	0.516	0.000
15	81	0.520	0.518	-0.002
15	82	0.518	0.520	0.002
15	83	0.516	0.516	0.000
15	84	0.514	0.514	0.000
30	86	0.518	0.512	-0.006
30	89	0.518	0.510	-0.008
30	91	0.518	0.508	-0.010
30	92	0.518	0.512	-0.006
30	94	0.518	0.514	-0.004
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.516	0.518	0.002
35	102	0.520	0.520	0.000
35	103	0.518	0.516	-0.002
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.516	0.508	-0.008
50	107	0.516	0.504	-0.012
50	108	0.514	0.504	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.510	-0.008
50	111	0.516	0.506	-0.010
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.518	0.000
55	128	0.516	0.514	-0.002
100	129	0.516	0.498	-0.018
100	131	0.516	0.500	-0.016
100	132	0.520	0.506	-0.014
100	133	0.516	0.500	-0.016
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.516	0.502	-0.014
100	140	0.512	0.498	-0.014
100	141	0.520	0.506	-0.014
100	142	0.512	0.498	-0.014
100	143	0.516	0.502	-0.014
100	144	0.524	0.506	-0.018
100	220	0.518	0.502	-0.016
100	221	0.516	0.502	-0.014
100	224	0.516	0.500	-0.016
100	225	0.516	0.502	-0.014
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.516	0.500	-0.016
100	243	0.516	0.504	-0.012
105	244	0.516	0.514	-0.002
105	246	0.516	0.514	-0.002
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.520	0.514	-0.002
	Max	0.524	0.522	0.002
	Average	0.517	0.511	-0.006
	Min	0.512	0.498	-0.018
	Std Dev	0.002	0.007	0.006



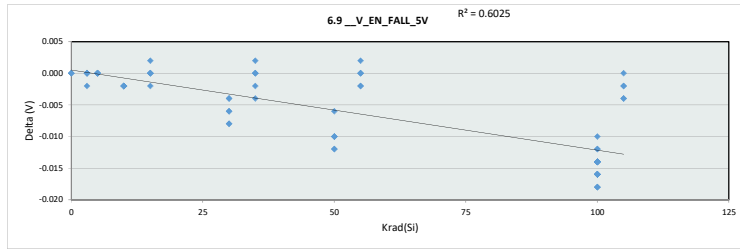
6.6_V_EN_FALL											
Test Site											
Tester											
Test Number											
Max Limit	0.55 V										
Min Limit	0.47 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.514	0.514	0.514	0.514	0.508	0.516	0.504	0.514	0.498	0.514
Average	0.519	0.517	0.518	0.518	0.516	0.517	0.511	0.518	0.507	0.516	0.502
Max	0.520	0.518	0.522	0.518	0.520	0.514	0.520	0.510	0.518	0.506	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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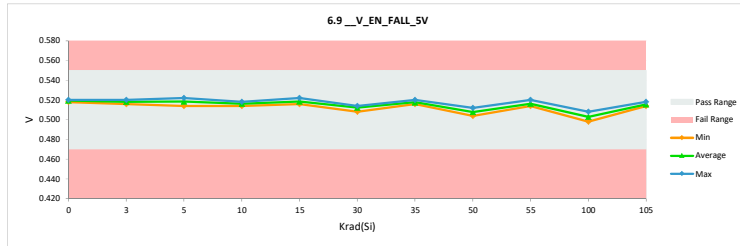
6.9_V_EN_FALL_5V			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.55	0.55	
Min Limit	0.47	0.47	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.518	0.000
3	63	0.516	0.516	0.000
3	64	0.520	0.518	-0.002
3	65	0.520	0.520	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.518	0.518	0.000
5	69	0.520	0.520	0.000
5	70	0.520	0.520	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	-0.002
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.520	0.518	-0.002
10	77	0.520	0.518	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.522	0.000
15	80	0.518	0.516	-0.002
15	81	0.520	0.520	0.000
15	82	0.520	0.520	0.000
15	83	0.516	0.516	0.000
15	84	0.514	0.516	0.002
30	86	0.518	0.514	-0.004
30	89	0.518	0.514	-0.004
30	91	0.516	0.508	-0.008
30	92	0.520	0.512	-0.008
30	94	0.520	0.514	-0.006
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.518	0.518	0.000
35	102	0.520	0.520	0.000
35	103	0.520	0.516	-0.004
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.518	0.508	-0.010
50	107	0.516	0.504	-0.012
50	108	0.516	0.506	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.512	-0.006
50	111	0.518	0.506	-0.012
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.520	0.002
55	128	0.516	0.516	0.000
100	129	0.516	0.498	-0.018
100	131	0.516	0.502	-0.014
100	132	0.522	0.506	-0.016
100	133	0.516	0.504	-0.012
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.518	0.504	-0.014
100	140	0.514	0.498	-0.016
100	141	0.520	0.506	-0.014
100	142	0.516	0.500	-0.016
100	143	0.518	0.502	-0.016
100	144	0.524	0.508	-0.016
100	220	0.518	0.504	-0.014
100	221	0.518	0.502	-0.016
100	224	0.516	0.502	-0.014
100	225	0.518	0.502	-0.016
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.518	0.500	-0.018
100	243	0.516	0.506	-0.010
105	244	0.518	0.516	-0.002
105	246	0.518	0.514	-0.004
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.516	0.514	-0.002
105	Max	0.524	0.522	0.002
105	Average	0.518	0.512	-0.006
105	Min	0.514	0.498	-0.018
105	Std Dev	0.002	0.007	0.007



6.9_V_EN_FALL			
Test Site			
Tester			
Test Number			
Max Limit	0.55	V	
Min Limit	0.47	V	

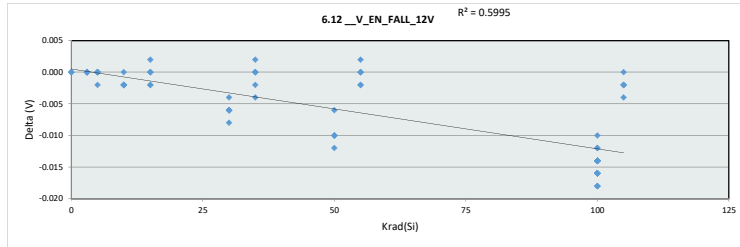
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.514	0.516	0.508	0.516	0.504	0.514	0.498	0.514
Average	0.519	0.518	0.518	0.518	0.518	0.512	0.518	0.508	0.516	0.503	0.516
Max	0.520	0.520	0.522	0.518	0.522	0.514	0.520	0.512	0.520	0.508	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



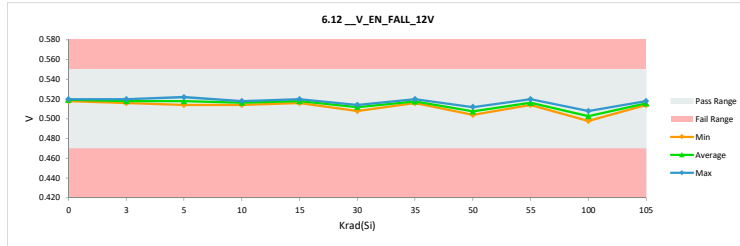
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6.12 V_EN_FALL_12V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	0.55	0.55
Min Limit	0.47	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.518	0.000
3	63	0.516	0.516	0.000
3	64	0.518	0.518	0.000
3	65	0.520	0.520	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.518	0.516	-0.002
5	69	0.520	0.520	0.000
5	70	0.520	0.520	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	-0.002
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.518	0.518	0.000
10	77	0.520	0.518	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.520	-0.002
15	80	0.518	0.516	-0.002
15	81	0.520	0.520	0.000
15	82	0.520	0.520	0.000
15	83	0.516	0.516	0.000
15	84	0.514	0.516	0.002
30	86	0.518	0.512	-0.006
30	89	0.518	0.514	-0.004
30	91	0.516	0.508	-0.008
30	92	0.518	0.512	-0.006
30	94	0.520	0.514	-0.006
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.518	0.518	0.000
35	102	0.520	0.520	0.000
35	103	0.520	0.516	-0.004
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.518	0.508	-0.010
50	107	0.516	0.504	-0.012
50	108	0.516	0.506	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.512	-0.006
50	111	0.516	0.506	-0.010
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.520	0.002
55	128	0.516	0.516	0.000
100	129	0.516	0.498	-0.018
100	131	0.516	0.502	-0.014
100	132	0.522	0.506	-0.016
100	133	0.516	0.504	-0.012
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.518	0.504	-0.014
100	140	0.514	0.498	-0.016
100	141	0.520	0.506	-0.014
100	142	0.516	0.500	-0.016
100	143	0.518	0.502	-0.016
100	144	0.524	0.508	-0.016
100	220	0.518	0.504	-0.014
100	221	0.518	0.502	-0.016
100	224	0.518	0.502	-0.016
100	225	0.518	0.502	-0.016
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.518	0.500	-0.018
100	243	0.516	0.506	-0.010
105	244	0.518	0.516	-0.002
105	246	0.516	0.514	-0.002
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.516	0.514	-0.002
105	Max	0.524	0.522	0.002
105	Average	0.518	0.512	-0.006
105	Min	0.514	0.498	-0.018
105	Std Dev	0.002	0.007	0.007



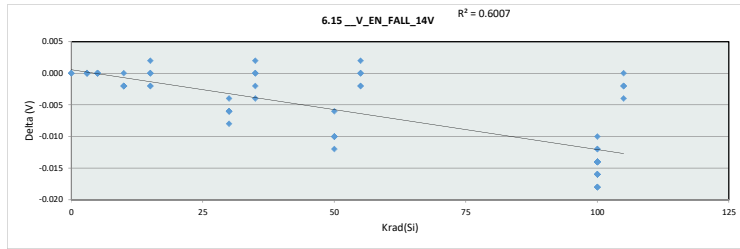
6.12 V_EN_FALL										
Test Site										
Tester										
Test Number										
Max Limit	0.55	V								
Min Limit	0.47	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.514	0.516	0.508	0.516	0.504	0.514	0.498
Average	0.519	0.518	0.518	0.516	0.518	0.512	0.518	0.508	0.516	0.503
Max	0.520	0.520	0.522	0.518	0.520	0.514	0.520	0.512	0.520	0.508
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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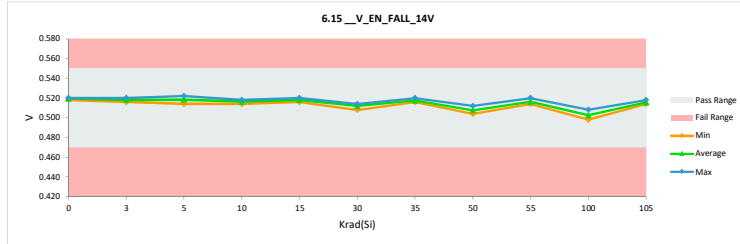
6.15_V_EN_FALL_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.518	0.000
3	63	0.516	0.516	0.000
3	64	0.518	0.518	0.000
3	65	0.520	0.520	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.518	0.518	0.000
5	69	0.520	0.520	0.000
5	70	0.520	0.520	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	-0.002
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.518	0.518	0.000
10	77	0.520	0.518	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.520	-0.002
15	80	0.518	0.516	-0.002
15	81	0.520	0.520	0.000
15	82	0.520	0.520	0.000
15	83	0.516	0.516	0.000
15	84	0.514	0.516	0.002
30	86	0.518	0.512	-0.006
30	89	0.518	0.514	-0.004
30	91	0.516	0.508	-0.008
30	92	0.518	0.512	-0.006
30	94	0.520	0.514	-0.006
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.518	0.518	0.000
35	102	0.520	0.520	0.000
35	103	0.520	0.516	-0.004
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.518	0.508	-0.010
50	107	0.516	0.504	-0.012
50	108	0.516	0.506	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.512	-0.006
50	111	0.516	0.506	-0.010
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.520	0.002
55	128	0.516	0.516	0.000
100	129	0.516	0.498	-0.018
100	131	0.516	0.502	-0.014
100	132	0.522	0.506	-0.016
100	133	0.516	0.504	-0.012
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.518	0.504	-0.014
100	140	0.512	0.498	-0.014
100	141	0.520	0.506	-0.014
100	142	0.516	0.498	-0.018
100	143	0.518	0.502	-0.016
100	144	0.524	0.508	-0.016
100	220	0.518	0.504	-0.014
100	221	0.518	0.502	-0.016
100	224	0.516	0.502	-0.014
100	225	0.518	0.502	-0.016
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.518	0.500	-0.018
100	243	0.516	0.506	-0.010
105	244	0.518	0.516	0.002
105	246	0.516	0.514	-0.002
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.516	0.514	-0.002
105	Max	0.524	0.522	0.002
105	Average	0.518	0.512	-0.006
105	Min	0.512	0.498	-0.018
105	Std Dev	0.002	0.007	0.007



6.15_V_EN_FALL	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

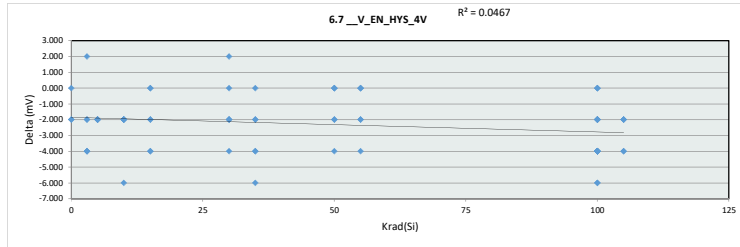
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.514	0.516	0.508	0.516	0.504	0.514	0.498	0.514
Average	0.519	0.518	0.518	0.518	0.518	0.512	0.518	0.508	0.516	0.503	0.516
Max	0.520	0.520	0.522	0.518	0.520	0.514	0.520	0.512	0.520	0.508	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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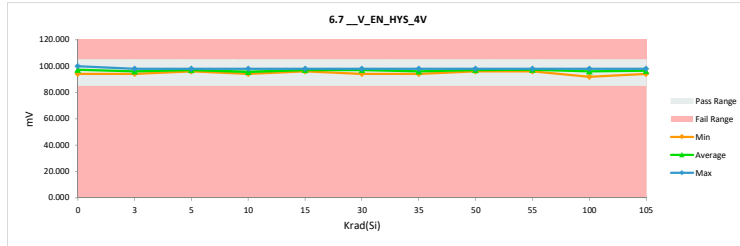
6.7_V_EN_HYS_4V		
Test Site		
Tester		
Test Number		
Unit	mV	mV
Max Limit	105	105
Min Limit	85	85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	100.000	100.000	0.000
0	118	96.000	94.000	-2.000
0	122	100.000	98.000	-2.000
3	61	100.000	96.000	-4.000
3	62	98.000	96.000	-2.000
3	63	96.000	98.000	2.000
3	64	100.000	96.000	-4.000
3	65	98.000	96.000	-2.000
3	66	98.000	94.000	-4.000
5	67	98.000	96.000	-2.000
5	68	100.000	98.000	-2.000
5	69	98.000	96.000	-2.000
5	70	100.000	98.000	-2.000
5	71	98.000	96.000	-2.000
5	72	100.000	98.000	-2.000
10	73	100.000	98.000	-2.000
10	74	98.000	96.000	-2.000
10	75	96.000	94.000	-2.000
10	76	100.000	94.000	-6.000
10	77	98.000	96.000	-2.000
10	78	98.000	96.000	-2.000
15	79	98.000	98.000	0.000
15	80	100.000	98.000	-2.000
15	81	98.000	98.000	0.000
15	82	100.000	96.000	-4.000
15	83	98.000	96.000	-2.000
15	84	100.000	96.000	-4.000
30	86	100.000	98.000	-2.000
30	89	98.000	98.000	0.000
30	91	96.000	98.000	2.000
30	92	100.000	98.000	-2.000
30	94	98.000	94.000	-4.000
30	95	98.000	96.000	-2.000
35	100	98.000	98.000	0.000
35	101	100.000	96.000	-4.000
35	102	98.000	96.000	-2.000
35	103	100.000	96.000	-4.000
35	104	98.000	96.000	-2.000
35	105	100.000	94.000	-6.000
50	106	98.000	98.000	0.000
50	107	98.000	98.000	0.000
50	108	98.000	96.000	-2.000
50	109	96.000	96.000	0.000
50	110	100.000	98.000	-2.000
50	111	100.000	96.000	-4.000
55	113	98.000	98.000	0.000
55	114	98.000	98.000	0.000
55	115	98.000	98.000	0.000
55	116	100.000	96.000	-4.000
55	126	100.000	98.000	-2.000
55	128	98.000	96.000	-2.000
100	129	100.000	98.000	-2.000
100	131	100.000	96.000	-4.000
100	132	100.000	94.000	-6.000
100	133	100.000	98.000	-2.000
100	134	98.000	94.000	-4.000
100	135	98.000	98.000	0.000
100	138	98.000	98.000	0.000
100	139	100.000	98.000	-2.000
100	140	100.000	96.000	-4.000
100	141	98.000	96.000	-2.000
100	142	100.000	96.000	-4.000
100	143	100.000	96.000	-4.000
100	144	98.000	94.000	-4.000
100	220	98.000	96.000	-2.000
100	221	100.000	96.000	-4.000
100	224	100.000	98.000	-2.000
100	225	98.000	94.000	-4.000
100	226	98.000	94.000	-4.000
100	228	100.000	98.000	-2.000
100	232	98.000	96.000	-2.000
100	233	98.000	92.000	-6.000
100	236	100.000	96.000	-4.000
100	238	98.000	98.000	0.000
100	243	98.000	94.000	-4.000
105	244	100.000	98.000	-2.000
105	246	98.000	96.000	-2.000
105	248	100.000	98.000	-2.000
105	374	98.000	94.000	-4.000
105	375	100.000	96.000	-4.000
105	Max	100.000	100.000	2.000
105	Average	98.775	96.450	-2.325
105	Min	96.000	92.000	-6.000
105	Std Dev	1.211	1.558	1.727



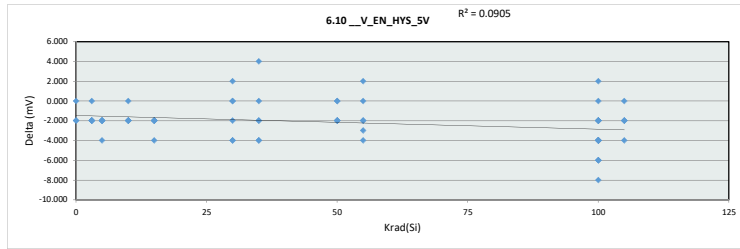
6.7_V_EN_HYS		
Test Site		
Tester		
Test Number		
Max Limit	105	mV
Min Limit	85	mV

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	96.000	94.000	96.000	94.000	94.000	96.000	96.000	92.000	94.000
Average	97.333	96.000	97.000	95.667	97.000	97.000	96.000	97.000	97.333	96.000	96.400
Max	100.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

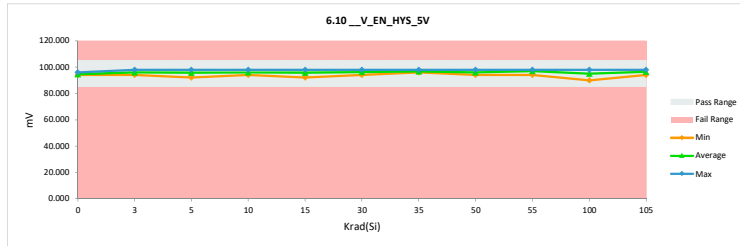


LDR TID Report
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6.10_V_EN_HYS_SV				
Test Site	Tester	Test Number	Unit	Delta
			mV	mV
Max Limit	105	105		
Min Limit	85	85		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	96.000	96.000	0.000
0	118	96.000	94.000	-2.000
0	122	96.000	94.000	-2.000
3	61	100.000	98.000	-2.000
3	62	98.000	96.000	-2.000
3	63	96.000	94.000	-2.000
3	64	98.000	98.000	0.000
3	65	96.000	94.000	-2.000
3	66	98.000	96.000	-2.000
5	67	94.000	92.000	-2.000
5	68	100.000	96.000	-4.000
5	69	98.000	96.000	-2.000
5	70	98.000	96.000	-2.000
5	71	98.000	96.000	-2.000
5	72	100.000	98.000	-2.000
10	73	100.000	98.000	-2.000
10	74	98.000	96.000	-2.000
10	75	96.000	94.000	-2.000
10	76	98.000	96.000	-2.000
10	77	96.000	96.000	0.000
10	78	98.000	96.000	-2.000
15	79	94.000	92.000	-2.000
15	80	100.000	98.000	-2.000
15	81	98.000	96.000	-2.000
15	82	98.000	96.000	-2.000
15	83	98.000	96.000	-2.000
15	84	100.000	96.000	-4.000
30	86	100.000	96.000	-4.000
30	89	98.000	94.000	-4.000
30	91	96.000	98.000	2.000
30	92	98.000	98.000	0.000
30	94	96.000	96.000	0.000
30	95	98.000	96.000	-2.000
35	100	94.000	98.000	4.000
35	101	100.000	96.000	-4.000
35	102	98.000	96.000	-2.000
35	103	98.000	96.000	-2.000
35	104	98.000	98.000	0.000
35	105	100.000	96.000	-4.000
50	106	96.000	94.000	-2.000
50	107	100.000	98.000	-2.000
50	108	96.000	94.000	-2.000
50	109	98.000	98.000	0.000
50	110	96.000	96.000	0.000
50	111	98.000	96.000	-2.000
55	113	98.000	98.000	0.000
55	114	100.000	98.000	-2.000
55	115	96.000	98.000	2.000
55	116	100.000	98.000	-2.000
55	126	100.000	96.000	-4.000
55	128	97.000	94.000	-3.000
100	129	100.000	98.000	-2.000
100	131	100.000	94.000	-6.000
100	132	96.000	94.000	-2.000
100	133	100.000	94.000	-6.000
100	134	98.000	94.000	-4.000
100	135	98.000	98.000	0.000
100	138	98.000	94.000	-4.000
100	139	98.000	90.000	-8.000
100	140	98.000	96.000	-2.000
100	141	98.000	94.000	-4.000
100	142	98.000	94.000	-4.000
100	143	98.000	96.000	-2.000
100	144	98.000	94.000	-4.000
100	220	98.000	96.000	-2.000
100	221	100.000	98.000	-2.000
100	224	98.000	94.000	-4.000
100	225	98.000	94.000	-4.000
100	226	98.000	94.000	-4.000
100	228	100.000	98.000	-2.000
100	232	98.000	96.000	-2.000
100	233	98.000	94.000	-4.000
100	236	100.000	96.000	-4.000
100	238	96.000	98.000	2.000
100	243	98.000	92.000	-6.000
105	244	98.000	96.000	-2.000
105	246	96.000	96.000	0.000
105	248	100.000	98.000	-2.000
105	374	98.000	94.000	-4.000
105	376	100.000	98.000	-2.000
Max		100.000	98.000	4.000
Average		97.963	95.775	-2.188
Min		94.000	90.000	-8.000
Std Dev		1.579	1.800	1.890



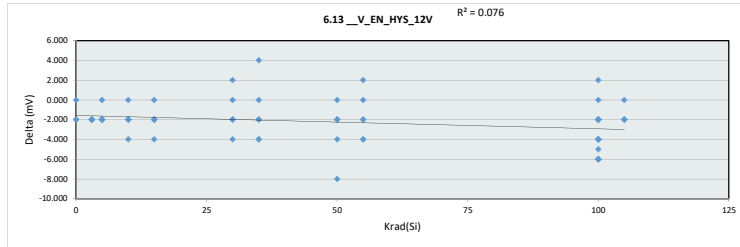
6.10_V_EN_HYS											
Test Site	Tester	Test Number	Unit	Delta							
			mV	mV							
Max Limit	105	105									
Min Limit	85	85									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	92.000	94.000	92.000	94.000	96.000	94.000	94.000	90.000	94.000
Average	94.667	96.000	95.667	96.000	95.667	96.333	96.667	96.000	97.000	95.000	96.400
Max	96.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000



LDR TID Report
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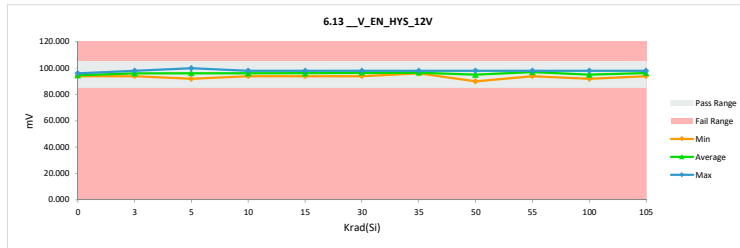
6.13 V_EN_HYS_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	96.000	96.000	0.000
0	118	96.000	94.000	-2.000
0	122	96.000	94.000	-2.000
3	61	100.000	98.000	-2.000
3	62	98.000	96.000	-2.000
3	63	96.000	94.000	-2.000
3	64	100.000	98.000	-2.000
3	65	96.000	94.000	-2.000
3	66	98.000	96.000	-2.000
5	67	94.000	92.000	-2.000
5	68	100.000	100.000	0.000
5	69	98.000	96.000	-2.000
5	70	98.000	96.000	-2.000
5	71	98.000	96.000	-2.000
5	72	100.000	98.000	-2.000
10	73	100.000	98.000	-2.000
10	74	98.000	96.000	-2.000
10	75	96.000	94.000	-2.000
10	76	100.000	96.000	-4.000
10	77	96.000	96.000	0.000
10	78	98.000	96.000	-2.000
15	79	94.000	94.000	0.000
15	80	100.000	98.000	-2.000
15	81	98.000	96.000	-2.000
15	82	98.000	96.000	-2.000
15	83	98.000	96.000	-2.000
15	84	100.000	96.000	-4.000
30	86	100.000	98.000	-2.000
30	89	98.000	94.000	-4.000
30	91	96.000	94.000	-2.000
30	92	100.000	98.000	-2.000
30	94	96.000	96.000	0.000
30	95	98.000	96.000	-2.000
35	100	94.000	98.000	4.000
35	101	100.000	96.000	-4.000
35	102	98.000	96.000	-2.000
35	103	98.000	96.000	-2.000
35	104	98.000	98.000	0.000
35	105	100.000	96.000	-4.000
50	106	96.000	94.000	-2.000
50	107	100.000	98.000	-2.000
50	108	96.000	94.000	-2.000
50	109	98.000	98.000	0.000
50	110	98.000	90.000	-8.000
50	111	100.000	96.000	-4.000
55	113	98.000	98.000	0.000
55	114	100.000	98.000	-2.000
55	115	96.000	98.000	2.000
55	116	100.000	98.000	-2.000
55	126	100.000	96.000	-4.000
55	128	98.000	94.000	-4.000
100	129	100.000	98.000	-2.000
100	131	100.000	94.000	-6.000
100	132	96.000	94.000	-2.000
100	133	100.000	94.000	-6.000
100	134	98.000	94.000	-4.000
100	135	98.000	98.000	0.000
100	138	98.000	94.000	-4.000
100	139	98.000	92.000	-6.000
100	140	98.000	96.000	-2.000
100	141	98.000	93.000	-5.000
100	142	98.000	94.000	-4.000
100	143	98.000	96.000	-2.000
100	144	100.000	94.000	-6.000
100	220	98.000	96.000	-2.000
100	221	100.000	98.000	-2.000
100	224	96.000	94.000	-2.000
100	225	98.000	94.000	-4.000
100	226	98.000	94.000	-4.000
100	228	100.000	98.000	-2.000
100	232	98.000	96.000	-2.000
100	233	98.000	94.000	-4.000
100	236	100.000	96.000	-4.000
100	238	96.000	98.000	2.000
100	243	98.000	92.000	-6.000
105	244	98.000	96.000	-2.000
105	246	98.000	96.000	-2.000
105	248	100.000	98.000	-2.000
105	374	94.000	94.000	0.000
105	375	100.000	98.000	-2.000
105	Max	100.000	100.000	4.000
105	Average	98.075	95.813	-2.263
105	Min	94.000	90.000	-8.000
105	Std Dev	1.697	1.903	1.960



6.13 V_EN_HYS	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

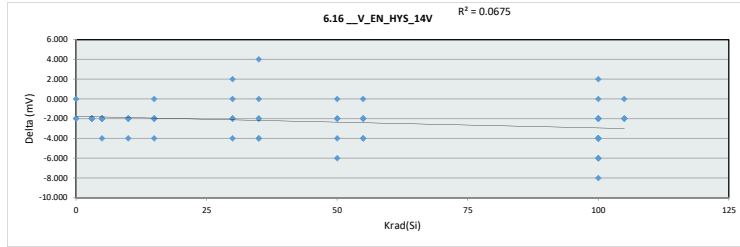
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	92.000	94.000	94.000	94.000	96.000	90.000	94.000	92.000	94.000
Average	94.667	96.000	96.333	96.000	96.000	96.667	96.667	95.000	97.000	95.042	96.400
Max	96.000	98.000	100.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000



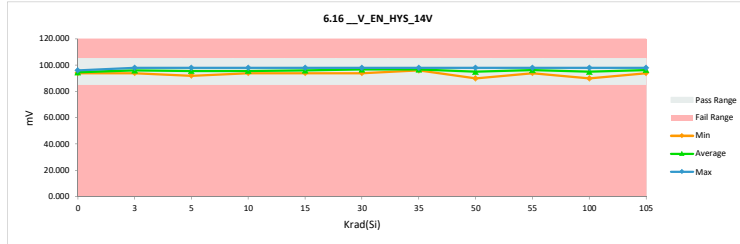
LDR TID Report
TPS7H5001-SP

6.16_V_EN_HYS_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	96.000	96.000	0.000
0	118	96.000	94.000	-2.000
0	122	96.000	94.000	-2.000
3	61	100.000	98.000	-2.000
3	62	98.000	96.000	-2.000
3	63	96.000	94.000	-2.000
3	64	100.000	98.000	-2.000
3	65	96.000	94.000	-2.000
3	66	98.000	96.000	-2.000
5	67	94.000	92.000	-2.000
5	68	100.000	96.000	-4.000
5	69	98.000	96.000	-2.000
5	70	98.000	96.000	-2.000
5	71	98.000	96.000	-2.000
5	72	100.000	98.000	-2.000
10	73	100.000	98.000	-2.000
10	74	98.000	96.000	-2.000
10	75	96.000	94.000	-2.000
10	76	100.000	96.000	-4.000
10	77	96.000	94.000	-2.000
10	78	98.000	96.000	-2.000
15	79	94.000	94.000	0.000
15	80	100.000	98.000	-2.000
15	81	98.000	96.000	-2.000
15	82	98.000	96.000	-2.000
15	83	98.000	96.000	-2.000
15	84	100.000	96.000	-4.000
30	86	100.000	98.000	-2.000
30	89	98.000	94.000	-4.000
30	91	96.000	94.000	-2.000
30	92	100.000	98.000	-2.000
30	94	96.000	96.000	0.000
30	95	98.000	96.000	-2.000
35	100	94.000	98.000	4.000
35	101	100.000	96.000	-4.000
35	102	98.000	96.000	-2.000
35	103	98.000	96.000	-2.000
35	104	98.000	98.000	0.000
35	105	100.000	96.000	-4.000
50	106	96.000	94.000	-2.000
50	107	100.000	98.000	-2.000
50	108	96.000	94.000	-2.000
50	109	98.000	98.000	0.000
50	110	96.000	90.000	-6.000
50	111	100.000	96.000	-4.000
55	113	98.000	98.000	0.000
55	114	100.000	98.000	-2.000
55	115	96.000	94.000	-2.000
55	116	100.000	98.000	-2.000
55	126	100.000	96.000	-4.000
55	128	98.000	94.000	-4.000
100	129	100.000	98.000	-2.000
100	131	100.000	94.000	-6.000
100	132	96.000	94.000	-2.000
100	133	100.000	94.000	-6.000
100	134	98.000	94.000	-4.000
100	135	98.000	98.000	0.000
100	138	98.000	94.000	-4.000
100	139	98.000	90.000	-8.000
100	140	100.000	96.000	-4.000
100	141	98.000	94.000	-4.000
100	142	98.000	96.000	-2.000
100	143	98.000	96.000	-2.000
100	144	98.000	94.000	-4.000
100	220	98.000	96.000	-2.000
100	221	100.000	98.000	-2.000
100	224	98.000	94.000	-4.000
100	225	98.000	94.000	-4.000
100	226	98.000	94.000	-4.000
100	228	100.000	98.000	-2.000
100	232	98.000	96.000	-2.000
100	233	98.000	94.000	-4.000
100	236	100.000	96.000	-4.000
100	238	96.000	98.000	2.000
100	243	98.000	92.000	-6.000
105	244	98.000	96.000	-2.000
105	246	98.000	96.000	-2.000
105	248	100.000	98.000	-2.000
105	374	94.000	94.000	0.000
105	376	100.000	98.000	-2.000
Max		100.000	98.000	4.000
Average		98.075	95.700	-2.375
Min		94.000	90.000	-8.000
Std Dev		1.697	1.885	1.831



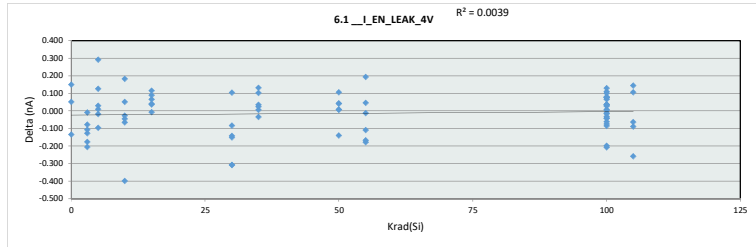
6.16_V_EN_HYS		5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	92.000	94.000	94.000	96.000	90.000	94.000	90.000	94.000
Average	94.667	96.000	95.667	95.667	96.000	96.667	95.000	96.333	95.083	96.400
Max	96.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000



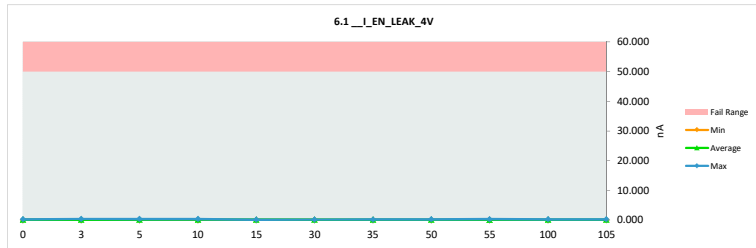
LDR TID Report
TPS7H5001-SP

6.1_I_EN_LEAK_4V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50 50
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.095	0.244	0.149
0	118	0.017	0.068	0.051
0	122	0.203	0.068	-0.135
3	61	0.029	0.020	-0.009
3	62	0.156	0.029	-0.127
3	63	0.400	0.322	-0.078
3	64	0.361	0.156	-0.205
3	65	0.166	0.059	-0.107
3	66	0.264	0.088	-0.176
5	67	0.039	0.020	-0.019
5	68	0.020	0.312	0.292
5	69	0.039	0.049	0.010
5	70	0.039	0.068	0.029
5	71	0.117	0.020	-0.097
5	72	0.059	0.185	0.126
10	73	0.029	0.212	0.183
10	74	0.156	0.207	0.051
10	75	0.400	0.002	-0.398
10	76	0.361	0.295	-0.066
10	77	0.166	0.120	-0.046
10	78	0.264	0.237	-0.027
15	79	0.039	0.076	0.037
15	80	0.020	0.134	0.114
15	81	0.039	0.105	0.066
15	82	0.039	0.129	0.090
15	83	0.117	0.110	-0.007
15	84	0.059	0.100	0.041
30	86	0.029	0.132	0.103
30	89	0.156	0.073	-0.083
30	91	0.400	0.093	-0.307
30	92	0.361	0.054	-0.307
30	94	0.166	0.024	-0.142
30	95	0.264	0.112	-0.152
35	100	0.039	0.171	0.132
35	101	0.020	0.122	0.102
35	102	0.039	0.063	0.024
35	103	0.039	0.073	0.034
35	104	0.117	0.122	0.005
35	105	0.059	0.024	-0.035
50	106	0.146	0.005	-0.141
50	107	0.095	0.102	0.007
50	108	0.041	0.083	0.042
50	109	0.041	0.083	0.042
50	110	0.027	0.034	0.007
50	111	0.134	0.239	0.105
55	113	0.203	0.024	-0.179
55	114	0.222	0.054	-0.168
55	115	0.066	0.112	0.046
55	116	0.124	0.015	-0.109
55	126	0.066	0.259	0.193
55	128	0.017	0.005	-0.012
100	129	0.105	0.142	0.037
100	131	0.124	0.083	-0.041
100	132	0.183	0.210	0.027
100	133	0.154	0.112	-0.042
100	134	0.129	0.054	-0.075
100	135	0.002	0.132	0.130
100	138	0.056	0.024	-0.032
100	139	0.173	0.112	-0.061
100	140	0.027	0.102	0.075
100	141	0.203	0.005	-0.198
100	142	0.022	0.122	0.100
100	143	0.115	0.102	-0.013
100	144	0.066	0.073	0.007
100	220	0.037	0.073	0.036
100	221	0.271	0.063	-0.208
100	224	0.090	0.073	-0.017
100	225	0.090	0.093	0.003
100	226	0.105	0.181	0.076
100	228	0.012	0.122	0.110
100	232	0.149	0.063	-0.086
100	233	0.115	0.181	0.066
100	236	0.061	0.093	0.032
100	238	0.056	0.132	0.076
100	243	0.051	0.132	0.081
105	244	0.085	0.190	0.105
105	246	0.134	0.044	-0.090
105	248	0.027	0.171	0.144
105	374	0.176	0.112	-0.064
105	376	0.264	0.005	-0.259
105	Max	0.400	0.322	0.292
105	Average	0.121	0.106	-0.014
105	Min	0.002	0.002	-0.398
105	Std Dev	0.102	0.074	0.123



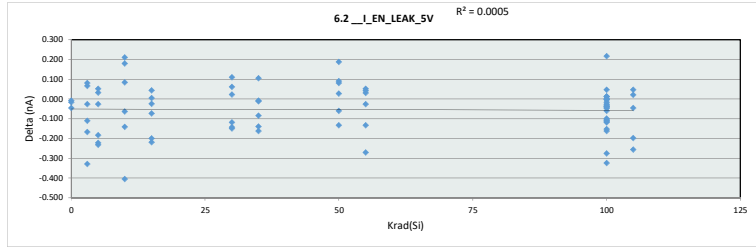
6.1_I_EN_LEAK	
Test Site	
Tester	
Test Number	
Max Limit	50 nA
Min Limit	nA
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	0.068 0.020 0.020 0.002 0.076 0.024 0.024 0.005 0.005 0.005 0.005
Average	0.127 0.112 0.109 0.179 0.109 0.081 0.096 0.091 0.078 0.103 0.104
Max	0.244 0.322 0.312 0.295 0.134 0.132 0.171 0.239 0.259 0.210 0.190
UL	50.000 50.000 50.000 50.000 50.000 50.000 50.000 50.000 50.000 50.000 50.000



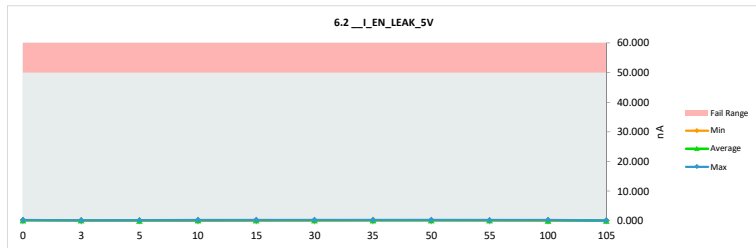
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6.2_I_EN_LEAK_5V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50 50
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.098	0.090	-0.008
0	118	0.127	0.110	-0.017
0	122	0.234	0.188	-0.046
3	61	0.049	0.115	0.066
3	62	0.088	0.061	-0.027
3	63	0.185	0.017	-0.168
3	64	0.039	0.120	0.081
3	65	0.195	0.085	-0.110
3	66	0.429	0.100	-0.329
5	67	0.020	0.071	0.051
5	68	0.156	0.129	-0.027
5	69	0.224	0.002	-0.222
5	70	0.127	0.159	0.032
5	71	0.273	0.090	-0.183
5	72	0.234	0.002	-0.232
10	73	0.049	0.229	0.180
10	74	0.088	0.024	-0.064
10	75	0.185	0.268	0.083
10	76	0.039	0.249	0.210
10	77	0.195	0.054	-0.141
10	78	0.429	0.024	-0.405
15	79	0.020	0.063	0.043
15	80	0.156	0.083	-0.073
15	81	0.224	0.229	0.005
15	82	0.127	0.102	-0.025
15	83	0.273	0.073	-0.200
15	84	0.234	0.015	-0.219
30	86	0.049	0.159	0.110
30	89	0.088	0.110	0.022
30	91	0.185	0.066	-0.119
30	92	0.039	0.100	0.061
30	94	0.195	0.046	-0.149
30	95	0.429	0.286	-0.143
35	100	0.020	0.012	-0.008
35	101	0.156	0.144	-0.012
35	102	0.224	0.061	-0.163
35	103	0.127	0.232	0.105
35	104	0.273	0.134	-0.139
35	105	0.234	0.149	-0.085
50	106	0.312	0.178	-0.134
50	107	0.059	0.149	0.090
50	108	0.020	0.207	0.187
50	109	0.117	0.198	0.081
50	110	0.068	0.095	0.027
50	111	0.127	0.066	-0.061
55	113	0.020	0.071	0.051
55	114	0.273	0.139	-0.134
55	115	0.137	0.168	0.031
55	116	0.156	0.129	-0.027
55	126	0.381	0.110	-0.271
55	128	0.078	0.120	0.042
100	129	0.176	0.139	-0.037
100	131	0.224	0.115	-0.109
100	132	0.049	0.051	0.002
100	133	0.322	0.046	-0.276
100	134	0.088	0.027	-0.061
100	135	0.224	0.105	-0.119
100	138	0.137	0.100	-0.037
100	139	0.117	0.129	0.012
100	140	0.185	0.032	-0.153
100	141	0.049	0.017	-0.032
100	142	0.224	0.061	-0.163
100	143	0.010	0.056	0.046
100	144	0.068	0.022	-0.046
100	220	0.029	0.002	-0.027
100	221	0.195	0.090	-0.105
100	224	0.020	0.237	0.217
100	225	0.078	0.032	-0.046
100	226	0.078	0.061	-0.017
100	228	0.215	0.212	-0.003
100	232	0.098	0.110	0.012
100	233	0.117	0.017	-0.100
100	236	0.146	0.032	-0.114
100	238	0.049	0.046	-0.003
100	243	0.400	0.076	-0.324
105	244	0.127	0.081	-0.046
105	246	0.322	0.066	-0.256
105	248	0.029	0.076	0.047
105	374	0.020	0.041	0.021
105	376	0.254	0.056	-0.198
105	Max	0.429	0.286	0.217
105	Average	0.154	0.100	-0.054
105	Min	0.010	0.002	-0.405
105	Std Dev	0.108	0.068	0.124



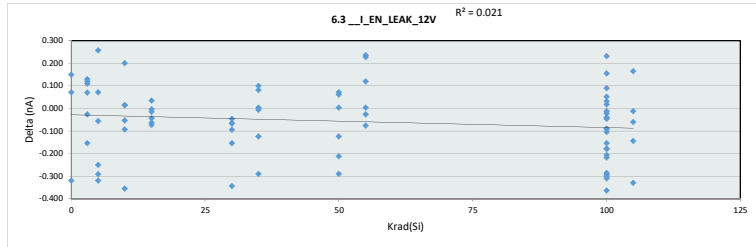
6.2_I_EN_LEAK	
Test Site	
Tester	
Test Number	
Max Limit	50 nA
Min Limit	nA
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105



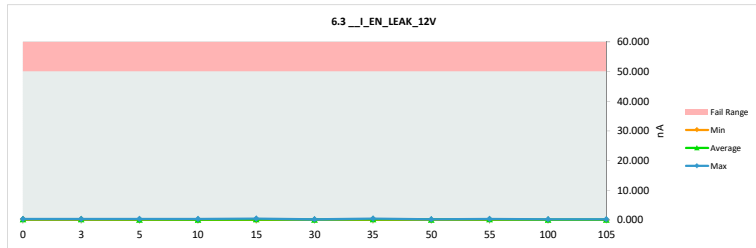
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6.3 I_EN_LEAK_12V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50 50
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.205	0.276	0.071
0	118	0.664	0.344	-0.320
0	122	0.039	0.188	0.149
3	61	0.371	0.344	-0.027
3	62	0.166	0.295	0.129
3	63	0.127	0.246	0.119
3	64	0.137	0.207	0.070
3	65	0.342	0.188	-0.154
3	66	0.176	0.286	0.110
5	67	0.117	0.373	0.256
5	68	0.107	0.178	0.071
5	69	0.117	0.061	-0.056
5	70	0.429	0.178	-0.251
5	71	0.332	0.041	-0.291
5	72	0.449	0.129	-0.320
10	73	0.371	0.015	-0.356
10	74	0.166	0.366	0.200
10	75	0.127	0.142	0.015
10	76	0.137	0.151	0.014
10	77	0.342	0.249	-0.093
10	78	0.176	0.122	-0.054
15	79	0.117	0.044	-0.073
15	80	0.107	0.044	-0.063
15	81	0.117	0.073	-0.044
15	82	0.429	0.425	-0.004
15	83	0.332	0.366	0.034
15	84	0.449	0.434	-0.015
30	86	0.371	0.027	-0.344
30	89	0.166	0.100	-0.066
30	91	0.127	0.081	-0.046
30	92	0.137	0.071	-0.066
30	94	0.342	0.188	-0.154
30	95	0.176	0.081	-0.095
35	100	0.117	0.110	-0.007
35	101	0.107	0.207	0.100
35	102	0.117	0.198	0.081
35	103	0.429	0.432	0.003
35	104	0.332	0.207	-0.125
35	105	0.449	0.159	-0.290
50	106	0.273	0.061	-0.212
50	107	0.166	0.237	0.071
50	108	0.410	0.120	-0.290
50	109	0.234	0.110	-0.124
50	110	0.107	0.110	0.003
50	111	0.088	0.149	0.061
55	113	0.059	0.178	0.119
55	114	0.254	0.178	-0.076
55	115	0.254	0.227	-0.027
55	116	0.351	0.354	0.003
55	126	0.107	0.334	0.227
55	128	0.059	0.295	0.236
100	129	0.303	0.012	-0.291
100	131	0.303	0.124	-0.179
100	132	0.390	0.183	-0.207
100	133	0.244	0.232	-0.012
100	134	0.244	0.222	-0.022
100	135	0.166	0.076	-0.090
100	138	0.010	0.164	0.154
100	139	0.273	0.095	-0.178
100	140	0.107	0.124	0.017
100	141	0.215	0.173	-0.042
100	142	0.351	0.051	-0.300
100	143	0.469	0.251	-0.218
100	144	0.361	0.051	-0.310
100	220	0.459	0.095	-0.364
100	221	0.156	0.115	-0.041
100	224	0.117	0.027	-0.090
100	225	0.205	0.051	-0.154
100	226	0.224	0.120	-0.104
100	228	0.059	0.290	0.231
100	232	0.176	0.129	-0.047
100	233	0.371	0.085	-0.286
100	236	0.000	0.090	0.090
100	238	0.137	0.188	0.051
100	243	0.098	0.129	0.031
105	244	0.273	0.129	-0.144
105	246	0.127	0.066	-0.061
105	248	0.254	0.242	-0.012
105	374	0.342	0.012	-0.330
105	376	0.078	0.242	0.164
105	375	0.664	0.434	0.256
Max		0.230	0.172	-0.058
Average		0.230	0.172	-0.058
Min		0.000	0.012	-0.364
Std Dev		0.134	0.107	0.157



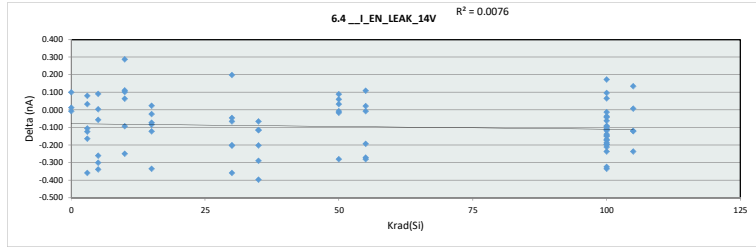
6.3 I_EN_LEAK											
Test Site											
Tester											
Test Number											
Max Limit	50	nA									
Min Limit		nA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.188	0.188	0.041	0.015	0.044	0.027	0.110	0.061	0.178	0.012	0.012
Average	0.269	0.261	0.160	0.174	0.231	0.091	0.219	0.131	0.261	0.128	0.138
Max	0.344	0.344	0.373	0.366	0.434	0.188	0.432	0.237	0.354	0.290	0.242
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



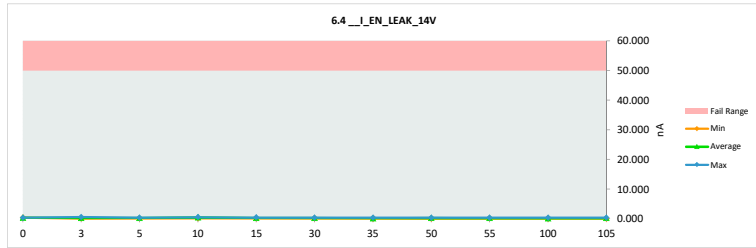
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6.4 I_EN_LEAK_14V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50 50
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.371	0.383	0.012
0	118	0.234	0.334	0.100
0	122	0.381	0.373	-0.008
3	61	0.127	0.159	0.032
3	62	0.400	0.237	-0.163
3	63	0.449	0.530	0.081
3	64	0.459	0.100	-0.359
3	65	0.361	0.237	-0.124
3	66	0.420	0.315	-0.105
5	67	0.429	0.168	-0.261
5	68	0.595	0.256	-0.339
5	69	0.381	0.081	-0.300
5	70	0.234	0.237	0.003
5	71	0.390	0.334	-0.056
5	72	0.224	0.091	-0.133
10	73	0.127	0.415	0.288
10	74	0.400	0.151	-0.249
10	75	0.449	0.356	-0.093
10	76	0.459	0.522	0.063
10	77	0.361	0.473	0.112
10	78	0.420	0.522	0.102
15	79	0.429	0.405	-0.024
15	80	0.595	0.259	-0.336
15	81	0.381	0.405	0.024
15	82	0.234	0.151	-0.083
15	83	0.390	0.268	-0.122
15	84	0.224	0.151	-0.073
30	86	0.127	0.325	0.198
30	89	0.400	0.198	-0.202
30	91	0.400	0.449	0.049
30	92	0.459	0.393	-0.066
30	94	0.361	0.315	-0.046
30	95	0.420	0.217	-0.203
35	100	0.429	0.139	-0.290
35	101	0.595	0.198	-0.397
35	102	0.381	0.315	-0.066
35	103	0.234	0.032	-0.202
35	104	0.390	0.276	-0.114
35	105	0.224	0.110	-0.114
50	106	0.215	0.276	0.061
50	107	0.322	0.354	0.032
50	108	0.185	0.168	-0.017
50	109	0.244	0.334	0.090
50	110	0.351	0.071	-0.280
50	111	0.342	0.334	-0.008
55	113	0.303	0.325	0.022
55	114	0.332	0.139	-0.193
55	115	0.361	0.090	-0.271
55	116	0.137	0.246	0.109
55	126	0.410	0.129	-0.281
55	128	0.303	0.295	-0.008
100	129	0.137	0.203	0.066
100	131	0.303	0.290	-0.013
100	132	0.371	0.203	-0.168
100	133	0.107	0.203	0.096
100	134	0.156	0.095	-0.061
100	135	0.351	0.203	-0.148
100	138	0.410	0.300	-0.110
100	139	0.322	0.212	-0.110
100	140	0.342	0.007	-0.335
100	141	0.517	0.320	-0.197
100	142	0.234	0.022	-0.212
100	143	0.117	0.290	0.173
100	144	0.185	0.012	-0.173
100	220	0.176	0.134	-0.042
100	221	0.322	0.183	-0.139
100	224	0.439	0.203	-0.236
100	225	0.361	0.164	-0.197
100	226	0.205	0.110	-0.095
100	228	0.410	0.222	-0.188
100	232	0.098	0.061	-0.037
100	233	0.185	0.066	-0.119
100	236	0.342	0.251	-0.091
100	238	0.439	0.290	-0.149
100	243	0.429	0.105	-0.324
105	244	0.254	0.134	-0.120
105	246	0.166	0.300	0.134
105	248	0.293	0.173	-0.120
105	374	0.254	0.017	-0.237
105	376	0.390	0.398	0.008
105	Max	0.595	0.530	0.288
105	Average	0.328	0.233	-0.094
105	Min	0.098	0.007	-0.397
105	Std Dev	0.119	0.124	0.148



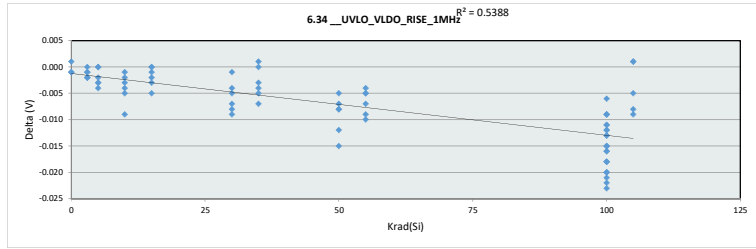
6.4 I EN LEAK											
Test Site											
Tester											
Test Number											
Max Limit	50 nA										
Min Limit	nA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.334	0.100	0.081	0.151	0.151	0.090	0.032	0.071	0.090	0.007	0.017
Average	0.363	0.263	0.232	0.407	0.273	0.256	0.178	0.256	0.204	0.173	0.204
Max	0.383	0.530	0.334	0.522	0.405	0.393	0.315	0.354	0.325	0.320	0.398
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



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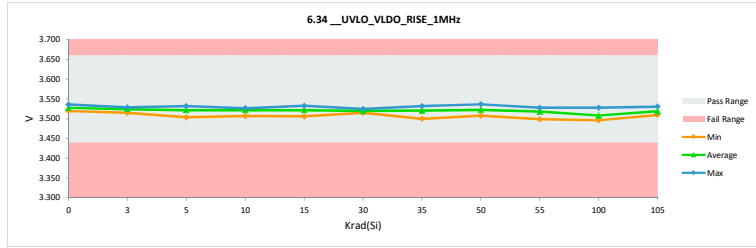
6.34 UVLO_VLDO_RISE_1MHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.519	3.520	0.001
0	118	3.528	3.527	-0.001
0	122	3.537	3.536	-0.001
3	61	3.525	3.523	-0.002
3	62	3.528	3.526	-0.002
3	63	3.516	3.515	-0.001
3	64	3.529	3.528	-0.001
3	65	3.528	3.526	-0.002
3	66	3.529	3.529	0.000
5	67	3.535	3.532	-0.003
5	68	3.507	3.504	-0.003
5	69	3.532	3.528	-0.004
5	70	3.524	3.524	0.000
5	71	3.523	3.523	0.000
5	72	3.523	3.521	-0.002
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.516	3.507	-0.009
10	76	3.529	3.525	-0.004
10	77	3.528	3.527	-0.001
10	78	3.529	3.527	-0.002
15	79	3.535	3.533	-0.002
15	80	3.507	3.506	-0.001
15	81	3.532	3.529	-0.003
15	82	3.524	3.524	0.000
15	83	3.523	3.518	-0.005
15	84	3.523	3.523	0.000
30	86	3.525	3.517	-0.008
30	89	3.528	3.521	-0.007
30	91	3.516	3.515	-0.001
30	92	3.529	3.520	-0.009
30	94	3.528	3.523	-0.005
30	95	3.529	3.525	-0.004
35	100	3.535	3.532	-0.003
35	101	3.507	3.500	-0.007
35	102	3.532	3.528	-0.004
35	103	3.524	3.525	0.001
35	104	3.523	3.518	-0.005
35	105	3.523	3.523	0.000
50	106	3.528	3.523	-0.005
50	107	3.534	3.519	-0.015
50	108	3.520	3.508	-0.012
50	109	3.539	3.531	-0.008
50	110	3.545	3.537	-0.008
50	111	3.525	3.518	-0.007
55	113	3.529	3.520	-0.009
55	114	3.504	3.499	-0.005
55	115	3.531	3.527	-0.004
55	116	3.526	3.516	-0.010
55	126	3.535	3.528	-0.007
55	128	3.524	3.519	-0.005
100	129	3.535	3.524	-0.011
100	131	3.518	3.509	-0.009
100	132	3.530	3.512	-0.018
100	133	3.521	3.509	-0.012
100	134	3.535	3.515	-0.020
100	135	3.520	3.502	-0.018
100	138	3.528	3.508	-0.020
100	139	3.527	3.512	-0.015
100	140	3.520	3.511	-0.009
100	141	3.528	3.506	-0.022
100	142	3.506	3.497	-0.009
100	143	3.526	3.511	-0.015
100	144	3.526	3.515	-0.011
100	220	3.529	3.506	-0.023
100	221	3.541	3.523	-0.018
100	224	3.513	3.497	-0.016
100	225	3.512	3.496	-0.016
100	226	3.541	3.528	-0.013
100	228	3.524	3.509	-0.015
100	232	3.525	3.504	-0.021
100	233	3.523	3.503	-0.020
100	236	3.516	3.503	-0.013
100	238	3.516	3.504	-0.012
100	243	3.513	3.507	-0.006
105	244	3.509	3.510	0.001
105	246	3.530	3.525	-0.005
105	248	3.527	3.519	-0.008
105	374	3.530	3.531	0.001
105	376	3.520	3.511	-0.009
105	Max	3.545	3.537	0.001
105	Average	3.525	3.518	-0.007
105	Min	3.504	3.496	-0.023
105	Std Dev	0.008	0.010	0.006



6.34 UVLO_VLD	
Test Site	
Tester	
Test Number	
Max Limit	3.66 V
Min Limit	3.44 V

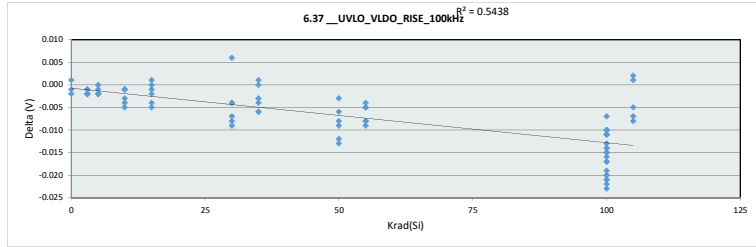
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.520	3.515	3.504	3.507	3.506	3.515	3.500	3.508	3.499	3.496	3.510
Average	3.528	3.525	3.522	3.522	3.522	3.520	3.521	3.523	3.518	3.509	3.519
Max	3.536	3.529	3.532	3.527	3.533	3.525	3.532	3.537	3.528	3.528	3.531
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



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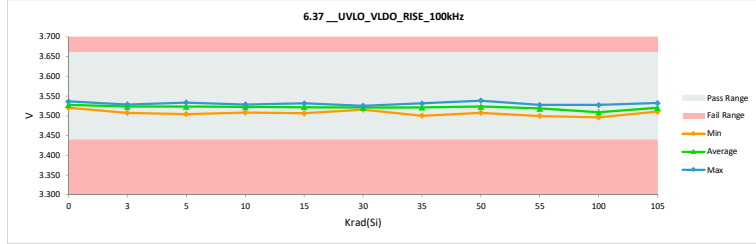
6.37 UVLO_VLDO_RISE_100kHz			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	3.66	3.66	
Min Limit	3.44	3.44	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.519	3.520	0.001
0	118	3.527	3.526	-0.001
0	122	3.538	3.536	-0.002
3	61	3.525	3.523	-0.002
3	62	3.528	3.526	-0.002
3	63	3.509	3.507	-0.002
3	64	3.529	3.528	-0.001
3	65	3.528	3.526	-0.002
3	66	3.529	3.528	-0.001
5	67	3.535	3.533	-0.002
5	68	3.506	3.504	-0.002
5	69	3.531	3.529	-0.002
5	70	3.525	3.525	0.000
5	71	3.524	3.522	-0.002
5	72	3.522	3.521	-0.001
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.509	3.508	-0.001
10	76	3.529	3.525	-0.004
10	77	3.528	3.527	-0.001
10	78	3.529	3.528	-0.001
15	79	3.535	3.531	-0.004
15	80	3.506	3.506	0.000
15	81	3.531	3.529	-0.002
15	82	3.525	3.524	-0.001
15	83	3.524	3.519	-0.005
15	84	3.522	3.523	0.001
30	86	3.525	3.517	-0.008
30	89	3.528	3.521	-0.007
30	91	3.509	3.515	0.006
30	92	3.529	3.520	-0.009
30	94	3.528	3.524	-0.004
30	95	3.529	3.525	-0.004
35	100	3.535	3.531	-0.004
35	101	3.506	3.500	-0.006
35	102	3.531	3.528	-0.003
35	103	3.525	3.525	0.000
35	104	3.524	3.518	-0.006
35	105	3.522	3.523	0.001
50	106	3.527	3.524	-0.003
50	107	3.533	3.520	-0.013
50	108	3.519	3.507	-0.012
50	109	3.540	3.531	-0.009
50	110	3.546	3.538	-0.008
50	111	3.524	3.518	-0.006
55	113	3.528	3.520	-0.008
55	114	3.504	3.499	-0.005
55	115	3.531	3.526	-0.005
55	116	3.525	3.516	-0.009
55	126	3.535	3.527	-0.008
55	128	3.524	3.520	-0.004
100	129	3.536	3.525	-0.011
100	131	3.519	3.509	-0.010
100	132	3.531	3.511	-0.020
100	133	3.520	3.509	-0.011
100	134	3.534	3.515	-0.019
100	135	3.520	3.503	-0.017
100	138	3.528	3.507	-0.021
100	139	3.527	3.512	-0.015
100	140	3.521	3.511	-0.010
100	141	3.528	3.505	-0.023
100	142	3.507	3.496	-0.011
100	143	3.526	3.512	-0.014
100	144	3.526	3.516	-0.010
100	220	3.529	3.507	-0.022
100	221	3.541	3.524	-0.017
100	224	3.513	3.497	-0.016
100	225	3.512	3.497	-0.015
100	226	3.542	3.527	-0.015
100	228	3.524	3.510	-0.014
100	232	3.525	3.504	-0.021
100	233	3.523	3.503	-0.020
100	236	3.516	3.503	-0.013
100	238	3.516	3.505	-0.011
100	243	3.514	3.507	-0.007
105	244	3.509	3.510	0.001
105	246	3.530	3.525	-0.005
105	248	3.527	3.519	-0.008
105	374	3.530	3.532	0.002
105	375	3.519	3.512	-0.007
105	Max	3.546	3.538	0.006
105	Average	3.525	3.518	-0.007
105	Min	3.504	3.496	-0.023
105	Std Dev	0.009	0.010	0.007



6.37 UVLO_VLD			
Test Site			
Tester			
Test Number			
Max Limit	3.66	V	
Min Limit	3.44	V	

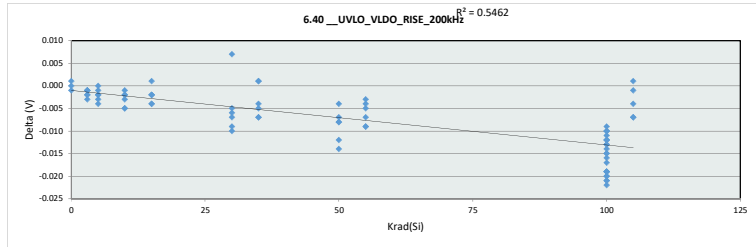
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.520	3.507	3.504	3.508	3.506	3.515	3.500	3.507	3.499	3.496	3.510
Average	3.527	3.523	3.522	3.522	3.522	3.520	3.521	3.523	3.518	3.509	3.520
Max	3.536	3.528	3.533	3.528	3.531	3.525	3.531	3.538	3.527	3.527	3.532
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



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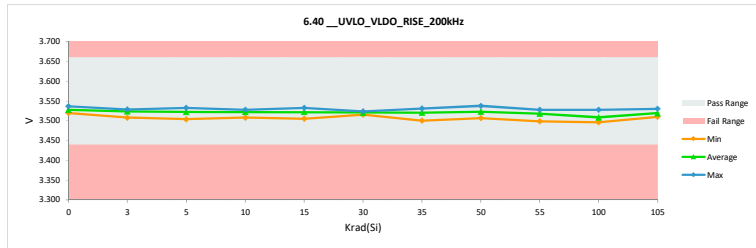
6.40 UVLO_VLDO_RISE_200kHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.519	3.520	0.001
0	118	3.527	3.526	-0.001
0	122	3.537	3.537	0.000
3	61	3.525	3.524	-0.001
3	62	3.528	3.526	-0.002
3	63	3.509	3.508	-0.001
3	64	3.530	3.527	-0.003
3	65	3.529	3.527	-0.002
3	66	3.530	3.529	-0.001
5	67	3.535	3.533	-0.002
5	68	3.507	3.504	-0.003
5	69	3.533	3.529	-0.004
5	70	3.525	3.525	0.000
5	71	3.524	3.522	-0.002
5	72	3.522	3.521	-0.001
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.509	3.508	-0.001
10	76	3.530	3.525	-0.005
10	77	3.529	3.527	-0.002
10	78	3.530	3.528	-0.002
15	79	3.535	3.533	-0.002
15	80	3.507	3.505	-0.002
15	81	3.533	3.529	-0.004
15	82	3.525	3.523	-0.002
15	83	3.524	3.520	-0.004
15	84	3.522	3.523	0.001
30	86	3.525	3.516	-0.009
30	89	3.528	3.521	-0.007
30	91	3.509	3.516	0.007
30	92	3.530	3.520	-0.010
30	94	3.529	3.524	-0.005
30	95	3.530	3.524	-0.006
35	100	3.535	3.531	-0.004
35	101	3.507	3.500	-0.007
35	102	3.533	3.528	-0.005
35	103	3.525	3.526	0.001
35	104	3.524	3.517	-0.007
35	105	3.522	3.523	0.001
50	106	3.528	3.524	-0.004
50	107	3.534	3.520	-0.014
50	108	3.519	3.507	-0.012
50	109	3.539	3.531	-0.008
50	110	3.546	3.538	-0.008
50	111	3.524	3.517	-0.007
55	113	3.529	3.520	-0.009
55	114	3.504	3.499	-0.005
55	115	3.531	3.527	-0.004
55	116	3.524	3.515	-0.009
55	126	3.535	3.528	-0.007
55	128	3.523	3.520	-0.003
100	129	3.536	3.524	-0.012
100	131	3.518	3.508	-0.010
100	132	3.530	3.511	-0.019
100	133	3.521	3.509	-0.012
100	134	3.534	3.515	-0.019
100	135	3.520	3.501	-0.019
100	138	3.528	3.508	-0.020
100	139	3.527	3.512	-0.015
100	140	3.521	3.512	-0.009
100	141	3.528	3.506	-0.022
100	142	3.506	3.496	-0.010
100	143	3.526	3.511	-0.015
100	144	3.526	3.516	-0.010
100	220	3.528	3.507	-0.021
100	221	3.542	3.523	-0.019
100	224	3.513	3.497	-0.016
100	225	3.513	3.496	-0.017
100	226	3.541	3.528	-0.013
100	228	3.524	3.510	-0.014
100	232	3.525	3.504	-0.021
100	233	3.523	3.503	-0.020
100	236	3.516	3.503	-0.013
100	238	3.516	3.504	-0.012
100	243	3.519	3.508	-0.011
105	244	3.509	3.510	0.001
105	246	3.530	3.526	-0.004
105	248	3.526	3.519	-0.007
105	374	3.531	3.530	-0.001
105	376	3.519	3.512	-0.007
105	Max	3.546	3.538	0.007
105	Average	3.525	3.518	-0.007
105	Min	3.504	3.496	-0.022
105	Std Dev	0.009	0.010	0.007



6.40 UVLO_VLD	
Test Site	
Tester	
Test Number	
Max Limit	3.66 V
Min Limit	3.44 V

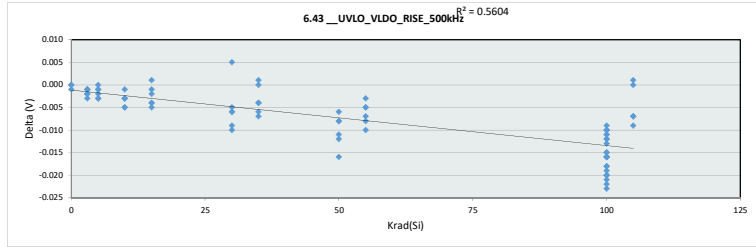
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.520	3.508	3.504	3.508	3.505	3.516	3.500	3.507	3.499	3.496	3.510
Average	3.528	3.524	3.522	3.522	3.522	3.520	3.521	3.523	3.518	3.509	3.519
Max	3.537	3.529	3.533	3.528	3.533	3.524	3.531	3.538	3.528	3.528	3.530
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



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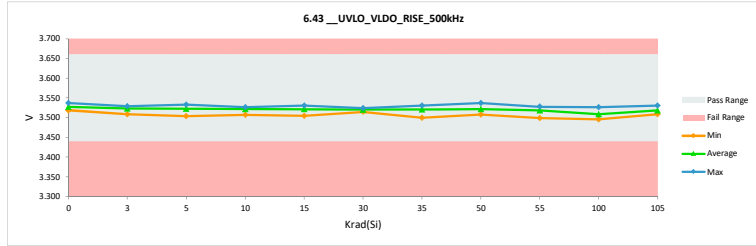
6.43 UVLO_VLDO_RISE_500kHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.519	3.519	0.000
0	118	3.528	3.527	-0.001
0	122	3.537	3.537	0.000
3	61	3.525	3.523	-0.002
3	62	3.528	3.525	-0.003
3	63	3.510	3.509	-0.001
3	64	3.530	3.529	-0.001
3	65	3.528	3.526	-0.002
3	66	3.530	3.529	-0.001
5	67	3.535	3.533	-0.002
5	68	3.507	3.504	-0.003
5	69	3.532	3.529	-0.003
5	70	3.525	3.525	0.000
5	71	3.524	3.523	-0.001
5	72	3.522	3.521	-0.001
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.510	3.507	-0.003
10	76	3.530	3.525	-0.005
10	77	3.528	3.527	-0.001
10	78	3.530	3.527	-0.003
15	79	3.535	3.531	-0.004
15	80	3.507	3.505	-0.002
15	81	3.532	3.528	-0.004
15	82	3.525	3.524	-0.001
15	83	3.524	3.519	-0.005
15	84	3.522	3.523	0.001
30	86	3.525	3.516	-0.009
30	89	3.528	3.522	-0.006
30	91	3.510	3.515	0.005
30	92	3.530	3.520	-0.010
30	94	3.528	3.523	-0.005
30	95	3.530	3.524	-0.006
35	100	3.535	3.531	-0.004
35	101	3.507	3.500	-0.007
35	102	3.532	3.528	-0.004
35	103	3.525	3.525	0.000
35	104	3.524	3.518	-0.006
35	105	3.522	3.523	0.001
50	106	3.528	3.516	-0.012
50	107	3.535	3.519	-0.016
50	108	3.519	3.508	-0.011
50	109	3.539	3.531	-0.008
50	110	3.545	3.537	-0.008
50	111	3.525	3.519	-0.006
55	113	3.528	3.520	-0.008
55	114	3.504	3.499	-0.005
55	115	3.530	3.527	-0.003
55	116	3.526	3.516	-0.010
55	126	3.535	3.528	-0.007
55	128	3.525	3.520	-0.005
100	129	3.536	3.524	-0.012
100	131	3.518	3.508	-0.010
100	132	3.531	3.512	-0.019
100	133	3.520	3.509	-0.011
100	134	3.535	3.515	-0.020
100	135	3.520	3.502	-0.018
100	138	3.528	3.507	-0.021
100	139	3.527	3.511	-0.016
100	140	3.521	3.512	-0.009
100	141	3.528	3.505	-0.023
100	142	3.506	3.496	-0.010
100	143	3.526	3.511	-0.015
100	144	3.527	3.516	-0.011
100	220	3.529	3.507	-0.022
100	221	3.541	3.523	-0.018
100	224	3.513	3.497	-0.016
100	225	3.512	3.496	-0.016
100	226	3.542	3.527	-0.015
100	228	3.525	3.509	-0.016
100	232	3.524	3.504	-0.020
100	233	3.524	3.504	-0.020
100	236	3.516	3.503	-0.013
100	238	3.517	3.505	-0.012
100	243	3.518	3.508	-0.010
105	244	3.509	3.509	0.000
105	246	3.532	3.525	-0.007
105	248	3.526	3.519	-0.007
105	374	3.530	3.531	0.001
105	376	3.520	3.511	-0.009
105	Max	3.545	3.537	0.005
105	Average	3.525	3.518	-0.008
105	Min	3.504	3.496	-0.023
105	Std Dev	0.009	0.010	0.007



6.43 UVLO_VLD	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

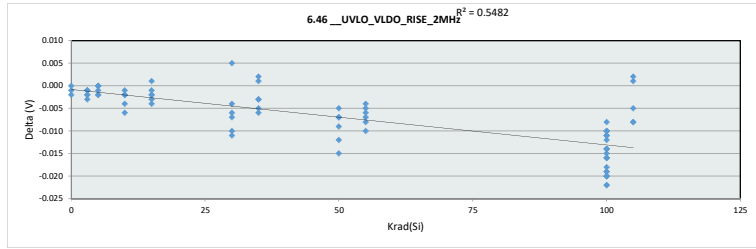
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.519	3.509	3.504	3.507	3.505	3.515	3.500	3.508	3.499	3.496	3.509
Average	3.528	3.524	3.523	3.522	3.522	3.520	3.521	3.522	3.518	3.509	3.519
Max	3.537	3.529	3.533	3.527	3.531	3.524	3.531	3.537	3.528	3.527	3.531
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



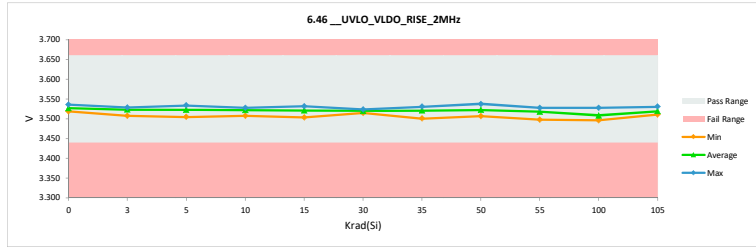
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6.46 UVLO_VLDO_RISE_2MHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.520	3.519	-0.001
0	118	3.527	3.527	0.000
0	122	3.538	3.536	-0.002
3	61	3.526	3.523	-0.003
3	62	3.528	3.527	-0.001
3	63	3.510	3.508	-0.002
3	64	3.530	3.528	-0.002
3	65	3.528	3.527	-0.001
3	66	3.530	3.529	-0.001
5	67	3.534	3.534	0.000
5	68	3.507	3.505	-0.002
5	69	3.531	3.529	-0.002
5	70	3.524	3.524	0.000
5	71	3.523	3.522	-0.001
5	72	3.522	3.522	0.000
10	73	3.526	3.520	-0.006
10	74	3.528	3.526	-0.002
10	75	3.510	3.508	-0.002
10	76	3.530	3.526	-0.004
10	77	3.528	3.527	-0.001
10	78	3.530	3.528	-0.002
15	79	3.534	3.532	-0.002
15	80	3.507	3.504	-0.003
15	81	3.531	3.529	-0.002
15	82	3.524	3.523	-0.001
15	83	3.523	3.519	-0.004
15	84	3.522	3.523	0.001
30	86	3.526	3.516	-0.010
30	89	3.528	3.521	-0.007
30	91	3.510	3.515	0.005
30	92	3.530	3.519	-0.011
30	94	3.528	3.524	-0.004
30	95	3.530	3.524	-0.006
35	100	3.534	3.531	-0.003
35	101	3.507	3.501	-0.006
35	102	3.531	3.528	-0.003
35	103	3.524	3.525	0.001
35	104	3.523	3.518	-0.005
35	105	3.522	3.524	0.002
50	106	3.528	3.523	-0.005
50	107	3.534	3.519	-0.015
50	108	3.519	3.507	-0.012
50	109	3.540	3.531	-0.009
50	110	3.545	3.538	-0.007
50	111	3.526	3.519	-0.007
55	113	3.529	3.521	-0.008
55	114	3.504	3.498	-0.006
55	115	3.531	3.527	-0.004
55	116	3.526	3.516	-0.010
55	126	3.535	3.528	-0.007
55	128	3.524	3.519	-0.005
100	129	3.535	3.524	-0.011
100	131	3.517	3.509	-0.008
100	132	3.531	3.511	-0.020
100	133	3.521	3.510	-0.011
100	134	3.534	3.515	-0.019
100	135	3.520	3.502	-0.018
100	138	3.527	3.507	-0.020
100	139	3.526	3.512	-0.014
100	140	3.522	3.511	-0.011
100	141	3.528	3.506	-0.022
100	142	3.507	3.497	-0.010
100	143	3.526	3.510	-0.016
100	144	3.526	3.516	-0.010
100	220	3.529	3.507	-0.022
100	221	3.541	3.522	-0.019
100	224	3.514	3.498	-0.016
100	225	3.512	3.496	-0.016
100	226	3.542	3.528	-0.014
100	228	3.524	3.509	-0.015
100	232	3.525	3.505	-0.020
100	233	3.523	3.503	-0.020
100	236	3.516	3.502	-0.014
100	238	3.515	3.505	-0.010
100	243	3.520	3.508	-0.012
105	244	3.509	3.511	0.002
105	246	3.531	3.526	-0.005
105	248	3.527	3.519	-0.008
105	374	3.530	3.531	0.001
105	375	3.519	3.511	-0.008
105	Max	3.545	3.538	0.005
105	Average	3.525	3.518	-0.007
105	Min	3.504	3.496	-0.022
105	Std Dev	0.009	0.010	0.007



6.46 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.66 V										
Min Limit	3.44 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.519	3.508	3.505	3.508	3.504	3.515	3.501	3.507	3.498	3.496	3.511
Average	3.527	3.524	3.523	3.523	3.522	3.520	3.521	3.523	3.518	3.509	3.520
Max	3.536	3.529	3.534	3.528	3.532	3.524	3.531	3.538	3.528	3.528	3.531
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

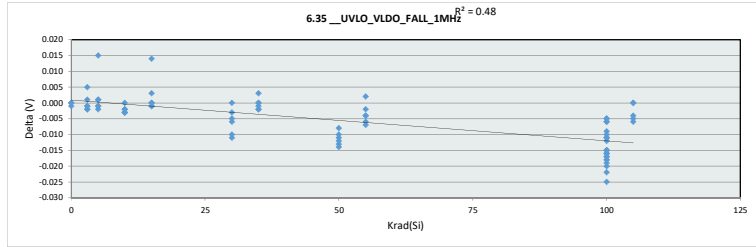


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6.35 UVLO_VLDO_FALL_1MHz

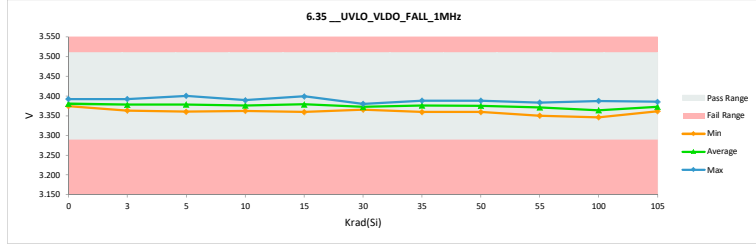
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.51
Min Limit	3.29

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.374	0.000
0	118	3.375	3.375	0.000
0	122	3.393	3.392	-0.001
3	61	3.373	3.378	0.005
3	62	3.377	3.376	-0.001
3	63	3.365	3.363	-0.002
3	64	3.381	3.379	-0.002
3	65	3.391	3.392	0.001
3	66	3.380	3.379	-0.001
5	67	3.385	3.400	0.015
5	68	3.359	3.360	0.001
5	69	3.390	3.388	-0.002
5	70	3.375	3.376	0.001
5	71	3.378	3.377	-0.001
5	72	3.370	3.369	-0.001
10	73	3.373	3.373	0.000
10	74	3.377	3.374	-0.003
10	75	3.365	3.362	-0.003
10	76	3.381	3.378	-0.003
10	77	3.391	3.389	-0.002
10	78	3.380	3.378	-0.002
15	79	3.385	3.399	0.014
15	80	3.359	3.359	0.000
15	81	3.390	3.389	-0.001
15	82	3.375	3.375	0.000
15	83	3.378	3.377	-0.001
15	84	3.370	3.373	0.003
30	86	3.373	3.367	-0.006
30	89	3.377	3.372	-0.005
30	91	3.365	3.365	0.000
30	92	3.381	3.371	-0.010
30	94	3.391	3.380	-0.011
30	95	3.380	3.377	-0.003
35	100	3.385	3.383	-0.002
35	101	3.359	3.359	0.000
35	102	3.390	3.388	-0.002
35	103	3.375	3.375	0.000
35	104	3.378	3.377	-0.001
35	105	3.370	3.373	0.003
50	106	3.391	3.377	-0.014
50	107	3.389	3.379	-0.010
50	108	3.371	3.359	-0.012
50	109	3.390	3.379	-0.011
50	110	3.396	3.388	-0.008
50	111	3.382	3.369	-0.013
55	113	3.377	3.375	-0.002
55	114	3.354	3.350	-0.004
55	115	3.379	3.381	0.002
55	116	3.373	3.367	-0.006
55	126	3.390	3.383	-0.007
55	128	3.372	3.368	-0.004
100	129	3.383	3.374	-0.009
100	131	3.381	3.359	-0.022
100	132	3.384	3.367	-0.017
100	133	3.380	3.374	-0.006
100	134	3.381	3.364	-0.017
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.390	3.372	-0.018
100	140	3.370	3.365	-0.005
100	141	3.377	3.357	-0.020
100	142	3.371	3.346	-0.025
100	143	3.375	3.360	-0.015
100	144	3.374	3.368	-0.006
100	220	3.379	3.369	-0.010
100	221	3.389	3.373	-0.016
100	224	3.367	3.348	-0.019
100	225	3.363	3.352	-0.011
100	226	3.392	3.387	-0.005
100	228	3.383	3.367	-0.016
100	232	3.378	3.360	-0.018
100	233	3.373	3.362	-0.011
100	236	3.385	3.369	-0.016
100	238	3.370	3.358	-0.012
100	243	3.368	3.357	-0.011
105	244	3.361	3.361	0.000
105	246	3.380	3.376	-0.004
105	248	3.375	3.370	-0.005
105	374	3.385	3.385	0.000
105	375	3.376	3.370	-0.006
105	Max	3.396	3.400	0.015
105	Average	3.378	3.372	-0.006
105	Min	3.354	3.346	-0.025
105	Std Dev	0.009	0.011	0.008



Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.51
Min Limit	3.29

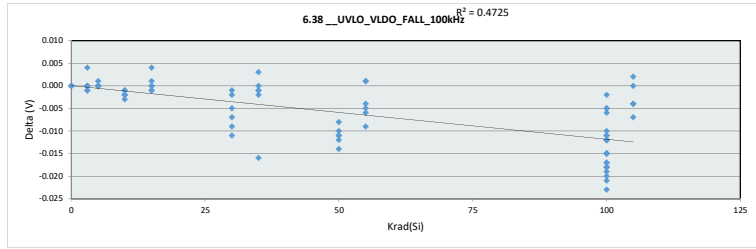
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.363	3.360	3.362	3.359	3.365	3.359	3.359	3.350	3.346
Average	3.380	3.378	3.378	3.376	3.379	3.372	3.376	3.375	3.371	3.364
Max	3.392	3.392	3.400	3.389	3.399	3.380	3.388	3.388	3.383	3.387
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



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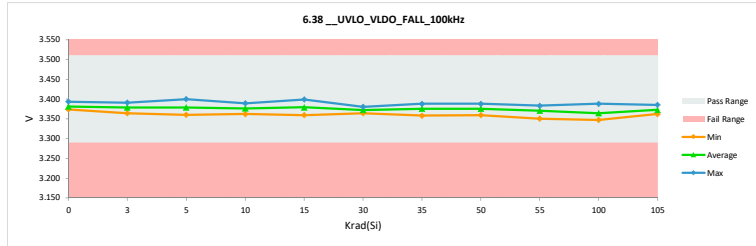
6.38 UVLO_VLDO_FALL_100kHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.51
Min Limit	3.29

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.374	0.000
0	118	3.376	3.376	0.000
0	122	3.393	3.393	0.000
3	61	3.374	3.378	0.004
3	62	3.377	3.377	0.000
3	63	3.365	3.364	-0.001
3	64	3.381	3.380	-0.001
3	65	3.391	3.391	0.000
3	66	3.380	3.380	0.000
5	67	3.400	3.400	0.000
5	68	3.359	3.360	0.001
5	69	3.389	3.389	0.000
5	70	3.375	3.375	0.000
5	71	3.378	3.378	0.000
5	72	3.370	3.370	0.000
10	73	3.374	3.373	-0.001
10	74	3.377	3.375	-0.002
10	75	3.365	3.362	-0.003
10	76	3.381	3.379	-0.002
10	77	3.391	3.389	-0.002
10	78	3.380	3.379	-0.001
15	79	3.400	3.399	-0.001
15	80	3.359	3.359	0.000
15	81	3.389	3.390	0.001
15	82	3.375	3.374	-0.001
15	83	3.378	3.378	0.000
15	84	3.370	3.374	0.004
30	86	3.374	3.367	-0.007
30	89	3.377	3.372	-0.005
30	91	3.365	3.364	-0.001
30	92	3.381	3.372	-0.009
30	94	3.391	3.380	-0.011
30	95	3.380	3.378	-0.002
35	100	3.400	3.384	-0.016
35	101	3.359	3.358	-0.001
35	102	3.389	3.388	-0.001
35	103	3.375	3.375	0.000
35	104	3.378	3.376	-0.002
35	105	3.370	3.373	0.003
50	106	3.391	3.377	-0.014
50	107	3.390	3.379	-0.011
50	108	3.370	3.359	-0.011
50	109	3.390	3.380	-0.010
50	110	3.396	3.388	-0.008
50	111	3.382	3.370	-0.012
55	113	3.376	3.377	0.001
55	114	3.355	3.350	-0.005
55	115	3.379	3.380	0.001
55	116	3.373	3.367	-0.006
55	126	3.392	3.383	-0.009
55	128	3.372	3.368	-0.004
100	129	3.384	3.373	-0.011
100	131	3.381	3.360	-0.021
100	132	3.385	3.368	-0.017
100	133	3.380	3.374	-0.006
100	134	3.382	3.364	-0.018
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.391	3.372	-0.019
100	140	3.369	3.367	-0.002
100	141	3.378	3.358	-0.020
100	142	3.370	3.347	-0.023
100	143	3.373	3.361	-0.012
100	144	3.373	3.368	-0.005
100	220	3.380	3.368	-0.012
100	221	3.389	3.374	-0.015
100	224	3.366	3.348	-0.018
100	225	3.364	3.352	-0.012
100	226	3.393	3.388	-0.005
100	228	3.384	3.367	-0.017
100	232	3.378	3.360	-0.018
100	233	3.373	3.363	-0.010
100	236	3.384	3.369	-0.015
100	238	3.370	3.358	-0.012
100	243	3.368	3.357	-0.011
105	244	3.360	3.362	0.002
105	246	3.380	3.376	-0.004
105	248	3.375	3.371	-0.004
105	374	3.385	3.385	0.000
105	376	3.376	3.369	-0.007
105	375	3.400	3.400	0.000
	Max	3.400	3.400	0.000
	Average	3.378	3.372	-0.006
	Min	3.355	3.347	-0.008
	Std Dev	0.010	0.011	0.007



6.38 UVLO_VLD	
Test Site	
Tester	
Test Number	
Max Limit	3.51
Min Limit	3.29
Unit	V

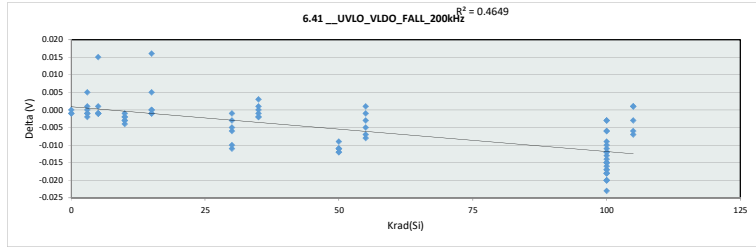
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.364	3.360	3.362	3.359	3.364	3.358	3.359	3.350	3.347	3.362
Average	3.381	3.378	3.379	3.376	3.379	3.372	3.376	3.376	3.371	3.364	3.373
Max	3.393	3.391	3.400	3.389	3.399	3.380	3.388	3.388	3.383	3.388	3.385
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



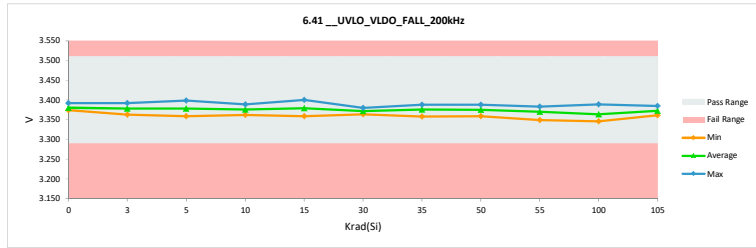
LDR TID Report
TPS7H5001-SP

6.41 UVLO_VLDO_FALL_200kHz			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	3.51	3.51	
Min Limit	3.29	3.29	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.374	0.000
0	118	3.376	3.375	-0.001
0	122	3.393	3.392	-0.001
3	61	3.374	3.379	0.005
3	62	3.378	3.377	-0.001
3	63	3.365	3.363	-0.002
3	64	3.381	3.380	-0.001
3	65	3.391	3.392	0.001
3	66	3.380	3.380	0.000
5	67	3.384	3.399	0.015
5	68	3.360	3.359	-0.001
5	69	3.389	3.388	-0.001
5	70	3.375	3.374	-0.001
5	71	3.378	3.377	-0.001
5	72	3.370	3.371	0.001
10	73	3.374	3.372	-0.002
10	74	3.378	3.374	-0.004
10	75	3.365	3.362	-0.003
10	76	3.381	3.378	-0.003
10	77	3.391	3.389	-0.002
10	78	3.380	3.379	-0.001
15	79	3.384	3.400	0.016
15	80	3.360	3.359	-0.001
15	81	3.389	3.389	0.000
15	82	3.375	3.374	-0.001
15	83	3.378	3.378	0.000
15	84	3.370	3.375	0.005
30	86	3.374	3.368	-0.006
30	89	3.378	3.373	-0.005
30	91	3.365	3.364	-0.001
30	92	3.381	3.371	-0.010
30	94	3.391	3.380	-0.011
30	95	3.380	3.377	-0.003
35	100	3.384	3.384	0.000
35	101	3.360	3.358	-0.002
35	102	3.389	3.388	-0.001
35	103	3.375	3.376	0.001
35	104	3.378	3.376	-0.002
35	105	3.370	3.373	0.003
50	106	3.390	3.378	-0.012
50	107	3.390	3.379	-0.011
50	108	3.371	3.359	-0.012
50	109	3.390	3.379	-0.011
50	110	3.397	3.388	-0.009
50	111	3.381	3.370	-0.011
55	113	3.377	3.376	-0.001
55	114	3.354	3.349	-0.005
55	115	3.379	3.380	0.001
55	116	3.373	3.366	-0.007
55	126	3.391	3.383	-0.008
55	128	3.372	3.369	-0.003
100	129	3.384	3.375	-0.009
100	131	3.383	3.360	-0.023
100	132	3.385	3.367	-0.018
100	133	3.380	3.374	-0.006
100	134	3.381	3.364	-0.017
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.391	3.373	-0.018
100	140	3.369	3.366	-0.003
100	141	3.378	3.358	-0.020
100	142	3.366	3.346	-0.020
100	143	3.375	3.360	-0.015
100	144	3.374	3.368	-0.006
100	220	3.379	3.369	-0.010
100	221	3.387	3.373	-0.014
100	224	3.366	3.346	-0.020
100	225	3.364	3.351	-0.013
100	226	3.392	3.389	-0.003
100	228	3.384	3.367	-0.017
100	232	3.378	3.360	-0.018
100	233	3.374	3.362	-0.012
100	236	3.385	3.369	-0.016
100	238	3.369	3.357	-0.012
100	243	3.368	3.357	-0.011
105	244	3.360	3.361	0.001
105	246	3.380	3.377	-0.003
105	248	3.376	3.370	-0.006
105	374	3.384	3.385	0.001
105	376	3.376	3.369	-0.007
105	375	3.397	3.400	0.016
Average		3.378	3.372	-0.006
Min		3.354	3.346	-0.023
Std Dev		0.009	0.011	0.008



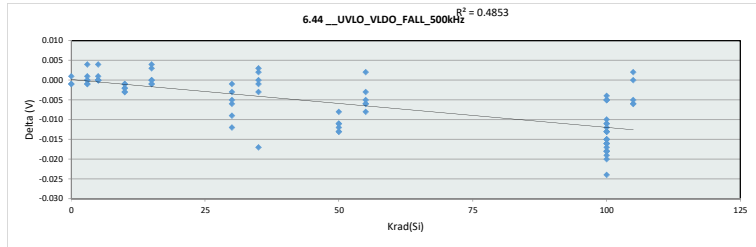
6.41 UVLO_VLD										
Test Site										
Tester										
Test Number										
Min Limit	3.51	V								
Max Limit	3.29	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.363	3.359	3.362	3.359	3.364	3.358	3.359	3.349	3.346
Average	3.380	3.379	3.378	3.376	3.379	3.372	3.376	3.376	3.371	3.364
Max	3.392	3.392	3.399	3.389	3.400	3.380	3.388	3.388	3.383	3.389
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



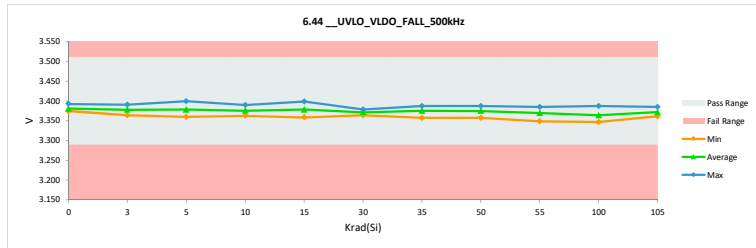
LDR TID Report
TPS7H5001-SP

6.44 UVLO_VLDO_FALL_500kHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.51
Min Limit	3.29

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.375	0.001
0	118	3.376	3.375	-0.001
0	122	3.394	3.393	-0.001
3	61	3.374	3.378	0.004
3	62	3.377	3.378	0.001
3	63	3.365	3.364	-0.001
3	64	3.381	3.380	-0.001
3	65	3.391	3.391	0.000
3	66	3.380	3.380	0.000
5	67	3.400	3.400	0.000
5	68	3.359	3.360	0.001
5	69	3.385	3.389	0.004
5	70	3.375	3.375	0.000
5	71	3.378	3.378	0.000
5	72	3.371	3.371	0.000
10	73	3.374	3.373	-0.001
10	74	3.377	3.374	-0.003
10	75	3.365	3.363	-0.002
10	76	3.381	3.378	-0.003
10	77	3.391	3.390	-0.001
10	78	3.380	3.378	-0.002
15	79	3.400	3.399	-0.001
15	80	3.359	3.359	0.000
15	81	3.385	3.388	0.003
15	82	3.375	3.375	0.000
15	83	3.378	3.377	-0.001
15	84	3.371	3.375	0.004
30	86	3.374	3.368	-0.006
30	89	3.377	3.372	-0.005
30	91	3.365	3.364	-0.001
30	92	3.381	3.372	-0.009
30	94	3.391	3.379	-0.012
30	95	3.380	3.377	-0.003
35	100	3.400	3.383	-0.017
35	101	3.359	3.358	-0.001
35	102	3.385	3.388	0.003
35	103	3.375	3.375	0.000
35	104	3.378	3.375	-0.003
35	105	3.371	3.373	0.002
50	106	3.391	3.378	-0.013
50	107	3.390	3.379	-0.011
50	108	3.371	3.358	-0.013
50	109	3.390	3.379	-0.011
50	110	3.396	3.388	-0.008
50	111	3.381	3.369	-0.012
55	113	3.376	3.371	-0.005
55	114	3.355	3.349	-0.006
55	115	3.379	3.381	0.002
55	116	3.374	3.366	-0.008
55	126	3.391	3.385	-0.006
55	128	3.372	3.369	-0.003
100	129	3.384	3.373	-0.011
100	131	3.383	3.359	-0.024
100	132	3.384	3.367	-0.017
100	133	3.379	3.374	-0.005
100	134	3.381	3.365	-0.016
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.390	3.372	-0.018
100	140	3.369	3.365	-0.004
100	141	3.377	3.357	-0.020
100	142	3.365	3.347	-0.018
100	143	3.374	3.361	-0.013
100	144	3.374	3.369	-0.005
100	220	3.380	3.369	-0.011
100	221	3.389	3.373	-0.016
100	224	3.367	3.348	-0.019
100	225	3.364	3.351	-0.013
100	226	3.393	3.388	-0.005
100	228	3.383	3.367	-0.016
100	232	3.378	3.360	-0.018
100	233	3.374	3.361	-0.013
100	236	3.384	3.369	-0.015
100	238	3.370	3.358	-0.012
100	243	3.367	3.357	-0.010
105	244	3.360	3.362	0.002
105	246	3.381	3.376	-0.005
105	248	3.376	3.370	-0.006
105	374	3.385	3.385	0.000
105	375	3.376	3.370	-0.006
105	376	3.400	3.400	0.004
	Max	3.378	3.372	-0.006
	Average	3.378	3.372	-0.006
	Min	3.355	3.347	-0.024
	Std Dev	0.010	0.011	0.007



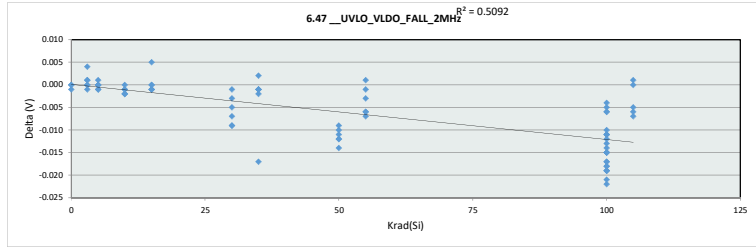
6.44 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.51 V										
Min Limit	3.29 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.375	3.364	3.360	3.363	3.359	3.364	3.358	3.358	3.349	3.347	3.362
Average	3.381	3.379	3.379	3.376	3.379	3.372	3.375	3.375	3.370	3.364	3.373
Max	3.393	3.391	3.400	3.390	3.399	3.379	3.388	3.388	3.385	3.388	3.385
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



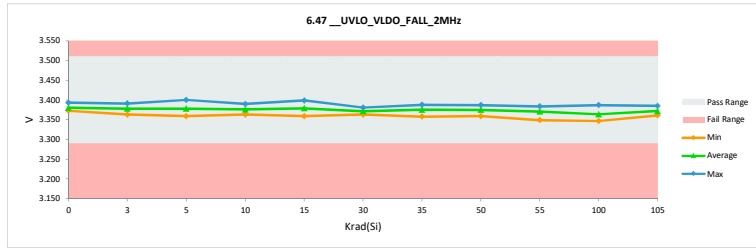
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6.47 UVLO_VLDO_FALL_2MHz			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	3.51	3.51	
Min Limit	3.29	3.29	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.373	-0.001
0	118	3.375	3.375	0.000
0	122	3.393	3.393	0.000
3	61	3.374	3.378	0.004
3	62	3.376	3.377	0.001
3	63	3.364	3.363	-0.001
3	64	3.380	3.380	0.000
3	65	3.390	3.391	0.001
3	66	3.380	3.380	0.000
5	67	3.400	3.400	0.000
5	68	3.359	3.359	0.000
5	69	3.389	3.388	-0.001
5	70	3.376	3.375	-0.001
5	71	3.377	3.378	0.001
5	72	3.370	3.370	0.000
10	73	3.374	3.372	-0.002
10	74	3.376	3.375	-0.001
10	75	3.364	3.363	-0.001
10	76	3.380	3.378	-0.002
10	77	3.390	3.390	0.000
10	78	3.380	3.378	-0.002
15	79	3.400	3.399	-0.001
15	80	3.359	3.359	0.000
15	81	3.389	3.388	-0.001
15	82	3.376	3.375	-0.001
15	83	3.377	3.377	0.000
15	84	3.370	3.375	0.005
30	86	3.374	3.367	-0.007
30	89	3.376	3.371	-0.005
30	91	3.364	3.363	-0.001
30	92	3.380	3.371	-0.009
30	94	3.390	3.381	-0.009
30	95	3.380	3.377	-0.003
35	100	3.400	3.383	-0.017
35	101	3.359	3.358	-0.001
35	102	3.389	3.388	-0.001
35	103	3.376	3.374	-0.002
35	104	3.377	3.376	-0.001
35	105	3.370	3.372	0.002
50	106	3.391	3.377	-0.014
50	107	3.390	3.380	-0.010
50	108	3.371	3.359	-0.012
50	109	3.389	3.378	-0.011
50	110	3.396	3.387	-0.009
50	111	3.381	3.369	-0.012
55	113	3.377	3.376	-0.001
55	114	3.355	3.349	-0.006
55	115	3.379	3.380	0.001
55	116	3.373	3.367	-0.006
55	126	3.391	3.384	-0.007
55	128	3.371	3.368	-0.003
100	129	3.383	3.373	-0.010
100	131	3.382	3.360	-0.022
100	132	3.386	3.367	-0.019
100	133	3.380	3.374	-0.006
100	134	3.381	3.364	-0.017
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.390	3.371	-0.019
100	140	3.370	3.366	-0.004
100	141	3.378	3.357	-0.021
100	142	3.366	3.347	-0.019
100	143	3.374	3.360	-0.014
100	144	3.374	3.369	-0.005
100	220	3.379	3.368	-0.011
100	221	3.388	3.373	-0.015
100	224	3.365	3.347	-0.018
100	225	3.364	3.351	-0.013
100	226	3.393	3.387	-0.006
100	228	3.383	3.366	-0.017
100	232	3.378	3.360	-0.018
100	233	3.374	3.363	-0.011
100	236	3.385	3.370	-0.015
100	238	3.370	3.358	-0.012
100	243	3.368	3.357	-0.011
105	244	3.360	3.361	0.001
105	246	3.381	3.376	-0.005
105	248	3.376	3.370	-0.006
105	374	3.385	3.385	0.000
105	376	3.376	3.369	-0.007
105	Max	3.400	3.400	0.005
105	Average	3.378	3.372	-0.006
105	Min	3.355	3.347	-0.022
105	Std Dev	0.010	0.011	0.007



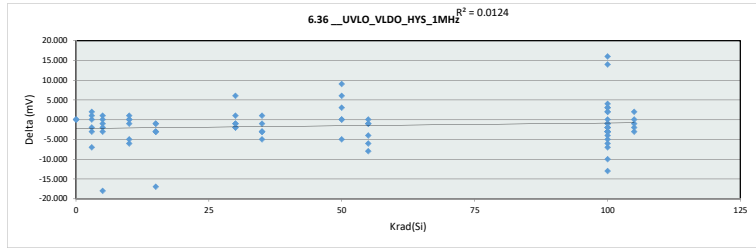
6.47 UVLO_VLD										
Test Site										
Tester										
Test Number										
Max Limit	3.51	V								
Min Limit	3.29	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.373	3.363	3.359	3.363	3.359	3.363	3.358	3.359	3.349	3.361
Average	3.380	3.378	3.378	3.376	3.379	3.372	3.375	3.375	3.371	3.364
Max	3.393	3.391	3.400	3.390	3.399	3.381	3.388	3.387	3.384	3.387
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



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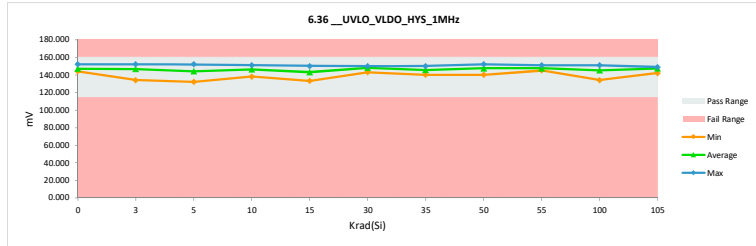
6.36 UVLO_VLDO_HYS_1MHz	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	160
Min Limit	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	145.000	145.000	0.000
0	118	152.000	152.000	0.000
0	122	144.000	144.000	0.000
3	61	152.000	145.000	-7.000
3	62	151.000	149.000	-2.000
3	63	151.000	152.000	1.000
3	64	148.000	150.000	2.000
3	65	137.000	134.000	-3.000
3	66	149.000	149.000	0.000
5	67	150.000	132.000	-18.000
5	68	147.000	145.000	-2.000
5	69	143.000	140.000	-3.000
5	70	149.000	149.000	0.000
5	71	145.000	146.000	1.000
5	72	153.000	152.000	-1.000
10	73	152.000	147.000	-5.000
10	74	151.000	151.000	0.000
10	75	151.000	145.000	-6.000
10	76	148.000	147.000	-1.000
10	77	137.000	138.000	1.000
10	78	149.000	149.000	0.000
15	79	150.000	133.000	-17.000
15	80	147.000	146.000	-1.000
15	81	143.000	140.000	-3.000
15	82	149.000	148.000	-1.000
15	83	145.000	142.000	-3.000
15	84	153.000	150.000	-3.000
30	86	152.000	150.000	-2.000
30	89	151.000	149.000	-2.000
30	91	151.000	150.000	-1.000
30	92	148.000	149.000	1.000
30	94	137.000	143.000	6.000
30	95	149.000	148.000	-1.000
35	100	150.000	149.000	-1.000
35	101	147.000	142.000	-5.000
35	102	143.000	140.000	-3.000
35	103	149.000	150.000	1.000
35	104	145.000	142.000	-3.000
35	105	153.000	150.000	-3.000
50	106	137.000	146.000	9.000
50	107	145.000	140.000	-5.000
50	108	149.000	149.000	0.000
50	109	149.000	152.000	3.000
50	110	149.000	149.000	0.000
50	111	143.000	149.000	6.000
55	113	153.000	145.000	-8.000
55	114	150.000	149.000	-1.000
55	115	152.000	146.000	-6.000
55	116	153.000	149.000	-4.000
55	126	145.000	145.000	0.000
55	128	152.000	151.000	-1.000
100	129	152.000	151.000	-1.000
100	131	136.000	150.000	14.000
100	132	147.000	145.000	-2.000
100	133	140.000	135.000	-5.000
100	134	154.000	151.000	-3.000
100	135	145.000	142.000	-3.000
100	138	149.000	145.000	-4.000
100	139	137.000	140.000	3.000
100	140	150.000	147.000	-3.000
100	141	151.000	149.000	-2.000
100	142	135.000	151.000	16.000
100	143	152.000	151.000	-1.000
100	144	153.000	147.000	-6.000
100	220	150.000	137.000	-13.000
100	221	152.000	150.000	-2.000
100	224	147.000	149.000	2.000
100	225	148.000	145.000	-3.000
100	226	149.000	142.000	-7.000
100	228	141.000	143.000	2.000
100	232	147.000	144.000	-3.000
100	233	150.000	140.000	-10.000
100	236	131.000	134.000	3.000
100	238	146.000	146.000	0.000
100	243	146.000	150.000	4.000
105	244	149.000	149.000	0.000
105	246	150.000	149.000	-1.000
105	248	152.000	149.000	-3.000
105	374	145.000	147.000	2.000
105	376	144.000	142.000	-2.000
105	Max	154.000	152.000	16.000
105	Average	147.388	145.900	-1.488
105	Min	131.000	132.000	-18.000
105	Std Dev	4.982	4.864	4.963



6.36 UVLO_VLD	
Test Site	
Tester	
Test Number	
Max Limit	160 mV
Min Limit	115 mV

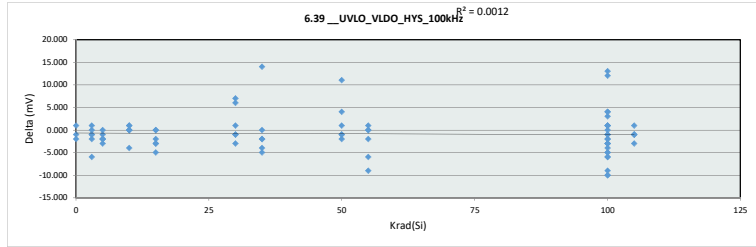
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	144.000	134.000	132.000	138.000	133.000	143.000	140.000	145.000	134.000	142.000	
Average	147.000	146.500	144.000	146.167	143.167	148.167	145.500	147.500	145.167	147.200	
Max	152.000	152.000	152.000	151.000	150.000	150.000	150.000	152.000	151.000	149.000	
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	



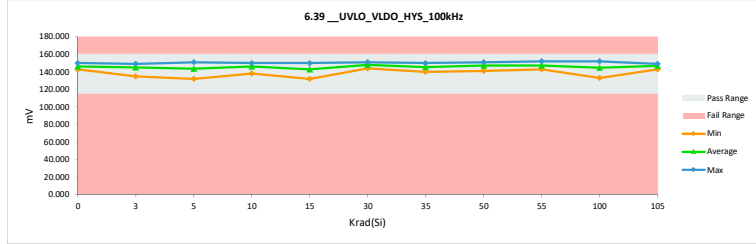
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6.39 UVLO_VLDO_HYS_100kHz	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	160
Min Limit	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	145.000	146.000	1.000
0	118	151.000	150.000	-1.000
0	122	145.000	143.000	-2.000
3	61	151.000	145.000	-6.000
3	62	150.000	149.000	-1.000
3	63	145.000	144.000	-1.000
3	64	147.000	148.000	1.000
3	65	137.000	135.000	-2.000
3	66	149.000	149.000	0.000
5	67	134.000	132.000	-2.000
5	68	147.000	144.000	-3.000
5	69	142.000	140.000	-2.000
5	70	150.000	150.000	0.000
5	71	146.000	144.000	-2.000
5	72	152.000	151.000	-1.000
10	73	151.000	147.000	-4.000
10	74	150.000	150.000	0.000
10	75	145.000	146.000	1.000
10	76	147.000	147.000	0.000
10	77	137.000	138.000	1.000
10	78	149.000	149.000	0.000
15	79	134.000	132.000	-2.000
15	80	147.000	147.000	0.000
15	81	142.000	139.000	-3.000
15	82	150.000	150.000	0.000
15	83	146.000	141.000	-5.000
15	84	152.000	149.000	-3.000
30	86	151.000	150.000	-1.000
30	89	150.000	149.000	-1.000
30	91	145.000	151.000	6.000
30	92	147.000	148.000	1.000
30	94	137.000	144.000	7.000
30	95	149.000	146.000	-3.000
35	100	134.000	148.000	14.000
35	101	147.000	142.000	-5.000
35	102	142.000	140.000	-2.000
35	103	150.000	150.000	0.000
35	104	146.000	142.000	-4.000
35	105	152.000	150.000	-2.000
50	106	136.000	147.000	11.000
50	107	143.000	141.000	-2.000
50	108	149.000	148.000	-1.000
50	109	150.000	151.000	1.000
50	110	150.000	149.000	-1.000
50	111	143.000	147.000	4.000
55	113	152.000	143.000	-9.000
55	114	149.000	149.000	0.000
55	115	152.000	146.000	-6.000
55	116	152.000	150.000	-2.000
55	126	143.000	144.000	1.000
55	128	152.000	152.000	0.000
100	129	152.000	152.000	0.000
100	131	137.000	149.000	12.000
100	132	146.000	144.000	-2.000
100	133	140.000	135.000	-5.000
100	134	152.000	151.000	-1.000
100	135	145.000	142.000	-3.000
100	138	150.000	144.000	-6.000
100	139	136.000	140.000	4.000
100	140	151.000	145.000	-6.000
100	141	150.000	148.000	-2.000
100	142	136.000	149.000	13.000
100	143	153.000	150.000	-3.000
100	144	153.000	148.000	-5.000
100	220	149.000	139.000	-10.000
100	221	151.000	150.000	-1.000
100	224	148.000	149.000	1.000
100	225	149.000	145.000	-4.000
100	226	149.000	140.000	-9.000
100	228	140.000	143.000	3.000
100	232	147.000	144.000	-3.000
100	233	150.000	140.000	-10.000
100	236	132.000	133.000	1.000
100	238	146.000	147.000	1.000
100	243	146.000	150.000	4.000
105	244	149.000	148.000	-1.000
105	246	150.000	149.000	-1.000
105	248	151.000	148.000	-3.000
105	374	146.000	147.000	1.000
105	376	144.000	143.000	-1.000
105	Max	153.000	152.000	14.000
105	Average	146.375	145.550	-0.825
105	Min	132.000	132.000	-10.000
105	Std Dev	5.371	4.752	4.380



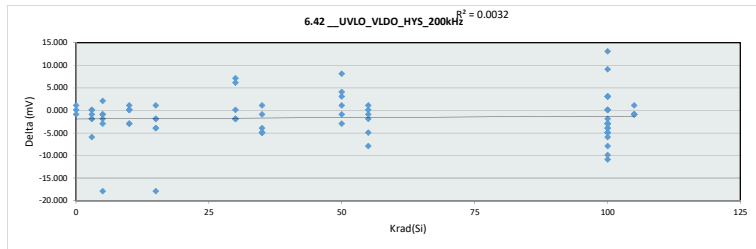
6.39 UVLO_VLD		0	3	5	10	15	30	35	50	55	100	105
LL		115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min		143.000	135.000	132.000	138.000	132.000	144.000	140.000	141.000	143.000	133.000	143.000
Average		146.333	145.000	143.500	146.167	143.000	148.000	145.333	147.167	147.333	144.875	147.000
Max		150.000	149.000	151.000	150.000	150.000	151.000	150.000	151.000	152.000	152.000	149.000
UL		160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000



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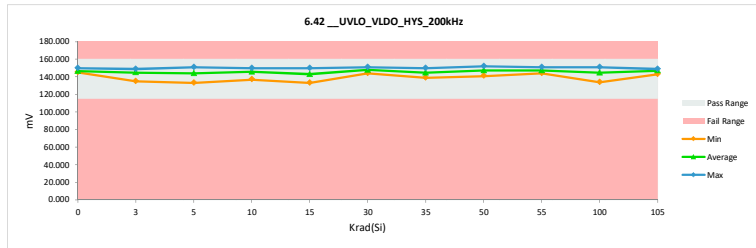
6.42 UVLO_VLDO_HYS_200kHz		
Test Site		
Tester		
Test Number		
Unit	mV	mV
Max Limit	160	160
Min Limit	115	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	145.000	145.000	0.000
0	118	151.000	150.000	-1.000
0	122	144.000	145.000	1.000
3	61	151.000	145.000	-6.000
3	62	150.000	149.000	-1.000
3	63	145.000	145.000	0.000
3	64	149.000	147.000	-2.000
3	65	137.000	135.000	-2.000
3	66	149.000	149.000	0.000
5	67	151.000	133.000	-18.000
5	68	147.000	145.000	-2.000
5	69	144.000	141.000	-3.000
5	70	149.000	151.000	2.000
5	71	146.000	145.000	-1.000
5	72	151.000	150.000	-1.000
10	73	151.000	148.000	-3.000
10	74	150.000	150.000	0.000
10	75	145.000	146.000	1.000
10	76	149.000	146.000	-3.000
10	77	137.000	137.000	0.000
10	78	149.000	149.000	0.000
15	79	151.000	133.000	-18.000
15	80	147.000	145.000	-2.000
15	81	144.000	140.000	-4.000
15	82	149.000	150.000	1.000
15	83	146.000	142.000	-4.000
15	84	151.000	149.000	-2.000
30	86	151.000	149.000	-2.000
30	89	150.000	148.000	-2.000
30	91	145.000	151.000	6.000
30	92	149.000	149.000	0.000
30	94	137.000	144.000	7.000
30	95	149.000	147.000	-2.000
35	100	151.000	147.000	-4.000
35	101	147.000	142.000	-5.000
35	102	144.000	139.000	-5.000
35	103	149.000	150.000	1.000
35	104	146.000	141.000	-5.000
35	105	151.000	150.000	-1.000
50	106	138.000	146.000	8.000
50	107	144.000	141.000	-3.000
50	108	149.000	148.000	-1.000
50	109	149.000	152.000	3.000
50	110	149.000	150.000	1.000
50	111	143.000	147.000	4.000
55	113	152.000	144.000	-8.000
55	114	150.000	149.000	-1.000
55	115	152.000	147.000	-5.000
55	116	151.000	149.000	-2.000
55	126	144.000	145.000	1.000
55	128	151.000	151.000	0.000
100	129	153.000	150.000	-3.000
100	131	135.000	148.000	13.000
100	132	146.000	144.000	-2.000
100	133	140.000	135.000	-5.000
100	134	153.000	150.000	-3.000
100	135	145.000	142.000	-3.000
100	138	150.000	145.000	-5.000
100	139	136.000	139.000	3.000
100	140	152.000	146.000	-6.000
100	141	151.000	148.000	-3.000
100	142	140.000	149.000	9.000
100	143	151.000	151.000	0.000
100	144	152.000	148.000	-4.000
100	220	149.000	138.000	-11.000
100	221	155.000	150.000	-5.000
100	224	148.000	151.000	3.000
100	225	149.000	145.000	-4.000
100	226	149.000	139.000	-10.000
100	228	140.000	143.000	3.000
100	232	147.000	144.000	-3.000
100	233	149.000	141.000	-8.000
100	236	131.000	134.000	3.000
100	238	147.000	147.000	0.000
100	243	151.000	151.000	0.000
105	244	148.000	149.000	1.000
105	246	150.000	149.000	-1.000
105	248	150.000	149.000	-1.000
105	374	147.000	146.000	-1.000
105	376	144.000	143.000	-1.000
105	375	155.000	152.000	-3.000
105	376	147.213	145.625	-1.588
105	377	131.000	133.000	-18.000
105	378	4.711	4.662	4.673



6.42 UVLO_VLD		
Test Site		
Tester		
Test Number		
Max Limit	160	mV
Min Limit	115	mV

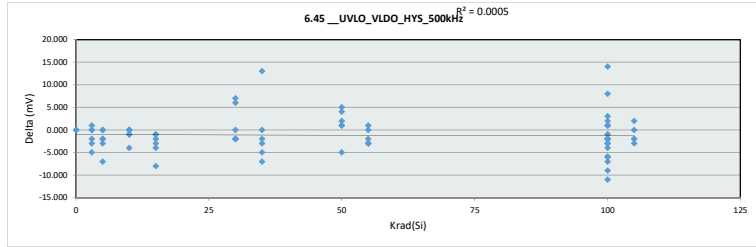
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	145.000	135.000	133.000	137.000	133.000	144.000	139.000	141.000	144.000	134.000	143.000
Average	146.667	145.000	144.167	146.000	143.167	148.000	144.833	147.333	147.500	144.917	147.200
Max	150.000	149.000	151.000	150.000	150.000	151.000	150.000	152.000	151.000	151.000	149.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000



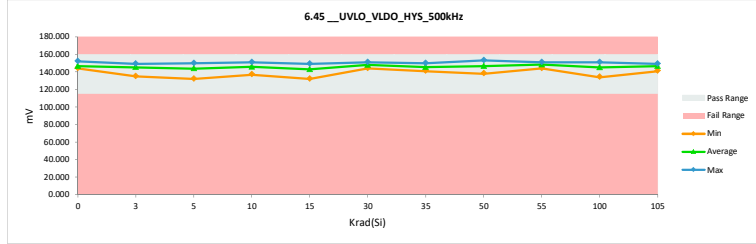
LDR TID Report
TPS7H5001-SP

6.45 UVLO_VLDO_HYS_500kHz	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	160
Min Limit	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	144.000	144.000	0.000
0	118	152.000	152.000	0.000
0	122	144.000	144.000	0.000
3	61	151.000	146.000	-5.000
3	62	151.000	148.000	-3.000
3	63	145.000	145.000	0.000
3	64	148.000	149.000	1.000
3	65	137.000	135.000	-2.000
3	66	149.000	149.000	0.000
5	67	135.000	132.000	-3.000
5	68	147.000	145.000	-2.000
5	69	148.000	141.000	-7.000
5	70	150.000	150.000	0.000
5	71	145.000	145.000	0.000
5	72	152.000	150.000	-2.000
10	73	151.000	147.000	-4.000
10	74	151.000	151.000	0.000
10	75	145.000	144.000	-1.000
10	76	148.000	147.000	-1.000
10	77	137.000	137.000	0.000
10	78	149.000	149.000	0.000
15	79	135.000	132.000	-3.000
15	80	147.000	146.000	-1.000
15	81	148.000	140.000	-8.000
15	82	150.000	149.000	-1.000
15	83	145.000	143.000	-2.000
15	84	152.000	148.000	-4.000
30	86	151.000	149.000	-2.000
30	89	151.000	149.000	-2.000
30	91	145.000	151.000	6.000
30	92	148.000	148.000	0.000
30	94	137.000	144.000	7.000
30	95	149.000	147.000	-2.000
35	100	135.000	148.000	13.000
35	101	147.000	142.000	-5.000
35	102	148.000	141.000	-7.000
35	103	150.000	150.000	0.000
35	104	145.000	142.000	-3.000
35	105	152.000	150.000	-2.000
50	106	137.000	138.000	1.000
50	107	145.000	140.000	-5.000
50	108	148.000	150.000	2.000
50	109	149.000	153.000	4.000
50	110	149.000	150.000	1.000
50	111	144.000	149.000	5.000
55	113	152.000	149.000	-3.000
55	114	150.000	150.000	0.000
55	115	150.000	147.000	-3.000
55	116	153.000	150.000	-3.000
55	126	143.000	144.000	1.000
55	128	153.000	151.000	-2.000
100	129	152.000	151.000	-1.000
100	131	135.000	149.000	14.000
100	132	147.000	145.000	-2.000
100	133	141.000	135.000	-6.000
100	134	153.000	150.000	-3.000
100	135	145.000	142.000	-3.000
100	138	150.000	144.000	-6.000
100	139	138.000	139.000	1.000
100	140	152.000	148.000	-4.000
100	141	151.000	149.000	-2.000
100	142	141.000	149.000	8.000
100	143	152.000	150.000	-2.000
100	144	153.000	147.000	-6.000
100	220	149.000	138.000	-11.000
100	221	152.000	150.000	-2.000
100	224	147.000	150.000	3.000
100	225	148.000	145.000	-3.000
100	226	149.000	140.000	-9.000
100	228	142.000	143.000	1.000
100	232	146.000	144.000	-2.000
100	233	150.000	143.000	-7.000
100	236	132.000	134.000	2.000
100	238	147.000	148.000	1.000
100	243	151.000	150.000	-1.000
105	244	149.000	147.000	-2.000
105	246	151.000	149.000	-2.000
105	248	149.000	149.000	0.000
105	374	145.000	147.000	2.000
105	376	144.000	141.000	-3.000
105	Max	153.000	153.000	14.000
105	Average	146.850	145.700	-1.150
105	Min	132.000	132.000	-11.000
105	Std Dev	5.139	4.853	4.041



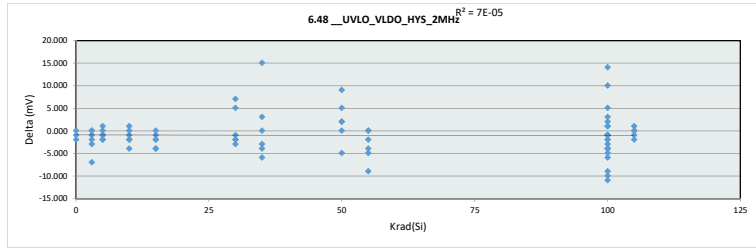
6.45 UVLO_VLD		0	3	5	10	15	30	35	50	55	100	105
LL		115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min		144.000	135.000	132.000	137.000	132.000	144.000	141.000	138.000	144.000	134.000	141.000
Average		146.667	145.333	143.833	145.833	143.000	148.000	145.500	146.667	148.500	145.125	146.600
Max		152.000	149.000	150.000	151.000	149.000	151.000	150.000	153.000	151.000	151.000	149.000
UL		160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000



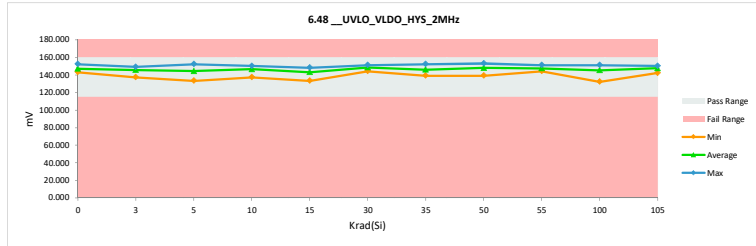
LDR TID Report
TPS7H5001-SP

6.48 UVLO_VLDO_HYS_2MHz		
Test Site		
Tester		
Test Number		
Unit	mV	mV
Max Limit	160	160
Min Limit	115	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	146.000	146.000	0.000
0	118	153.000	152.000	-1.000
0	122	145.000	145.000	-2.000
3	61	152.000	145.000	-7.000
3	62	152.000	149.000	-3.000
3	63	146.000	145.000	-1.000
3	64	150.000	148.000	-2.000
3	65	137.000	137.000	0.000
3	66	149.000	149.000	0.000
5	67	134.000	133.000	-1.000
5	68	148.000	146.000	-2.000
5	69	142.000	141.000	-1.000
5	70	148.000	149.000	1.000
5	71	146.000	144.000	-2.000
5	72	152.000	152.000	0.000
10	73	152.000	148.000	-4.000
10	74	152.000	150.000	-2.000
10	75	146.000	145.000	-1.000
10	76	150.000	148.000	-2.000
10	77	137.000	137.000	0.000
10	78	149.000	150.000	1.000
15	79	134.000	133.000	-1.000
15	80	148.000	146.000	-2.000
15	81	142.000	140.000	-2.000
15	82	148.000	148.000	0.000
15	83	146.000	142.000	-4.000
15	84	152.000	148.000	-4.000
30	86	152.000	149.000	-3.000
30	89	152.000	150.000	-2.000
30	91	146.000	151.000	5.000
30	92	150.000	148.000	-2.000
30	94	137.000	144.000	7.000
30	95	149.000	148.000	-1.000
35	100	134.000	149.000	15.000
35	101	148.000	142.000	-6.000
35	102	142.000	139.000	-3.000
35	103	148.000	151.000	3.000
35	104	146.000	142.000	-4.000
35	105	152.000	152.000	0.000
50	106	137.000	146.000	9.000
50	107	144.000	139.000	-5.000
50	108	148.000	148.000	0.000
50	109	151.000	153.000	2.000
50	110	149.000	151.000	2.000
50	111	145.000	150.000	5.000
55	113	153.000	144.000	-9.000
55	114	149.000	149.000	0.000
55	115	152.000	147.000	-5.000
55	116	153.000	149.000	-4.000
55	126	144.000	144.000	0.000
55	128	153.000	151.000	-2.000
100	129	152.000	151.000	-1.000
100	131	135.000	149.000	14.000
100	132	145.000	144.000	-1.000
100	133	140.000	136.000	-4.000
100	134	152.000	151.000	-1.000
100	135	145.000	142.000	-3.000
100	138	148.000	144.000	-4.000
100	139	136.000	141.000	5.000
100	140	152.000	146.000	-6.000
100	141	150.000	149.000	-1.000
100	142	141.000	151.000	10.000
100	143	152.000	150.000	-2.000
100	144	152.000	147.000	-5.000
100	220	150.000	139.000	-11.000
100	221	153.000	149.000	-4.000
100	224	149.000	151.000	2.000
100	225	149.000	145.000	-4.000
100	226	149.000	140.000	-9.000
100	228	140.000	143.000	3.000
100	232	147.000	145.000	-2.000
100	233	150.000	140.000	-10.000
100	236	131.000	132.000	1.000
100	238	146.000	147.000	1.000
100	243	152.000	151.000	-1.000
105	244	149.000	150.000	1.000
105	246	150.000	150.000	0.000
105	248	151.000	149.000	-2.000
105	374	146.000	146.000	0.000
105	376	143.000	142.000	-1.000
105	Max	153.000	153.000	15.000
105	Average	146.813	145.875	-0.938
105	Min	131.000	132.000	-11.000
105	Std Dev	5.526	4.845	4.385



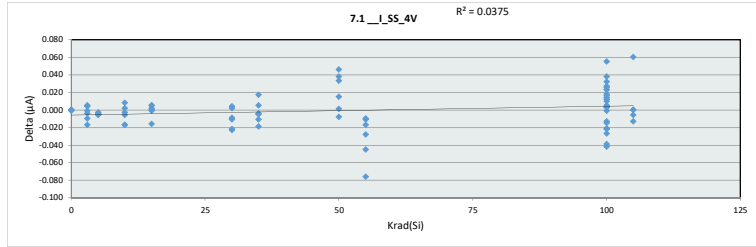
6.48 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	160	mV									
Min Limit	115	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	143.000	137.000	133.000	137.000	133.000	144.000	139.000	139.000	144.000	132.000	142.000
Average	147.000	145.500	144.167	146.333	142.833	148.333	145.833	147.833	147.333	145.125	147.400
Max	152.000	149.000	152.000	150.000	148.000	151.000	152.000	153.000	151.000	151.000	150.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000



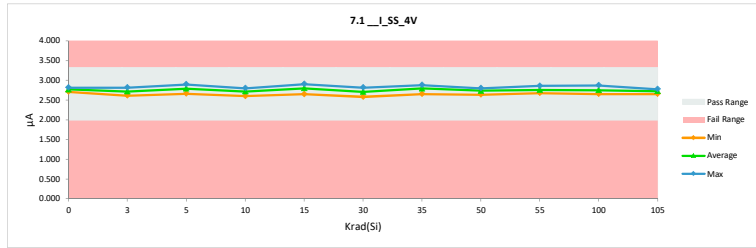
LDR TID Report
TPS7H5001-SP

7.1_I_SS_4V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	3.32
Min Limit	1.98

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.702	2.702	0.000
0	118	2.786	2.785	-0.001
0	122	2.808	2.808	0.000
3	61	2.747	2.730	-0.017
3	62	2.811	2.810	-0.001
3	63	2.697	2.702	0.005
3	64	2.784	2.780	-0.004
3	65	2.661	2.651	-0.010
3	66	2.601	2.605	0.004
5	67	2.692	2.687	-0.005
5	68	2.837	2.833	-0.004
5	69	2.660	2.657	-0.003
5	70	2.897	2.893	-0.004
5	71	2.834	2.829	-0.005
5	72	2.832	2.826	-0.006
10	73	2.747	2.730	-0.017
10	74	2.811	2.794	-0.017
10	75	2.697	2.699	0.002
10	76	2.784	2.792	0.008
10	77	2.661	2.658	-0.003
10	78	2.601	2.595	-0.006
15	79	2.692	2.697	0.005
15	80	2.837	2.836	-0.001
15	81	2.660	2.644	-0.016
15	82	2.897	2.902	0.005
15	83	2.834	2.834	0.000
15	84	2.832	2.834	0.002
30	86	2.747	2.751	0.004
30	89	2.811	2.813	0.002
30	91	2.697	2.688	-0.009
30	92	2.784	2.761	-0.023
30	94	2.661	2.650	-0.011
30	95	2.601	2.579	-0.022
35	100	2.692	2.687	-0.005
35	101	2.837	2.854	0.017
35	102	2.660	2.649	-0.011
35	103	2.897	2.878	-0.019
35	104	2.834	2.839	0.005
35	105	2.832	2.828	-0.004
50	106	2.705	2.720	0.015
50	107	2.755	2.793	0.038
50	108	2.671	2.717	0.046
50	109	2.721	2.754	0.033
50	110	2.779	2.780	0.001
50	111	2.643	2.635	-0.008
55	113	2.790	2.745	-0.045
55	114	2.758	2.741	-0.017
55	115	2.694	2.683	-0.011
55	116	2.851	2.823	-0.028
55	126	2.864	2.854	-0.010
55	128	2.749	2.673	-0.076
100	129	2.794	2.821	0.027
100	131	2.745	2.703	-0.042
100	132	2.761	2.748	-0.013
100	133	2.843	2.857	0.014
100	134	2.741	2.753	0.012
100	135	2.699	2.660	-0.039
100	138	2.739	2.762	0.023
100	139	2.652	2.670	0.018
100	140	2.832	2.835	0.003
100	141	2.698	2.677	-0.021
100	142	2.821	2.837	0.016
100	143	2.760	2.765	0.005
100	144	2.712	2.716	0.004
100	220	2.772	2.757	-0.015
100	221	2.691	2.690	-0.001
100	224	2.734	2.760	0.026
100	225	2.636	2.646	0.010
100	226	2.829	2.867	0.038
100	228	2.721	2.776	0.055
100	232	2.751	2.783	0.032
100	233	2.780	2.753	-0.027
100	236	2.661	2.664	0.003
100	238	2.702	2.725	0.023
100	243	2.713	2.691	-0.022
105	244	2.767	2.761	-0.006
105	246	2.680	2.740	0.060
105	248	2.645	2.645	0.000
105	374	2.767	2.767	0.000
105	376	2.701	2.688	-0.013
105	Max	2.897	2.902	0.006
105	Average	2.747	2.747	-0.001
105	Min	2.601	2.579	-0.076
105	Std Dev	0.074	0.076	0.021



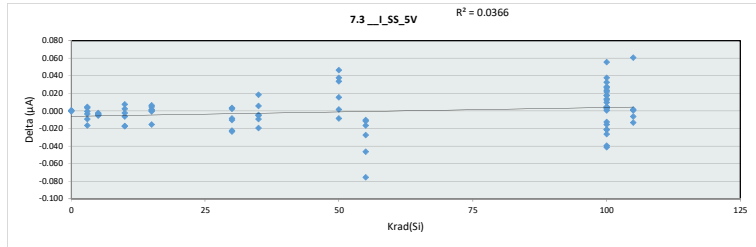
7.1_I_SS_4V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.702	2.605	2.657	2.595	2.644	2.579	2.649	2.635	2.673	2.646	2.645
Average	2.765	2.713	2.788	2.711	2.791	2.707	2.789	2.733	2.753	2.747	2.720
Max	2.808	2.810	2.893	2.794	2.902	2.813	2.878	2.793	2.854	2.867	2.767
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320



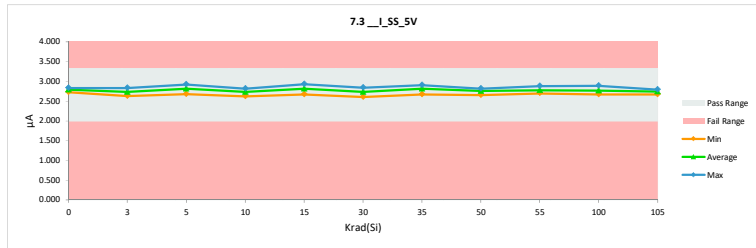
LDR TID Report
TPS7H5001-SP

7.3_I_SS_5V		
Test Site		
Tester		
Test Number		
Unit	µA	µA
Max Limit	3.32	3.32
Min Limit	1.98	1.98

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.724	2.724	0.000
0	118	2.808	2.808	0.000
0	122	2.831	2.830	-0.001
3	61	2.770	2.753	-0.017
3	62	2.834	2.833	-0.001
3	63	2.719	2.723	0.004
3	64	2.807	2.803	-0.004
3	65	2.682	2.672	-0.010
3	66	2.623	2.626	0.003
5	67	2.715	2.710	-0.005
5	68	2.859	2.855	-0.004
5	69	2.681	2.678	-0.003
5	70	2.920	2.916	-0.004
5	71	2.857	2.852	-0.005
5	72	2.856	2.850	-0.006
10	73	2.770	2.752	-0.018
10	74	2.834	2.816	-0.018
10	75	2.980	2.721	0.002
10	76	2.807	2.814	0.007
10	77	2.682	2.679	-0.003
10	78	2.623	2.616	-0.007
15	79	2.715	2.721	0.006
15	80	2.859	2.858	-0.001
15	81	2.681	2.665	-0.016
15	82	2.920	2.924	0.004
15	83	2.857	2.858	0.001
15	84	2.856	2.856	0.000
30	86	2.770	2.773	0.003
30	89	2.834	2.836	0.002
30	91	2.719	2.710	-0.009
30	92	2.807	2.783	-0.024
30	94	2.682	2.671	-0.011
30	95	2.623	2.600	-0.023
35	100	2.715	2.710	-0.005
35	101	2.859	2.877	0.018
35	102	2.681	2.671	-0.010
35	103	2.920	2.900	-0.020
35	104	2.857	2.862	0.005
35	105	2.856	2.850	-0.006
50	106	2.728	2.743	0.015
50	107	2.778	2.815	0.037
50	108	2.693	2.739	0.046
50	109	2.743	2.776	0.033
50	110	2.802	2.803	0.001
50	111	2.664	2.655	-0.009
55	113	2.813	2.766	-0.047
55	114	2.779	2.762	-0.017
55	115	2.716	2.704	-0.012
55	116	2.873	2.845	-0.028
55	126	2.887	2.876	-0.011
55	128	2.772	2.696	-0.076
100	129	2.816	2.843	0.027
100	131	2.768	2.726	-0.042
100	132	2.782	2.769	-0.013
100	133	2.866	2.879	0.013
100	134	2.763	2.775	0.012
100	135	2.722	2.682	-0.040
100	138	2.762	2.783	0.021
100	139	2.673	2.690	0.017
100	140	2.854	2.856	0.002
100	141	2.720	2.698	-0.022
100	142	2.844	2.861	0.017
100	143	2.781	2.785	0.004
100	144	2.734	2.737	0.003
100	220	2.794	2.778	-0.016
100	221	2.711	2.711	0.000
100	224	2.756	2.782	0.026
100	225	2.656	2.665	0.009
100	226	2.852	2.889	0.037
100	228	2.742	2.797	0.055
100	232	2.774	2.806	0.032
100	233	2.802	2.775	-0.027
100	236	2.681	2.685	0.004
100	238	2.723	2.746	0.023
100	243	2.734	2.712	-0.022
105	244	2.790	2.783	-0.007
105	246	2.701	2.761	0.060
105	248	2.665	2.665	0.000
105	374	2.788	2.789	0.001
105	376	2.725	2.711	-0.014
105	Max	2.920	2.924	0.060
105	Average	2.769	2.768	-0.001
105	Min	2.623	2.600	-0.076
105	Std Dev	0.074	0.076	0.021

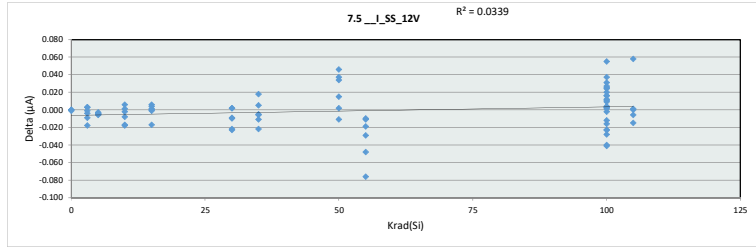


7.3_I_SS_5V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.724	2.626	2.678	2.616	2.665	2.600	2.671	2.655	2.696	2.665	2.665
Average	2.787	2.735	2.810	2.733	2.814	2.729	2.812	2.755	2.775	2.768	2.742
Max	2.830	2.833	2.916	2.816	2.924	2.836	2.900	2.815	2.876	2.889	2.789
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

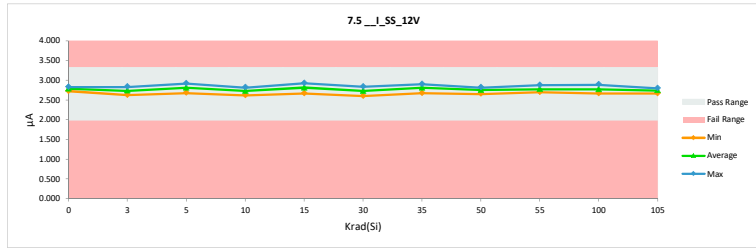


LDR TID Report
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7.5_I_SS_12V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	3.32	3.32		
Min Limit	1.98	1.98		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.725	2.725	0.000
0	118	2.811	2.810	-0.001
0	122	2.831	2.831	0.000
3	61	2.772	2.754	-0.018
3	62	2.834	2.833	-0.001
3	63	2.720	2.723	0.003
3	64	2.808	2.804	-0.004
3	65	2.683	2.674	-0.009
3	66	2.624	2.627	0.003
5	67	2.716	2.711	-0.005
5	68	2.859	2.855	-0.004
5	69	2.683	2.679	-0.004
5	70	2.921	2.918	-0.003
5	71	2.858	2.853	-0.005
5	72	2.856	2.850	-0.006
10	73	2.772	2.754	-0.018
10	74	2.834	2.817	-0.017
10	75	2.720	2.721	0.001
10	76	2.808	2.814	0.006
10	77	2.683	2.681	-0.002
10	78	2.624	2.616	-0.008
15	79	2.716	2.722	0.006
15	80	2.859	2.858	-0.001
15	81	2.683	2.666	-0.017
15	82	2.921	2.925	0.004
15	83	2.858	2.859	0.001
15	84	2.856	2.856	0.000
30	86	2.772	2.774	0.002
30	89	2.834	2.836	0.002
30	91	2.720	2.711	-0.009
30	92	2.808	2.785	-0.023
30	94	2.683	2.673	-0.010
30	95	2.624	2.602	-0.022
35	100	2.716	2.710	-0.006
35	101	2.859	2.877	0.018
35	102	2.683	2.672	-0.011
35	103	2.921	2.899	-0.022
35	104	2.858	2.863	0.005
35	105	2.856	2.851	-0.005
50	106	2.729	2.744	0.015
50	107	2.780	2.817	0.037
50	108	2.695	2.741	0.046
50	109	2.743	2.777	0.034
50	110	2.802	2.804	0.002
50	111	2.665	2.654	-0.011
55	113	2.815	2.767	-0.048
55	114	2.780	2.761	-0.019
55	115	2.716	2.705	-0.011
55	116	2.874	2.845	-0.029
55	126	2.888	2.878	-0.010
55	128	2.772	2.696	-0.076
100	129	2.818	2.845	0.027
100	131	2.769	2.728	-0.041
100	132	2.782	2.770	-0.012
100	133	2.868	2.880	0.012
100	134	2.764	2.775	0.011
100	135	2.722	2.682	-0.040
100	138	2.763	2.783	0.020
100	139	2.674	2.690	0.016
100	140	2.856	2.857	0.001
100	141	2.721	2.698	-0.023
100	142	2.845	2.861	0.016
100	143	2.781	2.785	0.004
100	144	2.734	2.737	0.003
100	220	2.795	2.779	-0.016
100	221	2.712	2.710	-0.002
100	224	2.757	2.782	0.025
100	225	2.658	2.667	0.009
100	226	2.853	2.890	0.037
100	228	2.743	2.798	0.055
100	232	2.775	2.806	0.031
100	233	2.803	2.775	-0.028
100	236	2.683	2.686	0.003
100	238	2.723	2.747	0.024
100	243	2.735	2.712	-0.023
105	244	2.791	2.785	-0.006
105	246	2.702	2.760	0.058
105	248	2.666	2.666	0.000
105	374	2.790	2.791	0.001
105	376	2.726	2.711	-0.015
105	Max	2.921	2.925	0.058
105	Average	2.770	2.769	-0.001
105	Min	2.624	2.602	-0.076
105	Std Dev	0.074	0.076	0.021



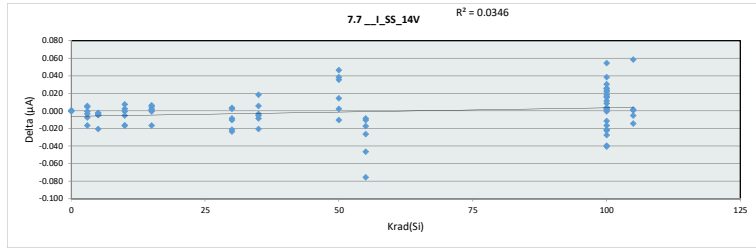
7.5_I_SS_12V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.725	2.627	2.679	2.616	2.666	2.602	2.672	2.654	2.696	2.667	2.666
Average	2.789	2.736	2.811	2.734	2.814	2.730	2.812	2.756	2.775	2.768	2.743
Max	2.831	2.833	2.918	2.817	2.925	2.836	2.899	2.817	2.878	2.890	2.791
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320



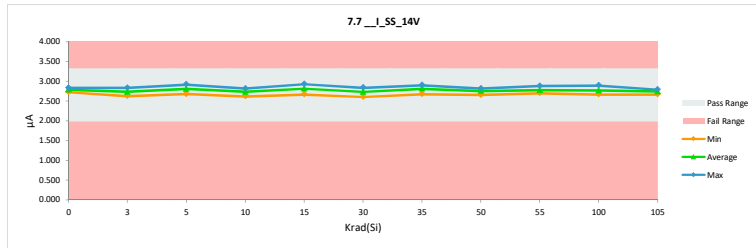
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7.7_I_SS_14V		
Test Site		
Tester		
Test Number		
Unit	µA	µA
Max Limit	3.32	3.32
Min Limit	1.98	1.98

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.725	2.725	0.000
0	118	2.810	2.810	0.000
0	122	2.831	2.830	-0.001
3	61	2.771	2.754	-0.017
3	62	2.834	2.833	-0.001
3	63	2.719	2.724	0.005
3	64	2.808	2.804	-0.004
3	65	2.683	2.675	-0.008
3	66	2.623	2.627	0.004
5	67	2.716	2.711	-0.005
5	68	2.860	2.855	-0.005
5	69	2.682	2.679	-0.003
5	70	2.921	2.918	-0.003
5	71	2.858	2.853	-0.005
5	72	2.856	2.835	-0.021
10	73	2.771	2.754	-0.017
10	74	2.834	2.817	-0.017
10	75	2.719	2.721	0.002
10	76	2.808	2.815	0.007
10	77	2.683	2.682	-0.001
10	78	2.623	2.617	-0.006
15	79	2.716	2.722	0.006
15	80	2.860	2.859	-0.001
15	81	2.682	2.665	-0.017
15	82	2.921	2.925	0.004
15	83	2.858	2.859	0.001
15	84	2.856	2.857	0.001
30	86	2.771	2.774	0.003
30	89	2.834	2.836	0.002
30	91	2.719	2.710	-0.009
30	92	2.808	2.784	-0.024
30	94	2.683	2.672	-0.011
30	95	2.623	2.601	-0.022
35	100	2.716	2.710	-0.006
35	101	2.860	2.878	0.018
35	102	2.682	2.673	-0.009
35	103	2.921	2.900	-0.021
35	104	2.858	2.863	0.005
35	105	2.856	2.852	-0.004
50	106	2.729	2.743	0.014
50	107	2.779	2.817	0.038
50	108	2.695	2.741	0.046
50	109	2.743	2.778	0.035
50	110	2.803	2.805	0.002
50	111	2.665	2.654	-0.011
55	113	2.815	2.768	-0.047
55	114	2.781	2.763	-0.018
55	115	2.716	2.705	-0.011
55	116	2.874	2.847	-0.027
55	126	2.888	2.879	-0.009
55	128	2.772	2.696	-0.076
100	129	2.819	2.844	0.025
100	131	2.769	2.728	-0.041
100	132	2.782	2.770	-0.012
100	133	2.867	2.882	0.015
100	134	2.765	2.776	0.011
100	135	2.722	2.682	-0.040
100	138	2.762	2.783	0.021
100	139	2.674	2.690	0.016
100	140	2.856	2.857	0.001
100	141	2.721	2.699	-0.022
100	142	2.844	2.862	0.018
100	143	2.781	2.784	0.003
100	144	2.735	2.737	0.002
100	220	2.795	2.778	-0.017
100	221	2.712	2.711	-0.001
100	224	2.757	2.782	0.025
100	225	2.658	2.666	0.008
100	226	2.853	2.891	0.038
100	228	2.744	2.798	0.054
100	232	2.776	2.806	0.030
100	233	2.804	2.776	-0.028
100	236	2.682	2.685	0.003
100	238	2.723	2.746	0.023
100	243	2.735	2.712	-0.023
105	244	2.791	2.785	-0.006
105	246	2.702	2.760	0.058
105	248	2.666	2.667	0.001
105	374	2.790	2.790	0.000
105	376	2.726	2.711	-0.015
105	375	2.921	2.925	0.004
105	Average	2.770	2.769	-0.001
105	Min	2.623	2.601	-0.022
105	Std Dev	0.074	0.076	0.021



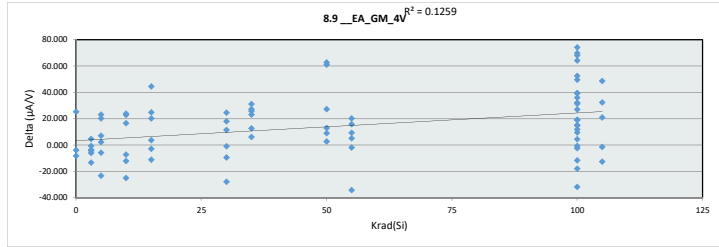
7.7_I_SS_14V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.725	2.627	2.679	2.617	2.665	2.601	2.673	2.654	2.696	2.666	2.667
Average	2.788	2.736	2.809	2.734	2.815	2.730	2.813	2.756	2.776	2.769	2.743
Max	2.830	2.833	2.918	2.817	2.925	2.836	2.900	2.817	2.879	2.891	2.790
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320



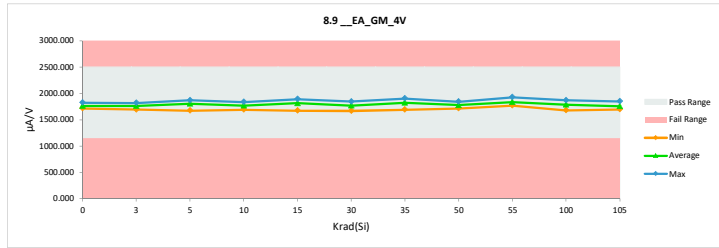
LDR TID Report
TPS7H5001-SP

8.9_EA_GM_4V	
Test Site	
Tester	
Test Number	
Unit	µA/V
Max Limit	2500
Min Limit	1150

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1720.652	1712.302	-8.350
0	118	1747.872	1743.857	-4.015
0	122	1795.843	1821.097	25.254
3	61	1831.434	1818.008	-13.426
3	62	1812.910	1817.506	4.596
3	63	1728.389	1722.321	-6.068
3	64	1810.738	1806.294	-4.444
3	65	1695.071	1691.414	-3.657
3	66	1706.354	1705.678	-0.676
5	67	1781.172	1801.319	20.147
5	68	1787.111	1794.019	6.908
5	69	1666.754	1668.835	2.081
5	70	1872.927	1866.998	-5.929
5	71	1864.032	1840.727	-23.305
5	72	1820.267	1843.205	22.938
10	73	1831.434	1819.142	-12.292
10	74	1812.910	1787.958	-24.952
10	75	1728.389	1752.217	23.828
10	76	1810.738	1833.555	22.817
10	77	1695.071	1687.772	-7.299
10	78	1706.354	1722.772	16.418
15	79	1781.172	1801.329	20.157
15	80	1787.111	1775.978	-11.133
15	81	1666.754	1670.422	3.668
15	82	1872.927	1869.953	-2.974
15	83	1864.032	1888.805	24.773
15	84	1820.267	1864.570	44.303
30	86	1831.434	1842.891	11.457
30	89	1812.910	1811.922	-0.988
30	91	1728.389	1746.399	18.010
30	92	1810.738	1801.329	-9.409
30	94	1695.071	1667.077	-27.994
30	95	1706.354	1730.743	24.389
35	100	1781.172	1806.858	25.686
35	101	1787.111	1799.667	12.556
35	102	1666.754	1689.719	22.965
35	103	1872.927	1900.111	27.184
35	104	1864.032	1869.942	5.910
35	105	1820.267	1851.340	31.073
50	106	1707.344	1734.500	27.156
50	107	1768.294	1781.172	12.878
50	108	1751.906	1812.910	61.004
50	109	1778.020	1840.737	62.717
50	110	1759.900	1768.872	8.972
50	111	1710.809	1713.508	2.699
55	113	1770.860	1768.872	-1.988
55	114	1848.240	1864.021	15.781
55	115	1823.630	1832.714	9.084
55	116	1790.381	1810.553	20.172
55	126	1919.788	1924.942	5.154
55	128	1852.301	1818.008	-34.293
100	129	1774.203	1823.630	49.427
100	131	1748.392	1779.566	31.174
100	132	1746.390	1728.398	-17.992
100	133	1814.273	1833.085	18.812
100	134	1784.934	1852.833	67.899
100	135	1842.456	1810.553	-31.903
100	138	1763.572	1802.829	39.257
100	139	1722.321	1758.305	35.984
100	140	1768.872	1778.378	9.506
100	141	1795.286	1792.811	-2.475
100	142	1823.630	1822.927	-0.703
100	143	1792.562	1866.562	74.000
100	144	1770.197	1784.953	14.756
100	220	1825.525	1844.745	19.220
100	221	1702.415	1729.209	26.794
100	224	1704.595	1719.532	14.937
100	225	1774.193	1762.609	-11.584
100	226	1730.600	1782.989	52.389
100	228	1765.344	1834.999	69.655
100	232	1699.466	1763.591	64.125
100	233	1763.572	1802.974	39.402
100	236	1661.481	1673.388	11.907
100	238	1774.794	1806.848	32.054
100	243	1702.406	1706.684	4.278
105	244	1731.422	1763.572	32.150
105	246	1706.354	1754.976	48.622
105	248	1695.645	1716.462	20.817
105	374	1844.988	1843.599	-1.389
105	376	1705.218	1692.625	-12.593
105	Max	1919.788	1924.942	74.000
105	Average	1772.655	1786.881	14.226
105	Min	1661.481	1667.077	-34.293
105	Std Dev	59.035	60.061	24.002



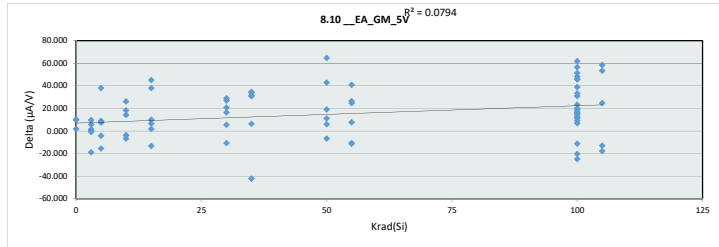
8.9_EA_GM_4V											
Test Site											
Tester											
Test Number											
Max Limit	2500 µA/V										
Min Limit	1150 µA/V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1712.302	1691.414	1668.835	1687.772	1670.422	1667.077	1689.719	1713.508	1768.872	1673.388	1692.625
Average	1759.085	1760.204	1802.517	1767.236	1811.843	1766.727	1819.606	1775.283	1836.518	1785.933	1754.247
Max	1821.097	1818.008	1866.998	1833.555	1888.805	1842.891	1900.111	1840.737	1924.942	1866.562	1843.599
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



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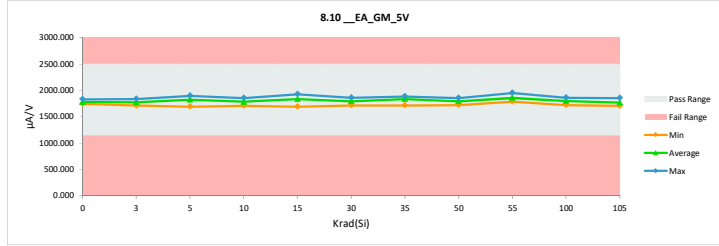
8.10_EA_GM_5V	
Test Site	
Tester	
Test Number	
Unit	µA/V
Max Limit	2500
Min Limit	1150

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1733.380	1742.698	9.318
0	118	1755.058	1765.019	9.961
0	122	1828.041	1829.297	1.256
3	61	1834.999	1815.765	-19.234
3	62	1820.514	1819.270	-1.244
3	63	1747.882	1753.078	5.196
3	64	1825.903	1835.311	9.408
3	65	1712.310	1712.258	-0.052
3	66	1726.744	1727.713	0.969
5	67	1795.065	1790.372	-4.693
5	68	1799.947	1807.773	7.826
5	69	1682.956	1691.490	8.534
5	70	1916.181	1900.111	-16.070
5	71	1881.898	1889.039	7.141
5	72	1845.425	1882.912	37.487
10	73	1834.999	1852.833	17.834
10	74	1820.514	1813.224	-7.290
10	75	1747.882	1761.600	13.718
10	76	1825.903	1851.729	25.826
10	77	1712.310	1708.039	-4.271
10	78	1726.744	1722.321	-4.423
15	79	1795.065	1801.329	6.264
15	80	1799.947	1801.329	1.382
15	81	1682.956	1692.625	9.669
15	82	1916.181	1902.556	-13.625
15	83	1881.898	1926.522	44.624
15	84	1845.425	1883.102	37.677
30	86	1834.999	1861.508	26.509
30	89	1820.514	1849.075	28.561
30	91	1747.882	1768.284	20.402
30	92	1825.903	1814.803	-11.100
30	94	1712.310	1717.297	4.987
30	95	1726.744	1742.707	15.963
35	100	1795.065	1825.913	30.848
35	101	1799.947	1834.287	34.340
35	102	1682.956	1716.304	33.348
35	103	1916.181	1873.513	-42.668
35	104	1881.898	1887.923	6.025
35	105	1845.425	1875.901	30.476
50	106	1729.984	1748.747	18.763
50	107	1790.630	1801.329	10.699
50	108	1767.632	1831.834	64.202
50	109	1813.537	1855.876	42.339
50	110	1786.749	1792.351	5.602
50	111	1728.897	1721.755	-7.142
55	113	1761.813	1787.939	26.126
55	114	1863.700	1887.934	24.234
55	115	1829.297	1869.755	40.458
55	116	1805.186	1812.411	7.225
55	126	1961.582	1950.442	-11.140
55	128	1852.311	1840.737	-11.574
100	129	1806.848	1852.311	45.463
100	131	1762.701	1777.935	15.234
100	132	1781.087	1755.692	-25.395
100	133	1834.999	1846.501	11.512
100	134	1801.329	1852.291	50.962
100	135	1828.061	1807.228	-20.833
100	138	1788.902	1777.106	-11.796
100	139	1737.563	1782.781	45.218
100	140	1806.848	1822.610	15.762
100	141	1807.968	1818.935	10.967
100	142	1804.691	1823.819	19.128
100	143	1834.999	1852.311	17.312
100	144	1791.041	1799.677	8.636
100	220	1846.867	1861.085	14.218
100	221	1704.196	1760.269	56.073
100	224	1726.104	1732.450	6.346
100	225	1779.557	1812.411	32.854
100	226	1760.787	1799.185	38.398
100	228	1773.612	1834.999	61.387
100	232	1733.022	1755.986	22.964
100	233	1808.708	1828.051	19.343
100	236	1674.557	1722.321	47.764
100	238	1777.775	1808.240	30.465
100	243	1716.190	1728.300	12.110
105	244	1722.321	1780.454	58.133
105	246	1717.305	1770.206	52.901
105	248	1709.998	1734.357	24.359
105	374	1869.671	1856.195	-13.476
105	376	1724.339	1706.094	-18.245
105	Max	1961.582	1950.442	64.202
105	Average	1790.116	1805.097	14.980
105	Min	1674.557	1691.490	-42.668
105	Std Dev	61.263	59.207	22.015



8.10_EA_GM_5V	
Test Site	
Tester	
Test Number	
Max Limit	2500 µA/V
Min Limit	1150 µA/V

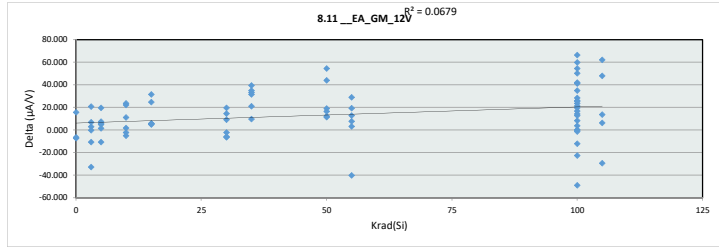
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1742.698	1712.258	1691.490	1708.039	1692.625	1717.297	1716.304	1721.755	1787.939	1722.321	1706.094
Average	1779.005	1777.233	1826.950	1784.958	1834.577	1792.279	1835.640	1791.982	1858.203	1800.521	1769.461
Max	1829.297	1835.311	1900.111	1852.833	1926.522	1861.508	1887.923	1855.876	1950.442	1861.085	1856.195
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



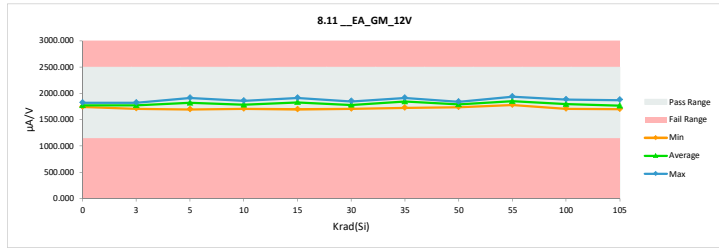
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8.11 EA GM 12V	
Test Site	
Tester	
Test Number	
Unit	µA/V
Max Limit	2500
Min Limit	1150

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1749.951	1743.232	-6.719
0	118	1748.155	1763.572	15.417
0	122	1828.320	1821.008	-7.312
3	61	1856.482	1823.620	-32.862
3	62	1816.601	1823.244	6.643
3	63	1737.563	1758.305	20.742
3	64	1823.125	1812.196	-10.929
3	65	1707.344	1710.242	2.898
3	66	1728.389	1728.007	-0.382
5	67	1801.329	1790.372	-10.957
5	68	1807.101	1808.513	1.412
5	69	1692.625	1697.506	4.881
5	70	1906.881	1914.213	7.332
5	71	1862.552	1881.898	19.346
5	72	1842.931	1848.434	5.503
10	73	1856.482	1858.158	1.676
10	74	1816.601	1827.593	10.992
10	75	1737.563	1759.632	22.069
10	76	1823.125	1846.501	23.376
10	77	1707.344	1705.106	-2.238
10	78	1728.389	1723.330	-5.059
15	79	1801.329	1806.848	5.519
15	80	1807.101	1812.411	5.310
15	81	1692.625	1697.497	4.872
15	82	1906.881	1912.446	5.565
15	83	1862.552	1894.008	31.456
15	84	1842.931	1867.632	24.701
30	86	1856.482	1850.370	-6.112
30	89	1816.601	1830.924	14.323
30	91	1737.563	1746.462	8.899
30	92	1823.125	1816.483	-6.642
30	94	1707.344	1704.872	-2.472
30	95	1728.389	1747.872	19.483
35	100	1801.329	1822.214	20.885
35	101	1807.101	1846.501	39.400
35	102	1692.625	1727.366	34.741
35	103	1906.881	1916.411	9.530
35	104	1862.552	1893.997	31.445
35	105	1842.931	1875.901	32.970
50	106	1740.411	1752.601	12.190
50	107	1790.391	1806.858	16.467
50	108	1767.753	1822.214	54.461
50	109	1795.834	1839.587	43.753
50	110	1789.598	1800.836	11.238
50	111	1717.297	1736.089	18.792
55	113	1774.193	1781.863	7.670
55	114	1872.927	1875.912	2.985
55	115	1837.864	1866.707	28.843
55	116	1803.158	1822.234	19.076
55	126	1924.942	1937.614	12.672
55	128	1864.021	1823.630	-40.391
100	129	1806.848	1834.989	28.141
100	131	1764.750	1784.174	19.424
100	132	1794.739	1745.572	-49.167
100	133	1829.297	1850.370	21.073
100	134	1823.006	1846.501	23.495
100	135	1832.144	1809.450	-22.694
100	138	1801.329	1801.329	0.000
100	139	1729.637	1795.834	66.197
100	140	1801.319	1818.008	16.689
100	141	1808.084	1816.129	8.045
100	142	1813.998	1828.041	14.043
100	143	1825.515	1885.239	59.724
100	144	1806.858	1806.848	-0.010
100	220	1858.158	1861.798	3.640
100	221	1705.236	1740.131	34.895
100	224	1722.321	1720.811	-1.510
100	225	1767.548	1780.161	12.613
100	226	1755.471	1797.209	41.738
100	228	1780.756	1834.989	54.233
100	232	1727.873	1768.872	40.999
100	233	1800.642	1826.459	25.817
100	236	1681.876	1707.344	25.468
100	238	1784.943	1834.989	50.046
100	243	1729.556	1717.297	-12.259
105	244	1737.051	1784.953	47.902
105	246	1706.884	1768.872	61.988
105	248	1708.700	1714.804	6.104
105	374	1860.910	1874.560	13.650
105	376	1732.450	1703.029	-29.421
105	Max	1924.942	1937.614	66.197
105	Average	1790.644	1804.248	13.604
105	Min	1681.876	1697.497	-49.167
105	Std Dev	58.886	59.591	21.992



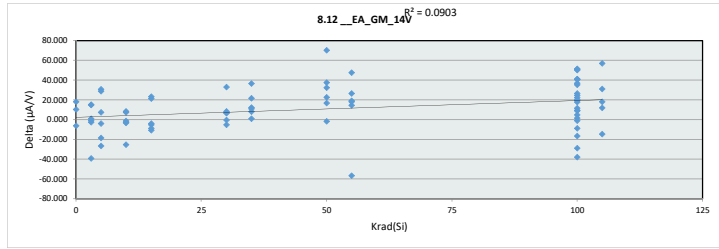
8.11 EA GM 12											
Test Site											
Tester											
Test Number											
Max Limit	2500 µA/V										
Min Limit	1150 µA/V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1743.232	1710.242	1697.506	1705.106	1697.497	1704.872	1727.366	1736.089	1781.863	1707.344	1703.029
Average	1775.937	1775.936	1823.489	1786.720	1831.807	1782.831	1847.065	1793.031	1851.327	1800.523	1769.244
Max	1821.008	1823.620	1914.213	1858.158	1912.446	1850.370	1916.411	1839.587	1937.614	1885.239	1874.560
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



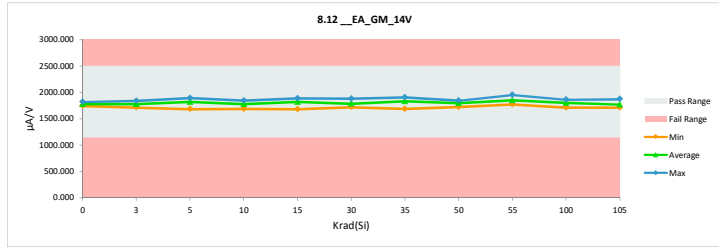
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8.12 EA GM 14V	
Test Site	
Tester	
Test Number	
Unit	µA/V
Max Limit	2500
Min Limit	1150

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1727.366	1745.045	17.679
0	118	1758.959	1768.872	9.913
0	122	1824.711	1818.008	-6.703
3	61	1846.501	1806.848	-39.653
3	62	1829.297	1827.872	-1.425
3	63	1742.698	1757.549	14.851
3	64	1824.463	1838.821	14.358
3	65	1712.302	1712.870	0.568
3	66	1720.493	1717.297	-3.196
5	67	1812.411	1793.472	-18.939
5	68	1805.613	1812.411	6.798
5	69	1687.280	1682.956	-4.324
5	70	1897.049	1869.953	-27.096
5	71	1864.032	1892.342	28.310
5	72	1837.693	1867.964	30.271
10	73	1846.501	1842.648	-3.853
10	74	1829.297	1827.414	-1.883
10	75	1742.698	1750.590	7.892
10	76	1824.463	1831.424	6.961
10	77	1712.302	1686.576	-25.726
10	78	1720.493	1717.297	-3.196
15	79	1812.411	1801.319	-11.092
15	80	1805.613	1801.329	-4.284
15	81	1687.280	1681.522	-5.758
15	82	1897.049	1887.923	-9.126
15	83	1864.032	1886.723	22.691
15	84	1837.693	1858.426	20.733
30	86	1846.501	1878.895	32.394
30	89	1829.297	1828.579	-0.718
30	91	1742.698	1749.030	6.332
30	92	1824.463	1818.935	-5.528
30	94	1712.302	1718.669	6.367
30	95	1720.493	1728.389	7.896
35	100	1812.411	1822.966	10.555
35	101	1805.613	1826.857	21.244
35	102	1687.280	1687.781	0.501
35	103	1897.049	1904.586	7.537
35	104	1864.032	1875.912	11.880
35	105	1837.693	1873.921	36.228
50	106	1724.676	1740.989	16.313
50	107	1784.953	1822.026	37.073
50	108	1759.642	1829.297	69.655
50	109	1812.411	1844.218	31.807
50	110	1786.578	1808.708	22.130
50	111	1725.350	1723.321	-2.029
55	113	1758.305	1772.422	14.117
55	114	1868.464	1886.055	17.591
55	115	1816.139	1863.390	47.251
55	116	1810.563	1829.297	18.734
55	126	1924.257	1950.442	26.185
55	128	1864.032	1806.858	-57.174
100	129	1822.917	1834.047	11.130
100	131	1759.051	1778.020	18.969
100	132	1783.956	1745.527	-38.429
100	133	1836.717	1860.672	23.955
100	134	1827.175	1847.060	19.885
100	135	1823.620	1806.760	-16.860
100	138	1801.329	1802.325	0.996
100	139	1747.872	1788.559	40.687
100	140	1817.998	1816.611	-1.387
100	141	1783.871	1818.806	34.935
100	142	1801.319	1852.301	50.982
100	143	1818.008	1844.187	26.179
100	144	1819.615	1790.381	-29.234
100	220	1852.311	1861.085	8.774
100	221	1707.692	1747.872	40.180
100	224	1727.375	1731.824	4.449
100	225	1761.461	1778.887	17.426
100	226	1751.760	1801.329	49.569
100	228	1790.381	1840.727	50.346
100	232	1763.572	1785.551	21.979
100	233	1806.858	1815.441	8.583
100	236	1675.769	1712.310	36.541
100	238	1777.775	1817.998	40.223
100	243	1732.065	1722.825	-9.240
105	244	1739.284	1795.834	56.550
105	246	1717.297	1747.872	30.575
105	248	1706.441	1723.994	17.553
105	374	1859.002	1870.547	11.545
105	376	1727.375	1712.302	-15.073
105	Max	1924.257	1950.442	69.655
105	Average	1790.322	1801.608	11.286
105	Min	1675.769	1681.522	-57.174
105	Std Dev	57.982	59.970	22.818



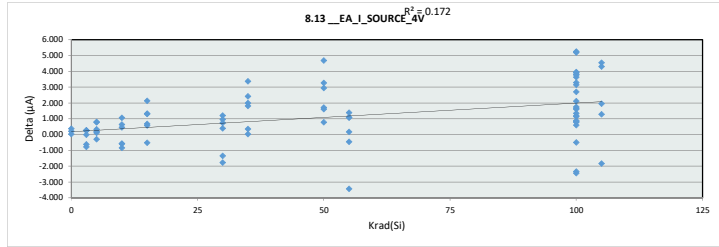
8.12 EA GM 14											
Test Site											
Tester											
Test Number											
Max Limit	2500 µA/V										
Min Limit	1150 µA/V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1745.045	1712.870	1682.956	1686.576	1681.522	1718.669	1687.781	1723.321	1772.422	1712.310	1712.302
Average	1777.308	1776.876	1819.850	1775.992	1819.540	1787.083	1832.004	1794.760	1851.411	1800.046	1770.110
Max	1818.008	1838.821	1892.342	1842.648	1887.923	1878.895	1904.586	1844.218	1950.442	1861.085	1870.547
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



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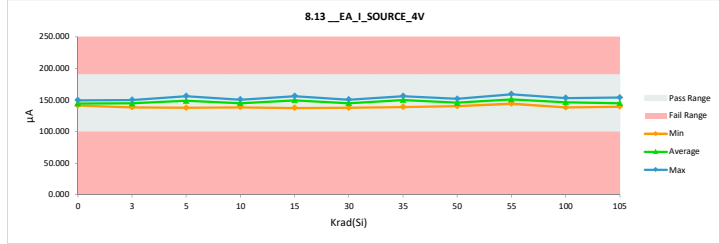
8.13 EA_I_SOURCE_4V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	140.651	140.845	0.194
0	118	143.043	143.056	0.013
0	122	148.956	149.325	0.369
3	61	149.422	148.622	-0.800
3	62	149.498	149.759	0.261
3	63	141.504	141.480	-0.024
3	64	149.209	149.443	0.234
3	65	138.718	138.083	-0.635
3	66	139.106	139.356	0.250
5	67	146.661	146.850	0.189
5	68	146.312	146.630	0.318
5	69	136.469	137.241	0.772
5	70	155.753	155.849	0.096
5	71	154.389	154.090	-0.299
5	72	149.521	150.307	0.786
10	73	149.422	148.824	-0.598
10	74	149.498	148.643	-0.855
10	75	141.504	142.134	0.630
10	76	149.209	150.253	1.044
10	77	138.718	138.119	-0.599
10	78	139.106	139.541	0.435
15	79	146.661	147.944	1.283
15	80	146.312	146.979	0.667
15	81	136.469	137.036	0.567
15	82	155.753	155.232	-0.521
15	83	154.389	155.704	1.315
15	84	149.521	151.637	2.116
30	86	149.422	150.369	0.947
30	89	149.498	150.229	0.731
30	91	141.504	141.899	0.396
30	92	149.209	147.419	-1.790
30	94	138.718	137.367	-1.351
30	95	139.106	140.292	1.186
35	100	146.661	149.075	2.414
35	101	146.312	149.672	3.360
35	102	136.469	138.460	1.991
35	103	155.753	155.768	0.015
35	104	154.389	154.730	0.341
35	105	149.521	151.318	1.797
50	106	140.265	141.965	1.700
50	107	144.182	147.111	2.929
50	108	143.729	146.991	3.262
50	109	146.800	151.469	4.669
50	110	143.722	145.314	1.592
50	111	139.166	139.936	0.770
55	113	143.822	143.988	0.166
55	114	153.088	154.152	1.064
55	115	150.260	151.642	1.382
55	116	146.426	147.488	1.062
55	126	159.480	159.006	-0.474
55	128	151.725	148.288	-3.437
100	129	145.716	148.860	3.144
100	131	142.808	143.585	0.777
100	132	143.352	140.904	-2.448
100	133	148.291	150.031	1.740
100	134	148.271	150.364	2.093
100	135	149.864	147.515	-2.349
100	138	144.962	146.296	1.334
100	139	141.841	145.607	3.766
100	140	146.809	147.587	0.778
100	141	145.189	146.352	1.163
100	142	147.741	148.609	0.868
100	143	147.641	152.881	5.240
100	144	146.013	147.617	1.604
100	220	150.885	152.024	1.139
100	221	138.278	142.215	3.937
100	224	139.091	139.675	0.584
100	225	143.554	145.220	1.666
100	226	142.215	146.118	3.903
100	228	144.558	149.731	5.173
100	232	140.989	144.614	3.625
100	233	146.943	150.226	3.283
100	236	135.132	137.831	2.699
100	238	144.767	148.572	3.805
100	243	139.397	138.889	-0.508
105	244	141.628	145.908	4.280
105	246	139.171	143.699	4.528
105	248	138.750	140.694	1.944
105	374	152.350	153.619	1.269
105	376	140.930	139.080	-1.850
105	Max	159.480	159.006	5.240
105	Average	145.527	146.641	1.114
105	Min	135.132	137.036	-3.437
105	Std Dev	5.370	5.293	1.761



8.13 EA_I_SOURCE	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

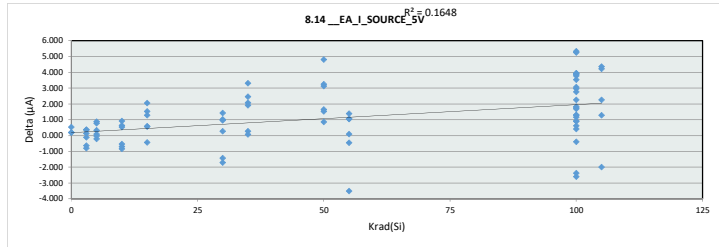
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	140.845	138.083	137.241	138.119	137.036	137.367	138.460	139.936	143.988	137.831	139.080
Average	144.409	144.457	148.495	144.586	149.089	144.594	149.837	145.464	150.761	146.305	144.600
Max	149.325	149.759	155.849	150.253	155.704	150.369	155.768	151.469	159.006	152.881	153.619
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



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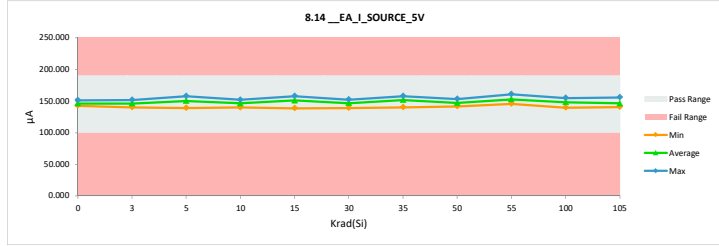
8.14 EA_I_SOURCE_5V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	142.229	142.397	0.168
0	118	144.538	144.707	0.169
0	122	150.453	150.961	0.508
3	61	151.142	150.315	-0.827
3	62	151.076	151.420	0.344
3	63	143.219	143.068	-0.151
3	64	150.915	151.005	0.090
3	65	140.364	139.698	-0.666
3	66	140.653	140.971	0.318
5	67	148.308	148.240	-0.068
5	68	148.000	148.294	0.294
5	69	138.014	138.755	0.741
5	70	157.440	157.497	0.057
5	71	156.018	155.779	-0.239
5	72	151.268	152.119	0.851
10	73	151.142	150.572	-0.570
10	74	151.076	150.197	-0.879
10	75	143.219	143.815	0.596
10	76	150.915	151.796	0.881
10	77	140.364	139.632	-0.732
10	78	140.653	141.161	0.508
15	79	148.308	149.568	1.260
15	80	148.000	148.542	0.542
15	81	138.014	138.554	0.540
15	82	157.440	156.968	-0.472
15	83	156.018	157.506	1.488
15	84	151.268	153.289	2.021
30	86	151.142	152.131	0.989
30	89	151.076	152.012	0.936
30	91	143.219	143.465	0.246
30	92	150.915	149.172	-1.743
30	94	140.364	138.910	-1.454
30	95	140.653	142.042	1.389
35	100	148.308	150.736	2.428
35	101	148.000	151.284	3.284
35	102	138.014	140.048	2.034
35	103	157.440	157.470	0.030
35	104	156.018	156.251	0.233
35	105	151.268	153.157	1.889
50	106	141.837	143.460	1.623
50	107	145.852	148.955	3.103
50	108	145.361	148.584	3.223
50	109	148.365	153.140	4.775
50	110	145.448	146.940	1.492
50	111	140.712	141.543	0.831
55	113	145.549	145.600	0.051
55	114	154.708	155.747	1.039
55	115	152.045	153.389	1.344
55	116	147.833	148.834	1.001
55	126	161.156	160.659	-0.497
55	128	153.469	149.932	-3.537
100	129	147.386	150.323	2.937
100	131	144.541	145.135	0.594
100	132	145.153	142.517	-2.636
100	133	150.196	151.908	1.712
100	134	149.930	152.154	2.224
100	135	151.559	149.157	-2.402
100	138	146.704	147.956	1.252
100	139	143.249	147.158	3.909
100	140	148.448	149.313	0.865
100	141	146.932	148.073	1.141
100	142	149.375	150.268	0.893
100	143	149.355	154.566	5.211
100	144	147.727	149.375	1.648
100	220	152.619	153.882	1.263
100	221	139.870	143.701	3.831
100	224	140.788	141.168	0.380
100	225	145.108	146.901	1.793
100	226	143.843	147.746	3.903
100	228	146.180	151.488	5.308
100	232	142.707	146.207	3.500
100	233	148.801	151.828	3.027
100	236	136.601	139.338	2.737
100	238	146.480	150.216	3.736
100	243	140.947	140.511	-0.436
105	244	143.138	147.322	4.184
105	246	140.839	145.158	4.319
105	248	140.188	142.411	2.223
105	374	154.197	155.451	1.254
105	375	142.514	140.489	-2.025
105	376	161.156	160.659	-0.497
Max				5.308
Average				1.098
Min				-3.537
Std Dev				1.775



8.14 EA_I_SOUR	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

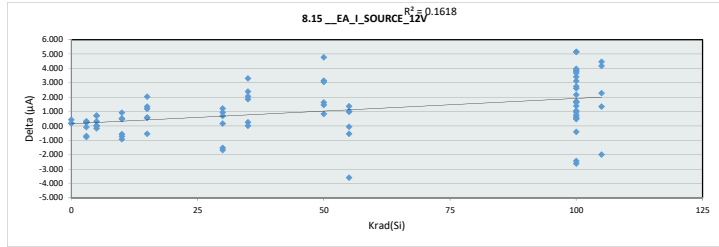
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.397	139.698	138.755	139.632	138.554	138.910	140.048	141.543	145.600	139.338	140.489
Average	146.022	146.080	150.114	146.196	150.738	146.289	151.491	147.104	152.360	147.954	146.166
Max	150.961	151.420	157.497	151.796	157.506	152.131	157.470	153.140	160.659	154.566	155.451
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



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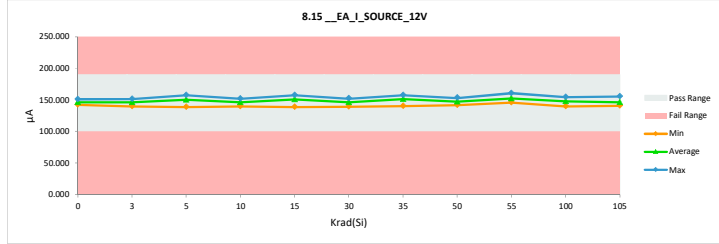
8.15 EA_I_SOURCE_12V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	142.269	142.456	0.187
0	118	144.699	144.876	0.177
0	122	150.635	151.057	0.422
3	61	151.213	150.441	-0.772
3	62	151.231	151.520	0.289
3	63	143.285	143.184	-0.101
3	64	150.936	151.164	0.228
3	65	140.503	139.794	-0.709
3	66	140.814	141.128	0.314
5	67	148.382	148.354	-0.028
5	68	148.033	148.349	0.316
5	69	138.129	138.820	0.691
5	70	157.610	157.620	0.010
5	71	156.078	155.899	-0.179
5	72	151.345	152.049	0.704
10	73	151.213	150.623	-0.590
10	74	151.231	150.293	-0.938
10	75	143.285	143.804	0.519
10	76	150.936	151.853	0.917
10	77	140.503	139.769	-0.734
10	78	140.814	141.277	0.463
15	79	148.382	149.566	1.184
15	80	148.033	148.610	0.577
15	81	138.129	138.688	0.559
15	82	157.610	157.051	-0.559
15	83	156.078	157.420	1.342
15	84	151.345	153.373	2.028
30	86	151.213	152.138	0.925
30	89	151.231	151.924	0.693
30	91	143.285	143.446	0.161
30	92	150.936	149.232	-1.704
30	94	140.503	138.971	-1.532
30	95	140.814	142.014	1.200
35	100	148.382	150.756	2.374
35	101	148.033	151.315	3.282
35	102	138.129	140.183	2.054
35	103	157.610	157.608	-0.002
35	104	156.078	156.331	0.253
35	105	151.345	153.184	1.839
50	106	141.868	143.491	1.623
50	107	146.073	149.200	3.127
50	108	145.504	148.542	3.038
50	109	148.391	153.147	4.756
50	110	145.612	147.058	1.446
50	111	140.913	141.733	0.820
55	113	145.737	145.678	-0.059
55	114	154.751	155.760	1.009
55	115	151.965	153.330	1.365
55	116	147.970	148.944	0.974
55	126	161.258	160.711	-0.547
55	128	153.559	149.951	-3.608
100	129	147.567	150.309	2.742
100	131	144.639	145.175	0.536
100	132	145.160	142.530	-2.630
100	133	150.303	151.988	1.685
100	134	150.026	152.187	2.161
100	135	151.672	149.218	-2.454
100	138	146.691	148.066	1.375
100	139	143.342	147.133	3.791
100	140	148.742	149.372	0.630
100	141	147.227	148.266	1.039
100	142	149.455	150.217	0.762
100	143	149.482	154.630	5.148
100	144	147.838	149.471	1.633
100	220	152.696	153.645	0.949
100	221	139.929	143.794	3.865
100	224	140.853	141.312	0.459
100	225	145.269	146.940	1.671
100	226	143.990	147.940	3.950
100	228	146.301	151.448	5.147
100	232	142.825	146.224	3.399
100	233	148.804	151.912	3.108
100	236	136.872	139.477	2.605
100	238	146.531	150.196	3.665
100	243	141.116	140.687	-0.429
105	244	143.336	147.495	4.159
105	246	140.912	145.367	4.455
105	248	140.304	142.573	2.269
105	374	154.309	155.640	1.331
105	376	142.728	140.720	-2.008
105	Max	161.258	160.711	-0.547
105	Average	147.285	148.345	1.060
105	Min	136.872	138.688	-3.608
105	Std Dev	5.395	5.319	1.768



8.15 EA_I_SOUR	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

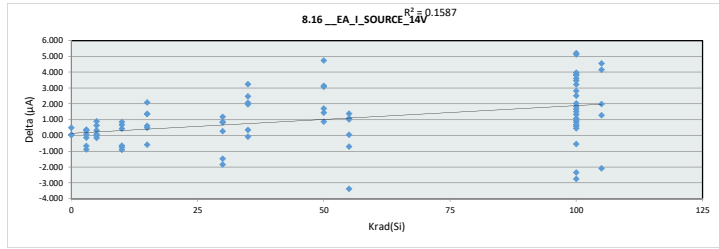
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.456	139.794	138.820	139.769	138.688	138.971	140.183	141.733	145.678	139.477	140.720
Average	146.130	146.205	150.182	146.270	150.785	146.288	151.563	147.195	152.396	148.006	146.359
Max	151.057	151.520	157.620	151.853	157.420	152.138	157.608	153.147	160.711	154.630	155.640
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



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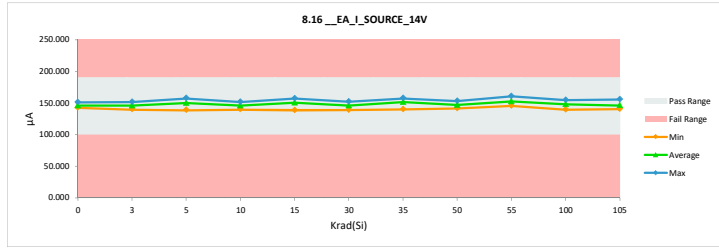
8.16 EA_I_SOURCE_14V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	142.401	142.420	0.019
0	118	144.644	144.641	-0.003
0	122	150.561	151.022	0.461
3	61	151.180	150.269	-0.911
3	62	151.147	151.483	0.336
3	63	143.236	143.067	-0.169
3	64	150.870	150.900	0.030
3	65	140.498	139.803	-0.695
3	66	140.793	141.064	0.271
5	67	148.223	148.280	0.057
5	68	147.968	148.264	0.296
5	69	138.127	138.736	0.609
5	70	157.546	157.510	-0.036
5	71	156.024	155.834	-0.190
5	72	151.279	152.140	0.861
10	73	151.180	150.518	-0.662
10	74	151.147	150.211	-0.936
10	75	143.236	143.876	0.640
10	76	150.870	151.704	0.834
10	77	140.498	139.725	-0.773
10	78	140.793	141.188	0.395
15	79	148.223	149.565	1.342
15	80	147.968	148.559	0.591
15	81	138.127	138.600	0.473
15	82	157.546	156.942	-0.604
15	83	156.024	157.355	1.331
15	84	151.279	153.350	2.071
30	86	151.180	152.002	0.822
30	89	151.147	151.964	0.817
30	91	143.236	143.479	0.243
30	92	150.870	149.014	-1.856
30	94	140.498	138.990	-1.508
30	95	140.793	141.941	1.148
35	100	148.223	150.661	2.438
35	101	147.968	151.190	3.222
35	102	138.127	140.163	2.036
35	103	157.546	157.438	-0.108
35	104	156.024	156.351	0.327
35	105	151.279	153.214	1.935
50	106	141.867	143.547	1.680
50	107	145.981	149.042	3.061
50	108	145.466	148.573	3.107
50	109	148.441	153.164	4.723
50	110	145.598	147.020	1.422
50	111	140.791	141.642	0.851
55	113	145.596	145.623	0.027
55	114	154.741	155.735	0.994
55	115	152.003	153.360	1.357
55	116	147.907	148.961	1.054
55	126	161.248	160.518	-0.730
55	128	153.467	150.063	-3.404
100	129	147.495	150.287	2.792
100	131	144.585	145.222	0.637
100	132	145.232	142.454	-2.778
100	133	150.148	151.943	1.795
100	134	150.024	152.016	1.992
100	135	151.574	149.207	-2.367
100	138	146.730	148.024	1.294
100	139	143.358	147.133	3.775
100	140	148.710	149.130	0.420
100	141	147.167	148.092	0.925
100	142	149.442	150.259	0.817
100	143	149.477	154.656	5.179
100	144	147.881	149.357	1.476
100	220	152.673	153.733	1.060
100	221	139.926	143.770	3.844
100	224	140.836	141.402	0.566
100	225	145.247	146.879	1.632
100	226	143.917	147.866	3.949
100	228	146.237	151.340	5.103
100	232	142.786	146.230	3.444
100	233	148.780	151.972	3.192
100	236	136.879	139.375	2.496
100	238	146.420	150.008	3.588
100	243	141.064	140.493	-0.571
105	244	143.296	147.425	4.129
105	246	140.793	145.319	4.526
105	248	140.428	142.393	1.965
105	374	154.289	155.542	1.253
105	376	142.636	140.533	-2.103
105	375	161.248	160.518	-0.730
105	376	161.248	160.518	-0.730
105	Average	147.243	148.284	1.042
105	Min	136.879	138.600	-3.404
105	Std Dev	5.382	5.316	1.774



8.16 EA_I_SOUR	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

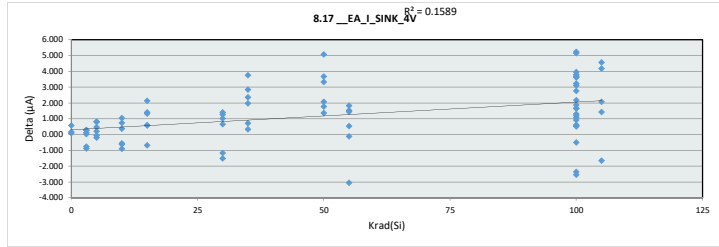
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.420	139.803	138.736	139.725	138.600	138.990	140.163	141.642	145.623	139.375	140.533
Average	146.028	146.098	150.127	146.204	150.729	146.232	151.503	147.165	152.377	147.952	146.242
Max	151.022	151.483	157.510	151.704	157.355	152.002	157.438	153.164	160.518	154.656	155.542
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



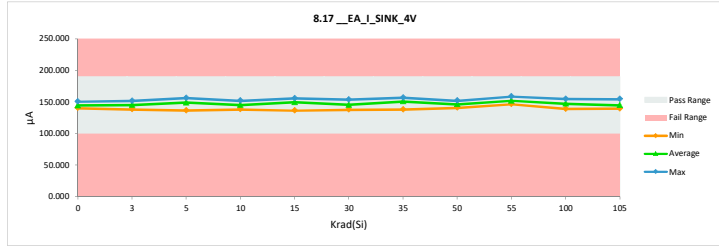
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8.17 EA_I_SINK_4V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	140.018	140.172	0.154
0	118	142.967	143.040	0.073
0	122	149.556	150.124	0.568
3	61	152.333	151.445	-0.888
3	62	148.168	148.430	0.262
3	63	142.376	142.391	0.015
3	64	149.422	149.525	0.103
3	65	138.736	137.972	-0.764
3	66	140.251	140.533	0.282
5	67	147.276	147.466	0.190
5	68	147.179	147.642	0.463
5	69	135.543	136.340	0.797
5	70	156.191	156.124	-0.067
5	71	154.011	153.816	-0.195
5	72	151.596	152.411	0.815
10	73	152.333	151.770	-0.563
10	74	148.168	147.255	-0.913
10	75	142.376	143.104	0.728
10	76	149.422	150.455	1.033
10	77	138.736	138.097	-0.639
10	78	140.251	140.614	0.363
15	79	147.276	148.672	1.396
15	80	147.179	147.766	0.587
15	81	135.543	136.106	0.563
15	82	156.191	155.491	-0.700
15	83	154.011	155.323	1.312
15	84	151.596	153.716	2.120
30	86	152.333	153.590	1.257
30	89	148.168	149.185	1.017
30	91	142.376	143.020	0.644
30	92	149.422	147.902	-1.520
30	94	138.736	137.558	-1.178
30	95	140.251	141.652	1.401
35	100	147.276	150.098	2.822
35	101	147.179	150.911	3.732
35	102	135.543	137.879	2.336
35	103	156.191	156.521	0.330
35	104	154.011	154.712	0.701
35	105	151.596	153.547	1.951
50	106	141.086	143.151	2.065
50	107	144.483	147.801	3.318
50	108	144.210	147.874	3.664
50	109	146.625	151.680	5.055
50	110	144.611	146.375	1.764
50	111	139.249	140.600	1.351
55	113	145.667	146.197	0.530
55	114	153.230	154.745	1.515
55	115	150.295	152.112	1.817
55	116	147.675	149.100	1.425
55	126	158.700	158.577	-0.123
55	128	152.617	149.549	-3.068
100	129	148.214	151.298	3.084
100	131	144.315	144.898	0.583
100	132	145.720	143.173	-2.547
100	133	149.941	151.594	1.653
100	134	149.543	151.714	2.171
100	135	149.652	147.289	-2.363
100	138	146.184	147.483	1.299
100	139	141.481	145.116	3.635
100	140	147.730	148.311	0.581
100	141	146.837	147.913	1.076
100	142	147.849	148.765	0.916
100	143	149.496	154.637	5.141
100	144	146.787	148.425	1.638
100	220	151.690	152.908	1.218
100	221	139.826	143.497	3.671
100	224	140.621	141.117	0.496
100	225	145.560	147.423	1.863
100	226	143.464	147.416	3.952
100	228	144.977	150.188	5.211
100	232	140.777	144.371	3.594
100	233	147.128	150.348	3.220
100	236	136.064	138.815	2.751
100	238	144.559	148.334	3.775
100	243	140.298	139.781	-0.517
105	244	140.434	144.603	4.169
105	246	139.344	143.905	4.561
105	248	139.197	141.253	2.056
105	374	152.775	154.191	1.416
105	376	141.010	139.347	-1.663
105	Max	158.700	158.577	5.211
105	Average	146.146	147.353	1.207
105	Min	135.543	136.106	-3.068
105	Std Dev	5.471	5.422	1.766



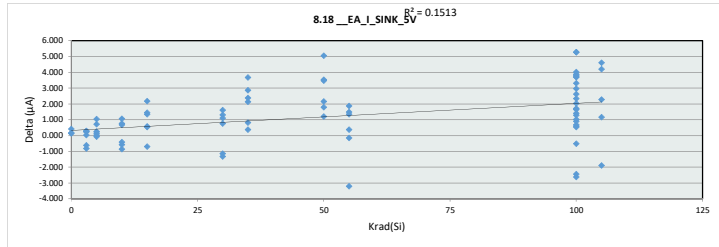
8.17 EA_I_SINK											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	140.172	137.972	136.340	138.097	136.106	137.558	137.879	140.600	146.197	138.815	139.347
Average	144.445	145.049	148.967	145.216	149.512	145.485	150.611	146.247	151.713	147.284	144.660
Max	150.124	151.445	156.124	151.770	155.491	153.590	156.521	151.680	158.577	154.637	154.191
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



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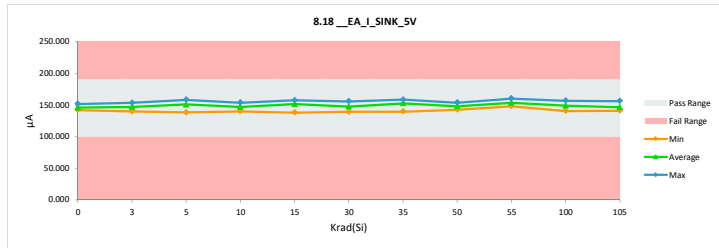
8.18 EA I SINK 5V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	141.845	141.951	0.106
0	118	144.567	144.704	0.137
0	122	151.324	151.699	0.375
3	61	154.118	153.261	-0.857
3	62	149.835	150.105	0.270
3	63	144.177	144.181	0.004
3	64	151.003	151.207	0.204
3	65	140.295	139.647	-0.648
3	66	141.977	142.221	0.244
5	67	148.969	149.083	0.114
5	68	149.037	149.258	0.221
5	69	137.352	138.046	0.694
5	70	157.986	157.977	-0.009
5	71	155.732	155.637	-0.095
5	72	153.292	154.311	1.019
10	73	154.118	153.670	-0.448
10	74	149.835	148.934	-0.901
10	75	144.177	144.896	0.719
10	76	151.003	152.024	1.021
10	77	140.295	139.681	-0.614
10	78	141.977	142.614	0.637
15	79	148.969	150.277	1.308
15	80	149.037	149.575	0.538
15	81	137.352	137.852	0.500
15	82	157.986	157.256	-0.730
15	83	155.732	157.150	1.418
15	84	153.292	155.439	2.147
30	86	154.118	155.381	1.263
30	89	149.835	150.896	1.061
30	91	144.177	144.895	0.718
30	92	151.003	149.637	-1.366
30	94	140.295	139.123	-1.172
30	95	141.977	143.555	1.578
35	100	148.969	151.796	2.827
35	101	149.037	152.671	3.634
35	102	137.352	139.445	2.093
35	103	157.986	158.329	0.343
35	104	155.732	156.520	0.788
35	105	153.292	155.647	2.355
50	106	142.742	144.857	2.115
50	107	146.312	149.759	3.447
50	108	146.019	149.512	3.493
50	109	148.334	153.360	5.026
50	110	146.378	148.144	1.766
50	111	141.073	142.242	1.169
55	113	147.581	147.931	0.350
55	114	155.024	156.476	1.452
55	115	152.045	153.878	1.833
55	116	149.290	150.594	1.304
55	126	160.462	160.276	-0.186
55	128	154.497	151.253	-3.244
100	129	150.153	153.079	2.926
100	131	146.071	146.635	0.564
100	132	147.512	144.852	-2.660
100	133	151.693	153.369	1.676
100	134	151.210	153.521	2.311
100	135	151.431	148.969	-2.462
100	138	147.885	149.252	1.367
100	139	143.029	146.805	3.776
100	140	149.629	150.135	0.506
100	141	148.739	149.736	0.997
100	142	149.627	150.501	0.874
100	143	151.304	156.550	5.246
100	144	148.652	150.271	1.619
100	220	153.552	154.808	1.256
100	221	141.409	145.256	3.847
100	224	142.244	142.893	0.649
100	225	147.208	149.217	2.009
100	226	145.156	149.150	3.994
100	228	146.783	152.024	5.241
100	232	142.562	146.223	3.661
100	233	149.032	152.310	3.278
100	236	137.731	140.329	2.598
100	238	146.393	150.064	3.671
100	243	141.911	141.369	-0.542
105	244	142.110	146.278	4.168
105	246	141.136	145.698	4.562
105	248	140.868	143.115	2.247
105	374	154.847	155.984	1.137
105	375	142.677	140.746	-1.931
105	376	160.462	160.276	-0.186
105	Max	147.892	149.100	1.208
105	Average	137.352	137.852	-3.244
105	Min	137.352	137.852	-3.244
105	Std Dev	5.496	5.464	1.785



8.18 EA I SINK	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

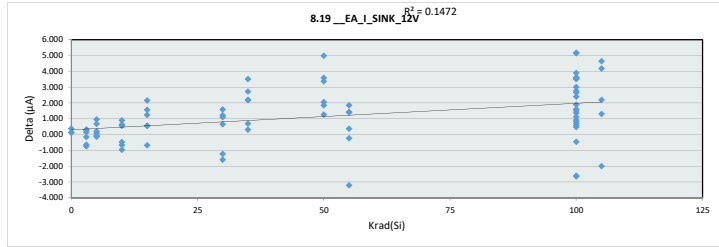
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	141.951	139.647	138.046	139.681	137.852	139.123	139.445	142.242	147.931	140.329	140.746
Average	146.118	146.770	150.719	146.970	151.258	147.248	152.401	147.979	153.401	149.055	146.364
Max	151.699	153.261	157.977	153.670	157.256	155.381	158.329	153.360	160.276	156.550	155.984
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



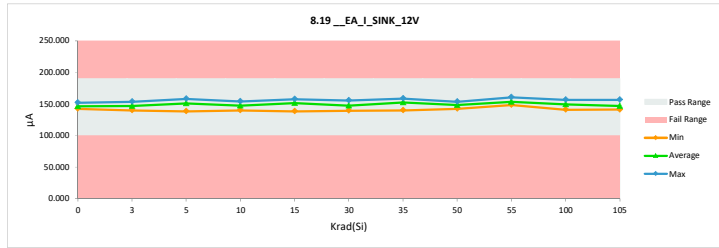
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8.19 EA I SINK 12V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	141.912	142.038	0.126
0	118	144.694	144.812	0.118
0	122	151.458	151.822	0.364
3	61	154.236	153.489	-0.747
3	62	150.007	150.312	0.305
3	63	144.245	144.089	-0.156
3	64	151.257	151.398	0.141
3	65	140.511	139.871	-0.640
3	66	142.085	142.369	0.284
5	67	149.118	149.198	0.080
5	68	149.139	149.335	0.196
5	69	137.458	138.128	0.670
5	70	158.143	158.067	-0.076
5	71	155.829	155.691	-0.138
5	72	153.489	154.447	0.958
10	73	154.236	153.750	-0.486
10	74	150.007	149.024	-0.983
10	75	144.245	144.871	0.626
10	76	151.257	152.136	0.879
10	77	140.511	139.838	-0.673
10	78	142.085	142.620	0.535
15	79	149.118	150.345	1.227
15	80	149.139	149.679	0.540
15	81	137.458	138.003	0.545
15	82	158.143	157.452	-0.691
15	83	155.829	157.394	1.565
15	84	153.489	155.623	2.134
30	86	154.236	155.449	1.213
30	89	150.007	151.104	1.097
30	91	144.245	144.899	0.654
30	92	151.257	149.663	-1.594
30	94	140.511	139.272	-1.239
30	95	142.085	143.662	1.577
35	100	149.118	151.832	2.714
35	101	149.139	152.631	3.492
35	102	137.458	139.617	2.159
35	103	158.143	158.437	0.294
35	104	155.829	156.511	0.682
35	105	153.489	155.673	2.184
50	106	142.884	144.917	2.033
50	107	146.447	149.813	3.366
50	108	146.137	149.709	3.572
50	109	148.412	153.384	4.972
50	110	146.542	148.376	1.834
50	111	141.178	142.428	1.250
55	113	147.727	148.082	0.355
55	114	155.100	156.510	1.410
55	115	152.234	154.065	1.831
55	116	149.392	150.780	1.388
55	126	160.623	160.371	-0.252
55	128	154.528	151.305	-3.223
100	129	150.271	153.028	2.757
100	131	146.180	146.741	0.561
100	132	147.602	144.943	-2.659
100	133	151.961	153.551	1.590
100	134	151.302	153.695	2.393
100	135	151.687	149.069	-2.618
100	138	147.996	149.341	1.345
100	139	143.120	146.660	3.540
100	140	149.779	150.248	0.469
100	141	148.889	149.799	0.910
100	142	149.734	150.548	0.814
100	143	151.401	156.543	5.142
100	144	148.704	150.200	1.496
100	220	153.704	154.809	1.105
100	221	141.642	145.237	3.595
100	224	142.311	142.996	0.685
100	225	147.413	149.291	1.878
100	226	145.314	149.204	3.890
100	228	146.865	152.021	5.156
100	232	142.626	146.207	3.581
100	233	149.205	152.204	2.999
100	236	137.838	140.494	2.656
100	238	146.468	149.976	3.508
100	243	142.022	141.559	-0.463
105	244	142.265	146.439	4.174
105	246	141.130	145.762	4.632
105	248	141.093	143.279	2.186
105	374	154.981	156.265	1.284
105	376	142.966	140.952	-2.014
105	Max	160.623	160.371	-0.252
105	Average	148.029	149.192	1.163
105	Min	137.458	138.003	-0.545
105	Std Dev	5.503	5.456	1.770



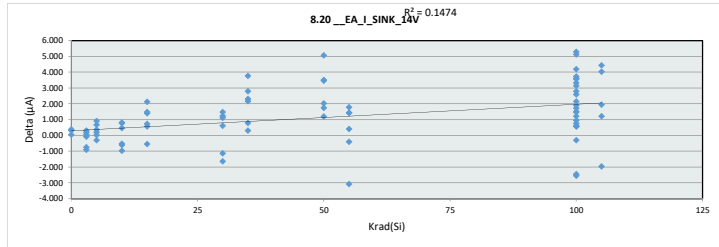
8.19 EA I SINK													
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.038	139.871	138.128	139.838	138.003	139.272	139.617	142.428	148.082	140.494	140.952		
Average	146.224	146.921	150.811	147.040	151.416	147.342	152.450	148.105	153.519	149.099	146.539		
Max	151.822	153.489	158.067	153.750	157.452	155.449	158.437	153.384	160.371	156.543	156.265		
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000		



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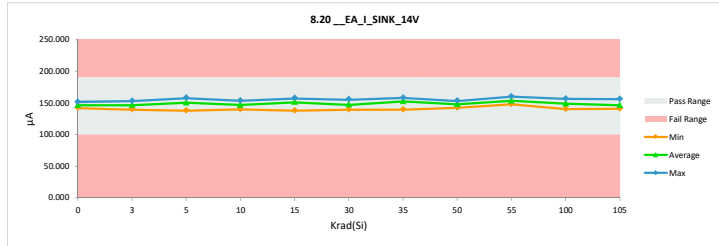
8.20 EA_I_SINK_14V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	141.925	142.215	0.290
0	118	144.791	144.812	0.021
0	122	151.436	151.799	0.363
3	61	154.209	153.280	-0.929
3	62	149.929	150.217	0.288
3	63	144.207	144.098	-0.109
3	64	151.270	151.309	0.039
3	65	140.457	139.680	-0.777
3	66	142.084	142.269	0.185
5	67	148.947	149.121	0.174
5	68	148.949	149.289	0.340
5	69	137.447	138.097	0.650
5	70	157.972	157.973	0.001
5	71	155.760	155.426	-0.334
5	72	153.439	154.328	0.889
10	73	154.209	153.664	-0.545
10	74	149.929	148.939	-0.990
10	75	144.207	144.960	0.753
10	76	151.270	152.048	0.778
10	77	140.457	139.837	-0.620
10	78	142.084	142.524	0.440
15	79	148.947	150.329	1.382
15	80	148.949	149.654	0.705
15	81	137.447	137.985	0.538
15	82	157.972	157.401	-0.571
15	83	155.760	157.220	1.460
15	84	153.439	155.545	2.106
30	86	154.209	155.418	1.209
30	89	149.929	151.022	1.093
30	91	144.207	144.800	0.593
30	92	151.270	149.616	-1.654
30	94	140.457	139.304	-1.153
30	95	142.084	143.547	1.463
35	100	148.947	151.719	2.772
35	101	148.949	152.687	3.738
35	102	137.447	139.593	2.146
35	103	157.972	158.251	0.279
35	104	155.760	156.526	0.766
35	105	153.439	155.730	2.291
50	106	142.888	144.895	2.007
50	107	146.460	149.945	3.485
50	108	146.010	149.455	3.445
50	109	148.325	153.358	5.033
50	110	146.582	148.308	1.726
50	111	141.168	142.336	1.168
55	113	147.660	148.044	0.384
55	114	155.106	156.503	1.397
55	115	152.197	153.954	1.757
55	116	149.295	150.711	1.416
55	126	160.594	160.176	-0.418
55	128	154.532	151.430	-3.102
100	129	150.204	152.971	2.767
100	131	146.173	146.731	0.558
100	132	147.569	144.998	-2.571
100	133	151.851	153.544	1.693
100	134	151.382	153.507	2.125
100	135	151.492	149.020	-2.472
100	138	147.915	149.373	1.458
100	139	143.079	146.626	3.547
100	140	149.696	150.230	0.534
100	141	148.890	149.613	0.723
100	142	149.630	150.566	0.936
100	143	151.425	156.681	5.256
100	144	148.709	150.191	1.482
100	220	153.626	154.818	1.192
100	221	141.598	145.290	3.692
100	224	142.451	143.024	0.573
100	225	147.407	149.290	1.883
100	226	145.230	149.390	4.160
100	228	146.896	151.992	5.096
100	232	142.615	146.185	3.570
100	233	149.156	152.268	3.112
100	236	137.872	140.433	2.561
100	238	146.488	149.793	3.305
100	243	141.998	141.666	-0.332
105	244	142.330	146.332	4.002
105	246	141.183	145.586	4.403
105	248	141.122	143.036	1.914
105	374	154.961	156.143	1.182
105	375	142.790	140.806	-1.984
105	376	160.594	160.176	-0.418
105	377	147.984	149.143	1.159
105	378	137.447	137.985	-0.538
105	379	5.479	5.442	-0.037



8.20 EA_I_SINK	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

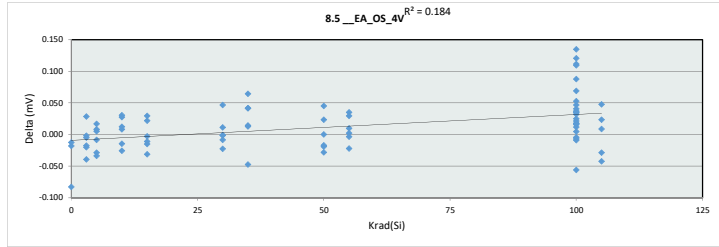
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.215	139.680	138.097	139.837	137.985	139.304	139.593	142.336	148.044	140.433	140.806
Average	146.275	146.809	150.706	146.995	151.356	147.285	152.418	148.050	153.470	149.092	146.381
Max	151.799	153.280	157.973	153.664	157.401	155.418	158.251	153.358	160.176	156.681	156.143
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



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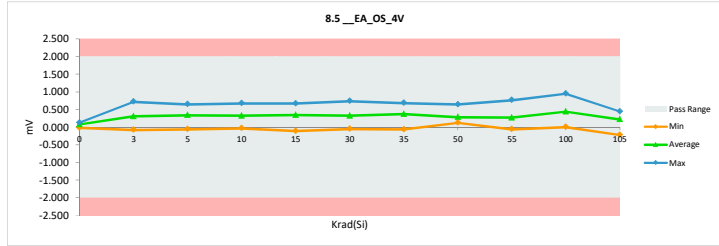
8.5_EA_OS_4V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

Krad(Si)	Serial #	PRE	POST	Delta
0	117	-0.003	-0.021	-0.018
0	118	0.137	0.125	-0.013
0	122	0.208	0.125	-0.083
3	61	0.688	0.716	0.028
3	62	0.010	-0.029	-0.040
3	63	0.373	0.353	-0.020
3	64	0.318	0.313	-0.005
3	65	-0.064	-0.081	-0.018
3	66	0.576	0.573	-0.002
5	67	0.393	0.398	0.005
5	68	0.455	0.426	-0.029
5	69	-0.080	-0.064	0.017
5	70	0.407	0.398	-0.009
5	71	0.256	0.222	-0.034
5	72	0.639	0.647	0.008
10	73	0.688	0.673	-0.015
10	74	0.010	0.019	0.008
10	75	0.373	0.385	0.012
10	76	0.318	0.292	-0.026
10	77	-0.064	-0.036	0.027
10	78	0.576	0.606	0.030
15	79	0.393	0.378	-0.015
15	80	0.455	0.477	0.022
15	81	-0.080	-0.112	-0.031
15	82	0.407	0.404	-0.003
15	83	0.256	0.245	-0.011
15	84	0.639	0.668	0.029
30	86	0.688	0.734	0.046
30	89	0.010	0.009	-0.001
30	91	0.373	0.372	-0.001
30	92	0.318	0.309	-0.009
30	94	-0.064	-0.052	0.011
30	95	0.576	0.553	-0.023
35	100	0.393	0.457	0.064
35	101	0.455	0.496	0.041
35	102	-0.080	-0.066	0.014
35	103	0.407	0.360	-0.047
35	104	0.256	0.269	0.012
35	105	0.639	0.681	0.042
50	106	0.594	0.638	0.045
50	107	0.125	0.124	0.000
50	108	0.236	0.260	0.023
50	109	0.140	0.123	-0.017
50	110	0.364	0.345	-0.019
50	111	0.226	0.197	-0.028
55	113	0.753	0.762	0.010
55	114	0.089	0.067	-0.022
55	115	0.094	0.096	0.002
55	116	0.328	0.362	0.035
55	126	-0.059	-0.062	-0.004
55	128	0.370	0.399	0.029
100	129	0.692	0.704	0.012
100	131	0.612	0.658	0.047
100	132	0.729	0.840	0.111
100	133	0.483	0.518	0.035
100	134	0.542	0.651	0.109
100	135	0.063	0.067	0.005
100	138	0.415	0.484	0.069
100	139	0.037	0.028	-0.009
100	140	0.299	0.324	0.025
100	141	0.493	0.514	0.021
100	142	0.361	0.305	-0.056
100	143	0.488	0.525	0.037
100	144	0.471	0.524	0.053
100	220	0.390	0.407	0.017
100	221	0.380	0.412	0.032
100	224	0.494	0.581	0.087
100	225	0.812	0.947	0.135
100	226	0.436	0.454	0.018
100	228	0.273	0.289	0.016
100	232	-0.118	0.003	0.120
100	233	0.187	0.228	0.041
100	236	0.461	0.456	-0.005
100	238	0.334	0.327	-0.007
100	243	0.285	0.319	0.034
105	244	-0.195	-0.224	-0.029
105	246	0.219	0.266	0.047
105	248	0.432	0.440	0.008
105	374	0.239	0.262	0.023
105	376	0.386	0.344	-0.042
105	Max	0.812	0.947	0.135
105	Average	0.328	0.340	0.012
105	Min	-0.195	-0.224	-0.083
105	Std Dev	0.239	0.254	0.039



8.5_EA_OS_4V	
Test Site	
Tester	
Test Number	
Max Limit	2 mV
Min Limit	-2 mV

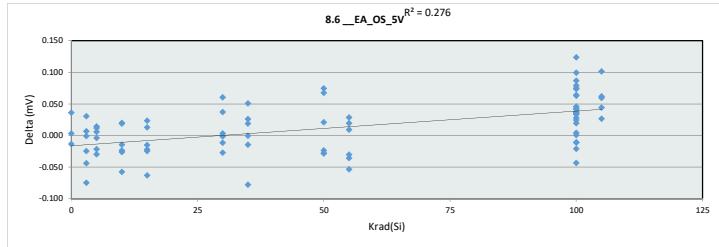
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.021	-0.081	-0.064	-0.036	-0.112	-0.052	-0.066	0.123	-0.062	0.003	-0.224
Average	0.076	0.308	0.338	0.323	0.343	0.321	0.366	0.281	0.271	0.440	0.218
Max	0.125	0.717	0.647	0.673	0.668	0.735	0.681	0.639	0.762	0.947	0.440
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000



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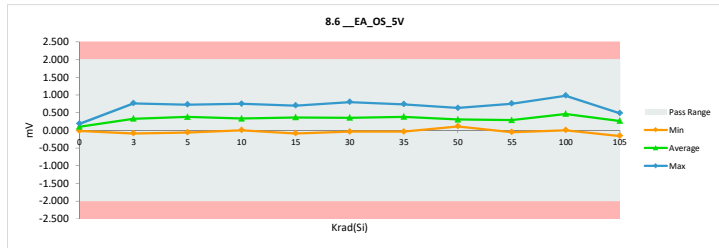
8.6_EA_OS_5V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

Krad(Si)	Serial #	PRE	POST	Delta
0	117	-0.023	-0.021	0.002
0	118	0.144	0.129	-0.014
0	122	0.146	0.181	0.036
3	61	0.760	0.759	-0.002
3	62	-0.021	0.009	0.030
3	63	0.389	0.363	-0.025
3	64	0.361	0.368	0.006
3	65	-0.016	-0.091	-0.075
3	66	0.591	0.547	-0.045
5	67	0.455	0.433	-0.023
5	68	0.519	0.488	-0.031
5	69	-0.069	-0.064	0.005
5	70	0.386	0.400	0.014
5	71	0.264	0.260	-0.004
5	72	0.713	0.724	0.011
10	73	0.760	0.745	-0.015
10	74	-0.021	-0.003	0.018
10	75	0.389	0.362	-0.027
10	76	0.361	0.337	-0.024
10	77	-0.016	0.004	0.019
10	78	0.591	0.533	-0.058
15	79	0.455	0.430	-0.025
15	80	0.519	0.455	-0.064
15	81	-0.069	-0.092	-0.023
15	82	0.386	0.409	0.023
15	83	0.264	0.277	0.012
15	84	0.713	0.697	-0.016
30	86	0.760	0.797	0.036
30	89	-0.021	0.038	0.060
30	91	0.389	0.377	-0.012
30	92	0.361	0.359	-0.002
30	94	-0.016	-0.044	-0.028
30	95	0.591	0.594	0.003
35	100	0.455	0.440	-0.015
35	101	0.519	0.518	-0.001
35	102	-0.069	-0.044	0.025
35	103	0.386	0.436	0.050
35	104	0.264	0.186	-0.078
35	105	0.713	0.731	0.018
50	106	0.657	0.628	-0.029
50	107	0.132	0.108	-0.024
50	108	0.270	0.290	0.020
50	109	0.100	0.174	0.074
50	110	0.404	0.375	-0.029
50	111	0.171	0.238	0.067
55	113	0.802	0.748	-0.054
55	114	0.091	0.110	0.019
55	115	0.146	0.109	-0.037
55	116	0.353	0.381	0.028
55	126	-0.026	-0.057	-0.031
55	128	0.390	0.399	0.009
100	129	0.648	0.721	0.073
100	131	0.645	0.677	0.033
100	132	0.817	0.858	0.041
100	133	0.558	0.620	0.062
100	134	0.592	0.691	0.099
100	135	0.057	0.135	0.078
100	138	0.475	0.499	0.024
100	139	0.083	0.061	-0.022
100	140	0.277	0.320	0.042
100	141	0.529	0.532	0.003
100	142	0.348	0.348	0.000
100	143	0.511	0.529	0.019
100	144	0.471	0.516	0.045
100	220	0.399	0.433	0.034
100	221	0.407	0.434	0.027
100	224	0.617	0.573	-0.044
100	225	0.850	0.974	0.123
100	226	0.484	0.472	-0.012
100	228	0.226	0.300	0.074
100	232	-0.067	-0.004	0.063
100	233	0.209	0.197	-0.012
100	236	0.424	0.510	0.086
100	238	0.323	0.360	0.037
100	243	0.331	0.367	0.036
105	244	-0.186	-0.160	0.026
105	246	0.203	0.247	0.044
105	248	0.378	0.479	0.100
105	374	0.249	0.310	0.061
105	376	0.365	0.423	0.058
105	Max	0.850	0.974	0.123
105	Average	0.350	0.362	0.012
105	Min	-0.186	-0.160	-0.078
105	Std Dev	0.255	0.257	0.042



8.6_EA_OS_5V	
Test Site	
Tester	
Test Number	
Max Limit	2
Min Limit	-2

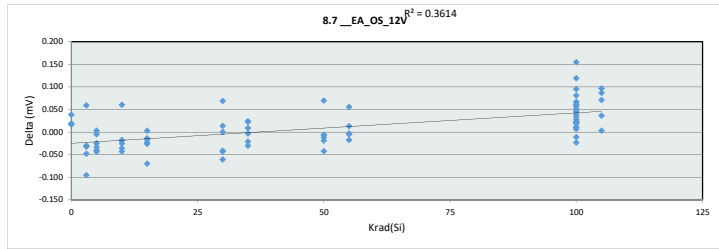
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.021	-0.091	-0.064	-0.003	-0.092	-0.044	-0.044	0.108	-0.057	-0.004	-0.160
Average	0.097	0.326	0.373	0.330	0.363	0.354	0.378	0.302	0.281	0.464	0.260
Max	0.181	0.759	0.724	0.745	0.697	0.797	0.731	0.628	0.748	0.974	0.479
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000



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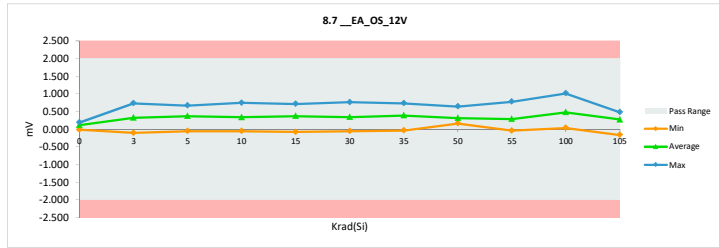
8.7_EA_OS_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

Krad(Si)	Serial #	PRE	POST	Delta
0	117	-0.038	-0.019	0.019
0	118	0.125	0.163	0.038
0	122	0.167	0.183	0.016
3	61	0.761	0.729	-0.032
3	62	0.043	0.013	-0.031
3	63	0.425	0.377	-0.048
3	64	0.288	0.346	0.058
3	65	-0.014	-0.110	-0.096
3	66	0.584	0.551	-0.033
5	67	0.450	0.417	-0.033
5	68	0.526	0.482	-0.044
5	69	-0.065	-0.063	0.003
5	70	0.424	0.419	-0.005
5	71	0.286	0.261	-0.025
5	72	0.705	0.664	-0.040
10	73	0.761	0.742	-0.018
10	74	0.043	0.024	-0.019
10	75	0.425	0.399	-0.026
10	76	0.288	0.347	0.060
10	77	-0.014	-0.057	-0.043
10	78	0.584	0.548	-0.036
15	79	0.450	0.436	-0.015
15	80	0.526	0.455	-0.071
15	81	-0.065	-0.083	-0.017
15	82	0.424	0.398	-0.026
15	83	0.286	0.263	-0.023
15	84	0.705	0.707	0.003
30	86	0.761	0.760	0.000
30	89	0.043	-0.018	-0.061
30	91	0.425	0.383	-0.042
30	92	0.288	0.356	0.069
30	94	-0.014	-0.058	-0.044
30	95	0.584	0.598	0.014
35	100	0.450	0.459	0.008
35	101	0.526	0.522	-0.004
35	102	-0.065	-0.042	0.023
35	103	0.424	0.394	-0.031
35	104	0.286	0.264	-0.022
35	105	0.705	0.728	0.023
50	106	0.644	0.637	-0.007
50	107	0.166	0.154	-0.012
50	108	0.289	0.247	-0.043
50	109	0.104	0.173	0.069
50	110	0.407	0.400	-0.008
50	111	0.265	0.246	-0.019
55	113	0.780	0.775	-0.005
55	114	0.107	0.120	0.013
55	115	0.070	0.065	-0.005
55	116	0.334	0.389	0.055
55	126	-0.028	-0.046	-0.018
55	128	0.404	0.401	-0.003
100	129	0.679	0.685	0.006
100	131	0.662	0.682	0.021
100	132	0.796	0.829	0.034
100	133	0.555	0.577	0.022
100	134	0.622	0.702	0.080
100	135	0.073	0.131	0.058
100	138	0.515	0.503	-0.011
100	139	0.077	0.087	0.010
100	140	0.308	0.359	0.051
100	141	0.554	0.599	0.045
100	142	0.342	0.352	0.011
100	143	0.511	0.532	0.021
100	144	0.456	0.575	0.119
100	220	0.399	0.422	0.023
100	221	0.388	0.454	0.067
100	224	0.581	0.620	0.039
100	225	0.854	1.008	0.154
100	226	0.478	0.496	0.018
100	228	0.310	0.286	-0.023
100	232	-0.066	0.029	0.095
100	233	0.198	0.263	0.065
100	236	0.490	0.512	0.022
100	238	0.347	0.374	0.027
100	243	0.315	0.375	0.060
105	244	-0.172	-0.169	0.003
105	246	0.260	0.296	0.036
105	248	0.380	0.476	0.096
105	374	0.242	0.313	0.071
105	376	0.351	0.438	0.087
105	Max	0.854	1.008	0.154
105	Average	0.357	0.366	0.010
105	Min	-0.172	-0.169	-0.003
105	Std Dev	0.250	0.257	0.045



8.7_EA_OS_12V	
Test Site	
Tester	
Test Number	
Max Limit	2
Min Limit	-2

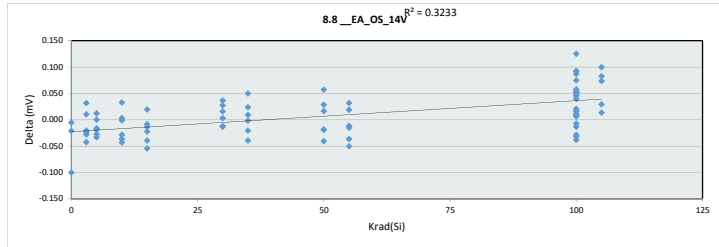
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.019	-0.110	-0.063	-0.057	-0.083	-0.058	-0.042	0.154	-0.046	0.029	-0.169
Average	0.109	0.318	0.363	0.334	0.363	0.337	0.387	0.309	0.284	0.477	0.271
Max	0.183	0.729	0.665	0.742	0.708	0.760	0.728	0.637	0.775	1.008	0.476
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000



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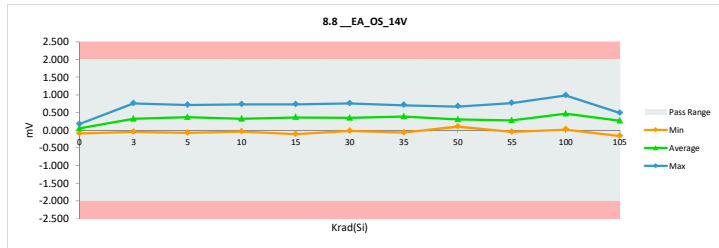
8.8_EA_OS_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.012	-0.090	-0.101
0	118	0.107	0.085	-0.022
0	122	0.182	0.176	-0.006
3	61	0.732	0.762	0.030
3	62	0.037	0.016	-0.021
3	63	0.402	0.373	-0.029
3	64	0.323	0.298	-0.024
3	65	-0.005	-0.049	-0.044
3	66	0.558	0.568	0.010
5	67	0.437	0.417	-0.020
5	68	0.526	0.492	-0.034
5	69	-0.086	-0.075	0.011
5	70	0.433	0.405	-0.028
5	71	0.301	0.283	-0.017
5	72	0.713	0.712	-0.001
10	73	0.732	0.730	-0.002
10	74	0.037	0.000	-0.037
10	75	0.402	0.358	-0.044
10	76	0.323	0.325	0.002
10	77	-0.005	-0.034	-0.029
10	78	0.558	0.590	0.032
15	79	0.437	0.428	-0.010
15	80	0.526	0.470	-0.056
15	81	-0.086	-0.109	-0.023
15	82	0.433	0.419	-0.014
15	83	0.301	0.260	-0.040
15	84	0.713	0.732	0.019
30	86	0.732	0.758	0.026
30	89	0.037	0.039	0.002
30	91	0.402	0.388	-0.014
30	92	0.323	0.338	0.015
30	94	-0.005	-0.018	-0.013
30	95	0.558	0.594	0.036
35	100	0.437	0.486	0.049
35	101	0.526	0.534	0.008
35	102	-0.086	-0.063	0.023
35	103	0.433	0.412	-0.021
35	104	0.301	0.260	-0.041
35	105	0.713	0.711	-0.003
50	106	0.646	0.674	0.027
50	107	0.145	0.103	-0.041
50	108	0.297	0.277	-0.020
50	109	0.120	0.176	0.056
50	110	0.410	0.390	-0.020
50	111	0.218	0.234	0.016
55	113	0.782	0.766	-0.016
55	114	0.127	0.090	-0.037
55	115	0.123	0.072	-0.051
55	116	0.337	0.367	0.030
55	126	-0.033	-0.046	-0.013
55	128	0.403	0.421	0.018
100	129	0.694	0.680	-0.014
100	131	0.659	0.669	0.009
100	132	0.777	0.782	0.005
100	133	0.541	0.594	0.053
100	134	0.597	0.688	0.091
100	135	0.087	0.133	0.045
100	138	0.488	0.533	0.045
100	139	0.086	0.106	0.020
100	140	0.323	0.284	-0.039
100	141	0.532	0.581	0.049
100	142	0.363	0.334	-0.030
100	143	0.513	0.527	0.014
100	144	0.472	0.546	0.074
100	220	0.398	0.415	0.016
100	221	0.379	0.429	0.051
100	224	0.563	0.615	0.053
100	225	0.859	0.983	0.124
100	226	0.468	0.435	-0.033
100	228	0.307	0.299	-0.008
100	232	-0.069	0.022	0.091
100	233	0.190	0.276	0.086
100	236	0.471	0.528	0.057
100	238	0.336	0.374	0.039
100	243	0.352	0.359	0.007
105	244	-0.235	-0.162	0.073
105	246	0.210	0.308	0.098
105	248	0.409	0.490	0.082
105	374	0.301	0.313	0.013
105	376	0.381	0.409	0.028
105	Max	0.859	0.983	0.124
105	Average	0.356	0.363	0.008
105	Min	-0.235	-0.162	-0.101
105	Std Dev	0.248	0.258	0.042



8.8_EA_OS_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

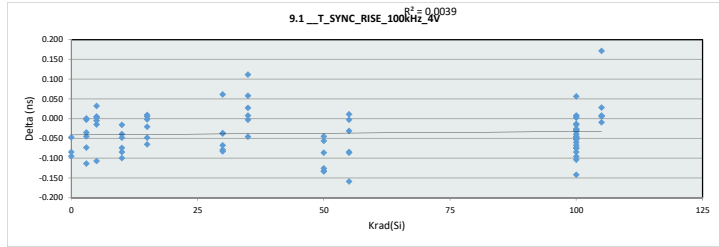
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.090	-0.049	-0.075	-0.034	-0.109	-0.018	-0.063	0.103	-0.046	0.022	-0.162
Average	0.057	0.328	0.373	0.328	0.367	0.350	0.390	0.309	0.278	0.466	0.272
Max	0.176	0.762	0.713	0.730	0.732	0.758	0.711	0.674	0.766	0.983	0.490
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000



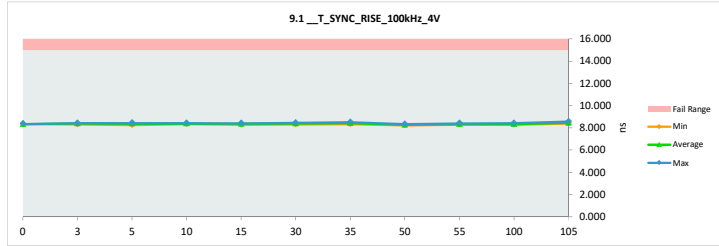
LDR TID Report
TPS7H5001-SP

9.1 T_SYNC_RISE_100kHz_4V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	15	15
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.423	8.328	-0.095
0	118	8.422	8.337	-0.085
0	122	8.400	8.353	-0.047
3	61	8.510	8.396	-0.114
3	62	8.475	8.430	-0.045
3	63	8.378	8.375	-0.003
3	64	8.389	8.316	-0.073
3	65	8.426	8.426	0.000
3	66	8.431	8.396	-0.035
5	67	8.377	8.379	0.002
5	68	8.393	8.388	-0.005
5	69	8.404	8.436	0.032
5	70	8.350	8.243	-0.107
5	71	8.432	8.437	0.005
5	72	8.390	8.375	-0.015
10	73	8.510	8.425	-0.085
10	74	8.475	8.401	-0.074
10	75	8.378	8.330	-0.048
10	76	8.389	8.350	-0.039
10	77	8.426	8.410	-0.016
10	78	8.431	8.331	-0.100
15	79	8.377	8.386	0.009
15	80	8.393	8.398	0.005
15	81	8.404	8.402	-0.002
15	82	8.350	8.285	-0.065
15	83	8.432	8.411	-0.021
15	84	8.390	8.342	-0.048
30	86	8.510	8.442	-0.068
30	89	8.475	8.392	-0.083
30	91	8.378	8.439	0.061
30	92	8.389	8.310	-0.079
30	94	8.426	8.388	-0.038
30	95	8.431	8.394	-0.037
35	100	8.377	8.331	-0.046
35	101	8.393	8.504	0.111
35	102	8.404	8.462	0.058
35	103	8.350	8.347	-0.003
35	104	8.432	8.459	0.027
35	105	8.390	8.398	0.008
50	106	8.391	8.258	-0.133
50	107	8.316	8.230	-0.086
50	108	8.424	8.291	-0.133
50	109	8.351	8.225	-0.126
50	110	8.386	8.330	-0.056
50	111	8.372	8.327	-0.045
55	113	8.454	8.295	-0.159
55	114	8.397	8.311	-0.086
55	115	8.398	8.313	-0.085
55	116	8.376	8.345	-0.031
55	126	8.336	8.333	-0.003
55	128	8.400	8.411	0.011
100	129	8.309	8.311	0.002
100	131	8.373	8.269	-0.104
100	132	8.365	8.317	-0.048
100	133	8.415	8.383	-0.032
100	134	8.375	8.300	-0.075
100	135	8.383	8.298	-0.085
100	138	8.451	8.309	-0.142
100	139	8.284	8.340	0.056
100	140	8.337	8.298	-0.039
100	141	8.426	8.352	-0.074
100	142	8.364	8.371	0.007
100	143	8.419	8.366	-0.053
100	144	8.321	8.329	0.008
100	220	8.372	8.304	-0.068
100	221	8.329	8.302	-0.027
100	224	8.409	8.393	-0.016
100	225	8.373	8.360	-0.013
100	226	8.468	8.423	-0.045
100	228	8.390	8.364	-0.026
100	232	8.361	8.312	-0.049
100	233	8.383	8.287	-0.096
100	236	8.431	8.371	-0.060
100	238	8.401	8.351	-0.050
100	243	8.443	8.413	-0.030
105	244	8.451	8.442	-0.009
105	246	8.393	8.421	0.028
105	248	8.383	8.554	0.171
105	374	8.462	8.470	0.008
105	375	8.375	8.390	0.015
105	Max	8.510	8.554	0.171
105	Average	8.399	8.363	-0.037
105	Min	8.284	8.225	-0.159
105	Std Dev	0.044	0.062	0.056



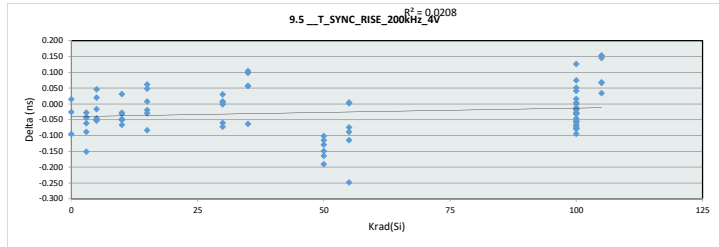
9.1 T_SYNC_RISE											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.328	8.316	8.243	8.330	8.285	8.310	8.331	8.225	8.295	8.269	8.380
Average	8.339	8.390	8.376	8.375	8.371	8.394	8.417	8.277	8.335	8.338	8.453
Max	8.353	8.430	8.437	8.425	8.411	8.442	8.504	8.330	8.411	8.423	8.554
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



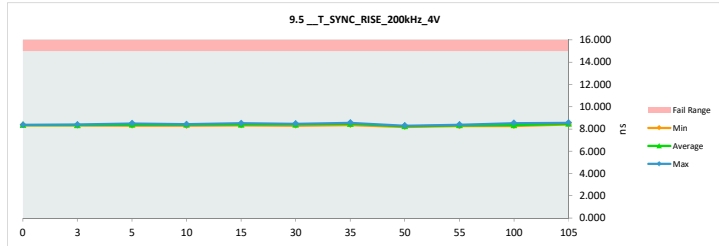
LDR TID Report
TPS7H5001-SP

9.5_T_SYNC_RISE_200kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.455	8.358	-0.097
0	118	8.294	8.307	0.013
0	122	8.384	8.357	-0.027
3	61	8.443	8.380	-0.063
3	62	8.408	8.363	-0.045
3	63	8.397	8.307	-0.090
3	64	8.341	8.298	-0.043
3	65	8.366	8.337	-0.029
3	66	8.451	8.298	-0.153
5	67	8.300	8.282	-0.018
5	68	8.433	8.478	0.045
5	69	8.382	8.400	0.018
5	70	8.329	8.274	-0.055
5	71	8.437	8.389	-0.048
5	72	8.401	8.351	-0.050
10	73	8.443	8.410	-0.033
10	74	8.408	8.379	-0.029
10	75	8.397	8.347	-0.050
10	76	8.341	8.273	-0.068
10	77	8.366	8.395	0.029
10	78	8.451	8.398	-0.053
15	79	8.300	8.306	0.006
15	80	8.433	8.493	0.060
15	81	8.382	8.351	-0.031
15	82	8.329	8.308	-0.021
15	83	8.437	8.484	0.047
15	84	8.401	8.316	-0.085
30	86	8.443	8.449	0.006
30	89	8.408	8.412	0.004
30	91	8.397	8.394	-0.003
30	92	8.341	8.279	-0.062
30	94	8.366	8.394	0.028
30	95	8.451	8.377	-0.074
35	100	8.300	8.402	0.102
35	101	8.433	8.530	0.097
35	102	8.382	8.437	0.055
35	103	8.329	8.385	0.056
35	104	8.437	8.534	0.097
35	105	8.401	8.336	-0.065
50	106	8.418	8.226	-0.192
50	107	8.314	8.184	-0.130
50	108	8.396	8.280	-0.116
50	109	8.306	8.203	-0.103
50	110	8.426	8.260	-0.166
50	111	8.423	8.272	-0.151
55	113	8.394	8.278	-0.116
55	114	8.373	8.283	-0.090
55	115	8.534	8.284	-0.250
55	116	8.375	8.375	0.000
55	126	8.320	8.244	-0.076
55	128	8.348	8.351	0.003
100	129	8.379	8.307	-0.072
100	131	8.367	8.309	-0.058
100	132	8.332	8.372	0.040
100	133	8.340	8.320	-0.020
100	134	8.328	8.309	-0.019
100	135	8.343	8.311	-0.032
100	138	8.358	8.298	-0.060
100	139	8.351	8.255	-0.096
100	140	8.326	8.324	-0.002
100	141	8.345	8.347	0.002
100	142	8.386	8.307	-0.079
100	143	8.393	8.360	-0.033
100	144	8.326	8.295	-0.031
100	220	8.312	8.362	0.050
100	221	8.337	8.256	-0.081
100	224	8.312	8.385	0.073
100	225	8.388	8.359	-0.029
100	226	8.407	8.339	-0.068
100	228	8.416	8.360	-0.056
100	232	8.295	8.247	-0.048
100	233	8.332	8.318	-0.014
100	236	8.400	8.322	-0.078
100	238	8.386	8.511	0.125
100	243	8.334	8.348	0.014
105	244	8.418	8.483	0.065
105	246	8.370	8.437	0.067
105	248	8.362	8.394	0.032
105	374	8.400	8.552	0.152
105	376	8.331	8.475	0.144
105	375	8.534	8.552	0.152
105	Average	8.377	8.351	-0.027
105	Min	8.294	8.184	-0.250
105	Std Dev	0.048	0.077	0.074



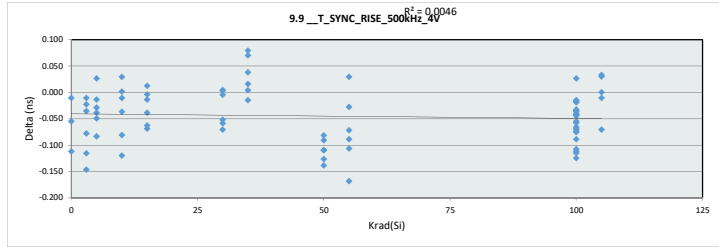
9.5_T_SYNC_RISE											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.307	8.298	8.274	8.273	8.306	8.279	8.336	8.184	8.244	8.247	8.394
Average	8.341	8.331	8.362	8.367	8.376	8.384	8.437	8.238	8.303	8.330	8.468
Max	8.358	8.380	8.478	8.410	8.493	8.449	8.534	8.280	8.375	8.511	8.552
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



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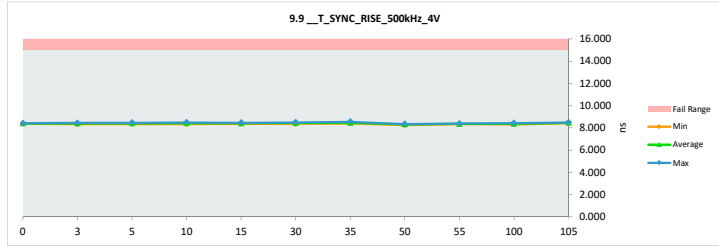
9.9_T_SYNC_RISE_500kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.513	8.401	-0.112
0	118	8.379	8.368	-0.011
0	122	8.418	8.363	-0.055
3	61	8.543	8.427	-0.116
3	62	8.449	8.426	-0.023
3	63	8.419	8.383	-0.036
3	64	8.392	8.314	-0.078
3	65	8.462	8.315	-0.147
3	66	8.398	8.387	-0.011
5	67	8.378	8.404	0.026
5	68	8.488	8.438	-0.050
5	69	8.488	8.449	-0.039
5	70	8.336	8.322	-0.014
5	71	8.439	8.410	-0.029
5	72	8.443	8.359	-0.084
10	73	8.543	8.423	-0.120
10	74	8.449	8.438	-0.011
10	75	8.419	8.382	-0.037
10	76	8.392	8.311	-0.081
10	77	8.462	8.463	0.001
10	78	8.398	8.427	0.029
15	79	8.378	8.374	-0.004
15	80	8.488	8.449	-0.039
15	81	8.488	8.419	-0.069
15	82	8.336	8.348	0.012
15	83	8.439	8.376	-0.063
15	84	8.443	8.429	-0.014
30	86	8.543	8.472	-0.071
30	89	8.449	8.390	-0.059
30	91	8.419	8.414	-0.005
30	92	8.392	8.340	-0.052
30	94	8.462	8.466	0.004
30	95	8.398	8.402	0.004
35	100	8.378	8.394	0.016
35	101	8.488	8.526	0.038
35	102	8.488	8.473	-0.015
35	103	8.336	8.415	0.079
35	104	8.439	8.509	0.070
35	105	8.443	8.447	0.004
50	106	8.401	8.319	-0.082
50	107	8.367	8.257	-0.110
50	108	8.415	8.324	-0.091
50	109	8.389	8.250	-0.139
50	110	8.425	8.315	-0.110
50	111	8.419	8.292	-0.127
55	113	8.462	8.355	-0.107
55	114	8.396	8.324	-0.072
55	115	8.460	8.371	-0.089
55	116	8.483	8.314	-0.169
55	126	8.300	8.329	0.029
55	128	8.375	8.347	-0.028
100	129	8.393	8.322	-0.071
100	131	8.413	8.347	-0.066
100	132	8.431	8.316	-0.115
100	133	8.421	8.380	-0.041
100	134	8.371	8.316	-0.055
100	135	8.433	8.365	-0.068
100	138	8.370	8.351	-0.019
100	139	8.342	8.307	-0.035
100	140	8.397	8.423	0.026
100	141	8.465	8.376	-0.089
100	142	8.434	8.376	-0.058
100	143	8.452	8.415	-0.037
100	144	8.386	8.311	-0.075
100	220	8.397	8.364	-0.033
100	221	8.420	8.312	-0.108
100	224	8.422	8.380	-0.042
100	225	8.440	8.395	-0.045
100	226	8.500	8.375	-0.125
100	228	8.429	8.413	-0.016
100	232	8.367	8.292	-0.075
100	233	8.397	8.378	-0.019
100	236	8.451	8.339	-0.112
100	238	8.437	8.422	-0.015
100	243	8.408	8.366	-0.042
105	244	8.504	8.433	-0.071
105	246	8.420	8.409	-0.011
105	248	8.443	8.476	0.033
105	374	8.480	8.480	0.000
105	375	8.406	8.436	0.030
105	Max	8.543	8.526	0.017
105	Average	8.427	8.382	-0.045
105	Min	8.300	8.250	-0.169
105	Std Dev	0.049	0.057	0.051



9.9_T_SYNC_RISE	
Test Site	
Tester	
Test Number	
Max Limit	15 ns
Min Limit	ns

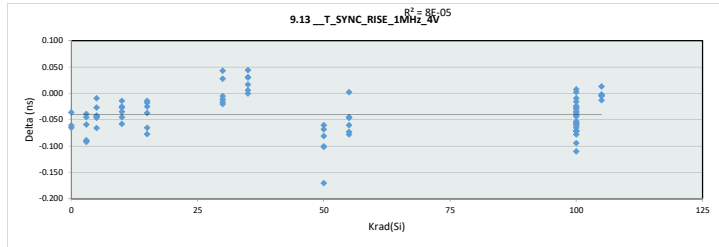
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.363	8.314	8.322	8.311	8.348	8.340	8.394	8.250	8.314	8.292	8.409
Average	8.377	8.375	8.397	8.407	8.399	8.414	8.461	8.293	8.340	8.360	8.447
Max	8.401	8.427	8.449	8.463	8.449	8.472	8.526	8.324	8.371	8.423	8.480
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



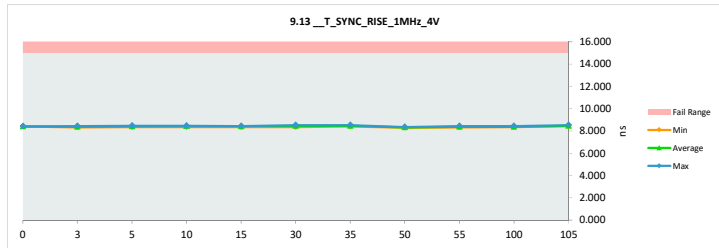
LDR TID Report
TPS7H5001-SP

9.13 T_SYNC_RISE_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.469	8.407	-0.062
0	118	8.433	8.396	-0.037
0	122	8.461	8.395	-0.066
3	61	8.493	8.401	-0.092
3	62	8.476	8.436	-0.040
3	63	8.457	8.364	-0.093
3	64	8.380	8.320	-0.060
3	65	8.470	8.424	-0.046
3	66	8.460	8.370	-0.090
5	67	8.430	8.387	-0.043
5	68	8.511	8.466	-0.045
5	69	8.460	8.450	-0.010
5	70	8.377	8.349	-0.028
5	71	8.469	8.422	-0.047
5	72	8.447	8.380	-0.067
10	73	8.493	8.467	-0.026
10	74	8.476	8.430	-0.046
10	75	8.457	8.429	-0.028
10	76	8.380	8.344	-0.036
10	77	8.470	8.411	-0.059
10	78	8.460	8.445	-0.015
15	79	8.430	8.352	-0.078
15	80	8.511	8.445	-0.066
15	81	8.460	8.445	-0.015
15	82	8.377	8.351	-0.026
15	83	8.469	8.431	-0.038
15	84	8.447	8.428	-0.019
30	86	8.493	8.487	-0.006
30	89	8.476	8.464	-0.012
30	91	8.457	8.484	0.027
30	92	8.380	8.359	-0.021
30	94	8.470	8.512	0.042
30	95	8.460	8.443	-0.017
35	100	8.430	8.446	0.016
35	101	8.511	8.510	-0.001
35	102	8.460	8.465	0.005
35	103	8.377	8.406	0.029
35	104	8.469	8.512	0.043
35	105	8.447	8.477	0.030
50	106	8.437	8.335	-0.102
50	107	8.375	8.314	-0.061
50	108	8.419	8.350	-0.069
50	109	8.393	8.291	-0.102
50	110	8.466	8.295	-0.171
50	111	8.415	8.333	-0.082
55	113	8.440	8.366	-0.074
55	114	8.437	8.391	-0.046
55	115	8.490	8.411	-0.079
55	116	8.469	8.422	-0.047
55	126	8.324	8.325	0.001
55	128	8.447	8.386	-0.061
100	129	8.408	8.365	-0.043
100	131	8.459	8.402	-0.057
100	132	8.363	8.364	0.001
100	133	8.436	8.391	-0.045
100	134	8.372	8.342	-0.030
100	135	8.434	8.361	-0.073
100	138	8.374	8.381	0.007
100	139	8.353	8.336	-0.017
100	140	8.401	8.365	-0.036
100	141	8.463	8.424	-0.039
100	142	8.444	8.408	-0.036
100	143	8.447	8.381	-0.066
100	144	8.405	8.347	-0.058
100	220	8.465	8.354	-0.111
100	221	8.412	8.333	-0.079
100	224	8.462	8.400	-0.062
100	225	8.455	8.383	-0.072
100	226	8.452	8.442	-0.010
100	228	8.464	8.392	-0.072
100	232	8.387	8.333	-0.054
100	233	8.382	8.354	-0.028
100	236	8.455	8.360	-0.095
100	238	8.448	8.423	-0.025
100	243	8.463	8.402	-0.061
105	244	8.513	8.507	-0.006
105	246	8.428	8.440	0.012
105	248	8.471	8.466	-0.005
105	374	8.475	8.461	-0.014
105	376	8.427	8.424	-0.003
105	Max	8.513	8.512	0.043
105	Average	8.441	8.401	-0.040
105	Min	8.324	8.291	-0.171
105	Std Dev	0.041	0.053	0.038



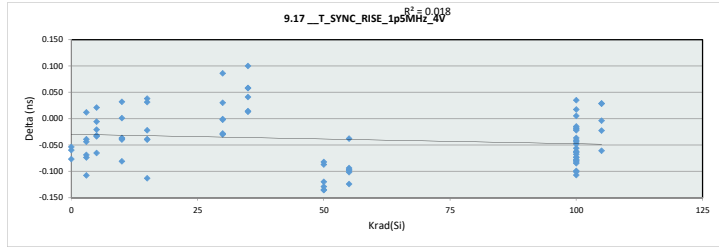
9.13 T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.395	8.320	8.349	8.344	8.351	8.359	8.406	8.291	8.325	8.333	8.424
Average	8.399	8.386	8.409	8.421	8.409	8.458	8.469	8.320	8.384	8.377	8.460
Max	8.407	8.436	8.466	8.467	8.445	8.512	8.512	8.350	8.422	8.442	8.507
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



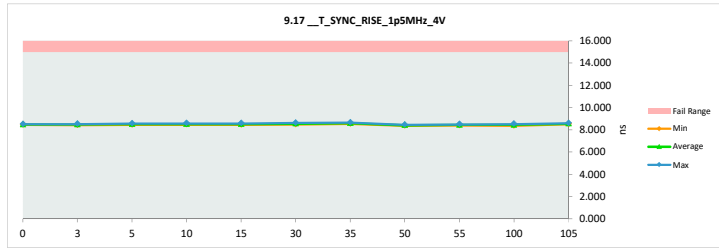
LDR TID Report
TPS7H5001-SP

9.17_T_SYNC_RISE_1p5MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.537	8.460	-0.077
0	118	8.504	8.444	-0.060
0	122	8.566	8.512	-0.054
3	61	8.617	8.509	-0.108
3	62	8.594	8.520	-0.074
3	63	8.518	8.474	-0.044
3	64	8.501	8.432	-0.069
3	65	8.528	8.489	-0.039
3	66	8.494	8.506	0.012
5	67	8.449	8.470	0.021
5	68	8.639	8.574	-0.065
5	69	8.567	8.533	-0.034
5	70	8.479	8.447	-0.032
5	71	8.541	8.520	-0.021
5	72	8.520	8.514	-0.006
10	73	8.617	8.580	-0.037
10	74	8.594	8.513	-0.081
10	75	8.518	8.519	0.001
10	76	8.501	8.463	-0.038
10	77	8.528	8.488	-0.040
10	78	8.494	8.526	0.032
15	79	8.449	8.480	0.031
15	80	8.639	8.526	-0.113
15	81	8.567	8.528	-0.039
15	82	8.479	8.439	-0.040
15	83	8.541	8.579	0.038
15	84	8.520	8.498	-0.022
30	86	8.617	8.615	-0.002
30	89	8.594	8.564	-0.030
30	91	8.518	8.548	0.030
30	92	8.501	8.473	-0.028
30	94	8.528	8.527	-0.001
30	95	8.494	8.580	0.086
35	100	8.449	8.549	0.100
35	101	8.639	8.652	0.013
35	102	8.567	8.581	0.014
35	103	8.479	8.520	0.041
35	104	8.541	8.599	0.058
35	105	8.520	8.578	0.058
50	106	8.553	8.417	-0.136
50	107	8.435	8.353	-0.082
50	108	8.513	8.426	-0.087
50	109	8.529	8.400	-0.129
50	110	8.571	8.436	-0.135
50	111	8.513	8.393	-0.120
55	113	8.519	8.425	-0.094
55	114	8.557	8.455	-0.102
55	115	8.521	8.423	-0.098
55	116	8.595	8.471	-0.124
55	126	8.489	8.392	-0.097
55	128	8.532	8.494	-0.038
100	129	8.513	8.412	-0.101
100	131	8.552	8.490	-0.062
100	132	8.488	8.466	-0.022
100	133	8.543	8.461	-0.082
100	134	8.487	8.469	-0.018
100	135	8.515	8.435	-0.080
100	138	8.453	8.412	-0.041
100	139	8.446	8.378	-0.068
100	140	8.492	8.455	-0.037
100	141	8.543	8.436	-0.107
100	142	8.534	8.470	-0.064
100	143	8.529	8.509	-0.020
100	144	8.478	8.463	-0.015
100	220	8.518	8.433	-0.085
100	221	8.480	8.381	-0.099
100	224	8.474	8.509	0.035
100	225	8.565	8.491	-0.074
100	226	8.557	8.513	-0.044
100	228	8.504	8.521	0.017
100	232	8.420	8.372	-0.048
100	233	8.484	8.489	0.005
100	236	8.544	8.471	-0.073
100	238	8.528	8.472	-0.056
100	243	8.546	8.468	-0.078
105	244	8.628	8.567	-0.061
105	246	8.516	8.544	0.028
105	248	8.566	8.543	-0.023
105	374	8.574	8.603	0.029
105	376	8.574	8.511	-0.064
105	Max	8.639	8.652	0.100
105	Average	8.528	8.489	-0.039
105	Min	8.420	8.353	-0.136
105	Std Dev	0.049	0.062	0.053



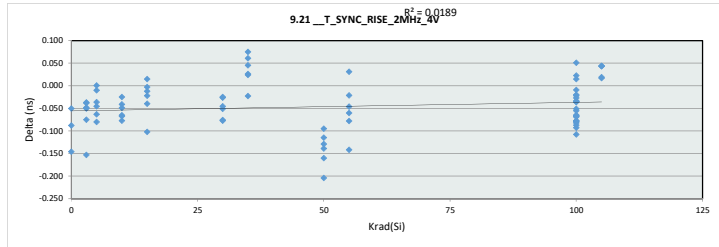
9.17_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.444	8.432	8.447	8.463	8.439	8.473	8.520	8.353	8.392	8.372	8.511
Average	8.472	8.488	8.510	8.515	8.508	8.551	8.580	8.404	8.443	8.457	8.554
Max	8.512	8.520	8.574	8.580	8.579	8.615	8.652	8.436	8.494	8.521	8.603
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



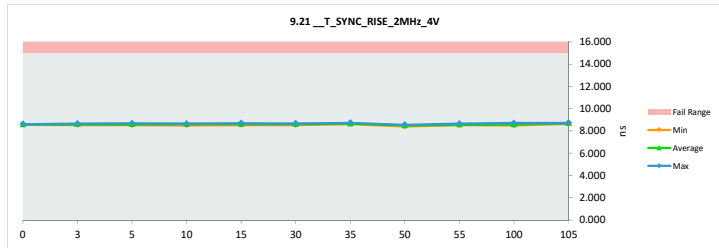
LDR TID Report
TPS7H5001-SP

9.21_T_SYNC_RISE_2MHZ_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.712	8.565	-0.147
0	118	8.650	8.561	-0.089
0	122	8.651	8.600	-0.051
3	61	8.722	8.646	-0.076
3	62	8.663	8.614	-0.049
3	63	8.699	8.545	-0.154
3	64	8.578	8.539	-0.039
3	65	8.643	8.592	-0.051
3	66	8.664	8.626	-0.038
5	67	8.577	8.566	-0.011
5	68	8.694	8.613	-0.081
5	69	8.673	8.673	0.000
5	70	8.558	8.521	-0.037
5	71	8.689	8.625	-0.064
5	72	8.637	8.591	-0.046
10	73	8.722	8.644	-0.078
10	74	8.663	8.613	-0.050
10	75	8.699	8.633	-0.066
10	76	8.578	8.510	-0.068
10	77	8.643	8.601	-0.042
10	78	8.664	8.638	-0.026
15	79	8.577	8.564	-0.013
15	80	8.694	8.653	-0.041
15	81	8.673	8.687	0.014
15	82	8.558	8.535	-0.023
15	83	8.689	8.586	-0.103
15	84	8.637	8.633	-0.004
30	86	8.722	8.644	-0.078
30	89	8.663	8.586	-0.077
30	91	8.699	8.653	-0.046
30	92	8.578	8.526	-0.052
30	94	8.643	8.616	-0.027
30	95	8.664	8.638	-0.026
35	100	8.577	8.651	0.074
35	101	8.694	8.717	0.023
35	102	8.673	8.717	0.044
35	103	8.558	8.618	0.060
35	104	8.689	8.665	-0.024
35	105	8.637	8.662	0.025
50	106	8.627	8.531	-0.096
50	107	8.553	8.413	-0.140
50	108	8.721	8.516	-0.205
50	109	8.591	8.430	-0.161
50	110	8.676	8.546	-0.130
50	111	8.657	8.541	-0.116
55	113	8.663	8.520	-0.143
55	114	8.645	8.584	-0.061
55	115	8.646	8.567	-0.079
55	116	8.689	8.642	-0.047
55	126	8.489	8.519	0.030
55	128	8.597	8.575	-0.022
100	129	8.580	8.545	-0.035
100	131	8.644	8.556	-0.088
100	132	8.588	8.505	-0.083
100	133	8.654	8.584	-0.070
100	134	8.615	8.537	-0.078
100	135	8.615	8.535	-0.080
100	138	8.575	8.565	-0.010
100	139	8.579	8.551	-0.028
100	140	8.625	8.588	-0.037
100	141	8.642	8.576	-0.066
100	142	8.655	8.586	-0.069
100	143	8.627	8.677	0.050
100	144	8.582	8.561	-0.021
100	220	8.638	8.559	-0.079
100	221	8.588	8.536	-0.052
100	224	8.581	8.546	-0.035
100	225	8.629	8.643	0.014
100	226	8.679	8.623	-0.056
100	228	8.698	8.604	-0.094
100	232	8.576	8.540	-0.036
100	233	8.627	8.561	-0.066
100	236	8.709	8.600	-0.109
100	238	8.673	8.695	0.022
100	243	8.631	8.608	-0.023
105	244	8.675	8.691	0.016
105	246	8.631	8.673	0.042
105	248	8.673	8.716	0.043
105	374	8.695	8.713	0.018
105	376	8.595	8.638	0.043
105	Max	8.722	8.717	0.074
105	Average	8.640	8.595	-0.046
105	Min	8.489	8.413	-0.205
105	Std Dev	0.050	0.062	0.055



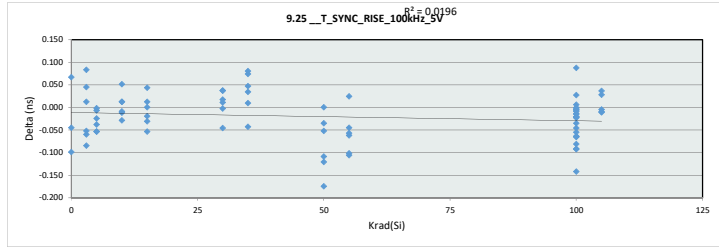
9.21_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.561	8.539	8.521	8.510	8.535	8.526	8.618	8.413	8.519	8.505	8.638
Average	8.575	8.594	8.598	8.607	8.610	8.611	8.672	8.496	8.568	8.578	8.686
Max	8.600	8.646	8.673	8.644	8.687	8.653	8.717	8.546	8.642	8.695	8.716
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



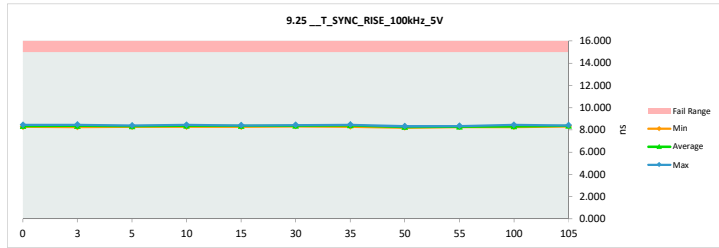
LDR TID Report
TPS7H5001-SP

9.25_T_SYNC_RISE_100kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.360	8.427	0.067
0	118	8.314	8.269	-0.045
0	122	8.357	8.258	-0.099
3	61	8.374	8.386	0.012
3	62	8.322	8.367	0.045
3	63	8.355	8.438	0.083
3	64	8.283	8.223	-0.060
3	65	8.394	8.309	-0.085
3	66	8.360	8.308	-0.052
5	67	8.293	8.268	-0.025
5	68	8.376	8.369	-0.007
5	69	8.371	8.369	-0.002
5	70	8.307	8.253	-0.054
5	71	8.388	8.334	-0.054
5	72	8.337	8.299	-0.038
10	73	8.374	8.425	0.051
10	74	8.322	8.334	0.012
10	75	8.355	8.343	-0.012
10	76	8.283	8.254	-0.029
10	77	8.394	8.385	-0.009
10	78	8.360	8.372	0.012
15	79	8.293	8.336	0.043
15	80	8.376	8.376	0.000
15	81	8.371	8.383	0.012
15	82	8.307	8.253	-0.054
15	83	8.388	8.357	-0.031
15	84	8.337	8.317	-0.020
30	86	8.374	8.411	0.037
30	89	8.322	8.319	-0.003
30	91	8.355	8.309	-0.046
30	92	8.283	8.300	0.017
30	94	8.394	8.405	0.011
30	95	8.360	8.397	0.037
35	100	8.293	8.373	0.080
35	101	8.376	8.410	0.034
35	102	8.371	8.445	0.074
35	103	8.307	8.264	-0.043
35	104	8.388	8.397	0.009
35	105	8.337	8.384	0.047
50	106	8.380	8.205	-0.175
50	107	8.248	8.213	-0.035
50	108	8.368	8.316	-0.052
50	109	8.308	8.187	-0.121
50	110	8.392	8.283	-0.109
50	111	8.279	8.279	0.000
55	113	8.347	8.241	-0.106
55	114	8.339	8.237	-0.102
55	115	8.348	8.303	-0.045
55	116	8.381	8.324	-0.057
55	126	8.226	8.250	0.024
55	128	8.359	8.297	-0.062
100	129	8.309	8.245	-0.064
100	131	8.341	8.319	-0.022
100	132	8.268	8.247	-0.021
100	133	8.369	8.303	-0.066
100	134	8.294	8.321	0.027
100	135	8.320	8.326	0.006
100	138	8.337	8.322	-0.015
100	139	8.257	8.251	-0.006
100	140	8.341	8.260	-0.081
100	141	8.403	8.311	-0.092
100	142	8.319	8.304	-0.015
100	143	8.389	8.296	-0.093
100	144	8.293	8.270	-0.023
100	220	8.323	8.268	-0.055
100	221	8.273	8.238	-0.035
100	224	8.448	8.306	-0.142
100	225	8.371	8.350	-0.021
100	226	8.339	8.426	0.087
100	228	8.326	8.317	-0.009
100	232	8.298	8.234	-0.064
100	233	8.292	8.291	-0.001
100	236	8.362	8.316	-0.046
100	238	8.325	8.320	-0.005
100	243	8.334	8.333	-0.001
105	244	8.383	8.378	-0.005
105	246	8.325	8.315	-0.010
105	248	8.376	8.365	-0.011
105	374	8.346	8.382	0.036
105	376	8.315	8.343	0.028
105	Max	8.448	8.445	0.087
105	Average	8.340	8.319	-0.021
105	Min	8.226	8.187	-0.175
105	Std Dev	0.041	0.060	0.052



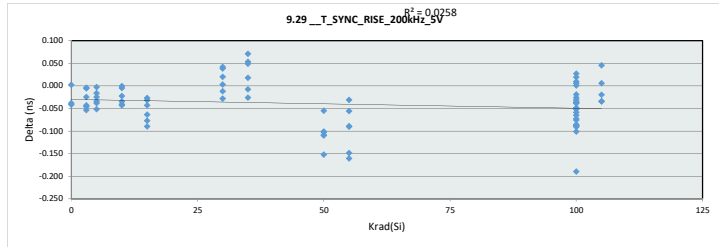
9.25_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.258	8.223	8.253	8.254	8.253	8.300	8.264	8.187	8.237	8.234	8.315
Average	8.318	8.339	8.315	8.352	8.337	8.357	8.379	8.247	8.275	8.299	8.357
Max	8.427	8.438	8.369	8.425	8.383	8.411	8.445	8.316	8.324	8.426	8.382
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



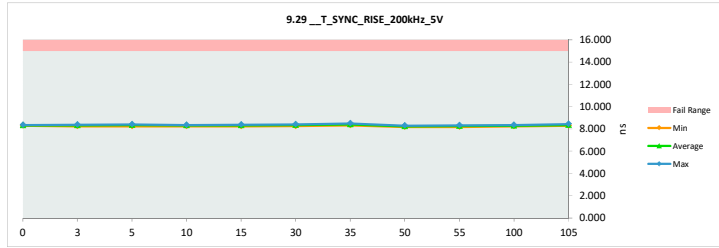
LDR TID Report
TPS7H5001-SP

9.29_T_SYNC_RISE_200kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.331	8.332	0.001
0	118	8.352	8.309	-0.043
0	122	8.323	8.284	-0.039
3	61	8.372	8.365	-0.007
3	62	8.370	8.326	-0.044
3	63	8.370	8.315	-0.055
3	64	8.272	8.225	-0.047
3	65	8.333	8.307	-0.026
3	66	8.348	8.343	-0.005
5	67	8.334	8.317	-0.017
5	68	8.408	8.404	-0.004
5	69	8.415	8.376	-0.039
5	70	8.262	8.236	-0.026
5	71	8.375	8.322	-0.053
5	72	8.407	8.372	-0.035
10	73	8.372	8.336	-0.036
10	74	8.370	8.328	-0.042
10	75	8.370	8.346	-0.024
10	76	8.272	8.228	-0.044
10	77	8.333	8.331	-0.002
10	78	8.348	8.342	-0.006
15	79	8.334	8.290	-0.044
15	80	8.408	8.375	-0.033
15	81	8.415	8.337	-0.078
15	82	8.262	8.234	-0.028
15	83	8.375	8.310	-0.065
15	84	8.407	8.316	-0.091
30	86	8.372	8.413	0.041
30	89	8.370	8.357	-0.013
30	91	8.370	8.372	0.002
30	92	8.272	8.243	-0.029
30	94	8.333	8.370	0.037
30	95	8.348	8.367	0.019
35	100	8.334	8.351	0.017
35	101	8.408	8.478	0.070
35	102	8.415	8.406	-0.009
35	103	8.262	8.310	0.048
35	104	8.375	8.427	0.052
35	105	8.407	8.380	-0.027
50	106	8.373	8.220	-0.153
50	107	8.280	8.170	-0.110
50	108	8.360	8.249	-0.111
50	109	8.265	8.209	-0.056
50	110	8.385	8.281	-0.104
50	111	8.351	8.249	-0.102
55	113	8.392	8.242	-0.150
55	114	8.357	8.300	-0.057
55	115	8.391	8.229	-0.162
55	116	8.395	8.304	-0.091
55	126	8.269	8.178	-0.091
55	128	8.358	8.326	-0.032
100	129	8.409	8.218	-0.191
100	131	8.366	8.313	-0.053
100	132	8.303	8.266	-0.037
100	133	8.362	8.302	-0.060
100	134	8.309	8.282	-0.027
100	135	8.345	8.256	-0.089
100	138	8.316	8.284	-0.032
100	139	8.253	8.271	0.018
100	140	8.317	8.251	-0.066
100	141	8.395	8.318	-0.077
100	142	8.360	8.258	-0.102
100	143	8.344	8.349	0.005
100	144	8.310	8.290	-0.020
100	220	8.328	8.354	0.026
100	221	8.262	8.262	0.000
100	224	8.363	8.289	-0.074
100	225	8.392	8.355	-0.037
100	226	8.404	8.353	-0.051
100	228	8.379	8.292	-0.087
100	232	8.308	8.216	-0.092
100	233	8.305	8.313	0.008
100	236	8.359	8.309	-0.050
100	238	8.410	8.319	-0.091
100	243	8.343	8.303	-0.040
105	244	8.380	8.424	0.044
105	246	8.319	8.298	-0.021
105	248	8.370	8.375	0.005
105	374	8.372	8.336	-0.036
105	376	8.360	8.325	-0.035
105	Max	8.415	8.478	0.070
105	Average	8.350	8.310	-0.040
105	Min	8.253	8.170	-0.191
105	Std Dev	0.044	0.059	0.049



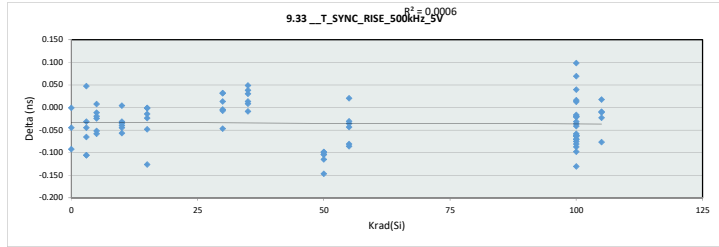
9.29_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.284	8.225	8.236	8.228	8.234	8.243	8.310	8.170	8.178	8.216	8.298
Average	8.308	8.314	8.338	8.319	8.310	8.354	8.392	8.230	8.263	8.293	8.352
Max	8.332	8.365	8.404	8.346	8.375	8.413	8.478	8.281	8.326	8.355	8.424
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



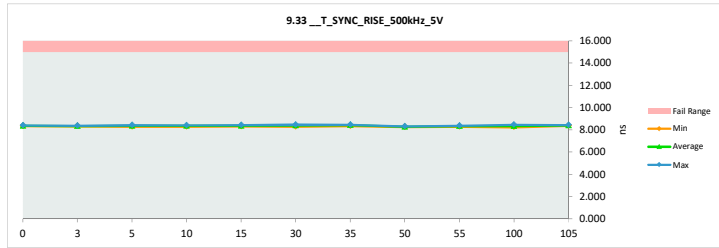
LDR TID Report
TPS7H5001-SP

9.33 T_SYNC_RISE_500kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.419	8.374	-0.045
0	118	8.395	8.394	-0.001
0	122	8.425	8.332	-0.093
3	61	8.432	8.366	-0.066
3	62	8.395	8.350	-0.045
3	63	8.368	8.336	-0.032
3	64	8.273	8.320	0.047
3	65	8.423	8.317	-0.106
3	66	8.429	8.323	-0.106
5	67	8.357	8.332	-0.025
5	68	8.439	8.427	-0.012
5	69	8.449	8.390	-0.059
5	70	8.295	8.275	-0.020
5	71	8.434	8.382	-0.052
5	72	8.377	8.384	0.007
10	73	8.432	8.400	-0.032
10	74	8.395	8.360	-0.035
10	75	8.368	8.323	-0.045
10	76	8.273	8.277	0.004
10	77	8.423	8.383	-0.040
10	78	8.429	8.372	-0.057
15	79	8.357	8.355	-0.002
15	80	8.439	8.390	-0.049
15	81	8.449	8.322	-0.127
15	82	8.295	8.294	-0.001
15	83	8.434	8.419	-0.015
15	84	8.377	8.353	-0.024
30	86	8.432	8.445	0.013
30	89	8.395	8.387	-0.008
30	91	8.368	8.399	0.031
30	92	8.273	8.268	-0.005
30	94	8.423	8.376	-0.047
30	95	8.429	8.460	0.031
35	100	8.357	8.395	0.038
35	101	8.439	8.452	0.013
35	102	8.449	8.440	-0.009
35	103	8.295	8.325	0.030
35	104	8.434	8.442	0.008
35	105	8.377	8.425	0.048
50	106	8.357	8.253	-0.104
50	107	8.334	8.235	-0.099
50	108	8.376	8.277	-0.099
50	109	8.337	8.232	-0.105
50	110	8.442	8.295	-0.147
50	111	8.406	8.291	-0.115
55	113	8.428	8.347	-0.081
55	114	8.369	8.333	-0.036
55	115	8.348	8.368	0.020
55	116	8.407	8.321	-0.086
55	126	8.327	8.283	-0.044
55	128	8.353	8.322	-0.031
100	129	8.314	8.326	0.012
100	131	8.372	8.330	-0.042
100	132	8.334	8.313	-0.021
100	133	8.347	8.445	0.098
100	134	8.332	8.261	-0.071
100	135	8.426	8.328	-0.098
100	138	8.331	8.311	-0.020
100	139	8.331	8.268	-0.063
100	140	8.358	8.326	-0.032
100	141	8.374	8.303	-0.071
100	142	8.399	8.318	-0.081
100	143	8.426	8.367	-0.059
100	144	8.321	8.304	-0.017
100	220	8.347	8.386	0.039
100	221	8.344	8.256	-0.088
100	224	8.303	8.372	0.069
100	225	8.398	8.377	-0.021
100	226	8.400	8.416	0.016
100	228	8.416	8.354	-0.062
100	232	8.298	8.234	-0.064
100	233	8.366	8.290	-0.076
100	236	8.398	8.361	-0.037
100	238	8.411	8.280	-0.131
100	243	8.421	8.359	-0.062
105	244	8.431	8.408	-0.023
105	246	8.447	8.370	-0.077
105	248	8.371	8.388	0.017
105	374	8.407	8.397	-0.010
105	376	8.413	8.402	-0.011
105	Max	8.449	8.460	0.098
105	Average	8.382	8.347	-0.035
105	Min	8.273	8.232	-0.147
105	Std Dev	0.048	0.056	0.049



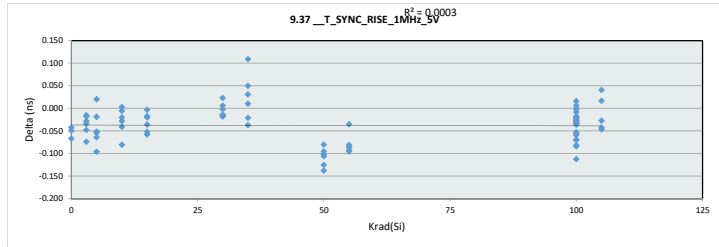
9.33 T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.332	8.317	8.275	8.277	8.294	8.268	8.325	8.232	8.283	8.234	8.370
Average	8.367	8.335	8.365	8.353	8.356	8.389	8.413	8.264	8.329	8.329	8.393
Max	8.394	8.366	8.427	8.400	8.419	8.460	8.452	8.295	8.368	8.445	8.408
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



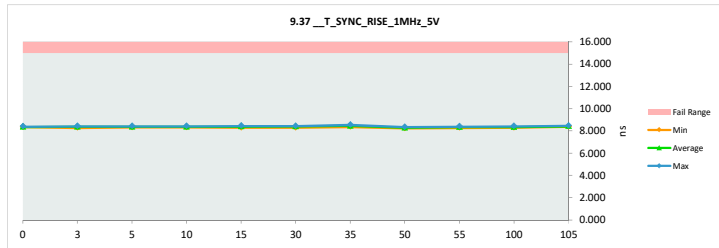
LDR TID Report
TPS7H5001-SP

9.37_T_SYNC_RISE_1MHZ_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.443	8.393	-0.050
0	118	8.416	8.348	-0.068
0	122	8.411	8.368	-0.043
3	61	8.435	8.419	-0.016
3	62	8.425	8.376	-0.049
3	63	8.444	8.369	-0.075
3	64	8.312	8.283	-0.029
3	65	8.413	8.378	-0.035
3	66	8.427	8.408	-0.019
5	67	8.367	8.386	0.019
5	68	8.484	8.419	-0.065
5	69	8.474	8.419	-0.055
5	70	8.338	8.318	-0.020
5	71	8.484	8.387	-0.097
5	72	8.424	8.372	-0.052
10	73	8.435	8.406	-0.029
10	74	8.425	8.404	-0.021
10	75	8.444	8.363	-0.081
10	76	8.312	8.314	0.002
10	77	8.413	8.371	-0.042
10	78	8.427	8.421	-0.006
15	79	8.367	8.363	-0.004
15	80	8.484	8.431	-0.053
15	81	8.474	8.456	-0.018
15	82	8.338	8.301	-0.037
15	83	8.484	8.425	-0.059
15	84	8.424	8.403	-0.021
30	86	8.435	8.440	0.005
30	89	8.425	8.422	-0.003
30	91	8.444	8.429	-0.015
30	92	8.312	8.293	-0.019
30	94	8.413	8.435	0.022
30	95	8.427	8.410	-0.017
35	100	8.367	8.475	0.108
35	101	8.484	8.533	0.049
35	102	8.474	8.436	-0.038
35	103	8.338	8.368	0.030
35	104	8.484	8.462	-0.022
35	105	8.424	8.433	0.009
50	106	8.425	8.286	-0.139
50	107	8.358	8.251	-0.107
50	108	8.420	8.294	-0.126
50	109	8.313	8.232	-0.081
50	110	8.433	8.337	-0.096
50	111	8.431	8.328	-0.103
55	113	8.436	8.348	-0.088
55	114	8.444	8.357	-0.087
55	115	8.466	8.370	-0.096
55	116	8.428	8.392	-0.036
55	126	8.372	8.278	-0.094
55	128	8.424	8.342	-0.082
100	129	8.343	8.306	-0.037
100	131	8.425	8.371	-0.054
100	132	8.316	8.296	-0.020
100	133	8.383	8.382	-0.001
100	134	8.384	8.313	-0.071
100	135	8.375	8.390	0.015
100	138	8.343	8.324	-0.019
100	139	8.377	8.321	-0.056
100	140	8.399	8.286	-0.113
100	141	8.397	8.388	-0.009
100	142	8.401	8.372	-0.029
100	143	8.406	8.379	-0.027
100	144	8.365	8.331	-0.034
100	220	8.384	8.324	-0.060
100	221	8.361	8.326	-0.035
100	224	8.363	8.369	0.006
100	225	8.419	8.395	-0.024
100	226	8.447	8.417	-0.030
100	228	8.439	8.418	-0.021
100	232	8.341	8.338	-0.003
100	233	8.387	8.353	-0.034
100	236	8.429	8.346	-0.083
100	238	8.439	8.354	-0.085
100	243	8.427	8.356	-0.071
105	244	8.438	8.410	-0.028
105	246	8.470	8.426	-0.044
105	248	8.440	8.456	0.016
105	374	8.418	8.458	0.040
105	376	8.420	8.373	-0.047
105	Max	8.484	8.533	0.108
105	Average	8.410	8.373	-0.038
105	Min	8.312	8.232	-0.139
105	Std Dev	0.046	0.055	0.042



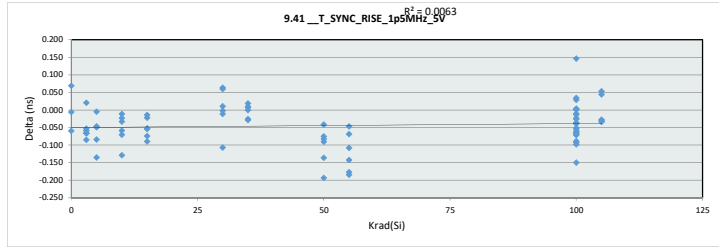
9.37_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.348	8.283	8.318	8.314	8.301	8.293	8.368	8.232	8.278	8.286	8.373
Average	8.370	8.372	8.384	8.380	8.397	8.405	8.451	8.288	8.348	8.352	8.425
Max	8.393	8.419	8.419	8.421	8.456	8.440	8.533	8.337	8.392	8.418	8.458
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



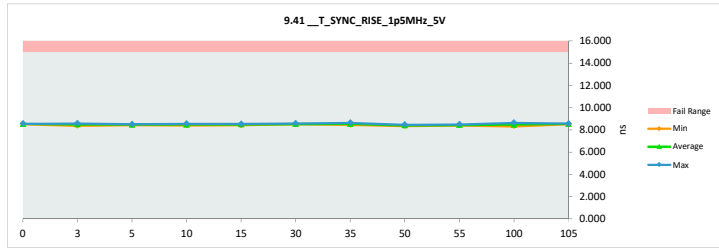
LDR TID Report
TPS7H5001-SP

9.41 T_SYNC_RISE_1p5MHz_5V			
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15		
Min Limit	15		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.563	8.503	-0.060
0	118	8.501	8.495	-0.006
0	122	8.456	8.524	0.068
3	61	8.538	8.558	0.020
3	62	8.598	8.539	-0.059
3	63	8.493	8.407	-0.086
3	64	8.422	8.356	-0.066
3	65	8.492	8.425	-0.067
3	66	8.544	8.490	-0.054
5	67	8.462	8.457	-0.005
5	68	8.606	8.470	-0.136
5	69	8.531	8.482	-0.049
5	70	8.447	8.398	-0.049
5	71	8.560	8.475	-0.085
5	72	8.495	8.448	-0.047
10	73	8.538	8.526	-0.012
10	74	8.598	8.469	-0.129
10	75	8.493	8.434	-0.059
10	76	8.422	8.388	-0.034
10	77	8.492	8.468	-0.024
10	78	8.544	8.473	-0.071
15	79	8.462	8.439	-0.023
15	80	8.606	8.516	-0.090
15	81	8.531	8.476	-0.055
15	82	8.447	8.394	-0.053
15	83	8.560	8.485	-0.075
15	84	8.495	8.480	-0.015
30	86	8.538	8.548	0.010
30	89	8.598	8.491	-0.107
30	91	8.493	8.490	-0.003
30	92	8.422	8.485	0.063
30	94	8.492	8.551	0.059
30	95	8.544	8.532	-0.012
35	100	8.462	8.436	-0.026
35	101	8.606	8.612	0.006
35	102	8.531	8.540	0.009
35	103	8.447	8.465	0.018
35	104	8.560	8.531	-0.029
35	105	8.495	8.494	-0.001
50	106	8.553	8.359	-0.194
50	107	8.412	8.329	-0.083
50	108	8.534	8.443	-0.091
50	109	8.491	8.354	-0.137
50	110	8.451	8.409	-0.042
50	111	8.475	8.399	-0.076
55	113	8.562	8.377	-0.185
55	114	8.489	8.381	-0.108
55	115	8.576	8.433	-0.143
55	116	8.509	8.440	-0.069
55	126	8.568	8.390	-0.178
55	128	8.519	8.472	-0.047
100	129	8.456	8.432	-0.024
100	131	8.544	8.446	-0.098
100	132	8.468	8.318	-0.150
100	133	8.472	8.475	0.003
100	134	8.395	8.429	0.034
100	135	8.485	8.423	-0.062
100	138	8.456	8.430	-0.026
100	139	8.460	8.408	-0.052
100	140	8.476	8.421	-0.055
100	141	8.554	8.491	-0.063
100	142	8.464	8.610	0.146
100	143	8.489	8.453	-0.036
100	144	8.485	8.396	-0.089
100	220	8.466	8.452	-0.014
100	221	8.501	8.408	-0.093
100	224	8.483	8.486	0.003
100	225	8.543	8.476	-0.067
100	226	8.530	8.490	-0.040
100	228	8.523	8.454	-0.069
100	232	8.411	8.439	0.028
100	233	8.434	8.423	-0.011
100	236	8.530	8.468	-0.062
100	238	8.579	8.507	-0.072
100	243	8.465	8.465	0.000
105	244	8.525	8.494	-0.031
105	246	8.509	8.481	-0.028
105	248	8.564	8.529	-0.035
105	374	8.473	8.526	0.053
105	376	8.496	8.540	0.044
105	Max	8.606	8.612	0.146
105	Average	8.506	8.461	-0.044
105	Min	8.395	8.318	-0.194
105	Std Dev	0.051	0.058	0.059



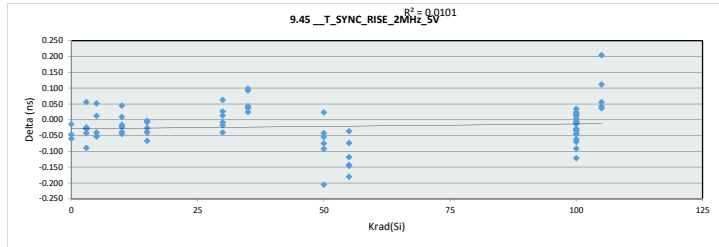
9.41 T_SYNC_R												
Test Site												
Tester												
Test Number												
Max Limit	15	ns										
Min Limit	15	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	8.495	8.356	8.398	8.388	8.394	8.485	8.436	8.329	8.377	8.318	8.481	
Average	8.507	8.463	8.455	8.460	8.465	8.516	8.513	8.382	8.416	8.450	8.514	
Max	8.524	8.558	8.482	8.526	8.516	8.551	8.612	8.443	8.472	8.610	8.540	
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	



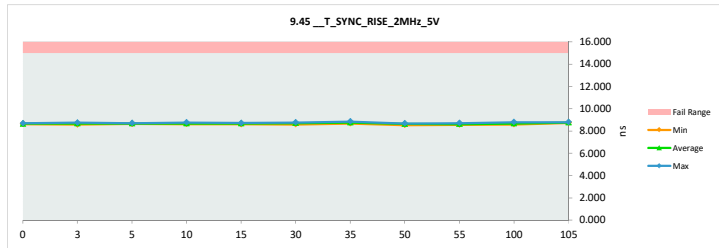
LDR TID Report
TPS7H5001-SP

9.45_T_SYNC_RISE_2MHz_5V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	15	15
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.672	8.611	-0.061
0	118	8.720	8.672	-0.048
0	122	8.659	8.643	-0.016
3	61	8.707	8.681	-0.026
3	62	8.676	8.730	0.054
3	63	8.665	8.574	-0.091
3	64	8.600	8.556	-0.044
3	65	8.701	8.670	-0.031
3	66	8.734	8.703	-0.031
5	67	8.655	8.613	-0.042
5	68	8.748	8.695	-0.053
5	69	8.716	8.662	-0.054
5	70	8.626	8.637	0.011
5	71	8.711	8.657	-0.054
5	72	8.646	8.696	0.050
10	73	8.707	8.750	0.043
10	74	8.676	8.636	-0.040
10	75	8.665	8.619	-0.046
10	76	8.600	8.608	0.008
10	77	8.701	8.683	-0.018
10	78	8.734	8.709	-0.025
15	79	8.655	8.613	-0.042
15	80	8.748	8.680	-0.068
15	81	8.716	8.688	-0.028
15	82	8.626	8.586	-0.040
15	83	8.711	8.701	-0.010
15	84	8.646	8.641	-0.005
30	86	8.707	8.697	-0.010
30	89	8.676	8.737	0.061
30	91	8.665	8.677	0.012
30	92	8.600	8.581	-0.019
30	94	8.701	8.726	0.025
30	95	8.734	8.692	-0.042
35	100	8.655	8.752	0.097
35	101	8.748	8.839	0.091
35	102	8.716	8.757	0.041
35	103	8.626	8.649	0.023
35	104	8.711	8.748	0.037
35	105	8.646	8.682	0.036
50	106	8.686	8.610	-0.076
50	107	8.718	8.511	-0.207
50	108	8.630	8.652	0.022
50	109	8.699	8.643	-0.056
50	110	8.713	8.669	-0.044
50	111	8.703	8.609	-0.094
55	113	8.725	8.687	-0.038
55	114	8.776	8.629	-0.147
55	115	8.752	8.608	-0.144
55	116	8.766	8.584	-0.182
55	126	8.659	8.539	-0.120
55	128	8.671	8.596	-0.075
100	129	8.651	8.669	0.018
100	131	8.701	8.630	-0.071
100	132	8.671	8.608	-0.063
100	133	8.712	8.677	-0.035
100	134	8.642	8.665	0.023
100	135	8.661	8.568	-0.093
100	138	8.665	8.635	-0.030
100	139	8.644	8.630	-0.014
100	140	8.659	8.644	-0.015
100	141	8.693	8.694	0.001
100	142	8.733	8.610	-0.123
100	143	8.680	8.713	0.033
100	144	8.655	8.675	0.020
100	220	8.688	8.675	-0.013
100	221	8.648	8.614	-0.034
100	224	8.653	8.645	-0.008
100	225	8.729	8.716	-0.013
100	226	8.772	8.783	0.011
100	228	8.747	8.739	-0.008
100	232	8.650	8.664	0.014
100	233	8.663	8.617	-0.046
100	236	8.720	8.675	-0.045
100	238	8.715	8.679	-0.036
100	243	8.745	8.698	-0.047
105	244	8.667	8.777	0.110
105	246	8.701	8.755	0.054
105	248	8.702	8.743	0.041
105	374	8.576	8.779	0.203
105	376	8.674	8.709	0.035
105	Max	8.776	8.839	0.203
105	Average	8.687	8.666	-0.021
105	Min	8.576	8.511	-0.207
105	Std Dev	0.043	0.061	0.063



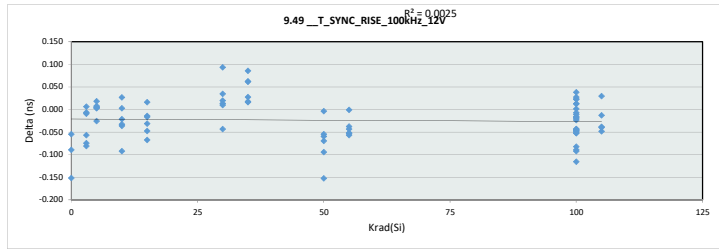
9.45_T_SYNC_R												
Test Site												
Tester												
Test Number												
Max Limit	15	ns										
Min Limit		ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	8.611	8.556	8.613	8.608	8.586	8.581	8.649	8.511	8.539	8.568	8.709	
Average	8.642	8.652	8.660	8.668	8.652	8.685	8.738	8.616	8.607	8.663	8.753	
Max	8.672	8.730	8.696	8.750	8.701	8.737	8.839	8.669	8.687	8.783	8.779	
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	



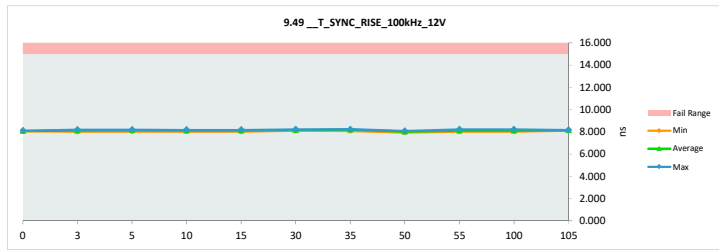
LDR TID Report TPS7H5001-SP

9.49_T_SYNC_RISE_100kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.254	8.102	-0.152
0	118	8.087	8.032	-0.055
0	122	8.197	8.107	-0.090
3	61	8.168	8.087	-0.081
3	62	8.172	8.162	-0.010
3	63	8.195	8.188	-0.007
3	64	8.077	8.002	-0.075
3	65	8.004	8.010	0.006
3	66	8.193	8.136	-0.057
5	67	8.130	8.134	0.004
5	68	8.188	8.195	0.007
5	69	8.200	8.174	-0.026
5	70	7.998	8.003	0.005
5	71	8.143	8.145	0.002
5	72	8.143	8.161	0.018
10	73	8.168	8.131	-0.037
10	74	8.172	8.174	0.002
10	75	8.195	8.102	-0.093
10	76	8.077	8.055	-0.022
10	77	8.004	8.030	0.026
10	78	8.193	8.160	-0.033
15	79	8.130	8.062	-0.068
15	80	8.188	8.173	-0.015
15	81	8.200	8.168	-0.032
15	82	7.998	8.014	0.016
15	83	8.143	8.126	-0.017
15	84	8.143	8.095	-0.048
30	86	8.168	8.124	-0.044
30	89	8.172	8.206	0.034
30	91	8.195	8.204	0.009
30	92	8.077	8.096	0.019
30	94	8.004	8.097	0.093
30	95	8.193	8.206	0.013
35	100	8.130	8.191	0.061
35	101	8.188	8.215	0.027
35	102	8.200	8.216	0.016
35	103	7.998	8.060	0.062
35	104	8.143	8.228	0.085
35	105	8.143	8.160	0.017
50	106	8.143	8.073	-0.070
50	107	8.083	7.930	-0.153
50	108	8.090	8.030	-0.060
50	109	8.191	8.046	-0.055
50	110	8.032	8.028	-0.004
50	111	8.160	8.065	-0.095
55	113	8.043	8.005	-0.038
55	114	8.191	8.147	-0.044
55	115	8.183	8.129	-0.054
55	116	8.180	8.123	-0.057
55	126	8.076	8.024	-0.052
55	128	8.223	8.222	-0.001
100	129	8.056	8.012	-0.044
100	131	8.219	8.136	-0.083
100	132	8.118	8.110	-0.008
100	133	8.038	8.050	0.012
100	134	8.075	8.097	0.022
100	135	8.191	8.175	-0.016
100	138	8.133	8.086	-0.047
100	139	8.125	8.035	-0.090
100	140	8.120	8.071	-0.049
100	141	8.237	8.226	-0.011
100	142	8.145	8.146	0.001
100	143	8.161	8.143	-0.018
100	144	8.122	8.134	0.012
100	220	8.165	8.141	-0.024
100	221	8.153	8.132	-0.021
100	224	8.210	8.094	-0.116
100	225	8.062	8.016	-0.046
100	226	8.117	8.144	0.027
100	228	8.135	8.083	-0.052
100	232	8.023	8.046	0.023
100	233	8.099	8.055	-0.044
100	236	8.153	8.100	-0.053
100	238	8.266	8.173	-0.093
100	243	8.115	8.153	0.038
105	244	8.163	8.123	-0.040
105	246	8.153	8.140	-0.013
105	248	8.133	8.162	0.029
105	374	8.152	8.113	-0.039
105	376	8.157	8.108	-0.049
105	Max	8.266	8.228	0.038
105	Average	8.136	8.112	-0.024
105	Min	7.998	7.930	-0.153
105	Std Dev	0.064	0.065	0.047



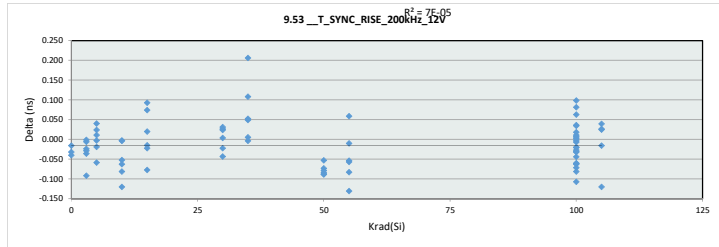
9.49_T_SYNC_R	
Test Site	
Tester	
Test Number	
Max Limit	15 ns
Min Limit	ns
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	8.032 8.002 8.003 8.030 8.014 8.096 8.060 7.930 8.005 8.012 8.108
Average	8.080 8.098 8.135 8.109 8.106 8.156 8.178 8.029 8.108 8.107 8.129
Max	8.107 8.188 8.195 8.174 8.173 8.206 8.228 8.073 8.222 8.226 8.162
UL	15.000 15.000 15.000 15.000 15.000 15.000 15.000 15.000 15.000 15.000 15.000



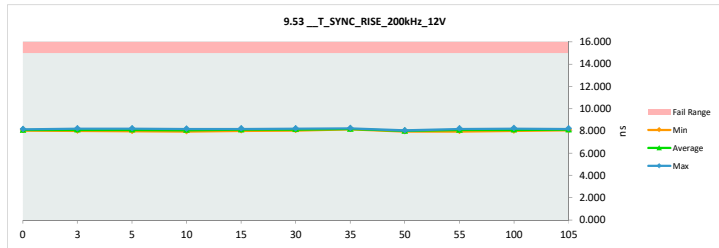
LDR TID Report
TPS7H5001-SP

9.53 T_SYNC_RISE_200kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.189	8.156	-0.033
0	118	8.068	8.047	-0.041
0	122	8.112	8.095	-0.017
3	61	8.149	8.112	-0.037
3	62	8.175	8.150	-0.025
3	63	8.208	8.201	-0.007
3	64	8.093	8.000	-0.093
3	65	8.038	8.008	-0.030
3	66	8.139	8.137	-0.002
5	67	8.103	8.083	-0.020
5	68	8.186	8.196	0.010
5	69	8.175	8.171	-0.004
5	70	7.945	7.968	0.023
5	71	8.197	8.137	-0.060
5	72	8.052	8.091	0.039
10	73	8.149	8.096	-0.053
10	74	8.175	8.171	-0.004
10	75	8.208	8.144	-0.064
10	76	8.093	7.972	-0.121
10	77	8.038	7.956	-0.082
10	78	8.139	8.134	-0.005
15	79	8.103	8.087	-0.016
15	80	8.186	8.163	-0.023
15	81	8.175	8.194	0.019
15	82	7.945	8.018	0.073
15	83	8.197	8.119	-0.078
15	84	8.052	8.144	0.092
30	86	8.149	8.152	0.003
30	89	8.175	8.198	0.023
30	91	8.208	8.185	-0.023
30	92	8.093	8.049	-0.044
30	94	8.038	8.063	0.025
30	95	8.139	8.169	0.030
35	100	8.103	8.154	0.051
35	101	8.186	8.190	0.004
35	102	8.175	8.223	0.048
35	103	7.945	8.150	0.205
35	104	8.197	8.192	-0.005
35	105	8.052	8.159	0.107
50	106	8.105	8.016	-0.089
50	107	8.012	7.938	-0.074
50	108	8.093	8.007	-0.086
50	109	8.112	8.041	-0.090
50	110	8.064	8.010	-0.054
50	111	8.099	8.019	-0.080
55	113	8.093	7.961	-0.132
55	114	8.206	8.151	-0.055
55	115	8.235	8.151	-0.084
55	116	8.153	8.142	-0.011
55	126	8.051	7.993	-0.058
55	128	8.135	8.193	0.058
100	129	8.093	8.011	-0.082
100	131	8.133	8.136	0.003
100	132	8.113	8.085	-0.028
100	133	8.086	8.053	-0.033
100	134	8.115	8.053	-0.062
100	135	8.164	8.164	0.000
100	138	8.106	8.123	0.017
100	139	8.070	8.006	-0.064
100	140	8.097	8.104	0.007
100	141	8.184	8.162	-0.022
100	142	8.159	8.194	0.035
100	143	8.152	8.162	0.010
100	144	8.129	8.121	-0.008
100	220	8.106	8.186	0.080
100	221	8.155	8.123	-0.032
100	224	8.122	8.101	-0.021
100	225	8.059	8.014	-0.045
100	226	8.123	8.050	-0.073
100	228	8.048	8.145	0.097
100	232	8.030	8.063	0.033
100	233	8.119	8.058	-0.061
100	236	8.122	8.014	-0.108
100	238	8.155	8.217	0.062
100	243	8.095	8.090	-0.005
105	244	8.215	8.094	-0.121
105	246	8.152	8.190	0.038
105	248	8.085	8.109	0.024
105	374	8.078	8.061	-0.017
105	376	8.137	8.162	0.025
105	375	8.235	8.223	0.205
	Max	8.119	8.104	-0.016
	Average	8.119	8.104	-0.016
	Min	7.945	7.938	-0.132
	Std Dev	0.062	0.073	0.058



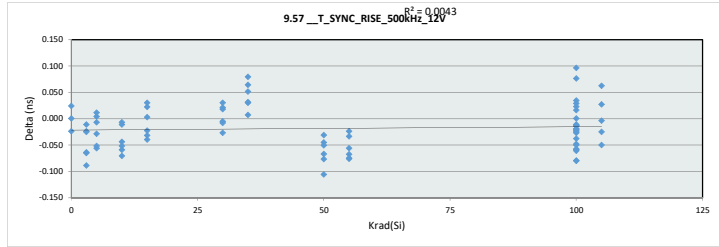
9.53 T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.047	8.000	7.968	7.956	8.018	8.049	8.150	7.938	7.961	8.006	8.061
Average	8.099	8.101	8.108	8.079	8.121	8.136	8.178	8.005	8.099	8.101	8.123
Max	8.156	8.201	8.196	8.171	8.194	8.198	8.223	8.041	8.193	8.217	8.190
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



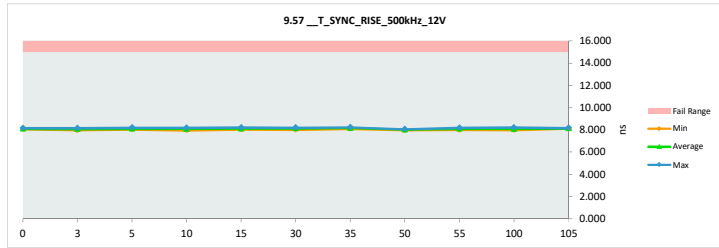
LDR TID Report
TPS7H5001-SP

9.57_T_SYNC_RISE_500kHz_12V			
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.162	8.138	-0.024
0	118	8.063	8.063	0.000
0	122	8.046	8.070	0.024
3	61	8.133	8.044	-0.089
3	62	8.154	8.130	-0.024
3	63	8.209	8.144	-0.065
3	64	8.034	7.970	-0.064
3	65	8.013	7.988	-0.025
3	66	8.133	8.122	-0.011
5	67	8.070	8.063	-0.007
5	68	8.172	8.176	0.004
5	69	8.192	8.140	-0.052
5	70	8.019	8.030	0.011
5	71	8.139	8.110	-0.029
5	72	8.137	8.081	-0.056
10	73	8.133	8.062	-0.071
10	74	8.154	8.147	-0.007
10	75	8.209	8.165	-0.044
10	76	8.034	8.023	-0.011
10	77	8.013	7.954	-0.059
10	78	8.133	8.081	-0.052
15	79	8.070	8.100	0.030
15	80	8.172	8.149	-0.023
15	81	8.192	8.195	0.003
15	82	8.019	8.041	0.022
15	83	8.139	8.099	-0.040
15	84	8.137	8.105	-0.032
30	86	8.133	8.125	-0.008
30	89	8.154	8.172	0.018
30	91	8.209	8.182	-0.027
30	92	8.034	8.055	0.021
30	94	8.013	8.008	-0.005
30	95	8.133	8.163	0.030
35	100	8.070	8.134	0.064
35	101	8.172	8.223	0.051
35	102	8.192	8.199	0.007
35	103	8.019	8.098	0.079
35	104	8.139	8.169	0.030
35	105	8.137	8.168	0.031
50	106	8.089	7.983	-0.106
50	107	8.002	7.951	-0.051
50	108	8.083	8.038	-0.045
50	109	8.113	8.046	-0.067
50	110	8.024	7.993	-0.031
50	111	8.092	8.015	-0.077
55	113	8.101	8.026	-0.075
55	114	8.201	8.125	-0.076
55	115	8.180	8.146	-0.034
55	116	8.175	8.119	-0.056
55	126	8.054	7.986	-0.068
55	128	8.184	8.160	-0.024
100	129	8.000	7.980	-0.020
100	131	8.172	8.092	-0.080
100	132	8.063	8.049	-0.014
100	133	7.999	7.977	-0.022
100	134	8.022	8.098	0.076
100	135	8.122	8.097	-0.025
100	138	8.114	8.066	-0.048
100	139	8.067	8.009	-0.058
100	140	8.008	8.104	0.096
100	141	8.167	8.190	0.023
100	142	8.097	8.097	0.000
100	143	8.140	8.127	-0.013
100	144	8.115	8.035	-0.080
100	220	8.103	8.090	-0.013
100	221	8.144	8.083	-0.061
100	224	8.114	8.094	-0.020
100	225	8.027	7.989	-0.038
100	226	8.069	8.098	0.029
100	228	8.120	8.070	-0.050
100	232	8.024	8.058	0.034
100	233	8.026	8.042	0.016
100	236	8.117	8.059	-0.058
100	238	8.187	8.176	-0.011
100	243	8.116	8.089	-0.027
105	244	8.176	8.126	-0.050
105	246	8.088	8.150	0.062
105	248	8.091	8.118	0.027
105	374	8.141	8.116	-0.025
105	376	8.106	8.102	-0.004
105	Max	8.209	8.223	0.096
105	Average	8.106	8.088	-0.018
105	Min	7.999	7.951	-0.106
105	Std Dev	0.061	0.064	0.042



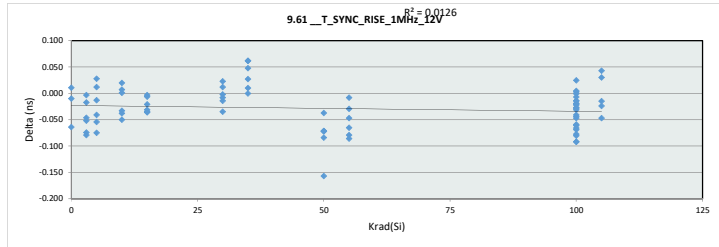
9.57_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.063	7.970	8.030	7.954	8.041	8.008	8.098	7.951	7.986	7.977	8.102
Average	8.090	8.066	8.100	8.072	8.115	8.118	8.165	8.004	8.094	8.074	8.122
Max	8.138	8.144	8.176	8.165	8.195	8.182	8.223	8.046	8.160	8.190	8.150
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



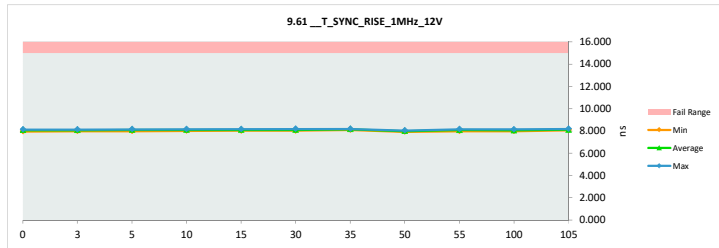
LDR TID Report
TPS7H5001-SP

9.61_T_SYNC_RISE_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.130	8.119	-0.011
0	118	8.012	7.947	-0.065
0	122	8.088	8.098	0.010
3	61	8.092	8.039	-0.053
3	62	8.137	8.119	-0.018
3	63	8.183	8.103	-0.080
3	64	8.046	7.971	-0.075
3	65	7.996	7.992	-0.004
3	66	8.103	8.056	-0.047
5	67	8.080	8.107	0.027
5	68	8.180	8.125	-0.055
5	69	8.132	8.143	0.011
5	70	8.017	7.975	-0.042
5	71	8.132	8.056	-0.076
5	72	8.094	8.080	-0.014
10	73	8.092	8.098	0.006
10	74	8.137	8.163	0.024
10	75	8.183	8.144	-0.039
10	76	8.046	7.995	-0.051
10	77	7.996	7.996	0.000
10	78	8.103	8.122	0.019
15	79	8.080	8.044	-0.036
15	80	8.180	8.143	-0.037
15	81	8.132	8.100	-0.032
15	82	8.017	8.010	-0.007
15	83	8.132	8.110	-0.022
15	84	8.094	8.090	-0.004
30	86	8.092	8.083	-0.009
30	89	8.137	8.134	-0.003
30	91	8.183	8.168	-0.015
30	92	8.046	8.068	0.022
30	94	7.996	8.007	0.011
30	95	8.103	8.067	-0.036
35	100	8.080	8.141	0.061
35	101	8.180	8.179	-0.001
35	102	8.132	8.179	0.047
35	103	8.017	8.078	0.061
35	104	8.132	8.141	0.009
35	105	8.094	8.120	0.026
50	106	8.113	7.955	-0.158
50	107	7.999	7.914	-0.085
50	108	8.062	7.989	-0.073
50	109	8.110	8.037	-0.073
50	110	7.972	7.934	-0.038
50	111	8.106	8.034	-0.072
55	113	8.048	7.961	-0.087
55	114	8.140	8.092	-0.048
55	115	8.167	8.101	-0.066
55	116	8.170	8.090	-0.080
55	126	8.025	7.995	-0.030
55	128	8.157	8.148	-0.009
100	129	8.007	7.992	-0.015
100	131	8.104	8.096	-0.008
100	132	8.089	8.029	-0.060
100	133	7.995	8.019	0.024
100	134	8.083	8.016	-0.067
100	135	8.135	8.112	-0.023
100	138	8.061	8.032	-0.029
100	139	8.036	8.034	-0.002
100	140	8.068	7.998	-0.070
100	141	8.148	8.128	-0.020
100	142	8.122	8.044	-0.078
100	143	8.083	8.038	-0.045
100	144	8.106	8.013	-0.093
100	220	8.098	8.066	-0.032
100	221	8.088	8.090	0.002
100	224	8.070	8.049	-0.021
100	225	8.069	7.988	-0.081
100	226	8.070	8.028	-0.042
100	228	8.116	8.023	-0.093
100	232	8.023	7.975	-0.048
100	233	8.055	7.993	-0.062
100	236	8.083	8.055	-0.028
100	238	8.118	8.122	0.004
100	243	8.087	8.072	-0.015
105	244	8.076	8.118	0.042
105	246	8.175	8.159	-0.016
105	248	8.099	8.051	-0.048
105	374	8.087	8.062	-0.025
105	376	8.073	8.102	0.029
105	Max	8.183	8.179	0.061
105	Average	8.091	8.063	-0.029
105	Min	7.972	7.914	-0.158
105	Std Dev	0.052	0.062	0.040



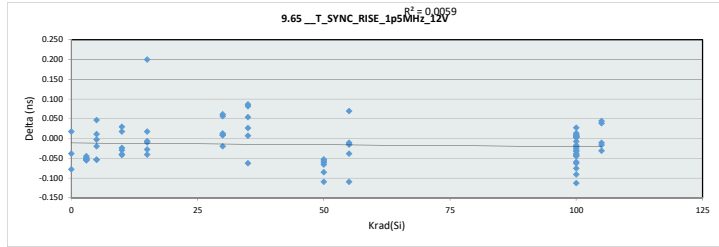
9.61_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.947	7.971	7.975	7.995	8.010	8.007	8.078	7.914	7.961	7.975	8.051
Average	8.055	8.047	8.081	8.076	8.083	8.088	8.140	7.977	8.065	8.042	8.098
Max	8.119	8.119	8.143	8.144	8.143	8.168	8.179	8.037	8.148	8.128	8.159
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



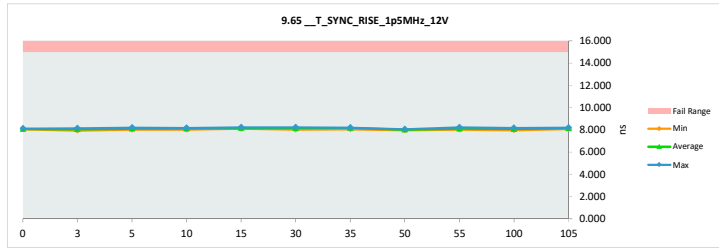
LDR TID Report
TPS7H5001-SP

9.65_T_SYNC_RISE_1p5MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.131	8.092	-0.039
0	118	8.019	8.036	0.017
0	122	8.159	8.081	-0.078
3	61	8.123	8.069	-0.054
3	62	8.198	8.142	-0.056
3	63	8.165	8.112	-0.053
3	64	7.991	7.942	-0.049
3	65	8.008	7.963	-0.045
3	66	8.148	8.096	-0.052
5	67	8.096	8.093	-0.003
5	68	8.236	8.182	-0.054
5	69	8.159	8.105	-0.054
5	70	7.969	8.015	0.046
5	71	8.163	8.143	-0.020
5	72	8.115	8.126	0.011
10	73	8.123	8.099	-0.024
10	74	8.198	8.158	-0.040
10	75	8.165	8.135	-0.030
10	76	7.991	8.020	0.029
10	77	8.008	8.025	0.017
10	78	8.148	8.106	-0.042
15	79	8.096	8.113	0.017
15	80	8.236	8.208	-0.028
15	81	8.159	8.118	-0.041
15	82	7.969	8.168	0.199
15	83	8.163	8.156	-0.007
15	84	8.115	8.105	-0.010
30	86	8.123	8.131	0.008
30	89	8.198	8.178	-0.020
30	91	8.165	8.173	0.008
30	92	7.991	8.052	0.061
30	94	8.008	8.020	0.012
30	95	8.148	8.204	0.056
35	100	8.096	8.150	0.054
35	101	8.236	8.173	-0.063
35	102	8.159	8.166	0.007
35	103	7.969	8.055	0.086
35	104	8.163	8.189	0.026
35	105	8.115	8.196	0.081
50	106	8.118	8.008	-0.110
50	107	8.010	7.953	-0.057
50	108	8.094	8.041	-0.053
50	109	8.113	8.027	-0.086
50	110	8.046	7.985	-0.061
50	111	8.083	8.017	-0.066
55	113	8.032	8.017	-0.015
55	114	8.124	8.085	-0.039
55	115	8.139	8.208	0.069
55	116	8.177	8.067	-0.110
55	126	8.032	8.017	-0.015
55	128	8.193	8.182	-0.011
100	129	8.000	7.957	-0.043
100	131	8.183	8.153	-0.030
100	132	8.113	8.119	0.006
100	133	8.010	7.965	-0.045
100	134	8.105	8.080	-0.025
100	135	8.108	8.100	-0.008
100	138	8.068	8.095	0.027
100	139	8.107	8.031	-0.076
100	140	8.100	8.037	-0.063
100	141	8.125	8.107	-0.018
100	142	8.137	8.104	-0.033
100	143	8.124	8.131	0.007
100	144	8.154	8.041	-0.113
100	220	8.092	8.101	0.009
100	221	8.127	8.036	-0.091
100	224	8.082	8.084	0.002
100	225	8.026	7.987	-0.039
100	226	8.075	8.032	-0.043
100	228	8.117	8.057	-0.060
100	232	8.002	8.006	0.004
100	233	8.004	8.007	0.003
100	236	8.096	8.101	0.005
100	238	8.164	8.143	-0.021
100	243	8.064	8.077	0.013
105	244	8.136	8.119	-0.017
105	246	8.197	8.186	-0.011
105	248	8.092	8.130	0.038
105	374	8.036	8.080	0.044
105	376	8.109	8.078	-0.031
	Max	8.236	8.208	0.199
	Average	8.104	8.088	-0.016
	Min	7.969	7.942	-0.113
	Std Dev	0.068	0.067	0.049



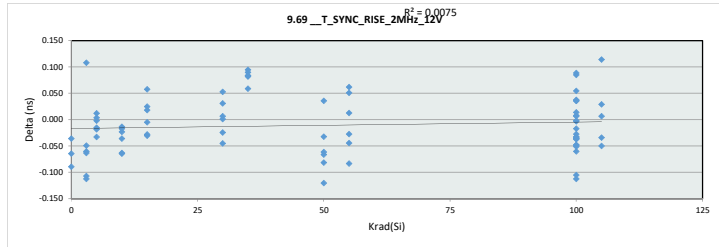
9.65_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.036	7.942	8.015	8.020	8.105	8.020	8.055	7.953	8.017	7.957	8.078
Average	8.070	8.054	8.111	8.091	8.145	8.126	8.155	8.005	8.096	8.065	8.119
Max	8.092	8.142	8.182	8.158	8.208	8.204	8.196	8.041	8.208	8.153	8.186
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



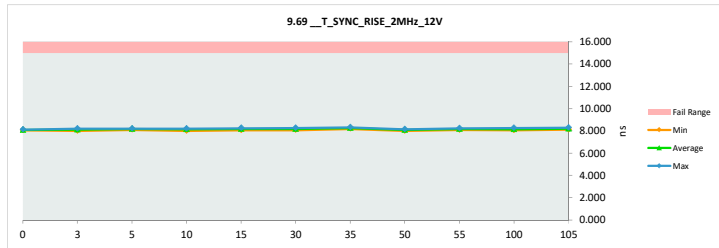
LDR TID Report
TPS7H5001-SP

9.69_T_SYNC_RISE_2MHz_12V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	15	15
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.158	8.068	-0.090
0	118	8.098	8.061	-0.037
0	122	8.178	8.113	-0.065
3	61	8.189	8.139	-0.050
3	62	8.260	8.199	-0.061
3	63	8.239	8.175	-0.064
3	64	8.105	7.997	-0.108
3	65	8.024	8.131	0.107
3	66	8.218	8.105	-0.113
5	67	8.145	8.129	-0.016
5	68	8.224	8.190	-0.034
5	69	8.186	8.189	0.003
5	70	8.093	8.104	0.011
5	71	8.226	8.207	-0.019
5	72	8.172	8.169	-0.003
10	73	8.189	8.175	-0.014
10	74	8.260	8.195	-0.065
10	75	8.239	8.215	-0.024
10	76	8.105	8.068	-0.037
10	77	8.024	8.006	-0.018
10	78	8.218	8.154	-0.064
15	79	8.145	8.202	0.057
15	80	8.224	8.248	0.024
15	81	8.186	8.203	0.017
15	82	8.093	8.062	-0.031
15	83	8.226	8.220	-0.006
15	84	8.172	8.143	-0.029
30	86	8.189	8.164	-0.025
30	89	8.260	8.260	0.000
30	91	8.239	8.193	-0.046
30	92	8.105	8.111	0.006
30	94	8.024	8.076	0.052
30	95	8.218	8.248	0.030
35	100	8.145	8.234	0.089
35	101	8.224	8.307	0.083
35	102	8.186	8.267	0.081
35	103	8.093	8.187	0.094
35	104	8.226	8.284	0.058
35	105	8.172	8.254	0.082
50	106	8.175	8.108	-0.067
50	107	8.092	8.010	-0.082
50	108	8.049	8.084	0.035
50	109	8.180	8.147	-0.033
50	110	8.113	8.051	-0.062
50	111	8.166	8.045	-0.121
55	113	8.024	8.085	0.061
55	114	8.173	8.185	0.012
55	115	8.305	8.221	-0.084
55	116	8.248	8.203	-0.045
55	126	8.071	8.121	0.050
55	128	8.271	8.243	-0.028
100	129	8.052	8.089	0.037
100	131	8.225	8.176	-0.049
100	132	8.131	8.185	0.054
100	133	8.054	8.089	0.035
100	134	8.152	8.114	-0.038
100	135	8.225	8.260	0.035
100	138	8.163	8.057	-0.106
100	139	8.162	8.113	-0.049
100	140	8.131	8.095	-0.036
100	141	8.241	8.128	-0.113
100	142	8.236	8.175	-0.061
100	143	8.190	8.196	0.006
100	144	8.178	8.141	-0.037
100	220	8.211	8.193	-0.018
100	221	8.193	8.159	-0.034
100	224	8.177	8.174	-0.003
100	225	8.045	8.129	0.084
100	226	8.169	8.141	-0.028
100	228	8.211	8.159	-0.052
100	232	8.060	8.056	-0.004
100	233	8.122	8.088	-0.034
100	236	8.168	8.256	0.088
100	238	8.245	8.252	0.007
100	243	8.186	8.199	0.013
105	244	8.209	8.215	0.006
105	246	8.180	8.293	0.113
105	248	8.156	8.184	0.028
105	374	8.226	8.175	-0.051
105	376	8.160	8.125	-0.035
105		8.305	8.307	0.113
	Max			
	Average	8.168	8.157	-0.010
	Min	8.024	7.997	-0.121
	Std Dev	0.066	0.071	0.056



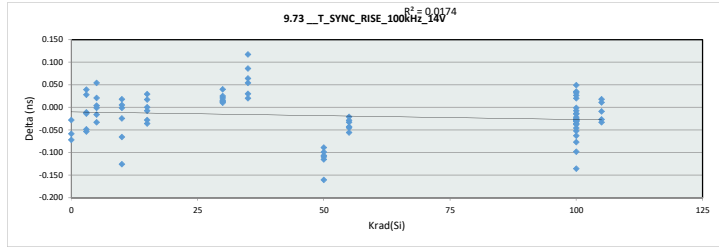
9.69_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.061	7.997	8.104	8.006	8.062	8.076	8.187	8.010	8.085	8.056	8.125
Average	8.081	8.124	8.165	8.136	8.180	8.175	8.256	8.074	8.176	8.151	8.198
Max	8.113	8.199	8.207	8.215	8.248	8.260	8.307	8.147	8.243	8.260	8.293
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



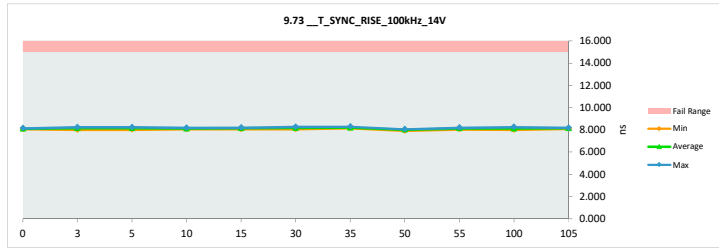
LDR TID Report
TPS7H5001-SP

9.73_T_SYNC_RISE_100kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.197	8.125	-0.072
0	118	8.105	8.046	-0.059
0	122	8.120	8.092	-0.028
3	61	8.106	8.145	0.039
3	62	8.229	8.218	-0.011
3	63	8.219	8.165	-0.054
3	64	8.036	7.987	-0.049
3	65	8.055	8.083	0.028
3	66	8.181	8.167	-0.014
5	67	8.113	8.117	0.004
5	68	8.183	8.167	-0.016
5	69	8.160	8.214	0.054
5	70	8.043	8.010	-0.033
5	71	8.143	8.164	0.021
5	72	8.158	8.157	-0.001
10	73	8.106	8.111	0.005
10	74	8.229	8.163	-0.066
10	75	8.219	8.093	-0.126
10	76	8.036	8.054	0.018
10	77	8.055	8.054	-0.001
10	78	8.181	8.156	-0.025
15	79	8.113	8.142	0.029
15	80	8.183	8.147	-0.036
15	81	8.160	8.177	0.017
15	82	8.043	8.043	0.000
15	83	8.143	8.115	-0.028
15	84	8.158	8.150	-0.008
30	86	8.106	8.131	0.025
30	89	8.229	8.245	0.016
30	91	8.219	8.229	0.010
30	92	8.036	8.057	0.021
30	94	8.055	8.095	0.040
30	95	8.181	8.194	0.013
35	100	8.113	8.133	0.020
35	101	8.183	8.213	0.030
35	102	8.160	8.277	0.117
35	103	8.043	8.129	0.086
35	104	8.143	8.207	0.064
35	105	8.158	8.212	0.054
50	106	8.116	8.027	-0.089
50	107	8.064	7.903	-0.161
50	108	8.149	8.127	-0.022
50	109	8.147	8.040	-0.107
50	110	8.060	7.950	-0.110
50	111	8.150	8.051	-0.099
55	113	8.078	8.033	-0.045
55	114	8.216	8.183	-0.033
55	115	8.178	8.149	-0.029
55	116	8.173	8.130	-0.043
55	126	8.096	8.040	-0.056
55	128	8.205	8.184	-0.021
100	129	8.019	8.004	-0.015
100	131	8.156	8.182	0.026
100	132	8.163	8.127	-0.036
100	133	8.020	7.989	-0.031
100	134	8.060	8.109	0.049
100	135	8.172	8.204	0.032
100	138	8.113	8.061	-0.052
100	139	8.075	8.067	-0.008
100	140	8.082	8.081	-0.001
100	141	8.219	8.189	-0.030
100	142	8.136	8.073	-0.063
100	143	8.121	8.092	-0.029
100	144	8.128	8.114	-0.014
100	220	8.173	8.147	-0.026
100	221	8.151	8.105	-0.046
100	224	8.133	8.095	-0.038
100	225	8.103	8.026	-0.077
100	226	8.142	8.006	-0.136
100	228	8.144	8.118	-0.026
100	232	8.113	8.015	-0.098
100	233	8.091	8.069	-0.022
100	236	8.122	8.094	-0.028
100	238	8.205	8.240	0.035
100	243	8.145	8.165	0.020
105	244	8.156	8.147	-0.009
105	246	8.159	8.170	0.011
105	248	8.162	8.135	-0.027
105	374	8.128	8.095	-0.033
105	376	8.069	8.087	0.018
105	Max	8.229	8.277	0.117
105	Average	8.132	8.114	-0.018
105	Min	8.019	7.903	-0.161
105	Std Dev	0.056	0.073	0.050



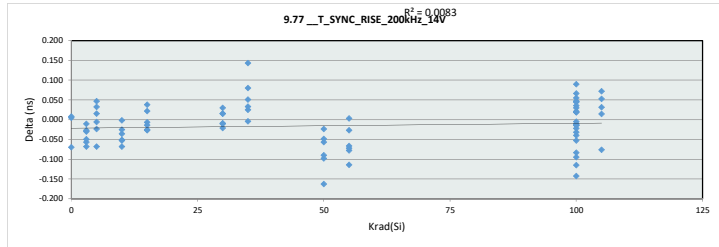
9.73_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.046	7.987	8.010	8.054	8.043	8.057	8.129	7.903	8.033	7.989	8.087
Average	8.088	8.128	8.138	8.105	8.129	8.159	8.195	8.001	8.120	8.099	8.127
Max	8.125	8.218	8.214	8.163	8.177	8.245	8.277	8.051	8.184	8.240	8.170
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



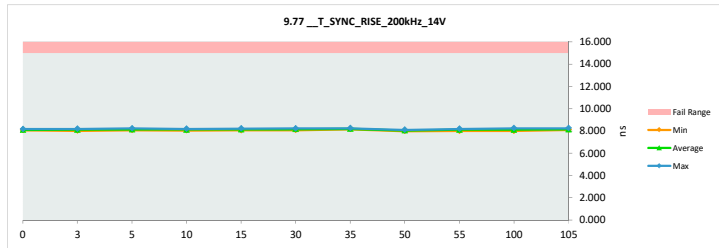
LDR TID Report
TPS7H5001-SP

9.77_T_SYNC_RISE_200kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.148	8.152	0.004
0	118	8.039	8.046	0.007
0	122	8.132	8.061	-0.071
3	61	8.146	8.134	-0.012
3	62	8.215	8.146	-0.069
3	63	8.225	8.167	-0.058
3	64	8.027	7.996	-0.031
3	65	8.067	8.016	-0.051
3	66	8.160	8.132	-0.028
5	67	8.134	8.127	-0.007
5	68	8.176	8.207	0.031
5	69	8.206	8.137	-0.069
5	70	7.998	8.044	0.046
5	71	8.177	8.152	-0.025
5	72	8.089	8.103	0.014
10	73	8.146	8.092	-0.054
10	74	8.215	8.163	-0.052
10	75	8.225	8.156	-0.069
10	76	8.027	8.024	-0.003
10	77	8.067	8.030	-0.037
10	78	8.160	8.134	-0.026
15	79	8.134	8.108	-0.026
15	80	8.176	8.197	0.021
15	81	8.206	8.191	-0.015
15	82	7.998	8.035	0.037
15	83	8.177	8.169	-0.008
15	84	8.089	8.061	-0.028
30	86	8.146	8.124	-0.022
30	89	8.215	8.203	-0.012
30	91	8.225	8.215	-0.010
30	92	8.027	8.056	0.029
30	94	8.067	8.081	0.014
30	95	8.160	8.174	0.014
35	100	8.134	8.158	0.024
35	101	8.176	8.208	0.032
35	102	8.206	8.201	-0.005
35	103	7.998	8.140	0.142
35	104	8.177	8.227	0.050
35	105	8.089	8.168	0.079
50	106	8.126	8.035	-0.091
50	107	8.025	7.975	-0.050
50	108	8.130	8.031	-0.099
50	109	8.164	8.000	-0.164
50	110	8.105	8.047	-0.058
50	111	8.103	8.078	-0.025
55	113	8.047	7.979	-0.068
55	114	8.148	8.150	0.002
55	115	8.250	8.135	-0.115
55	116	8.182	8.154	-0.028
55	126	8.075	8.002	-0.073
55	128	8.249	8.170	-0.079
100	129	8.101	7.985	-0.116
100	131	8.154	8.148	-0.006
100	132	8.114	8.099	-0.015
100	133	8.052	8.070	0.018
100	134	8.121	8.025	-0.096
100	135	8.131	8.177	0.046
100	138	8.113	8.156	0.043
100	139	8.074	8.033	-0.041
100	140	8.086	8.071	-0.015
100	141	8.178	8.195	0.017
100	142	8.128	8.217	0.089
100	143	8.153	8.198	0.045
100	144	8.090	8.110	0.020
100	220	8.121	8.067	-0.054
100	221	8.138	8.123	-0.015
100	224	8.086	8.140	0.054
100	225	8.032	7.999	-0.033
100	226	8.072	8.101	0.029
100	228	8.149	8.005	-0.144
100	232	8.004	8.038	0.034
100	233	8.085	8.073	-0.012
100	236	8.087	8.152	0.065
100	238	8.197	8.112	-0.085
100	243	8.107	8.084	-0.023
105	244	8.151	8.074	-0.077
105	246	8.146	8.217	0.071
105	248	8.116	8.146	0.030
105	374	8.060	8.073	0.013
105	376	8.079	8.130	0.051
105	Max	8.250	8.227	0.142
105	Average	8.124	8.109	-0.015
105	Min	7.998	7.975	-0.164
105	Std Dev	0.063	0.068	0.055



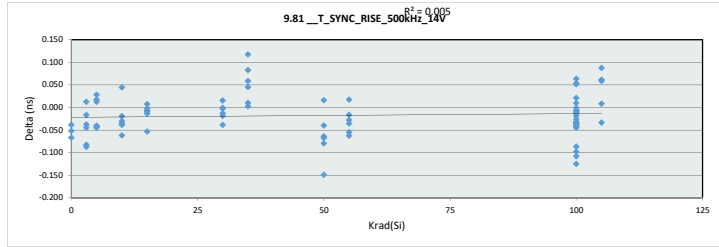
9.77_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.046	7.996	8.044	8.024	8.035	8.056	8.140	7.975	7.979	7.985	8.073
Average	8.086	8.099	8.128	8.100	8.127	8.142	8.184	8.028	8.098	8.099	8.128
Max	8.152	8.167	8.207	8.163	8.197	8.215	8.227	8.078	8.170	8.217	8.217
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



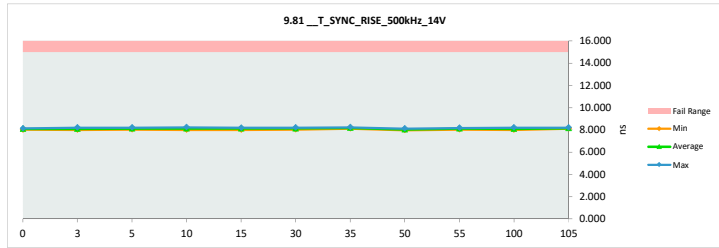
LDR TID Report
TPS7H5001-SP

9.81_T_SYNC_RISE_500kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.163	8.111	-0.052
0	118	8.066	7.999	-0.067
0	122	8.122	8.083	-0.039
3	61	8.126	8.088	-0.038
3	62	8.165	8.177	0.012
3	63	8.174	8.157	-0.017
3	64	8.083	8.000	-0.083
3	65	8.023	7.978	-0.045
3	66	8.172	8.084	-0.088
5	67	8.067	8.095	0.028
5	68	8.224	8.183	-0.041
5	69	8.178	8.136	-0.042
5	70	7.989	8.006	0.017
5	71	8.118	8.130	0.012
5	72	8.127	8.082	-0.045
10	73	8.126	8.096	-0.030
10	74	8.165	8.209	0.044
10	75	8.174	8.154	-0.020
10	76	8.083	8.021	-0.062
10	77	8.023	7.984	-0.039
10	78	8.172	8.137	-0.035
15	79	8.067	8.074	0.007
15	80	8.224	8.170	-0.054
15	81	8.178	8.164	-0.014
15	82	7.989	7.982	-0.007
15	83	8.118	8.115	-0.003
15	84	8.127	8.117	-0.010
30	86	8.126	8.123	-0.003
30	89	8.165	8.146	-0.019
30	91	8.174	8.189	0.015
30	92	8.083	8.082	-0.001
30	94	8.023	8.010	-0.013
30	95	8.172	8.133	-0.039
35	100	8.067	8.125	0.058
35	101	8.224	8.226	0.002
35	102	8.178	8.188	0.010
35	103	7.989	8.106	0.117
35	104	8.118	8.200	0.082
35	105	8.127	8.172	0.045
50	106	8.094	8.030	-0.064
50	107	7.957	7.973	0.016
50	108	8.051	7.971	-0.080
50	109	8.126	8.058	-0.068
50	110	8.155	8.006	-0.149
50	111	8.138	8.098	-0.040
55	113	8.072	8.009	-0.063
55	114	8.163	8.135	-0.028
55	115	8.206	8.151	-0.055
55	116	8.171	8.135	-0.036
55	126	8.027	8.010	-0.017
55	128	8.150	8.167	0.017
100	129	8.000	7.981	-0.019
100	131	8.123	8.115	-0.008
100	132	8.075	8.062	-0.013
100	133	8.045	8.013	-0.032
100	134	8.110	8.068	-0.042
100	135	8.113	8.164	0.051
100	138	8.099	8.062	-0.037
100	139	8.039	8.031	-0.008
100	140	8.137	8.039	-0.098
100	141	8.140	8.133	-0.007
100	142	8.168	8.060	-0.108
100	143	8.086	8.149	0.063
100	144	8.102	8.068	-0.034
100	220	8.095	8.104	0.009
100	221	8.149	8.024	-0.125
100	224	8.120	8.094	-0.026
100	225	8.060	7.973	-0.087
100	226	8.088	8.087	-0.001
100	228	8.119	8.074	-0.045
100	232	8.012	7.972	-0.040
100	233	8.035	8.024	-0.011
100	236	8.096	8.117	0.021
100	238	8.136	8.189	0.053
100	243	8.090	8.053	-0.037
105	244	8.135	8.101	-0.034
105	246	8.169	8.177	0.008
105	248	8.050	8.137	0.087
105	374	8.060	8.118	0.058
105	376	8.067	8.128	0.061
105	Max	8.224	8.226	0.117
105	Average	8.109	8.091	-0.018
105	Min	7.957	7.971	-0.149
105	Std Dev	0.060	0.067	0.048



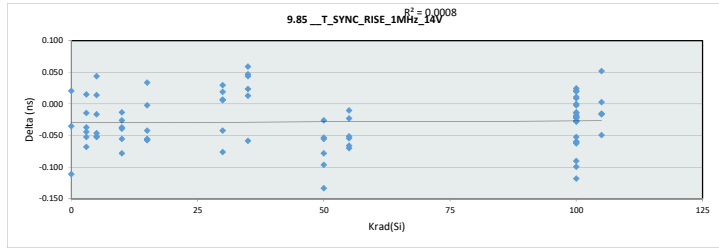
9.81_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.999	7.978	8.006	7.984	7.982	8.010	8.106	7.971	8.009	7.972	8.101
Average	8.064	8.081	8.105	8.100	8.104	8.114	8.170	8.023	8.101	8.069	8.132
Max	8.111	8.177	8.183	8.209	8.170	8.189	8.226	8.098	8.167	8.189	8.177
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



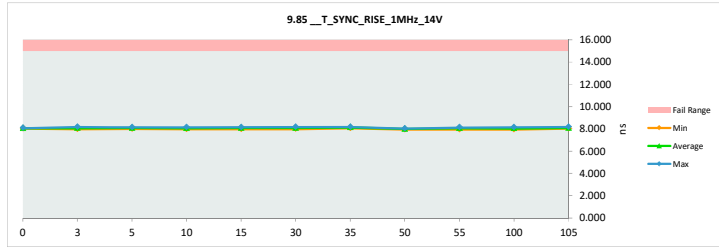
LDR TID Report
TPS7H5001-SP

9.85_T_SYNC_RISE_1MHz_14V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	15	15
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.184	8.072	-0.112
0	118	8.006	8.026	0.020
0	122	8.089	8.053	-0.036
3	61	8.091	8.053	-0.038
3	62	8.157	8.171	0.014
3	63	8.170	8.117	-0.053
3	64	8.018	8.003	-0.015
3	65	8.029	7.984	-0.045
3	66	8.173	8.104	-0.069
5	67	8.070	8.083	0.013
5	68	8.193	8.140	-0.053
5	69	8.109	8.152	0.043
5	70	8.009	7.992	-0.017
5	71	8.144	8.092	-0.052
5	72	8.135	8.088	-0.047
10	73	8.091	8.051	-0.040
10	74	8.157	8.130	-0.027
10	75	8.170	8.132	-0.038
10	76	8.018	8.004	-0.014
10	77	8.029	7.973	-0.056
10	78	8.173	8.094	-0.079
15	79	8.070	8.067	-0.003
15	80	8.193	8.135	-0.058
15	81	8.109	8.142	0.033
15	82	8.009	7.966	-0.043
15	83	8.144	8.087	-0.057
15	84	8.135	8.079	-0.056
30	86	8.091	8.109	0.018
30	89	8.157	8.162	0.005
30	91	8.170	8.176	0.006
30	92	8.018	8.047	0.029
30	94	8.029	7.986	-0.043
30	95	8.173	8.096	-0.077
35	100	8.070	8.128	0.058
35	101	8.193	8.134	-0.059
35	102	8.109	8.152	0.043
35	103	8.009	8.055	0.046
35	104	8.144	8.167	0.023
35	105	8.135	8.147	0.012
50	106	8.104	8.007	-0.097
50	107	7.990	7.934	-0.056
50	108	8.051	8.024	-0.027
50	109	8.088	8.034	-0.054
50	110	8.077	7.943	-0.134
50	111	8.092	8.013	-0.079
55	113	8.003	7.992	-0.011
55	114	8.145	8.074	-0.071
55	115	8.154	8.130	-0.024
55	116	8.155	8.088	-0.067
55	126	8.007	7.955	-0.052
55	128	8.169	8.114	-0.055
100	129	7.973	7.983	0.010
100	131	8.169	8.069	-0.100
100	132	8.046	8.065	0.019
100	133	7.987	7.995	0.008
100	134	8.030	8.010	-0.020
100	135	8.118	8.115	-0.003
100	138	8.048	8.047	-0.001
100	139	8.064	8.041	-0.023
100	140	8.084	8.056	-0.028
100	141	8.136	8.073	-0.063
100	142	8.079	8.057	-0.022
100	143	8.104	8.089	-0.015
100	144	8.126	8.035	-0.091
100	220	8.102	8.088	-0.014
100	221	8.122	8.069	-0.053
100	224	8.072	8.044	-0.028
100	225	8.105	7.986	-0.119
100	226	8.019	8.043	0.024
100	228	8.086	8.026	-0.060
100	232	8.006	7.945	-0.061
100	233	7.985	8.006	0.021
100	236	8.075	8.056	-0.019
100	238	8.157	8.135	-0.022
100	243	8.069	8.040	-0.029
105	244	8.126	8.076	-0.050
105	246	8.113	8.164	0.051
105	248	8.055	8.038	-0.017
105	374	8.063	8.065	0.002
105	376	8.090	8.074	-0.016
105	Max	8.193	8.176	0.058
105	Average	8.093	8.064	-0.028
105	Min	7.973	7.934	-0.134
105	Std Dev	0.060	0.060	0.041



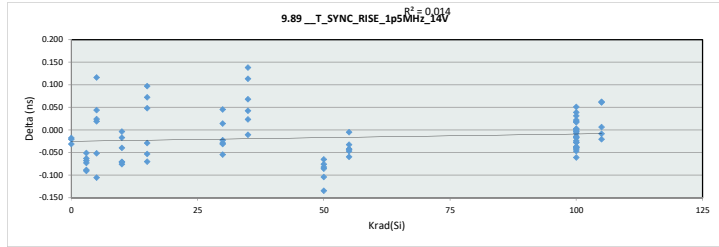
9.85_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.026	7.984	7.992	7.973	7.966	7.986	8.055	7.934	7.955	7.945	8.038
Average	8.050	8.072	8.091	8.064	8.079	8.096	8.131	7.993	8.059	8.045	8.083
Max	8.072	8.171	8.152	8.132	8.142	8.176	8.167	8.034	8.130	8.135	8.164
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



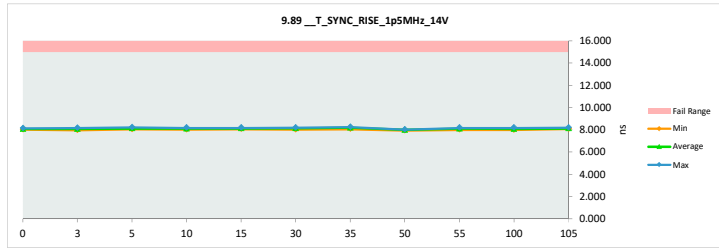
LDR TID Report
TPS7H5001-SP

9.89_T_SYNC_RISE_1p5MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.156	8.138	-0.018
0	118	8.039	8.018	-0.021
0	122	8.130	8.099	-0.031
3	61	8.139	8.071	-0.068
3	62	8.219	8.156	-0.063
3	63	8.223	8.150	-0.073
3	64	8.092	8.003	-0.089
3	65	8.048	7.957	-0.091
3	66	8.137	8.086	-0.051
5	67	8.022	8.041	0.019
5	68	8.187	8.211	0.024
5	69	8.131	8.175	0.044
5	70	7.994	8.110	0.116
5	71	8.183	8.077	-0.106
5	72	8.159	8.107	-0.052
10	73	8.139	8.068	-0.071
10	74	8.219	8.179	-0.040
10	75	8.223	8.147	-0.076
10	76	8.092	8.021	-0.071
10	77	8.048	8.031	-0.017
10	78	8.137	8.133	-0.004
15	79	8.022	8.119	0.097
15	80	8.187	8.158	-0.029
15	81	8.131	8.179	0.048
15	82	7.994	8.066	0.072
15	83	8.183	8.113	-0.070
15	84	8.159	8.106	-0.053
30	86	8.139	8.153	0.014
30	89	8.219	8.188	-0.031
30	91	8.223	8.168	-0.055
30	92	8.092	8.063	-0.029
30	94	8.048	8.025	-0.023
30	95	8.137	8.182	0.045
35	100	8.022	8.160	0.138
35	101	8.187	8.176	-0.011
35	102	8.131	8.244	0.113
35	103	7.994	8.062	0.068
35	104	8.183	8.206	0.023
35	105	8.159	8.201	0.042
50	106	8.109	8.005	-0.104
50	107	8.003	7.938	-0.065
50	108	8.103	8.021	-0.082
50	109	8.152	8.017	-0.135
50	110	8.100	8.014	-0.086
50	111	8.080	8.004	-0.076
55	113	8.041	8.008	-0.033
55	114	8.146	8.099	-0.047
55	115	8.222	8.162	-0.060
55	116	8.145	8.102	-0.043
55	126	8.077	8.034	-0.043
55	128	8.183	8.178	-0.005
100	129	8.048	8.008	-0.040
100	131	8.148	8.134	-0.014
100	132	8.100	8.096	-0.004
100	133	8.009	8.010	0.001
100	134	8.112	8.095	-0.017
100	135	8.105	8.108	0.003
100	138	8.084	8.103	0.019
100	139	8.063	8.080	0.017
100	140	8.054	8.015	-0.039
100	141	8.140	8.179	0.039
100	142	8.185	8.142	-0.043
100	143	8.141	8.104	-0.037
100	144	8.104	8.102	-0.002
100	220	8.073	8.104	0.031
100	221	8.103	8.064	-0.039
100	224	8.141	8.113	-0.028
100	225	7.996	8.018	0.022
100	226	8.091	8.084	-0.007
100	228	8.073	8.049	-0.024
100	232	8.050	7.989	-0.061
100	233	7.980	8.031	0.051
100	236	8.079	8.062	-0.017
100	238	8.206	8.159	-0.047
100	243	8.067	8.039	-0.028
105	244	8.150	8.129	-0.021
105	246	8.125	8.187	0.062
105	248	8.109	8.100	-0.009
105	374	8.052	8.113	0.061
105	376	8.073	8.079	0.006
105	Max	8.223	8.244	0.138
105	Average	8.112	8.095	-0.017
105	Min	7.980	7.938	-0.135
105	Std Dev	0.065	0.066	0.054



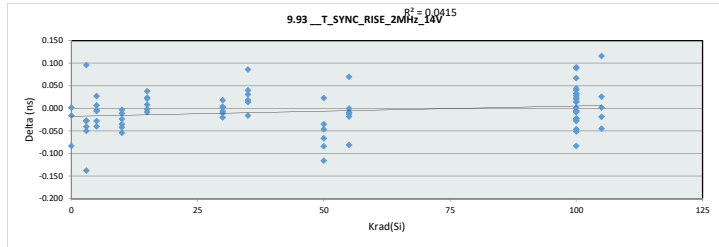
9.89_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.018	7.957	8.041	8.021	8.066	8.025	8.062	7.938	8.008	7.989	8.079
Average	8.085	8.071	8.120	8.097	8.124	8.130	8.175	8.000	8.097	8.079	8.122
Max	8.138	8.156	8.211	8.179	8.179	8.188	8.244	8.021	8.178	8.179	8.187
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



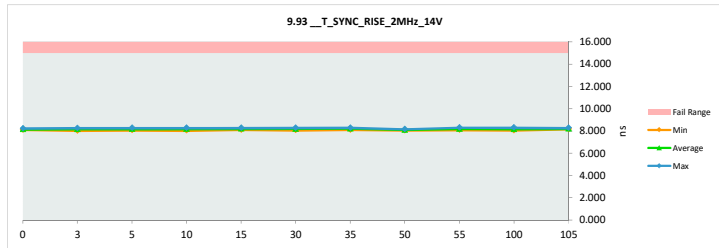
LDR TID Report
TPS7H5001-SP

9.93_T_SYNC_RISE_2MHz_14V			
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.209	8.210	0.001
0	118	8.111	8.094	-0.017
0	122	8.206	8.122	-0.084
3	61	8.199	8.157	-0.042
3	62	8.260	8.232	-0.028
3	63	8.246	8.195	-0.051
3	64	8.129	7.990	-0.139
3	65	8.045	8.140	0.095
3	66	8.254	8.225	-0.029
5	67	8.151	8.157	0.006
5	68	8.232	8.258	0.026
5	69	8.209	8.202	-0.007
5	70	8.087	8.058	-0.029
5	71	8.220	8.179	-0.041
5	72	8.179	8.174	-0.005
10	73	8.199	8.163	-0.036
10	74	8.260	8.248	-0.012
10	75	8.246	8.242	-0.004
10	76	8.129	8.074	-0.055
10	77	8.045	8.020	-0.025
10	78	8.254	8.211	-0.043
15	79	8.151	8.174	0.023
15	80	8.232	8.229	-0.003
15	81	8.209	8.246	0.037
15	82	8.087	8.094	0.007
15	83	8.220	8.211	-0.009
15	84	8.179	8.200	0.021
30	86	8.199	8.216	0.017
30	89	8.260	8.261	0.001
30	91	8.246	8.225	-0.021
30	92	8.129	8.118	-0.011
30	94	8.045	8.048	0.003
30	95	8.254	8.247	-0.007
35	100	8.151	8.236	0.085
35	101	8.232	8.262	0.030
35	102	8.209	8.192	-0.017
35	103	8.087	8.100	0.013
35	104	8.220	8.237	0.017
35	105	8.179	8.218	0.039
50	106	8.194	8.109	-0.085
50	107	8.073	8.037	-0.036
50	108	8.041	8.069	0.022
50	109	8.181	8.134	-0.047
50	110	8.130	8.063	-0.067
50	111	8.185	8.068	-0.117
55	113	8.015	8.084	0.069
55	114	8.247	8.165	-0.082
55	115	8.269	8.259	-0.010
55	116	8.207	8.188	-0.019
55	126	8.098	8.085	-0.013
55	128	8.288	8.287	-0.001
100	129	8.050	8.041	-0.009
100	131	8.221	8.198	-0.023
100	132	8.157	8.171	0.014
100	133	8.078	8.100	0.022
100	134	8.141	8.112	-0.029
100	135	8.194	8.284	0.090
100	138	8.158	8.110	-0.048
100	139	8.119	8.067	-0.052
100	140	8.129	8.102	-0.027
100	141	8.212	8.212	0.000
100	142	8.215	8.242	0.027
100	143	8.165	8.205	0.040
100	144	8.204	8.120	-0.084
100	220	8.162	8.175	0.013
100	221	8.178	8.154	-0.024
100	224	8.146	8.177	0.031
100	225	8.049	8.069	0.020
100	226	8.143	8.136	-0.007
100	228	8.196	8.189	-0.007
100	232	8.104	8.057	-0.047
100	233	8.050	8.093	0.043
100	236	8.154	8.242	0.088
100	238	8.219	8.285	0.066
100	243	8.159	8.191	0.032
105	244	8.199	8.179	-0.020
105	246	8.257	8.258	0.001
105	248	8.181	8.206	0.025
105	374	8.062	8.177	0.115
105	375	8.188	8.142	-0.046
105	Max	8.288	8.287	0.115
105	Average	8.170	8.164	-0.006
105	Min	8.015	7.990	-0.139
105	Std Dev	0.067	0.072	0.046



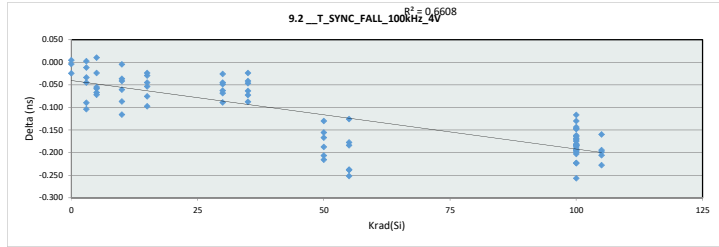
9.93_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.094	7.990	8.058	8.020	8.094	8.048	8.100	8.037	8.084	8.041	8.142
Average	8.142	8.157	8.171	8.160	8.192	8.186	8.208	8.080	8.178	8.156	8.192
Max	8.210	8.232	8.258	8.248	8.246	8.261	8.262	8.134	8.287	8.285	8.258
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



LDR TID Report
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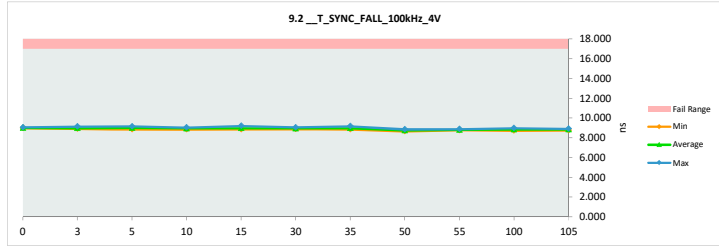
9.2_T_SYNC_FALL_100kHz_4V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	17	17
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.007	8.982	-0.025
0	118	9.034	9.030	-0.004
0	122	8.952	8.956	0.004
3	61	9.014	8.910	-0.104
3	62	9.097	9.099	0.002
3	63	8.939	8.894	-0.045
3	64	8.890	8.878	-0.012
3	65	8.935	8.901	-0.034
3	66	9.044	8.954	-0.090
5	67	8.882	8.827	-0.055
5	68	9.196	9.124	-0.072
5	69	9.038	8.979	-0.059
5	70	8.885	8.861	-0.024
5	71	8.940	8.950	0.010
5	72	9.140	9.073	-0.067
10	73	9.014	8.898	-0.116
10	74	9.097	9.010	-0.087
10	75	8.939	8.934	-0.005
10	76	8.890	8.829	-0.061
10	77	8.935	8.893	-0.042
10	78	9.044	9.007	-0.037
15	79	8.882	8.806	-0.076
15	80	9.196	9.172	-0.024
15	81	9.038	9.008	-0.030
15	82	8.885	8.831	-0.054
15	83	8.940	8.895	-0.045
15	84	9.140	9.042	-0.098
30	86	9.014	8.925	-0.089
30	89	9.097	9.028	-0.069
30	91	8.939	8.876	-0.063
30	92	8.890	8.845	-0.045
30	94	8.935	8.909	-0.026
30	95	9.044	8.995	-0.049
35	100	8.882	8.818	-0.064
35	101	9.196	9.149	-0.047
35	102	9.038	8.950	-0.088
35	103	8.885	8.861	-0.024
35	104	8.940	8.898	-0.042
35	105	9.140	9.067	-0.073
50	106	8.913	8.746	-0.167
50	107	8.809	8.621	-0.188
50	108	8.992	8.836	-0.156
50	109	8.956	8.636	-0.130
50	110	9.002	8.795	-0.207
50	111	8.930	8.714	-0.216
55	113	9.067	8.815	-0.252
55	114	8.998	8.759	-0.239
55	115	9.025	8.787	-0.238
55	116	8.982	8.798	-0.184
55	126	8.911	8.733	-0.178
55	128	8.967	8.841	-0.126
100	129	9.000	8.804	-0.196
100	131	9.086	8.893	-0.193
100	132	9.117	8.934	-0.183
100	133	9.121	8.937	-0.184
100	134	9.054	8.860	-0.194
100	135	9.027	8.852	-0.175
100	138	8.969	8.807	-0.162
100	139	8.949	8.765	-0.184
100	140	9.034	8.904	-0.130
100	141	9.004	8.823	-0.181
100	142	9.004	8.819	-0.185
100	143	8.942	8.798	-0.144
100	144	8.889	8.686	-0.203
100	220	9.036	8.889	-0.147
100	221	8.916	8.693	-0.223
100	224	8.989	8.872	-0.117
100	225	9.085	8.886	-0.199
100	226	8.992	8.768	-0.224
100	228	9.065	8.917	-0.148
100	232	8.962	8.793	-0.169
100	233	8.966	8.778	-0.188
100	236	8.945	8.688	-0.257
100	238	9.043	8.871	-0.172
100	243	9.001	8.836	-0.165
105	244	9.017	8.811	-0.206
105	246	9.100	8.872	-0.228
105	248	8.987	8.827	-0.160
105	374	8.918	8.723	-0.195
105	376	9.039	8.860	-0.179
105	375	9.196	9.172	0.010
Max		8.995	8.876	-0.119
Average		8.995	8.876	-0.119
Min		8.766	8.621	-0.257
Std Dev		0.087	0.112	0.076



9.2_T_SYNC_FALL		
Test Site		
Tester		
Test Number		
Max Limit	17	ns
Min Limit	ns	

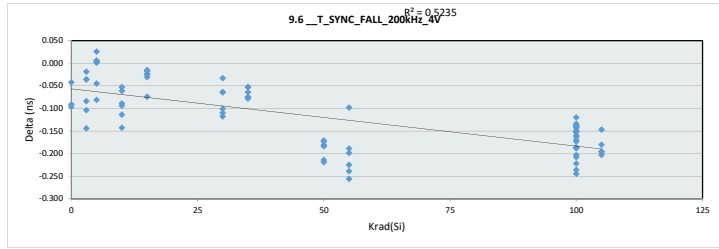
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.956	8.878	8.827	8.829	8.806	8.845	8.818	8.621	8.733	8.686	8.723
Average	8.989	8.939	8.969	8.929	8.959	8.930	8.957	8.725	8.789	8.828	8.819
Max	9.030	9.099	9.124	9.010	9.172	9.028	9.149	8.836	8.841	8.937	8.872
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



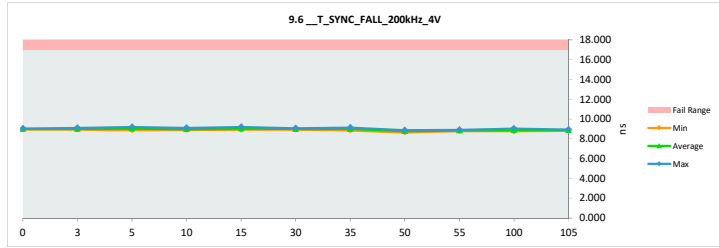
LDR TID Report
TPS7H5001-SP

9.6_T_SYNC_FALL_200kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.072	9.029	-0.043
0	118	9.137	9.045	-0.092
0	122	9.047	8.949	-0.098
3	61	9.081	8.976	-0.105
3	62	9.190	9.105	-0.085
3	63	9.071	8.926	-0.145
3	64	8.968	8.931	-0.037
3	65	8.995	8.975	-0.020
3	66	9.093	9.056	-0.037
5	67	8.917	8.917	0.000
5	68	9.194	9.199	0.005
5	69	9.023	9.026	0.003
5	70	8.966	8.884	-0.082
5	71	9.042	8.996	-0.046
5	72	9.181	9.206	0.025
10	73	9.081	8.937	-0.144
10	74	9.190	9.095	-0.095
10	75	9.071	8.956	-0.115
10	76	8.968	8.906	-0.062
10	77	8.995	8.941	-0.054
10	78	9.093	9.003	-0.090
15	79	8.917	8.898	-0.019
15	80	9.194	9.178	-0.016
15	81	9.023	8.998	-0.025
15	82	8.966	8.891	-0.075
15	83	9.042	9.010	-0.032
15	84	9.181	9.155	-0.026
30	86	9.081	8.962	-0.119
30	89	9.190	9.079	-0.111
30	91	9.071	8.969	-0.102
30	92	8.968	8.934	-0.034
30	94	8.995	8.930	-0.065
30	95	9.093	9.028	-0.065
35	100	8.917	8.841	-0.076
35	101	9.194	9.140	-0.054
35	102	9.023	8.969	-0.054
35	103	8.966	8.901	-0.065
35	104	9.042	8.963	-0.079
35	105	9.181	9.105	-0.076
50	106	8.982	8.797	-0.185
50	107	8.871	8.699	-0.172
50	108	9.100	8.880	-0.220
50	109	8.814	8.632	-0.182
50	110	9.028	8.813	-0.215
50	111	8.953	8.779	-0.174
55	113	9.149	8.892	-0.257
55	114	9.024	8.784	-0.240
55	115	9.062	8.836	-0.226
55	116	9.068	8.868	-0.200
55	126	8.974	8.784	-0.190
55	128	8.967	8.868	-0.099
100	129	9.058	8.905	-0.153
100	131	9.123	8.935	-0.188
100	132	9.171	9.036	-0.135
100	133	9.113	8.939	-0.174
100	134	9.076	8.913	-0.163
100	135	9.054	8.865	-0.189
100	138	9.046	8.902	-0.144
100	139	8.946	8.784	-0.162
100	140	9.066	8.945	-0.121
100	141	9.032	8.823	-0.209
100	142	9.053	8.807	-0.246
100	143	8.963	8.825	-0.138
100	144	8.949	8.762	-0.187
100	220	9.115	8.961	-0.154
100	221	8.961	8.757	-0.204
100	224	9.100	8.950	-0.150
100	225	9.066	8.904	-0.162
100	226	9.013	8.872	-0.141
100	228	9.093	8.953	-0.140
100	232	9.009	8.847	-0.162
100	233	9.025	8.788	-0.237
100	236	8.984	8.761	-0.223
100	238	9.056	8.914	-0.142
100	243	9.028	8.857	-0.171
105	244	9.047	8.850	-0.197
105	246	9.109	8.928	-0.181
105	248	9.060	8.856	-0.204
105	374	8.959	8.811	-0.148
105	376	9.133	8.936	-0.197
105	375	9.194	9.206	0.025
Max		9.048	8.925	-0.122
Average		9.048	8.925	-0.122
Min		8.814	8.632	-0.257
Std Dev		0.081	0.113	0.071



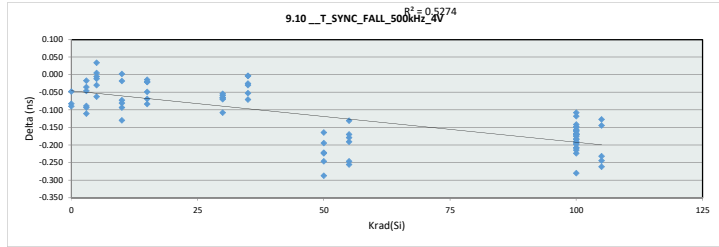
9.6_T_SYNC_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.949	8.926	8.884	8.906	8.891	8.930	8.841	8.632	8.784	8.757	8.811
Average	9.008	8.995	9.038	8.973	9.022	8.984	8.987	8.767	8.839	8.875	8.876
Max	9.045	9.105	9.206	9.095	9.178	9.079	9.140	8.880	8.892	9.036	8.936
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



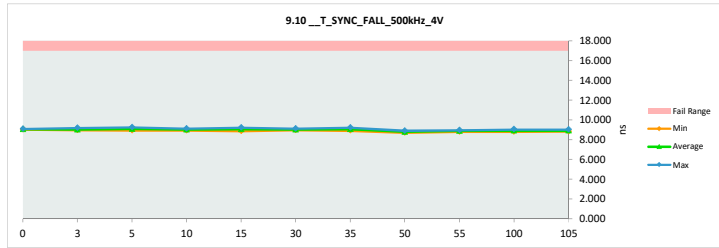
LDR TID Report
TPS7H5001-SP

9.10_T_SYNC_FALL_500kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.155	9.072	-0.083
0	118	9.129	9.039	-0.090
0	122	9.067	9.019	-0.048
3	61	9.008	8.973	-0.035
3	62	9.191	9.174	-0.017
3	63	9.065	8.971	-0.094
3	64	9.065	8.954	-0.111
3	65	9.094	9.005	-0.089
3	66	9.109	9.063	-0.046
5	67	8.930	8.918	-0.012
5	68	9.273	9.243	-0.030
5	69	9.068	9.073	0.005
5	70	8.939	8.934	-0.005
5	71	9.087	9.024	-0.063
5	72	9.206	9.240	0.034
10	73	9.008	8.990	-0.018
10	74	9.191	9.097	-0.094
10	75	9.065	8.992	-0.073
10	76	9.065	8.935	-0.130
10	77	9.094	9.013	-0.081
10	78	9.109	9.111	0.002
15	79	8.930	8.861	-0.069
15	80	9.273	9.224	-0.049
15	81	9.068	9.047	-0.021
15	82	8.939	8.925	-0.014
15	83	9.087	9.003	-0.084
15	84	9.206	9.185	-0.021
30	86	9.008	8.954	-0.054
30	89	9.191	9.126	-0.065
30	91	9.065	9.006	-0.059
30	92	9.065	8.957	-0.108
30	94	9.094	9.024	-0.070
30	95	9.109	9.043	-0.066
35	100	8.930	8.905	-0.025
35	101	9.273	9.220	-0.053
35	102	9.068	9.065	-0.003
35	103	8.939	8.936	-0.003
35	104	9.087	9.016	-0.071
35	105	9.206	9.176	-0.030
50	106	8.981	8.759	-0.222
50	107	8.897	8.732	-0.165
50	108	9.190	8.902	-0.288
50	109	8.940	8.693	-0.247
50	110	9.100	8.876	-0.224
50	111	9.009	8.814	-0.195
55	113	9.160	8.913	-0.247
55	114	9.037	8.846	-0.191
55	115	9.065	8.886	-0.179
55	116	9.083	8.952	-0.131
55	126	9.046	8.790	-0.256
55	128	9.052	8.882	-0.170
100	129	9.035	8.885	-0.150
100	131	9.243	9.035	-0.208
100	132	9.198	9.022	-0.176
100	133	9.148	8.978	-0.170
100	134	9.117	8.934	-0.183
100	135	9.125	8.910	-0.215
100	138	9.058	8.897	-0.161
100	139	8.994	8.824	-0.170
100	140	9.159	8.988	-0.171
100	141	9.031	8.874	-0.157
100	142	9.088	8.903	-0.185
100	143	8.956	8.848	-0.108
100	144	8.982	8.834	-0.148
100	220	9.154	8.930	-0.224
100	221	8.986	8.791	-0.195
100	224	9.088	8.946	-0.142
100	225	9.185	8.905	-0.280
100	226	9.035	8.917	-0.118
100	228	9.204	9.013	-0.191
100	232	9.049	8.882	-0.167
100	233	9.065	8.894	-0.171
100	236	8.998	8.790	-0.208
100	238	9.098	8.900	-0.198
100	243	9.061	8.903	-0.158
105	244	9.141	8.909	-0.232
105	246	9.150	9.023	-0.127
105	248	9.138	8.876	-0.262
105	374	8.962	8.817	-0.145
105	376	9.165	8.920	-0.245
105	Max	9.273	9.243	0.034
105	Average	9.083	8.961	-0.122
105	Min	8.897	8.693	-0.288
105	Std Dev	0.089	0.116	0.081



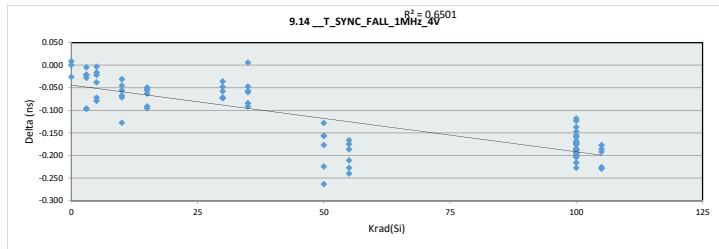
9.10_T_SYNC_FALL_500kHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.019	8.954	8.918	8.935	8.861	8.954	8.905	8.693	8.790	8.790	8.817
Average	9.043	9.023	9.072	9.023	9.041	9.018	9.053	8.796	8.878	8.908	8.909
Max	9.072	9.174	9.243	9.111	9.224	9.126	9.220	8.902	8.952	9.035	9.023
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



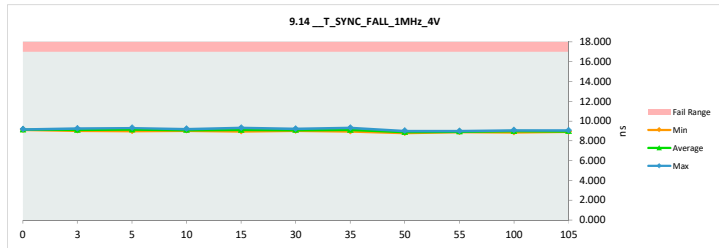
LDR TID Report
TPS7H5001-SP

9.14_T_SYNC_FALL_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.159	9.158	-0.001
0	118	9.173	9.146	-0.027
0	122	9.076	9.084	0.008
3	61	9.133	9.104	-0.029
3	62	9.257	9.235	-0.022
3	63	9.136	9.039	-0.097
3	64	9.050	9.027	-0.023
3	65	9.151	9.053	-0.098
3	66	9.189	9.183	-0.006
5	67	8.999	8.982	-0.017
5	68	9.383	9.303	-0.080
5	69	9.165	9.161	-0.004
5	70	9.004	8.981	-0.023
5	71	9.158	9.085	-0.073
5	72	9.299	9.260	-0.039
10	73	9.133	9.076	-0.057
10	74	9.257	9.189	-0.068
10	75	9.136	9.064	-0.072
10	76	9.050	9.018	-0.032
10	77	9.151	9.023	-0.128
10	78	9.189	9.143	-0.046
15	79	8.999	8.945	-0.054
15	80	9.383	9.291	-0.092
15	81	9.165	9.115	-0.050
15	82	9.004	8.940	-0.064
15	83	9.158	9.062	-0.096
15	84	9.299	9.242	-0.057
30	86	9.133	9.096	-0.037
30	89	9.257	9.198	-0.059
30	91	9.136	9.064	-0.072
30	92	9.050	9.001	-0.049
30	94	9.151	9.077	-0.074
30	95	9.189	9.115	-0.074
35	100	8.999	8.951	-0.048
35	101	9.383	9.291	-0.092
35	102	9.165	9.107	-0.058
35	103	9.004	9.009	0.005
35	104	9.158	9.097	-0.061
35	105	9.299	9.214	-0.085
50	106	9.136	8.872	-0.264
50	107	8.990	8.765	-0.225
50	108	9.121	8.992	-0.129
50	109	8.949	8.771	-0.178
50	110	9.122	8.965	-0.157
50	111	9.043	8.886	-0.157
55	113	9.211	8.983	-0.228
55	114	9.136	8.895	-0.241
55	115	9.143	8.931	-0.212
55	116	9.102	8.926	-0.176
55	126	9.052	8.885	-0.167
55	128	9.124	8.937	-0.187
100	129	9.164	8.947	-0.217
100	131	9.227	9.027	-0.200
100	132	9.256	9.051	-0.205
100	133	9.223	9.048	-0.175
100	134	9.191	9.004	-0.187
100	135	9.126	9.007	-0.119
100	138	9.153	8.976	-0.177
100	139	9.043	8.882	-0.161
100	140	9.248	9.045	-0.203
100	141	9.143	8.984	-0.159
100	142	9.139	8.983	-0.156
100	143	9.105	8.981	-0.124
100	144	9.030	8.841	-0.189
100	220	9.206	9.034	-0.172
100	221	9.039	8.866	-0.173
100	224	9.181	9.043	-0.138
100	225	9.211	9.017	-0.194
100	226	9.125	8.977	-0.148
100	228	9.225	9.030	-0.195
100	232	9.095	8.909	-0.186
100	233	9.115	8.915	-0.200
100	236	9.071	8.843	-0.228
100	238	9.176	9.007	-0.169
100	243	9.158	8.968	-0.190
105	244	9.155	8.925	-0.230
105	246	9.201	8.974	-0.227
105	248	9.133	8.947	-0.186
105	374	9.082	8.904	-0.178
105	376	9.226	9.033	-0.193
	Max	9.383	9.303	0.008
	Average	9.147	9.026	-0.121
	Min	8.949	8.765	-0.264
	Std Dev	0.091	0.116	0.074



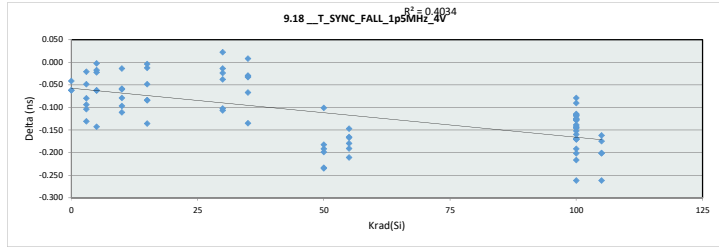
9.14_T_SYNC_FALL_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.084	9.027	8.981	9.018	8.940	9.001	8.951	8.765	8.885	8.841	8.904
Average	9.129	9.107	9.129	9.086	9.099	9.092	9.112	8.875	8.926	8.974	8.957
Max	9.158	9.235	9.303	9.189	9.291	9.198	9.291	8.992	8.983	9.051	9.033
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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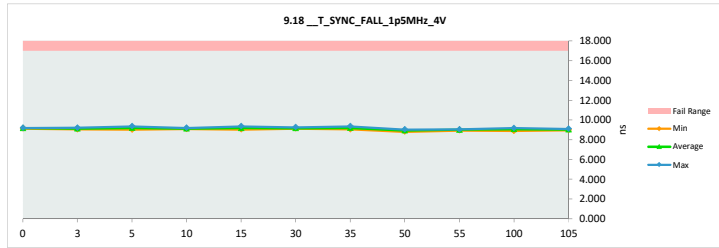
9.18_T_SYNC_FALL_1p5MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.244	9.182	-0.062
0	118	9.230	9.168	-0.062
0	122	9.145	9.103	-0.042
3	61	9.208	9.077	-0.131
3	62	9.289	9.185	-0.104
3	63	9.149	9.100	-0.049
3	64	9.120	9.026	-0.094
3	65	9.148	9.068	-0.080
3	66	9.209	9.188	-0.021
5	67	9.061	8.998	-0.063
5	68	9.480	9.337	-0.143
5	69	9.209	9.191	-0.018
5	70	9.055	9.052	-0.003
5	71	9.158	9.135	-0.023
5	72	9.347	9.285	-0.062
10	73	9.208	9.111	-0.097
10	74	9.289	9.178	-0.111
10	75	9.149	9.135	-0.014
10	76	9.120	9.060	-0.060
10	77	9.148	9.069	-0.079
10	78	9.209	9.150	-0.059
15	79	9.061	9.057	-0.004
15	80	9.480	9.344	-0.136
15	81	9.209	9.196	-0.013
15	82	9.055	9.006	-0.049
15	83	9.158	9.074	-0.084
15	84	9.347	9.263	-0.084
30	86	9.208	9.101	-0.107
30	89	9.289	9.186	-0.103
30	91	9.149	9.111	-0.038
30	92	9.120	9.106	-0.014
30	94	9.148	9.124	-0.024
30	95	9.209	9.231	0.022
35	100	9.061	9.031	-0.030
35	101	9.480	9.345	-0.135
35	102	9.209	9.177	-0.032
35	103	9.055	9.063	0.008
35	104	9.158	9.125	-0.033
35	105	9.347	9.280	-0.067
50	106	9.138	8.904	-0.234
50	107	8.962	8.881	-0.101
50	108	9.216	9.017	-0.199
50	109	8.968	8.776	-0.192
50	110	9.183	9.000	-0.183
50	111	9.152	8.917	-0.235
55	113	9.173	9.026	-0.147
55	114	9.202	9.011	-0.191
55	115	9.172	8.992	-0.180
55	116	9.214	9.003	-0.211
55	126	9.077	8.911	-0.166
55	128	9.160	8.993	-0.167
100	129	9.184	9.024	-0.160
100	131	9.284	9.115	-0.169
100	132	9.282	9.155	-0.127
100	133	9.291	9.146	-0.145
100	134	9.254	9.062	-0.192
100	135	9.202	9.075	-0.127
100	138	9.164	9.039	-0.125
100	139	9.077	8.935	-0.142
100	140	9.217	9.088	-0.129
100	141	9.249	8.987	-0.262
100	142	9.214	9.068	-0.146
100	143	9.102	9.011	-0.091
100	144	9.062	8.946	-0.116
100	220	9.242	9.071	-0.171
100	221	9.035	8.864	-0.171
100	224	9.223	9.104	-0.119
100	225	9.234	9.032	-0.202
100	226	9.139	9.024	-0.115
100	228	9.229	9.150	-0.079
100	232	9.127	8.988	-0.139
100	233	9.120	9.002	-0.118
100	236	9.122	8.951	-0.171
100	238	9.211	9.059	-0.152
100	243	9.251	9.034	-0.217
105	244	9.255	8.993	-0.262
105	246	9.230	9.068	-0.162
105	248	9.208	9.006	-0.202
105	374	9.112	8.937	-0.175
105	376	9.253	9.052	-0.201
105	Max	9.480	9.345	0.022
105	Average	9.189	9.075	-0.114
105	Min	8.968	8.776	-0.262
105	Std Dev	0.098	0.109	0.069



9.18_T_SYNC_Fa	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

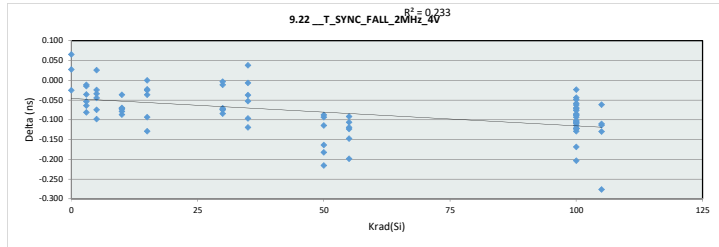
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.103	9.026	8.998	9.060	9.006	9.101	9.031	8.776	8.911	8.864	8.937
Average	9.151	9.107	9.166	9.117	9.157	9.143	9.170	8.916	8.989	9.039	9.011
Max	9.182	9.188	9.337	9.178	9.344	9.231	9.345	9.017	9.026	9.155	9.068
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



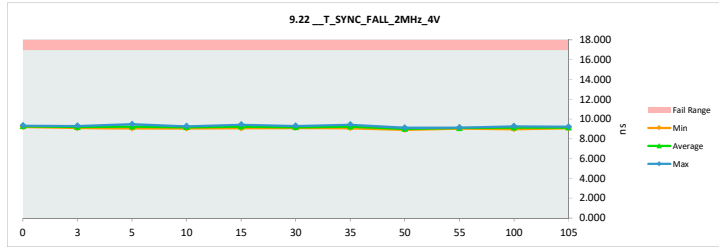
LDR TID Report
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9.22_T_SYNC_FALL_2MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.207	9.271	0.064
0	118	9.335	9.308	-0.027
0	122	9.161	9.187	0.026
3	61	9.206	9.140	-0.066
3	62	9.351	9.296	-0.055
3	63	9.244	9.161	-0.083
3	64	9.116	9.079	-0.037
3	65	9.142	9.129	-0.013
3	66	9.264	9.248	-0.016
5	67	9.072	9.096	0.024
5	68	9.542	9.466	-0.076
5	69	9.246	9.200	-0.046
5	70	9.072	9.046	-0.026
5	71	9.236	9.136	-0.100
5	72	9.426	9.390	-0.036
10	73	9.206	9.135	-0.071
10	74	9.351	9.263	-0.088
10	75	9.244	9.164	-0.080
10	76	9.116	9.042	-0.074
10	77	9.142	9.104	-0.038
10	78	9.264	9.190	-0.074
15	79	9.072	9.070	-0.002
15	80	9.542	9.412	-0.130
15	81	9.246	9.208	-0.038
15	82	9.072	9.048	-0.024
15	83	9.236	9.141	-0.095
15	84	9.426	9.399	-0.027
30	86	9.206	9.130	-0.076
30	89	9.351	9.276	-0.075
30	91	9.244	9.158	-0.086
30	92	9.116	9.103	-0.013
30	94	9.142	9.137	-0.005
30	95	9.264	9.192	-0.072
35	100	9.072	9.064	-0.008
35	101	9.542	9.421	-0.121
35	102	9.246	9.207	-0.039
35	103	9.072	9.108	0.036
35	104	9.236	9.138	-0.098
35	105	9.426	9.372	-0.054
50	106	9.163	8.998	-0.165
50	107	9.018	8.902	-0.116
50	108	9.200	9.111	-0.089
50	109	9.013	8.918	-0.095
50	110	9.271	9.054	-0.217
50	111	9.118	8.934	-0.184
55	113	9.208	9.087	-0.121
55	114	9.240	9.091	-0.149
55	115	9.257	9.057	-0.200
55	116	9.247	9.139	-0.108
55	126	9.153	9.028	-0.125
55	128	9.175	9.082	-0.093
100	129	9.276	9.152	-0.124
100	131	9.383	9.213	-0.170
100	132	9.316	9.238	-0.078
100	133	9.412	9.207	-0.205
100	134	9.245	9.181	-0.064
100	135	9.141	9.091	-0.050
100	138	9.211	9.124	-0.087
100	139	9.087	9.062	-0.025
100	140	9.353	9.263	-0.090
100	141	9.158	9.113	-0.045
100	142	9.314	9.198	-0.116
100	143	9.191	9.102	-0.089
100	144	9.138	9.030	-0.108
100	220	9.340	9.229	-0.111
100	221	9.100	8.977	-0.123
100	224	9.297	9.226	-0.071
100	225	9.184	9.054	-0.130
100	226	9.205	9.081	-0.124
100	228	9.327	9.232	-0.095
100	232	9.186	9.112	-0.074
100	233	9.150	9.043	-0.107
100	236	9.070	9.010	-0.060
100	238	9.259	9.155	-0.104
100	243	9.270	9.147	-0.123
105	244	9.352	9.074	-0.278
105	246	9.340	9.209	-0.131
105	248	9.249	9.137	-0.112
105	374	9.151	9.088	-0.063
105	376	9.278	9.163	-0.115
	Max	9.542	9.466	0.064
	Average	9.231	9.149	-0.082
	Min	9.013	8.902	-0.278
	Std Dev	0.116	0.112	0.058



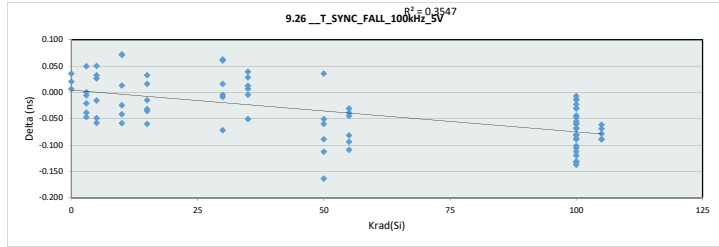
9.22_T_SYNC_FALL_2MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.187	9.079	9.046	9.042	9.048	9.103	9.064	8.902	9.028	8.977	9.074
Average	9.255	9.176	9.222	9.150	9.213	9.166	9.218	8.986	9.081	9.135	9.134
Max	9.308	9.296	9.466	9.263	9.412	9.276	9.421	9.111	9.139	9.263	9.209
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



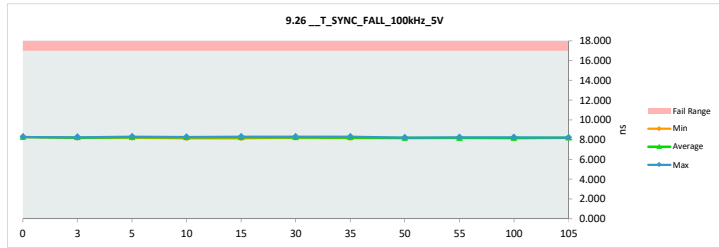
LDR TID Report
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9.26_T_SYNC_FALL_100kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.261	8.281	0.020
0	118	8.236	8.271	0.035
0	122	8.261	8.267	0.006
3	61	8.186	8.180	-0.006
3	62	8.304	8.257	-0.047
3	63	8.230	8.209	-0.021
3	64	8.213	8.213	0.000
3	65	8.192	8.241	0.049
3	66	8.282	8.243	-0.039
5	67	8.174	8.224	0.050
5	68	8.266	8.298	0.032
5	69	8.277	8.219	-0.058
5	70	8.193	8.219	0.026
5	71	8.275	8.226	-0.049
5	72	8.284	8.268	-0.016
10	73	8.186	8.257	0.071
10	74	8.304	8.262	-0.042
10	75	8.230	8.243	0.013
10	76	8.213	8.154	-0.059
10	77	8.192	8.264	0.072
10	78	8.282	8.257	-0.025
15	79	8.174	8.138	-0.036
15	80	8.266	8.298	0.032
15	81	8.277	8.245	-0.032
15	82	8.193	8.209	0.016
15	83	8.275	8.260	-0.015
15	84	8.284	8.224	-0.060
30	86	8.186	8.246	0.060
30	89	8.304	8.299	-0.005
30	91	8.230	8.221	-0.009
30	92	8.213	8.229	0.016
30	94	8.192	8.254	0.062
30	95	8.282	8.210	-0.072
35	100	8.174	8.181	0.007
35	101	8.266	8.294	0.028
35	102	8.277	8.272	-0.005
35	103	8.193	8.232	0.039
35	104	8.275	8.224	-0.051
35	105	8.284	8.296	0.012
50	106	8.260	8.209	-0.051
50	107	8.216	8.156	-0.060
50	108	8.297	8.133	-0.164
50	109	8.262	8.149	-0.113
50	110	8.257	8.168	-0.089
50	111	8.161	8.196	0.035
55	113	8.218	8.173	-0.045
55	114	8.258	8.164	-0.094
55	115	8.277	8.238	-0.039
55	116	8.247	8.216	-0.031
55	126	8.276	8.167	-0.109
55	128	8.252	8.170	-0.082
100	129	8.297	8.191	-0.106
100	131	8.284	8.253	-0.031
100	132	8.291	8.231	-0.060
100	133	8.317	8.179	-0.138
100	134	8.329	8.196	-0.133
100	135	8.236	8.176	-0.060
100	138	8.266	8.164	-0.102
100	139	8.235	8.146	-0.089
100	140	8.259	8.179	-0.080
100	141	8.259	8.177	-0.082
100	142	8.277	8.189	-0.088
100	143	8.185	8.171	-0.014
100	144	8.239	8.195	-0.044
100	220	8.205	8.198	-0.007
100	221	8.271	8.151	-0.120
100	224	8.304	8.191	-0.113
100	225	8.286	8.217	-0.069
100	226	8.216	8.202	-0.014
100	228	8.292	8.224	-0.068
100	232	8.219	8.189	-0.030
100	233	8.245	8.197	-0.048
100	236	8.204	8.149	-0.055
100	238	8.315	8.184	-0.131
100	243	8.207	8.184	-0.023
105	244	8.299	8.210	-0.089
105	246	8.295	8.206	-0.089
105	248	8.255	8.193	-0.062
105	374	8.237	8.168	-0.069
105	376	8.277	8.198	-0.079
105	Max	8.329	8.299	0.072
105	Average	8.250	8.213	-0.037
105	Min	8.161	8.133	-0.164
105	Std Dev	0.041	0.042	0.054



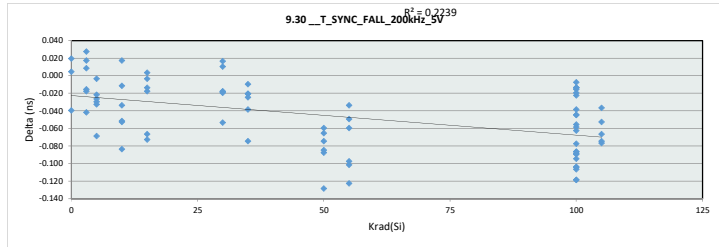
9.26_T_SYNC_FALL_100kHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.267	8.180	8.219	8.154	8.138	8.210	8.181	8.133	8.164	8.146	8.168
Average	8.273	8.224	8.242	8.240	8.229	8.243	8.250	8.169	8.188	8.189	8.195
Max	8.281	8.257	8.298	8.264	8.298	8.299	8.296	8.209	8.238	8.253	8.210
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



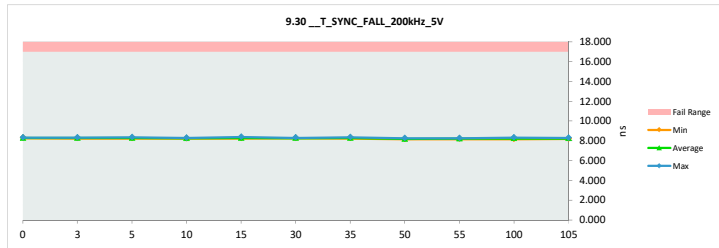
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9.30_T_SYNC_FALL_200kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.313	8.332	0.019
0	118	8.323	8.283	-0.040
0	122	8.269	8.273	0.004
3	61	8.307	8.324	0.017
3	62	8.332	8.316	-0.016
3	63	8.282	8.290	0.008
3	64	8.284	8.242	-0.042
3	65	8.241	8.268	0.027
3	66	8.336	8.318	-0.018
5	67	8.280	8.258	-0.022
5	68	8.372	8.368	-0.004
5	69	8.312	8.282	-0.030
5	70	8.274	8.241	-0.033
5	71	8.339	8.270	-0.069
5	72	8.326	8.299	-0.027
10	73	8.307	8.223	-0.084
10	74	8.332	8.279	-0.053
10	75	8.282	8.248	-0.034
10	76	8.284	8.272	-0.012
10	77	8.241	8.258	0.017
10	78	8.336	8.284	-0.052
15	79	8.280	8.276	-0.004
15	80	8.372	8.375	0.003
15	81	8.312	8.245	-0.067
15	82	8.274	8.256	-0.018
15	83	8.339	8.266	-0.073
15	84	8.326	8.312	-0.014
30	86	8.307	8.289	-0.018
30	89	8.332	8.312	-0.020
30	91	8.282	8.298	0.016
30	92	8.284	8.265	-0.019
30	94	8.241	8.251	0.010
30	95	8.336	8.282	-0.054
35	100	8.280	8.241	-0.039
35	101	8.372	8.351	-0.021
35	102	8.312	8.287	-0.025
35	103	8.274	8.253	-0.021
35	104	8.339	8.264	-0.075
35	105	8.326	8.316	-0.010
50	106	8.266	8.178	-0.088
50	107	8.288	8.159	-0.129
50	108	8.327	8.252	-0.075
50	109	8.234	8.168	-0.066
50	110	8.308	8.223	-0.085
50	111	8.271	8.211	-0.060
55	113	8.279	8.245	-0.034
55	114	8.321	8.219	-0.102
55	115	8.304	8.206	-0.098
55	116	8.299	8.239	-0.060
55	126	8.307	8.184	-0.123
55	128	8.294	8.244	-0.050
100	129	8.337	8.250	-0.087
100	131	8.354	8.235	-0.119
100	132	8.349	8.260	-0.089
100	133	8.328	8.265	-0.063
100	134	8.325	8.220	-0.105
100	135	8.305	8.282	-0.023
100	138	8.298	8.239	-0.059
100	139	8.320	8.213	-0.107
100	140	8.336	8.291	-0.045
100	141	8.284	8.165	-0.119
100	142	8.329	8.273	-0.056
100	143	8.286	8.191	-0.095
100	144	8.254	8.209	-0.045
100	220	8.333	8.317	-0.016
100	221	8.269	8.249	-0.020
100	224	8.323	8.284	-0.039
100	225	8.316	8.238	-0.078
100	226	8.270	8.256	-0.014
100	228	8.329	8.225	-0.104
100	232	8.307	8.217	-0.090
100	233	8.275	8.267	-0.008
100	236	8.252	8.238	-0.014
100	238	8.295	8.281	-0.014
100	243	8.282	8.195	-0.087
105	244	8.327	8.290	-0.037
105	246	8.331	8.254	-0.077
105	248	8.310	8.257	-0.053
105	374	8.272	8.205	-0.067
105	376	8.307	8.232	-0.075
105	Max	8.372	8.375	0.027
105	Average	8.305	8.259	-0.046
105	Min	8.234	8.159	-0.129
105	Std Dev	0.031	0.044	0.038



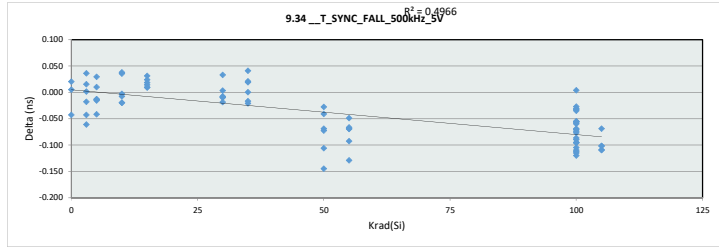
9.30_T_SYNC_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.273	8.242	8.241	8.223	8.245	8.251	8.241	8.159	8.184	8.165	8.205
Average	8.296	8.293	8.286	8.261	8.288	8.283	8.285	8.199	8.223	8.244	8.248
Max	8.332	8.324	8.368	8.284	8.375	8.312	8.351	8.252	8.245	8.317	8.290
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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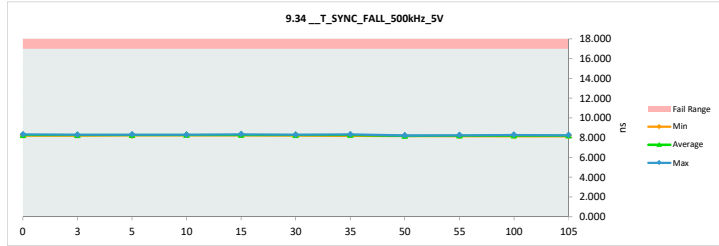
9.34 T_SYNC_FALL_500kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.303	8.308	0.005
0	118	8.240	8.197	-0.043
0	122	8.251	8.251	0.020
3	61	8.264	8.279	0.015
3	62	8.287	8.226	-0.061
3	63	8.236	8.218	-0.018
3	64	8.233	8.234	0.001
3	65	8.251	8.208	-0.043
3	66	8.246	8.282	0.036
5	67	8.199	8.186	-0.013
5	68	8.307	8.294	-0.013
5	69	8.246	8.230	-0.016
5	70	8.216	8.245	0.029
5	71	8.264	8.222	-0.042
5	72	8.270	8.280	0.010
10	73	8.264	8.244	-0.020
10	74	8.287	8.267	-0.020
10	75	8.236	8.271	0.035
10	76	8.233	8.225	-0.008
10	77	8.251	8.248	-0.003
10	78	8.246	8.284	0.038
15	79	8.199	8.230	0.031
15	80	8.307	8.325	0.018
15	81	8.246	8.270	0.024
15	82	8.216	8.225	0.009
15	83	8.264	8.273	0.009
15	84	8.270	8.284	0.014
30	86	8.264	8.245	-0.019
30	89	8.287	8.278	-0.009
30	91	8.236	8.269	0.033
30	92	8.233	8.223	-0.010
30	94	8.251	8.243	-0.008
30	95	8.246	8.249	0.003
35	100	8.199	8.218	0.019
35	101	8.307	8.307	0.000
35	102	8.246	8.267	0.021
35	103	8.216	8.199	-0.017
35	104	8.264	8.243	-0.021
35	105	8.270	8.311	0.041
50	106	8.291	8.185	-0.106
50	107	8.214	8.173	-0.041
50	108	8.293	8.220	-0.073
50	109	8.200	8.172	-0.028
50	110	8.272	8.127	-0.145
50	111	8.245	8.176	-0.069
55	113	8.272	8.223	-0.049
55	114	8.258	8.189	-0.069
55	115	8.296	8.203	-0.093
55	116	8.252	8.182	-0.070
55	126	8.259	8.130	-0.129
55	128	8.293	8.226	-0.067
100	129	8.248	8.143	-0.105
100	131	8.325	8.230	-0.095
100	132	8.314	8.218	-0.096
100	133	8.281	8.166	-0.115
100	134	8.299	8.179	-0.120
100	135	8.292	8.182	-0.110
100	138	8.220	8.162	-0.058
100	139	8.214	8.137	-0.077
100	140	8.277	8.206	-0.071
100	141	8.286	8.190	-0.096
100	142	8.266	8.192	-0.074
100	143	8.224	8.228	0.004
100	144	8.196	8.169	-0.027
100	220	8.294	8.207	-0.087
100	221	8.230	8.197	-0.033
100	224	8.296	8.261	-0.035
100	225	8.285	8.225	-0.060
100	226	8.261	8.205	-0.056
100	228	8.279	8.224	-0.055
100	232	8.233	8.202	-0.031
100	233	8.243	8.187	-0.056
100	236	8.268	8.178	-0.090
100	238	8.293	8.180	-0.113
100	243	8.241	8.172	-0.069
105	244	8.269	8.200	-0.069
105	246	8.327	8.225	-0.102
105	248	8.298	8.188	-0.110
105	374	8.238	8.129	-0.109
105	376	8.330	8.228	-0.102
105	375	8.330	8.325	0.004
	Max	8.330	8.325	0.004
	Average	8.260	8.221	-0.039
	Min	8.196	8.127	-0.145
	Std Dev	0.033	0.045	0.048



9.34 T_SYNC FA	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

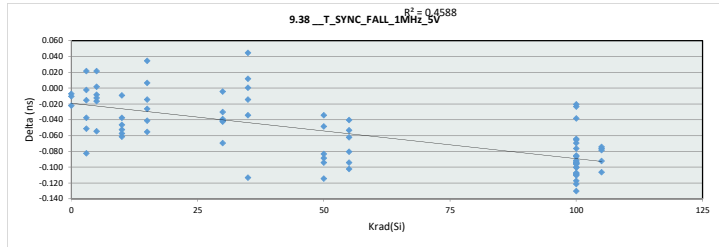
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.197	8.208	8.186	8.225	8.225	8.223	8.199	8.127	8.130	8.137	8.129
Average	8.252	8.241	8.243	8.257	8.268	8.251	8.258	8.176	8.192	8.193	8.194
Max	8.308	8.282	8.294	8.284	8.325	8.278	8.311	8.220	8.226	8.261	8.228
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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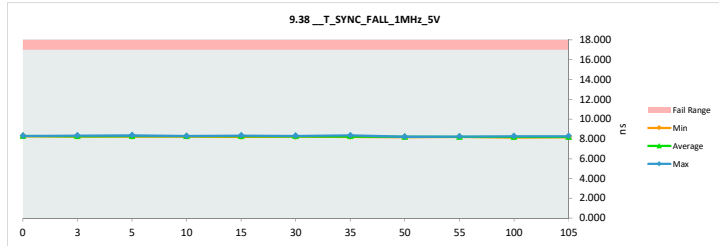
9.38 T_SYNC_FALL_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.313	8.290	-0.023
0	118	8.291	8.283	-0.008
0	122	8.273	8.262	-0.011
3	61	8.262	8.283	0.021
3	62	8.331	8.328	-0.003
3	63	8.296	8.244	-0.052
3	64	8.249	8.233	-0.016
3	65	8.303	8.220	-0.083
3	66	8.298	8.260	-0.038
5	67	8.227	8.218	-0.009
5	68	8.336	8.337	0.001
5	69	8.274	8.295	0.021
5	70	8.242	8.229	-0.013
5	71	8.295	8.240	-0.055
5	72	8.316	8.299	-0.017
10	73	8.262	8.252	-0.010
10	74	8.331	8.269	-0.062
10	75	8.296	8.249	-0.047
10	76	8.249	8.211	-0.038
10	77	8.303	8.250	-0.053
10	78	8.298	8.240	-0.058
15	79	8.227	8.261	0.034
15	80	8.336	8.321	-0.015
15	81	8.274	8.280	0.006
15	82	8.242	8.200	-0.042
15	83	8.295	8.239	-0.056
15	84	8.316	8.289	-0.027
30	86	8.262	8.222	-0.040
30	89	8.331	8.288	-0.043
30	91	8.296	8.265	-0.031
30	92	8.249	8.207	-0.042
30	94	8.303	8.233	-0.070
30	95	8.298	8.293	-0.005
35	100	8.227	8.271	0.044
35	101	8.336	8.336	0.000
35	102	8.274	8.239	-0.035
35	103	8.242	8.227	-0.015
35	104	8.295	8.181	-0.114
35	105	8.316	8.327	0.011
50	106	8.254	8.205	-0.049
50	107	8.252	8.168	-0.084
50	108	8.294	8.199	-0.095
50	109	8.275	8.161	-0.115
50	110	8.277	8.188	-0.089
50	111	8.211	8.176	-0.035
55	113	8.251	8.197	-0.054
55	114	8.262	8.181	-0.081
55	115	8.258	8.217	-0.041
55	116	8.263	8.200	-0.063
55	126	8.285	8.190	-0.095
55	128	8.307	8.204	-0.103
100	129	8.306	8.205	-0.101
100	131	8.343	8.247	-0.096
100	132	8.326	8.256	-0.070
100	133	8.293	8.171	-0.122
100	134	8.326	8.208	-0.118
100	135	8.274	8.209	-0.065
100	138	8.308	8.214	-0.094
100	139	8.268	8.191	-0.077
100	140	8.294	8.183	-0.111
100	141	8.286	8.190	-0.096
100	142	8.274	8.208	-0.066
100	143	8.268	8.160	-0.108
100	144	8.252	8.157	-0.095
100	220	8.318	8.226	-0.092
100	221	8.251	8.164	-0.087
100	224	8.294	8.185	-0.109
100	225	8.316	8.215	-0.101
100	226	8.258	8.219	-0.039
100	228	8.283	8.262	-0.021
100	232	8.271	8.140	-0.131
100	233	8.264	8.240	-0.024
100	236	8.246	8.160	-0.086
100	238	8.317	8.207	-0.110
100	243	8.274	8.177	-0.097
105	244	8.262	8.155	-0.107
105	246	8.317	8.240	-0.077
105	248	8.280	8.187	-0.093
105	374	8.225	8.150	-0.075
105	375	8.318	8.239	-0.079
105	376	8.343	8.337	0.004
	Max	8.283	8.228	-0.056
	Average	8.283	8.228	-0.056
	Min	8.211	8.140	-0.131
	Std Dev	0.031	0.047	0.042



9.38 T_SYNC FA	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns
Krad(Si)	0
LL	3
Min	8.262
Average	8.278
Max	8.290
UL	17.000

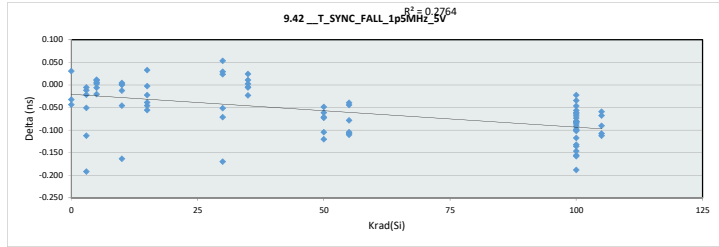
	3	5	10	15	30	35	50	55	100	105
Min	8.220	8.218	8.211	8.200	8.207	8.181	8.161	8.181	8.140	8.150
Average	8.261	8.270	8.245	8.265	8.251	8.264	8.183	8.198	8.200	8.194
Max	8.328	8.337	8.269	8.321	8.293	8.336	8.205	8.217	8.262	8.240
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



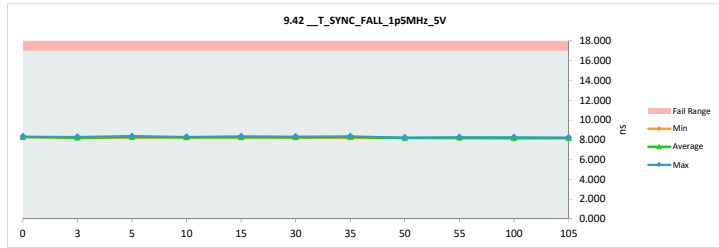
LDR TID Report
TPS7H5001-SP

9.42_T_SYNC_FALL_1p5MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.302	8.258	-0.044
0	118	8.282	8.312	0.030
0	122	8.300	8.267	-0.033
3	61	8.300	8.187	-0.113
3	62	8.419	8.227	-0.192
3	63	8.220	8.208	-0.012
3	64	8.216	8.194	-0.022
3	65	8.249	8.198	-0.051
3	66	8.272	8.266	-0.006
5	67	8.215	8.208	-0.007
5	68	8.340	8.351	0.011
5	69	8.244	8.223	-0.021
5	70	8.238	8.240	0.002
5	71	8.277	8.282	0.005
5	72	8.332	8.341	0.009
10	73	8.300	8.254	-0.046
10	74	8.419	8.255	-0.164
10	75	8.220	8.221	0.001
10	76	8.216	8.220	0.004
10	77	8.249	8.249	0.000
10	78	8.272	8.259	-0.013
15	79	8.215	8.192	-0.023
15	80	8.340	8.337	-0.003
15	81	8.244	8.276	0.032
15	82	8.238	8.199	-0.039
15	83	8.277	8.231	-0.046
15	84	8.332	8.276	-0.056
30	86	8.300	8.228	-0.072
30	89	8.419	8.249	-0.170
30	91	8.220	8.249	0.029
30	92	8.216	8.239	0.023
30	94	8.249	8.302	0.053
30	95	8.272	8.220	-0.052
35	100	8.215	8.239	0.024
35	101	8.340	8.334	-0.006
35	102	8.244	8.254	0.010
35	103	8.238	8.214	-0.024
35	104	8.277	8.279	0.002
35	105	8.332	8.327	-0.005
50	106	8.258	8.137	-0.121
50	107	8.211	8.162	-0.049
50	108	8.300	8.195	-0.105
50	109	8.250	8.177	-0.073
50	110	8.272	8.200	-0.072
50	111	8.244	8.182	-0.062
55	113	8.243	8.138	-0.105
55	114	8.274	8.167	-0.107
55	115	8.253	8.213	-0.040
55	116	8.221	8.176	-0.045
55	126	8.321	8.242	-0.079
55	128	8.325	8.214	-0.111
100	129	8.280	8.199	-0.081
100	131	8.265	8.230	-0.035
100	132	8.394	8.205	-0.189
100	133	8.385	8.249	-0.136
100	134	8.342	8.184	-0.158
100	135	8.230	8.147	-0.083
100	138	8.256	8.164	-0.092
100	139	8.252	8.151	-0.101
100	140	8.283	8.218	-0.065
100	141	8.270	8.183	-0.087
100	142	8.310	8.241	-0.069
100	143	8.220	8.158	-0.062
100	144	8.287	8.188	-0.099
100	220	8.284	8.237	-0.047
100	221	8.245	8.112	-0.133
100	224	8.258	8.177	-0.081
100	225	8.302	8.245	-0.057
100	226	8.302	8.184	-0.118
100	228	8.299	8.152	-0.147
100	232	8.254	8.231	-0.023
100	233	8.272	8.190	-0.082
100	236	8.218	8.116	-0.102
100	238	8.248	8.174	-0.074
100	243	8.294	8.138	-0.156
105	244	8.237	8.169	-0.068
105	246	8.306	8.198	-0.108
105	248	8.296	8.205	-0.091
105	374	8.190	8.130	-0.060
105	376	8.302	8.189	-0.113
105	Max	8.419	8.351	0.053
105	Average	8.276	8.218	-0.058
105	Min	8.190	8.112	-0.192
105	Std Dev	0.050	0.053	0.056



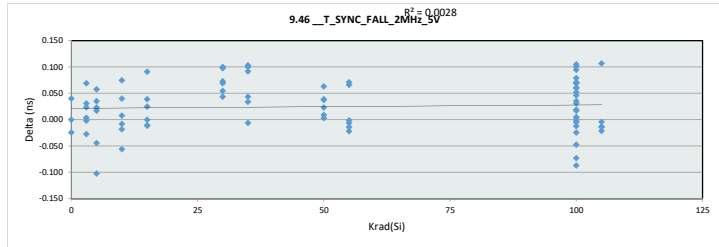
9.42_T_SYNC_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.258	8.187	8.208	8.220	8.192	8.220	8.214	8.137	8.138	8.112	8.130
Average	8.279	8.213	8.274	8.243	8.252	8.248	8.275	8.176	8.192	8.186	8.178
Max	8.312	8.266	8.351	8.259	8.337	8.302	8.334	8.200	8.242	8.249	8.205
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



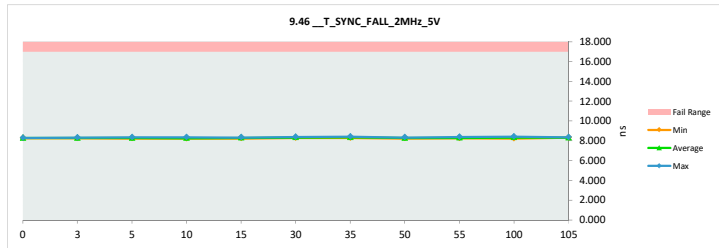
LDR TID Report
TPS7H5001-SP

9.46 T_SYNC_FALL_2MHZ_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.228	8.267	0.039
0	118	8.296	8.295	-0.001
0	122	8.289	8.264	-0.025
3	61	8.283	8.280	-0.003
3	62	8.291	8.314	0.023
3	63	8.232	8.300	0.068
3	64	8.283	8.255	-0.028
3	65	8.233	8.263	0.030
3	66	8.296	8.299	0.003
5	67	8.330	8.285	-0.045
5	68	8.313	8.347	0.034
5	69	8.254	8.270	0.016
5	70	8.228	8.285	0.057
5	71	8.272	8.294	0.022
5	72	8.345	8.242	-0.103
10	73	8.283	8.226	-0.057
10	74	8.291	8.365	0.074
10	75	8.232	8.271	0.039
10	76	8.283	8.274	-0.009
10	77	8.233	8.214	-0.019
10	78	8.296	8.303	0.007
15	79	8.330	8.329	-0.001
15	80	8.313	8.337	0.024
15	81	8.254	8.242	-0.012
15	82	8.228	8.318	0.090
15	83	8.272	8.310	0.038
15	84	8.345	8.333	-0.012
30	86	8.283	8.380	0.097
30	89	8.291	8.363	0.072
30	91	8.232	8.286	0.054
30	92	8.283	8.326	0.043
30	94	8.233	8.301	0.068
30	95	8.296	8.395	0.099
35	100	8.330	8.363	0.033
35	101	8.313	8.412	0.099
35	102	8.254	8.297	0.043
35	103	8.228	8.330	0.102
35	104	8.272	8.363	0.091
35	105	8.345	8.338	-0.007
50	106	8.326	8.334	0.008
50	107	8.249	8.251	0.002
50	108	8.263	8.300	0.037
50	109	8.207	8.245	0.038
50	110	8.281	8.343	0.062
50	111	8.260	8.282	0.022
55	113	8.317	8.387	0.070
55	114	8.257	8.322	0.065
55	115	8.299	8.292	-0.007
55	116	8.312	8.297	-0.015
55	126	8.280	8.257	-0.023
55	128	8.305	8.303	-0.002
100	129	8.278	8.347	0.069
100	131	8.377	8.411	0.034
100	132	8.318	8.422	0.104
100	133	8.370	8.388	0.018
100	134	8.330	8.408	0.078
100	135	8.282	8.234	-0.048
100	138	8.261	8.361	0.100
100	139	8.252	8.320	0.068
100	140	8.312	8.406	0.094
100	141	8.312	8.310	-0.002
100	142	8.313	8.225	-0.088
100	143	8.299	8.286	-0.013
100	144	8.328	8.322	-0.006
100	220	8.389	8.315	-0.074
100	221	8.283	8.353	0.070
100	224	8.287	8.346	0.059
100	225	8.311	8.356	0.045
100	226	8.337	8.388	0.051
100	228	8.394	8.369	-0.025
100	232	8.341	8.344	0.003
100	233	8.266	8.326	0.060
100	236	8.275	8.279	0.004
100	238	8.344	8.374	0.030
100	243	8.335	8.351	0.016
105	244	8.351	8.329	-0.022
105	246	8.343	8.338	-0.005
105	248	8.354	8.339	-0.015
105	374	8.208	8.314	0.106
105	375	8.363	8.349	-0.014
105	376	8.394	8.422	0.028
Max		8.394	8.422	0.028
Average		8.293	8.318	0.025
Min		8.207	8.214	-0.103
Std Dev		0.043	0.049	0.047



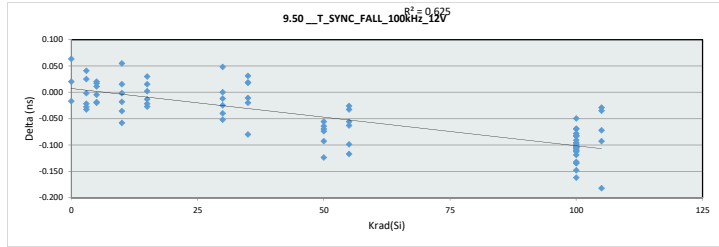
9.46 T_SYNC FA											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.264	8.255	8.242	8.214	8.242	8.286	8.297	8.245	8.257	8.225	8.314
Average	8.275	8.285	8.287	8.276	8.312	8.342	8.351	8.293	8.310	8.343	8.334
Max	8.295	8.314	8.347	8.365	8.337	8.395	8.412	8.343	8.387	8.422	8.349
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



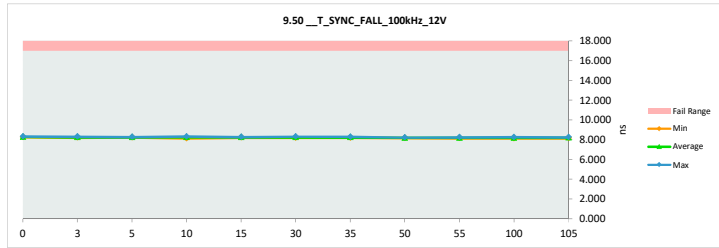
LDR TID Report
TPS7H5001-SP

9.50_T_SYNC_FALL_100kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.242	8.305	0.063
0	118	8.306	8.289	-0.017
0	122	8.213	8.233	0.020
3	61	8.267	8.234	-0.033
3	62	8.236	8.234	-0.002
3	63	8.161	8.202	0.041
3	64	8.189	8.214	0.025
3	65	8.323	8.301	-0.022
3	66	8.263	8.235	-0.028
5	67	8.220	8.231	0.011
5	68	8.256	8.236	-0.020
5	69	8.203	8.220	0.017
5	70	8.268	8.249	-0.019
5	71	8.230	8.225	-0.005
5	72	8.246	8.266	0.020
10	73	8.267	8.282	0.015
10	74	8.236	8.234	-0.002
10	75	8.161	8.125	-0.036
10	76	8.189	8.244	0.055
10	77	8.323	8.305	-0.018
10	78	8.263	8.205	-0.058
15	79	8.220	8.193	-0.027
15	80	8.256	8.234	-0.022
15	81	8.203	8.233	0.030
15	82	8.268	8.255	-0.013
15	83	8.230	8.232	0.002
15	84	8.246	8.261	0.015
30	86	8.267	8.215	-0.052
30	89	8.236	8.196	-0.040
30	91	8.161	8.209	0.048
30	92	8.189	8.189	0.000
30	94	8.323	8.311	-0.012
30	95	8.263	8.238	-0.025
35	100	8.220	8.251	0.031
35	101	8.256	8.274	0.018
35	102	8.203	8.192	-0.011
35	103	8.268	8.188	-0.080
35	104	8.230	8.210	-0.020
35	105	8.246	8.264	0.018
50	106	8.264	8.190	-0.074
50	107	8.265	8.172	-0.093
50	108	8.263	8.207	-0.056
50	109	8.209	8.145	-0.064
50	110	8.324	8.200	-0.124
50	111	8.257	8.187	-0.070
55	113	8.282	8.226	-0.056
55	114	8.229	8.166	-0.063
55	115	8.183	8.150	-0.033
55	116	8.248	8.131	-0.117
55	126	8.292	8.193	-0.099
55	128	8.213	8.187	-0.026
100	129	8.317	8.214	-0.103
100	131	8.306	8.158	-0.148
100	132	8.264	8.195	-0.069
100	133	8.340	8.242	-0.098
100	134	8.280	8.145	-0.135
100	135	8.244	8.161	-0.083
100	138	8.280	8.173	-0.107
100	139	8.286	8.181	-0.105
100	140	8.302	8.224	-0.078
100	141	8.269	8.159	-0.110
100	142	8.233	8.128	-0.105
100	143	8.287	8.153	-0.134
100	144	8.240	8.157	-0.083
100	220	8.277	8.145	-0.132
100	221	8.225	8.114	-0.111
100	224	8.311	8.149	-0.162
100	225	8.338	8.247	-0.091
100	226	8.260	8.190	-0.070
100	228	8.278	8.228	-0.050
100	232	8.274	8.192	-0.082
100	233	8.312	8.193	-0.119
100	236	8.281	8.168	-0.113
100	238	8.299	8.198	-0.101
100	243	8.314	8.218	-0.096
105	244	8.292	8.220	-0.072
105	246	8.216	8.187	-0.029
105	248	8.280	8.187	-0.093
105	374	8.270	8.235	-0.035
105	376	8.316	8.134	-0.182
105	Max	8.340	8.311	0.029
105	Average	8.257	8.208	-0.049
105	Min	8.161	8.114	-0.047
105	Std Dev	0.042	0.045	0.055



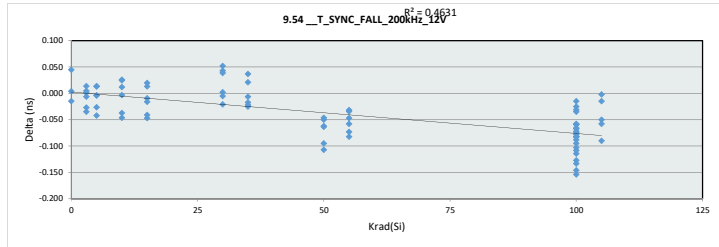
9.50_T_SYNC_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.233	8.202	8.220	8.125	8.193	8.189	8.188	8.145	8.131	8.114	8.134
Average	8.276	8.237	8.238	8.233	8.235	8.226	8.230	8.184	8.176	8.181	8.193
Max	8.305	8.301	8.266	8.305	8.261	8.311	8.274	8.207	8.226	8.247	8.235
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



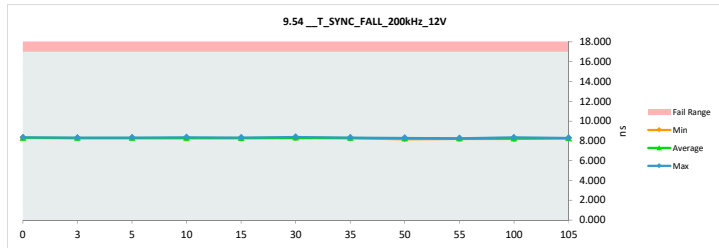
LDR TID Report
TPS7H5001-SP

9.54_T_SYNC_FALL_200kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.302	8.286	-0.016
0	118	8.333	8.336	0.003
0	122	8.246	8.290	0.044
3	61	8.291	8.295	0.004
3	62	8.302	8.274	-0.028
3	63	8.230	8.243	0.013
3	64	8.273	8.273	0.000
3	65	8.339	8.303	-0.036
3	66	8.291	8.284	-0.007
5	67	8.249	8.244	-0.005
5	68	8.314	8.326	0.012
5	69	8.298	8.311	0.013
5	70	8.280	8.253	-0.027
5	71	8.263	8.258	-0.005
5	72	8.309	8.266	-0.043
10	73	8.291	8.287	-0.004
10	74	8.302	8.255	-0.047
10	75	8.230	8.254	0.024
10	76	8.273	8.235	-0.038
10	77	8.339	8.350	0.011
10	78	8.291	8.316	0.025
15	79	8.249	8.268	0.019
15	80	8.314	8.304	-0.010
15	81	8.298	8.310	0.012
15	82	8.280	8.232	-0.048
15	83	8.263	8.246	-0.017
15	84	8.309	8.267	-0.042
30	86	8.291	8.342	0.051
30	89	8.302	8.280	-0.022
30	91	8.230	8.268	0.038
30	92	8.273	8.274	0.001
30	94	8.339	8.381	0.042
30	95	8.291	8.285	-0.006
35	100	8.249	8.231	-0.018
35	101	8.314	8.307	-0.007
35	102	8.298	8.272	-0.026
35	103	8.280	8.316	0.036
35	104	8.263	8.283	0.020
35	105	8.309	8.286	-0.023
50	106	8.300	8.253	-0.047
50	107	8.298	8.202	-0.096
50	108	8.357	8.293	-0.064
50	109	8.245	8.157	-0.108
50	110	8.340	8.277	-0.063
50	111	8.270	8.219	-0.051
55	113	8.340	8.257	-0.083
55	114	8.236	8.203	-0.033
55	115	8.252	8.178	-0.074
55	116	8.263	8.228	-0.035
55	126	8.261	8.213	-0.048
55	128	8.253	8.194	-0.059
100	129	8.347	8.288	-0.059
100	131	8.351	8.236	-0.115
100	132	8.339	8.255	-0.084
100	133	8.357	8.268	-0.089
100	134	8.312	8.229	-0.083
100	135	8.279	8.196	-0.083
100	138	8.324	8.249	-0.075
100	139	8.342	8.195	-0.147
100	140	8.363	8.331	-0.032
100	141	8.267	8.171	-0.096
100	142	8.324	8.220	-0.104
100	143	8.285	8.206	-0.079
100	144	8.273	8.213	-0.060
100	220	8.289	8.180	-0.109
100	221	8.262	8.202	-0.060
100	224	8.308	8.292	-0.016
100	225	8.388	8.320	-0.068
100	226	8.350	8.283	-0.067
100	228	8.384	8.229	-0.155
100	232	8.303	8.169	-0.134
100	233	8.275	8.249	-0.026
100	236	8.326	8.198	-0.128
100	238	8.285	8.249	-0.036
100	243	8.318	8.245	-0.073
105	244	8.292	8.241	-0.051
105	246	8.302	8.243	-0.059
105	248	8.291	8.288	-0.003
105	374	8.304	8.288	-0.016
105	376	8.330	8.239	-0.091
	Max	8.388	8.381	0.051
	Average	8.298	8.259	-0.038
	Min	8.230	8.157	-0.155
	Std Dev	0.036	0.046	0.047



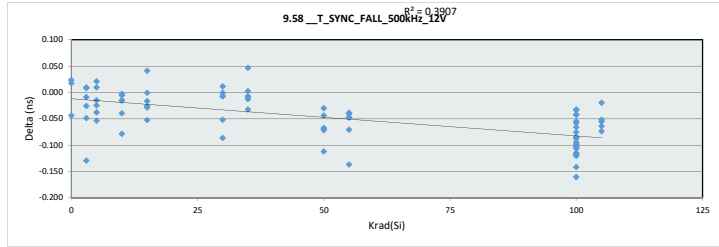
9.54_T_SYNC_FA											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.286	8.243	8.244	8.235	8.232	8.268	8.231	8.157	8.178	8.169	8.239
Average	8.304	8.279	8.276	8.283	8.271	8.305	8.283	8.234	8.212	8.236	8.260
Max	8.336	8.303	8.326	8.350	8.310	8.381	8.316	8.293	8.257	8.331	8.288
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



LDR TID Report
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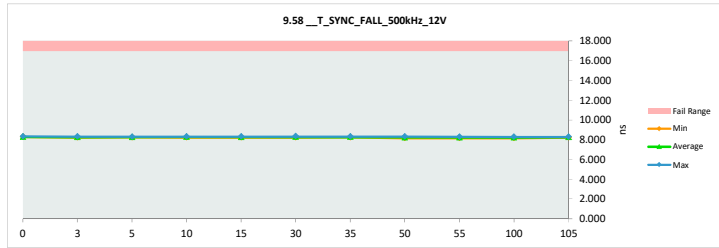
9.58_T_SYNC_FALL_500kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.312	8.268	-0.044
0	118	8.302	8.319	0.017
0	122	8.240	8.263	0.023
3	61	8.311	8.285	-0.026
3	62	8.243	8.251	0.008
3	63	8.198	8.207	0.009
3	64	8.321	8.191	-0.130
3	65	8.349	8.300	-0.049
3	66	8.273	8.264	-0.009
5	67	8.235	8.220	-0.015
5	68	8.298	8.273	-0.025
5	69	8.295	8.241	-0.054
5	70	8.231	8.240	0.009
5	71	8.290	8.252	-0.038
5	72	8.238	8.259	0.021
10	73	8.311	8.308	-0.003
10	74	8.243	8.227	-0.016
10	75	8.198	8.192	-0.006
10	76	8.321	8.242	-0.079
10	77	8.349	8.309	-0.040
10	78	8.273	8.258	-0.015
15	79	8.235	8.206	-0.029
15	80	8.298	8.297	-0.001
15	81	8.295	8.278	-0.017
15	82	8.231	8.205	-0.026
15	83	8.290	8.237	-0.053
15	84	8.238	8.279	0.041
30	86	8.311	8.259	-0.052
30	89	8.243	8.236	-0.007
30	91	8.198	8.197	-0.001
30	92	8.321	8.234	-0.087
30	94	8.349	8.341	-0.008
30	95	8.273	8.284	0.011
35	100	8.235	8.227	-0.008
35	101	8.298	8.265	-0.033
35	102	8.295	8.297	0.002
35	103	8.231	8.222	-0.009
35	104	8.290	8.277	-0.013
35	105	8.238	8.284	0.046
50	106	8.289	8.217	-0.072
50	107	8.271	8.241	-0.030
50	108	8.293	8.249	-0.044
50	109	8.226	8.158	-0.068
50	110	8.357	8.288	-0.069
50	111	8.248	8.136	-0.112
55	113	8.322	8.283	-0.039
55	114	8.230	8.182	-0.048
55	115	8.243	8.202	-0.041
55	116	8.281	8.210	-0.071
55	126	8.236	8.189	-0.047
55	128	8.274	8.137	-0.137
100	129	8.323	8.228	-0.095
100	131	8.333	8.214	-0.119
100	132	8.295	8.194	-0.101
100	133	8.381	8.282	-0.099
100	134	8.351	8.275	-0.076
100	135	8.237	8.152	-0.085
100	138	8.296	8.201	-0.095
100	139	8.303	8.142	-0.161
100	140	8.305	8.217	-0.088
100	141	8.294	8.152	-0.142
100	142	8.292	8.206	-0.086
100	143	8.294	8.236	-0.058
100	144	8.231	8.197	-0.034
100	220	8.300	8.185	-0.115
100	221	8.206	8.163	-0.043
100	224	8.286	8.231	-0.055
100	225	8.337	8.271	-0.066
100	226	8.317	8.214	-0.103
100	228	8.329	8.222	-0.107
100	232	8.323	8.207	-0.116
100	233	8.320	8.199	-0.121
100	236	8.289	8.247	-0.042
100	238	8.300	8.194	-0.106
100	243	8.289	8.256	-0.033
105	244	8.290	8.234	-0.056
105	246	8.276	8.212	-0.064
105	248	8.290	8.238	-0.052
105	374	8.290	8.270	-0.020
105	376	8.276	8.202	-0.074
105	Max	8.381	8.341	0.046
105	Average	8.283	8.234	-0.049
105	Min	8.198	8.136	-0.161
105	Std Dev	0.041	0.044	0.045



9.58_T_SYNC_FALL_500kHz_12V	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

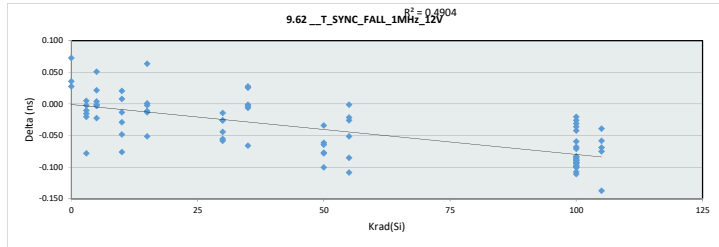
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.263	8.191	8.220	8.192	8.205	8.197	8.222	8.136	8.137	8.142	8.202
Average	8.283	8.250	8.248	8.256	8.250	8.259	8.262	8.215	8.201	8.212	8.231
Max	8.319	8.300	8.273	8.309	8.297	8.341	8.297	8.288	8.283	8.282	8.270
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



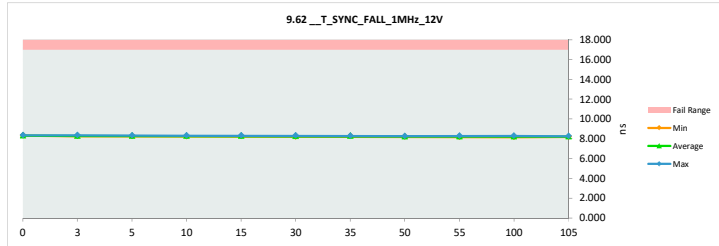
LDR TID Report
TPS7H5001-SP

9.62_T_SYNC_FALL_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.281	8.308	0.027
0	118	8.306	8.341	0.035
0	122	8.276	8.348	0.072
3	61	8.296	8.280	-0.016
3	62	8.280	8.259	-0.021
3	63	8.262	8.251	-0.011
3	64	8.266	8.270	0.004
3	65	8.351	8.348	-0.003
3	66	8.335	8.256	-0.079
5	67	8.255	8.258	0.003
5	68	8.306	8.302	-0.004
5	69	8.291	8.312	0.021
5	70	8.216	8.266	0.050
5	71	8.294	8.292	-0.002
5	72	8.308	8.285	-0.023
10	73	8.296	8.303	0.007
10	74	8.280	8.266	-0.014
10	75	8.262	8.213	-0.049
10	76	8.266	8.286	0.020
10	77	8.351	8.321	-0.030
10	78	8.335	8.258	-0.077
15	79	8.255	8.243	-0.012
15	80	8.306	8.303	-0.003
15	81	8.291	8.277	-0.014
15	82	8.216	8.279	0.063
15	83	8.294	8.242	-0.052
15	84	8.308	8.308	0.000
30	86	8.296	8.281	-0.015
30	89	8.280	8.253	-0.027
30	91	8.262	8.235	-0.027
30	92	8.266	8.207	-0.059
30	94	8.351	8.306	-0.045
30	95	8.335	8.279	-0.056
35	100	8.255	8.280	0.025
35	101	8.306	8.304	-0.002
35	102	8.291	8.284	-0.007
35	103	8.216	8.243	0.027
35	104	8.294	8.290	-0.004
35	105	8.308	8.241	-0.067
50	106	8.295	8.230	-0.065
50	107	8.312	8.233	-0.079
50	108	8.331	8.253	-0.078
50	109	8.222	8.187	-0.035
50	110	8.368	8.267	-0.101
50	111	8.282	8.220	-0.062
55	113	8.298	8.296	-0.002
55	114	8.247	8.195	-0.052
55	115	8.277	8.250	-0.027
55	116	8.259	8.237	-0.022
55	126	8.318	8.232	-0.086
55	128	8.279	8.170	-0.109
100	129	8.371	8.281	-0.090
100	131	8.305	8.221	-0.084
100	132	8.312	8.243	-0.069
100	133	8.389	8.301	-0.088
100	134	8.330	8.237	-0.093
100	135	8.244	8.160	-0.084
100	138	8.292	8.193	-0.099
100	139	8.299	8.239	-0.060
100	140	8.330	8.237	-0.093
100	141	8.298	8.208	-0.090
100	142	8.265	8.244	-0.021
100	143	8.263	8.236	-0.027
100	144	8.296	8.184	-0.112
100	220	8.303	8.202	-0.101
100	221	8.278	8.170	-0.108
100	224	8.329	8.231	-0.098
100	225	8.380	8.286	-0.094
100	226	8.313	8.270	-0.043
100	228	8.294	8.257	-0.037
100	232	8.302	8.201	-0.101
100	233	8.330	8.235	-0.095
100	236	8.288	8.202	-0.086
100	238	8.244	8.212	-0.032
100	243	8.311	8.239	-0.072
105	244	8.317	8.247	-0.070
105	246	8.274	8.198	-0.076
105	248	8.307	8.267	-0.040
105	374	8.300	8.241	-0.059
105	376	8.332	8.194	-0.138
	Max	8.389	8.348	0.072
	Average	8.295	8.254	-0.042
	Min	8.216	8.160	-0.138
	Std Dev	0.036	0.042	0.046



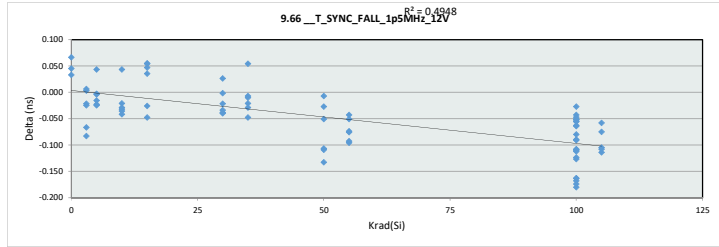
9.62_T_SYNC_FALL_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.308	8.251	8.258	8.213	8.242	8.207	8.241	8.187	8.170	8.160	8.194
Average	8.332	8.277	8.286	8.275	8.275	8.260	8.274	8.232	8.230	8.229	8.229
Max	8.348	8.348	8.312	8.321	8.308	8.306	8.304	8.267	8.296	8.301	8.267
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



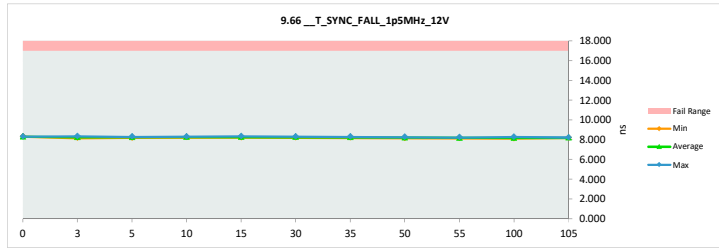
LDR TID Report
TPS7H5001-SP

9.66_T_SYNC_FALL_1p5MHz_12V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	17	17
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.251	8.296	0.045
0	118	8.260	8.293	0.033
0	122	8.252	8.318	0.066
3	61	8.302	8.305	0.003
3	62	8.311	8.228	-0.083
3	63	8.187	8.162	-0.025
3	64	8.277	8.210	-0.067
3	65	8.339	8.345	0.006
3	66	8.285	8.263	-0.022
5	67	8.201	8.185	-0.016
5	68	8.319	8.296	-0.023
5	69	8.219	8.262	0.043
5	70	8.268	8.265	-0.003
5	71	8.252	8.248	-0.004
5	72	8.297	8.272	-0.025
10	73	8.302	8.273	-0.029
10	74	8.311	8.269	-0.042
10	75	8.187	8.230	0.043
10	76	8.277	8.241	-0.036
10	77	8.339	8.318	-0.021
10	78	8.285	8.253	-0.032
15	79	8.201	8.256	0.055
15	80	8.319	8.293	-0.026
15	81	8.219	8.266	0.047
15	82	8.268	8.322	0.054
15	83	8.252	8.204	-0.048
15	84	8.297	8.332	0.035
30	86	8.302	8.268	-0.034
30	89	8.311	8.309	-0.002
30	91	8.187	8.213	0.026
30	92	8.277	8.238	-0.039
30	94	8.339	8.299	-0.040
30	95	8.285	8.263	-0.022
35	100	8.201	8.255	0.054
35	101	8.319	8.271	-0.048
35	102	8.219	8.190	-0.029
35	103	8.268	8.258	-0.010
35	104	8.252	8.231	-0.021
35	105	8.297	8.290	-0.007
50	106	8.299	8.248	-0.051
50	107	8.283	8.256	-0.027
50	108	8.309	8.200	-0.109
50	109	8.231	8.224	-0.007
50	110	8.357	8.250	-0.107
50	111	8.286	8.153	-0.133
55	113	8.281	8.230	-0.051
55	114	8.222	8.126	-0.096
55	115	8.213	8.170	-0.043
55	116	8.245	8.169	-0.076
55	126	8.321	8.228	-0.093
55	128	8.275	8.201	-0.074
100	129	8.336	8.212	-0.124
100	131	8.307	8.195	-0.112
100	132	8.304	8.248	-0.056
100	133	8.319	8.272	-0.047
100	134	8.374	8.211	-0.163
100	135	8.206	8.143	-0.063
100	138	8.265	8.213	-0.052
100	139	8.251	8.187	-0.064
100	140	8.297	8.189	-0.108
100	141	8.278	8.098	-0.180
100	142	8.303	8.176	-0.127
100	143	8.212	8.169	-0.043
100	144	8.219	8.139	-0.080
100	220	8.228	8.173	-0.055
100	221	8.225	8.115	-0.110
100	224	8.338	8.175	-0.163
100	225	8.415	8.241	-0.174
100	226	8.299	8.248	-0.051
100	228	8.363	8.195	-0.168
100	232	8.263	8.154	-0.109
100	233	8.238	8.186	-0.052
100	236	8.245	8.154	-0.091
100	238	8.245	8.218	-0.027
100	243	8.290	8.200	-0.090
105	244	8.284	8.176	-0.108
105	246	8.232	8.174	-0.058
105	248	8.297	8.222	-0.075
105	374	8.280	8.175	-0.105
105	376	8.319	8.205	-0.114
	Max	8.415	8.345	0.066
	Average	8.276	8.228	-0.049
	Min	8.187	8.098	-0.180
	Std Dev	0.047	0.054	0.058



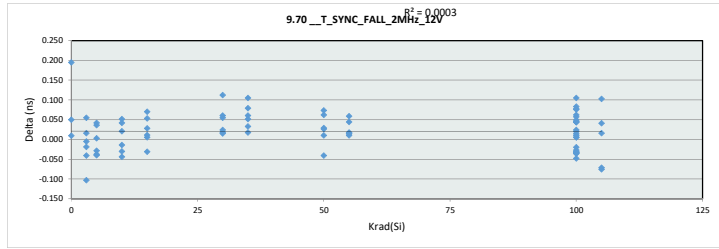
9.66_T_SYNC_Fa											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.293	8.162	8.185	8.230	8.204	8.213	8.190	8.153	8.126	8.098	8.174
Average	8.302	8.252	8.255	8.264	8.279	8.265	8.249	8.222	8.187	8.188	8.190
Max	8.318	8.345	8.296	8.318	8.332	8.309	8.290	8.256	8.230	8.272	8.222
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



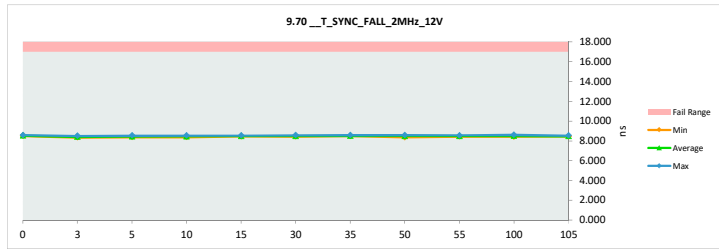
LDR TID Report
TPS7H5001-SP

9.70_T_SYNC_FALL_2MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.359	8.553	0.194
0	118	8.487	8.495	0.008
0	122	8.419	8.468	0.049
3	61	8.492	8.472	-0.020
3	62	8.424	8.439	0.015
3	63	8.397	8.391	-0.006
3	64	8.370	8.328	-0.042
3	65	8.467	8.363	-0.104
3	66	8.362	8.416	0.054
5	67	8.411	8.446	0.035
5	68	8.501	8.503	0.002
5	69	8.418	8.388	-0.030
5	70	8.388	8.429	0.041
5	71	8.381	8.340	-0.041
5	72	8.521	8.482	-0.039
10	73	8.492	8.512	0.020
10	74	8.424	8.409	-0.015
10	75	8.397	8.352	-0.045
10	76	8.370	8.420	0.050
10	77	8.467	8.436	-0.031
10	78	8.362	8.403	0.041
15	79	8.411	8.438	0.027
15	80	8.501	8.511	0.010
15	81	8.418	8.422	0.004
15	82	8.388	8.440	0.052
15	83	8.381	8.450	0.069
15	84	8.521	8.489	-0.032
30	86	8.492	8.546	0.054
30	89	8.424	8.438	0.014
30	91	8.397	8.414	0.017
30	92	8.370	8.429	0.059
30	94	8.467	8.490	0.023
30	95	8.362	8.473	0.111
35	100	8.411	8.461	0.050
35	101	8.501	8.517	0.016
35	102	8.418	8.477	0.059
35	103	8.388	8.492	0.104
35	104	8.381	8.459	0.078
35	105	8.521	8.553	0.032
50	106	8.467	8.476	0.009
50	107	8.452	8.524	0.072
50	108	8.432	8.457	0.025
50	109	8.414	8.442	0.028
50	110	8.496	8.557	0.061
50	111	8.373	8.331	-0.042
55	113	8.482	8.525	0.043
55	114	8.355	8.413	0.058
55	115	8.419	8.434	0.015
55	116	8.458	8.471	0.013
55	126	8.496	8.505	0.009
55	128	8.425	8.441	0.016
100	129	8.531	8.535	0.004
100	131	8.431	8.473	0.042
100	132	8.413	8.495	0.082
100	133	8.554	8.518	-0.036
100	134	8.480	8.523	0.043
100	135	8.365	8.421	0.056
100	138	8.461	8.509	0.048
100	139	8.454	8.420	-0.034
100	140	8.488	8.532	0.044
100	141	8.385	8.408	0.023
100	142	8.477	8.440	-0.037
100	143	8.474	8.425	-0.049
100	144	8.474	8.453	-0.021
100	220	8.467	8.486	0.019
100	221	8.382	8.391	0.009
100	224	8.463	8.429	-0.034
100	225	8.460	8.521	0.061
100	226	8.455	8.530	0.075
100	228	8.516	8.592	0.076
100	232	8.481	8.494	0.013
100	233	8.436	8.440	0.004
100	236	8.421	8.389	-0.032
100	238	8.386	8.490	0.104
100	243	8.483	8.455	-0.028
105	244	8.500	8.427	-0.073
105	246	8.500	8.423	-0.077
105	248	8.403	8.504	0.101
105	374	8.396	8.436	0.040
105	376	8.424	8.439	0.015
105	Max	8.554	8.592	0.194
105	Average	8.439	8.459	0.020
105	Min	8.355	8.328	-0.104
105	Std Dev	0.051	0.055	0.049



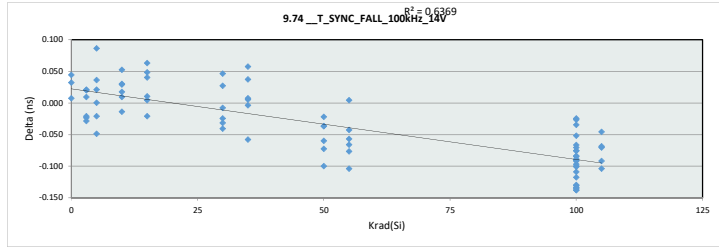
9.70_T_SYNC_FALL_2MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.468	8.328	8.340	8.352	8.422	8.414	8.459	8.331	8.413	8.389	8.423
Average	8.505	8.402	8.431	8.422	8.458	8.465	8.493	8.465	8.474	8.446	
Max	8.553	8.472	8.503	8.512	8.511	8.546	8.553	8.557	8.525	8.592	8.504
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



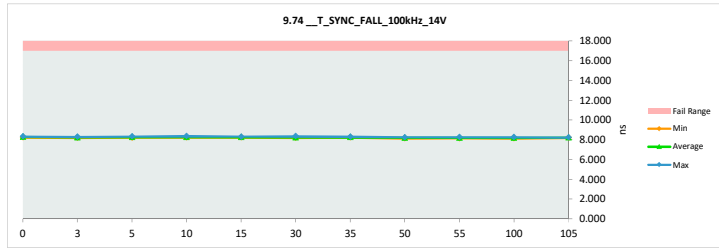
LDR TID Report
TPS7H5001-SP

9.74_T_SYNC_FALL_100kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.269	8.301	0.032
0	118	8.251	8.295	0.044
0	122	8.215	8.222	0.007
3	61	8.221	8.242	0.021
3	62	8.216	8.187	-0.029
3	63	8.186	8.162	-0.024
3	64	8.203	8.212	0.009
3	65	8.298	8.277	-0.021
3	66	8.218	8.238	0.020
5	67	8.165	8.201	0.036
5	68	8.279	8.300	0.021
5	69	8.201	8.287	0.086
5	70	8.204	8.204	0.000
5	71	8.236	8.187	-0.049
5	72	8.257	8.236	-0.021
10	73	8.221	8.238	0.017
10	74	8.216	8.245	0.029
10	75	8.186	8.216	0.030
10	76	8.203	8.212	0.009
10	77	8.298	8.350	0.052
10	78	8.218	8.204	-0.014
15	79	8.165	8.213	0.048
15	80	8.279	8.283	0.004
15	81	8.201	8.241	0.040
15	82	8.204	8.267	0.063
15	83	8.236	8.215	-0.021
15	84	8.257	8.267	0.010
30	86	8.221	8.267	0.046
30	89	8.216	8.208	-0.008
30	91	8.186	8.161	-0.025
30	92	8.203	8.162	-0.041
30	94	8.298	8.325	0.027
30	95	8.218	8.186	-0.032
35	100	8.165	8.202	0.037
35	101	8.279	8.284	0.005
35	102	8.201	8.258	0.057
35	103	8.204	8.211	0.007
35	104	8.236	8.232	-0.004
35	105	8.257	8.199	-0.058
50	106	8.254	8.154	-0.100
50	107	8.235	8.213	-0.022
50	108	8.266	8.193	-0.073
50	109	8.170	8.110	-0.060
50	110	8.258	8.221	-0.037
50	111	8.205	8.168	-0.037
55	113	8.292	8.235	-0.057
55	114	8.186	8.143	-0.043
55	115	8.198	8.121	-0.077
55	116	8.187	8.191	0.004
55	126	8.264	8.160	-0.104
55	128	8.222	8.156	-0.066
100	129	8.306	8.222	-0.084
100	131	8.268	8.167	-0.101
100	132	8.246	8.170	-0.076
100	133	8.342	8.204	-0.138
100	134	8.325	8.195	-0.130
100	135	8.296	8.161	-0.135
100	138	8.206	8.181	-0.025
100	139	8.267	8.136	-0.131
100	140	8.286	8.215	-0.071
100	141	8.268	8.134	-0.134
100	142	8.259	8.183	-0.076
100	143	8.266	8.214	-0.052
100	144	8.205	8.113	-0.092
100	220	8.270	8.152	-0.118
100	221	8.201	8.110	-0.091
100	224	8.280	8.191	-0.089
100	225	8.345	8.236	-0.109
100	226	8.326	8.229	-0.097
100	228	8.266	8.239	-0.027
100	232	8.244	8.177	-0.067
100	233	8.269	8.131	-0.138
100	236	8.276	8.191	-0.085
100	238	8.219	8.184	-0.035
100	243	8.279	8.179	-0.100
105	244	8.269	8.198	-0.071
105	246	8.267	8.163	-0.104
105	248	8.271	8.202	-0.069
105	374	8.254	8.208	-0.046
105	376	8.307	8.215	-0.092
105	Max	8.345	8.350	0.006
105	Average	8.243	8.207	-0.036
105	Min	8.165	8.110	-0.138
105	Std Dev	0.043	0.050	0.057



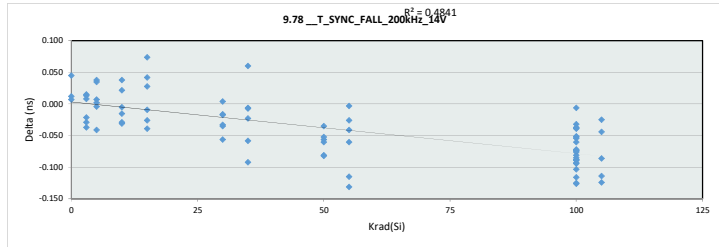
9.74_T_SYNC_FALL_100kHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.222	8.162	8.187	8.204	8.213	8.161	8.199	8.110	8.121	8.110	8.163
Average	8.273	8.220	8.236	8.244	8.248	8.218	8.231	8.177	8.168	8.180	8.197
Max	8.301	8.277	8.300	8.350	8.283	8.325	8.284	8.221	8.235	8.239	8.215
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



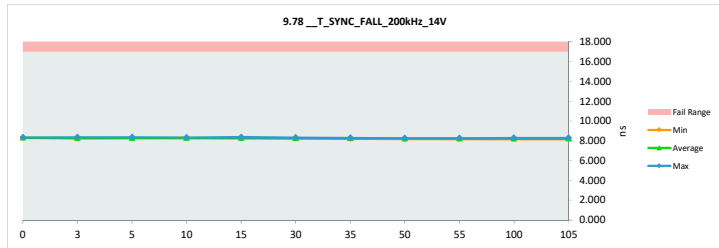
LDR TID Report
TPS7H5001-SP

9.78_T_SYNC_FALL_200kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.296	8.307	0.011
0	118	8.284	8.328	0.044
0	122	8.284	8.290	0.006
3	61	8.315	8.322	0.007
3	62	8.286	8.256	-0.030
3	63	8.256	8.234	-0.022
3	64	8.271	8.285	0.014
3	65	8.328	8.340	0.012
3	66	8.314	8.276	-0.038
5	67	8.216	8.253	0.037
5	68	8.294	8.300	0.006
5	69	8.286	8.287	0.001
5	70	8.294	8.252	-0.042
5	71	8.299	8.294	-0.005
5	72	8.320	8.354	0.034
10	73	8.315	8.285	-0.030
10	74	8.286	8.254	-0.032
10	75	8.256	8.240	-0.016
10	76	8.271	8.308	0.037
10	77	8.328	8.322	-0.006
10	78	8.314	8.335	0.021
15	79	8.216	8.257	0.041
15	80	8.294	8.367	0.073
15	81	8.286	8.259	-0.027
15	82	8.294	8.284	-0.010
15	83	8.299	8.326	0.027
15	84	8.320	8.280	-0.040
30	86	8.315	8.279	-0.036
30	89	8.286	8.252	-0.034
30	91	8.256	8.259	0.003
30	92	8.271	8.254	-0.017
30	94	8.328	8.310	-0.018
30	95	8.314	8.257	-0.057
35	100	8.216	8.275	0.059
35	101	8.294	8.270	-0.024
35	102	8.286	8.279	-0.007
35	103	8.294	8.201	-0.093
35	104	8.299	8.291	-0.008
35	105	8.320	8.261	-0.059
50	106	8.289	8.232	-0.057
50	107	8.306	8.223	-0.083
50	108	8.312	8.230	-0.082
50	109	8.234	8.170	-0.061
50	110	8.305	8.252	-0.053
50	111	8.279	8.243	-0.036
55	113	8.300	8.239	-0.061
55	114	8.261	8.219	-0.042
55	115	8.262	8.235	-0.027
55	116	8.258	8.254	-0.004
55	126	8.364	8.232	-0.132
55	128	8.302	8.186	-0.116
100	129	8.336	8.303	-0.033
100	131	8.322	8.240	-0.082
100	132	8.284	8.244	-0.040
100	133	8.342	8.267	-0.075
100	134	8.330	8.269	-0.061
100	135	8.277	8.270	-0.007
100	138	8.314	8.225	-0.089
100	139	8.288	8.233	-0.055
100	140	8.314	8.261	-0.053
100	141	8.307	8.180	-0.127
100	142	8.331	8.241	-0.090
100	143	8.288	8.236	-0.052
100	144	8.320	8.203	-0.117
100	220	8.291	8.202	-0.089
100	221	8.282	8.156	-0.126
100	224	8.323	8.250	-0.073
100	225	8.405	8.301	-0.104
100	226	8.340	8.265	-0.075
100	228	8.291	8.252	-0.039
100	232	8.304	8.218	-0.086
100	233	8.316	8.222	-0.094
100	236	8.304	8.227	-0.077
100	238	8.289	8.251	-0.038
100	243	8.333	8.238	-0.095
105	244	8.345	8.220	-0.125
105	246	8.277	8.162	-0.115
105	248	8.323	8.236	-0.087
105	374	8.325	8.280	-0.045
105	375	8.300	8.274	-0.026
105	Max	8.405	8.367	0.038
105	Average	8.298	8.259	-0.039
105	Min	8.216	8.156	-0.132
105	Std Dev	0.031	0.042	0.047



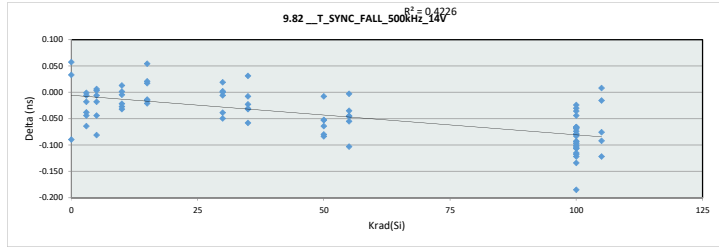
9.78_T_SYNC_FALL_200kHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.290	8.234	8.252	8.240	8.257	8.252	8.201	8.170	8.186	8.156	8.162
Average	8.308	8.286	8.290	8.291	8.296	8.269	8.263	8.225	8.228	8.240	8.234
Max	8.328	8.340	8.354	8.335	8.367	8.310	8.291	8.252	8.254	8.303	8.280
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



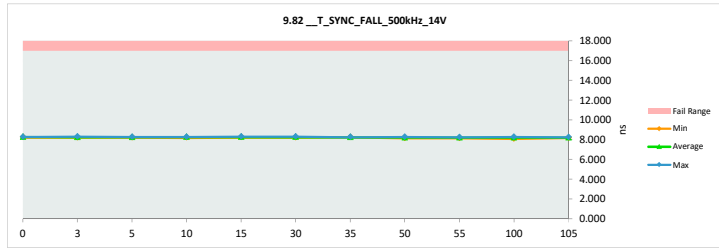
LDR TID Report
TPS7H5001-SP

9.82_T_SYNC_FALL_500kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.320	8.230	-0.090
0	118	8.244	8.277	0.033
0	122	8.228	8.285	0.057
3	61	8.265	8.227	-0.038
3	62	8.275	8.211	-0.064
3	63	8.218	8.200	-0.018
3	64	8.252	8.246	-0.006
3	65	8.310	8.309	-0.001
3	66	8.283	8.239	-0.044
5	67	8.240	8.222	-0.018
5	68	8.264	8.270	0.006
5	69	8.260	8.254	-0.006
5	70	8.220	8.223	0.003
5	71	8.304	8.223	-0.081
5	72	8.291	8.247	-0.044
10	73	8.265	8.266	0.001
10	74	8.275	8.248	-0.027
10	75	8.218	8.196	-0.022
10	76	8.252	8.265	0.013
10	77	8.310	8.278	-0.032
10	78	8.283	8.278	-0.005
15	79	8.240	8.226	-0.014
15	80	8.264	8.285	0.021
15	81	8.260	8.314	0.054
15	82	8.220	8.237	0.017
15	83	8.304	8.283	-0.021
15	84	8.291	8.275	-0.016
30	86	8.265	8.284	0.019
30	89	8.275	8.236	-0.039
30	91	8.218	8.218	0.000
30	92	8.252	8.246	-0.006
30	94	8.310	8.312	0.002
30	95	8.283	8.233	-0.050
35	100	8.240	8.232	-0.008
35	101	8.264	8.233	-0.031
35	102	8.260	8.237	-0.023
35	103	8.220	8.251	0.031
35	104	8.304	8.246	-0.058
35	105	8.291	8.259	-0.032
50	106	8.294	8.210	-0.084
50	107	8.248	8.240	-0.008
50	108	8.354	8.273	-0.080
50	109	8.205	8.152	-0.053
50	110	8.296	8.243	-0.053
50	111	8.243	8.179	-0.064
55	113	8.291	8.236	-0.055
55	114	8.245	8.142	-0.103
55	115	8.216	8.171	-0.045
55	116	8.247	8.212	-0.035
55	126	8.248	8.245	-0.003
55	128	8.231	8.185	-0.046
100	129	8.343	8.237	-0.106
100	131	8.306	8.213	-0.093
100	132	8.300	8.231	-0.069
100	133	8.354	8.253	-0.101
100	134	8.352	8.218	-0.134
100	135	8.252	8.145	-0.107
100	138	8.247	8.173	-0.074
100	139	8.253	8.149	-0.104
100	140	8.301	8.277	-0.024
100	141	8.264	8.079	-0.185
100	142	8.225	8.189	-0.036
100	143	8.223	8.179	-0.044
100	144	8.227	8.197	-0.030
100	220	8.261	8.182	-0.079
100	221	8.269	8.147	-0.122
100	224	8.290	8.210	-0.080
100	225	8.351	8.257	-0.094
100	226	8.296	8.178	-0.118
100	228	8.294	8.228	-0.066
100	232	8.315	8.218	-0.097
100	233	8.289	8.222	-0.067
100	236	8.293	8.210	-0.083
100	238	8.274	8.208	-0.066
100	243	8.323	8.208	-0.115
105	244	8.276	8.200	-0.076
105	246	8.297	8.175	-0.122
105	248	8.261	8.245	-0.016
105	374	8.245	8.253	0.008
105	376	8.291	8.199	-0.092
	Max	8.354	8.314	0.057
	Average	8.272	8.227	-0.044
	Min	8.205	8.079	-0.185
	Std Dev	0.036	0.043	0.047



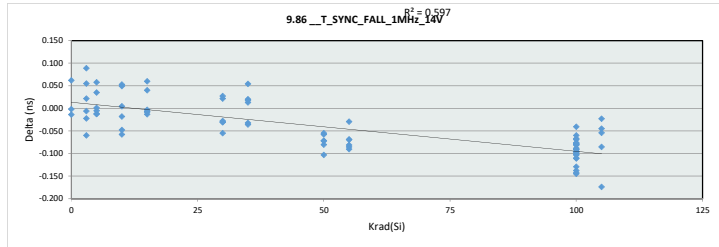
9.82_T_SYNC_FALL_500kHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.230	8.200	8.222	8.196	8.226	8.218	8.232	8.152	8.142	8.079	8.175
Average	8.264	8.239	8.240	8.255	8.270	8.255	8.243	8.216	8.199	8.200	8.214
Max	8.285	8.309	8.270	8.278	8.314	8.312	8.259	8.273	8.245	8.277	8.253
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



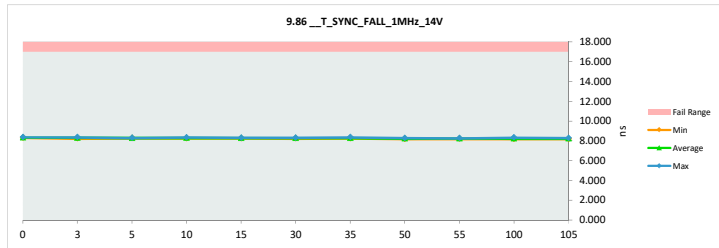
LDR TID Report
TPS7H5001-SP

9.86_T_SYNC_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.288	8.285	-0.003
0	118	8.302	8.363	0.061
0	122	8.306	8.291	-0.015
3	61	8.256	8.310	0.054
3	62	8.215	8.303	0.088
3	63	8.246	8.223	-0.023
3	64	8.318	8.257	-0.061
3	65	8.318	8.339	0.021
3	66	8.314	8.307	-0.007
5	67	8.242	8.242	0.000
5	68	8.294	8.288	-0.006
5	69	8.274	8.261	-0.013
5	70	8.266	8.253	-0.013
5	71	8.252	8.286	0.034
5	72	8.224	8.281	0.057
10	73	8.256	8.304	0.048
10	74	8.215	8.267	0.052
10	75	8.246	8.227	-0.019
10	76	8.318	8.259	-0.059
10	77	8.318	8.322	0.004
10	78	8.314	8.265	-0.049
15	79	8.242	8.233	-0.009
15	80	8.294	8.286	-0.008
15	81	8.274	8.270	-0.004
15	82	8.266	8.252	-0.014
15	83	8.252	8.291	0.039
15	84	8.224	8.283	0.059
30	86	8.256	8.282	0.026
30	89	8.215	8.236	0.021
30	91	8.246	8.214	-0.032
30	92	8.318	8.289	-0.029
30	94	8.318	8.287	-0.031
30	95	8.314	8.258	-0.056
35	100	8.242	8.254	0.012
35	101	8.294	8.347	0.053
35	102	8.274	8.241	-0.033
35	103	8.266	8.229	-0.037
35	104	8.252	8.269	0.017
35	105	8.224	8.243	0.019
50	106	8.288	8.229	-0.059
50	107	8.305	8.232	-0.073
50	108	8.287	8.231	-0.056
50	109	8.246	8.157	-0.081
50	110	8.364	8.260	-0.104
50	111	8.253	8.180	-0.073
55	113	8.311	8.241	-0.070
55	114	8.295	8.213	-0.082
55	115	8.263	8.177	-0.086
55	116	8.303	8.212	-0.091
55	126	8.288	8.218	-0.070
55	128	8.236	8.206	-0.030
100	129	8.332	8.250	-0.082
100	131	8.332	8.228	-0.104
100	132	8.333	8.194	-0.139
100	133	8.361	8.281	-0.080
100	134	8.321	8.210	-0.111
100	135	8.260	8.183	-0.077
100	138	8.271	8.174	-0.097
100	139	8.296	8.228	-0.068
100	140	8.336	8.255	-0.081
100	141	8.310	8.219	-0.091
100	142	8.253	8.183	-0.070
100	143	8.262	8.165	-0.097
100	144	8.289	8.177	-0.112
100	220	8.280	8.177	-0.103
100	221	8.252	8.163	-0.089
100	224	8.324	8.228	-0.096
100	225	8.366	8.305	-0.061
100	226	8.323	8.231	-0.092
100	228	8.280	8.238	-0.042
100	232	8.323	8.223	-0.100
100	233	8.361	8.215	-0.146
100	236	8.328	8.198	-0.130
100	238	8.349	8.205	-0.144
100	243	8.287	8.208	-0.079
105	244	8.289	8.234	-0.055
105	246	8.272	8.226	-0.046
105	248	8.334	8.159	-0.175
105	374	8.286	8.262	-0.024
105	375	8.321	8.235	-0.086
105	376	8.366	8.363	0.003
105	Max	8.286	8.244	-0.043
105	Min	8.215	8.157	-0.058
105	Std Dev	0.038	0.045	0.057



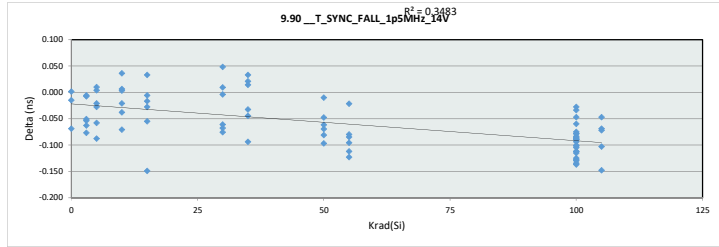
9.86_T_SYNC_Fa	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	8.285 8.223 8.242 8.227 8.233 8.214 8.229 8.157 8.177 8.163 8.159
Average	8.313 8.290 8.269 8.274 8.269 8.261 8.264 8.215 8.211 8.214 8.223
Max	8.363 8.339 8.288 8.322 8.291 8.289 8.347 8.260 8.241 8.305 8.262
UL	17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000



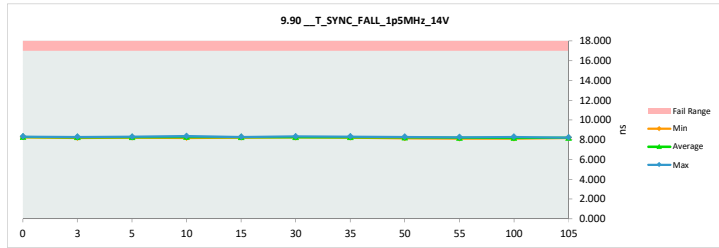
LDR TID Report
TPS7H5001-SP

9.90_T_SYNC_FALL_1p5MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.243	8.244	0.001
0	118	8.306	8.291	-0.015
0	122	8.330	8.261	-0.069
3	61	8.316	8.262	-0.054
3	62	8.314	8.251	-0.063
3	63	8.166	8.160	-0.006
3	64	8.230	8.222	-0.008
3	65	8.315	8.264	-0.051
3	66	8.316	8.239	-0.077
5	67	8.260	8.202	-0.058
5	68	8.274	8.284	0.010
5	69	8.217	8.196	-0.021
5	70	8.354	8.266	-0.088
5	71	8.235	8.239	0.004
5	72	8.284	8.256	-0.028
10	73	8.316	8.245	-0.071
10	74	8.314	8.293	-0.021
10	75	8.166	8.172	0.006
10	76	8.230	8.233	0.003
10	77	8.315	8.351	0.036
10	78	8.316	8.278	-0.038
15	79	8.260	8.205	-0.055
15	80	8.274	8.268	-0.006
15	81	8.217	8.200	-0.017
15	82	8.354	8.205	-0.149
15	83	8.235	8.268	0.033
15	84	8.284	8.256	-0.028
30	86	8.316	8.240	-0.076
30	89	8.314	8.253	-0.061
30	91	8.166	8.214	0.048
30	92	8.230	8.239	0.009
30	94	8.315	8.311	-0.004
30	95	8.316	8.248	-0.068
35	100	8.260	8.215	-0.045
35	101	8.274	8.307	0.033
35	102	8.217	8.231	0.014
35	103	8.354	8.260	-0.094
35	104	8.235	8.202	-0.033
35	105	8.284	8.305	0.021
50	106	8.281	8.200	-0.081
50	107	8.300	8.252	-0.048
50	108	8.304	8.207	-0.097
50	109	8.217	8.207	-0.010
50	110	8.326	8.264	-0.062
50	111	8.215	8.145	-0.070
55	113	8.232	8.210	-0.022
55	114	8.211	8.099	-0.112
55	115	8.269	8.173	-0.096
55	116	8.256	8.176	-0.080
55	126	8.361	8.238	-0.123
55	128	8.285	8.200	-0.085
100	129	8.343	8.213	-0.130
100	131	8.301	8.207	-0.094
100	132	8.322	8.197	-0.125
100	133	8.350	8.238	-0.112
100	134	8.320	8.234	-0.086
100	135	8.224	8.134	-0.090
100	138	8.330	8.193	-0.137
100	139	8.205	8.130	-0.075
100	140	8.266	8.187	-0.079
100	141	8.216	8.104	-0.112
100	142	8.281	8.197	-0.084
100	143	8.218	8.184	-0.034
100	144	8.264	8.129	-0.135
100	220	8.283	8.155	-0.128
100	221	8.204	8.099	-0.105
100	224	8.260	8.172	-0.088
100	225	8.314	8.267	-0.047
100	226	8.289	8.174	-0.115
100	228	8.266	8.238	-0.028
100	232	8.254	8.152	-0.102
100	233	8.250	8.190	-0.060
100	236	8.245	8.155	-0.090
100	238	8.243	8.142	-0.101
100	243	8.247	8.169	-0.078
105	244	8.258	8.189	-0.069
105	246	8.223	8.176	-0.047
105	248	8.338	8.190	-0.148
105	374	8.287	8.215	-0.072
105	376	8.275	8.172	-0.103
	Max	8.361	8.351	0.048
	Average	8.273	8.215	-0.058
	Min	8.166	8.099	-0.149
	Std Dev	0.047	0.051	0.048



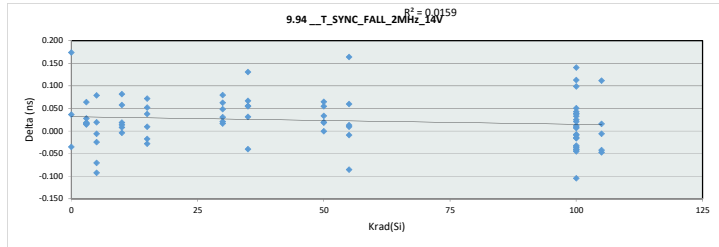
9.90_T_SYNC FA											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.244	8.160	8.196	8.172	8.200	8.214	8.202	8.145	8.099	8.099	8.172
Average	8.265	8.233	8.241	8.262	8.234	8.251	8.253	8.213	8.183	8.178	8.188
Max	8.291	8.264	8.284	8.351	8.268	8.311	8.307	8.264	8.238	8.267	8.215
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



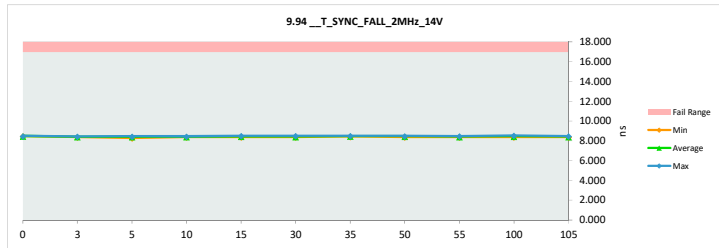
LDR TID Report
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9.94_T_SYNC_FALL_2MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.330	8.505	0.175
0	118	8.535	8.501	-0.034
0	122	8.444	8.482	0.038
3	61	8.448	8.464	0.016
3	62	8.432	8.461	0.029
3	63	8.373	8.391	0.018
3	64	8.366	8.386	0.020
3	65	8.376	8.441	0.065
3	66	8.397	8.415	0.018
5	67	8.448	8.425	-0.023
5	68	8.539	8.470	-0.069
5	69	8.410	8.405	-0.005
5	70	8.357	8.437	0.080
5	71	8.395	8.304	-0.091
5	72	8.446	8.467	0.021
10	73	8.448	8.463	0.015
10	74	8.432	8.429	-0.003
10	75	8.373	8.393	0.020
10	76	8.366	8.425	0.059
10	77	8.376	8.459	0.083
10	78	8.397	8.406	0.009
15	79	8.448	8.459	0.011
15	80	8.539	8.523	-0.016
15	81	8.410	8.383	-0.027
15	82	8.357	8.430	0.073
15	83	8.395	8.434	0.039
15	84	8.446	8.499	0.053
30	86	8.448	8.529	0.081
30	89	8.432	8.450	0.018
30	91	8.373	8.437	0.064
30	92	8.366	8.416	0.050
30	94	8.376	8.398	0.022
30	95	8.397	8.429	0.032
35	100	8.448	8.505	0.057
35	101	8.539	8.500	-0.039
35	102	8.410	8.443	0.033
35	103	8.357	8.489	0.132
35	104	8.395	8.451	0.056
35	105	8.446	8.514	0.068
50	106	8.484	8.505	0.021
50	107	8.449	8.505	0.056
50	108	8.395	8.461	0.066
50	109	8.430	8.431	0.001
50	110	8.490	8.510	0.020
50	111	8.351	8.386	0.035
55	113	8.330	8.495	0.165
55	114	8.385	8.396	0.011
55	115	8.394	8.455	0.061
55	116	8.442	8.456	0.014
55	126	8.531	8.447	-0.084
55	128	8.431	8.423	-0.008
100	129	8.552	8.512	-0.040
100	131	8.414	8.448	0.034
100	132	8.429	8.543	0.114
100	133	8.535	8.548	0.013
100	134	8.470	8.492	0.022
100	135	8.375	8.401	0.026
100	138	8.488	8.528	0.040
100	139	8.491	8.451	-0.040
100	140	8.448	8.458	0.010
100	141	8.387	8.395	0.008
100	142	8.559	8.456	-0.103
100	143	8.458	8.422	-0.036
100	144	8.462	8.454	-0.008
100	220	8.483	8.449	-0.034
100	221	8.426	8.413	-0.013
100	224	8.457	8.451	-0.006
100	225	8.515	8.500	-0.015
100	226	8.478	8.522	0.044
100	228	8.510	8.549	0.039
100	232	8.537	8.506	-0.031
100	233	8.348	8.448	0.100
100	236	8.400	8.452	0.052
100	238	8.351	8.493	0.142
100	243	8.453	8.409	-0.044
105	244	8.502	8.461	-0.041
105	246	8.389	8.406	0.017
105	248	8.383	8.496	0.113
105	374	8.451	8.446	-0.005
105	376	8.432	8.386	-0.046
105	375	8.559	8.549	0.010
Max		8.559	8.549	0.010
Average		8.432	8.455	0.023
Min		8.330	8.304	-0.026
Std Dev		0.058	0.046	0.053



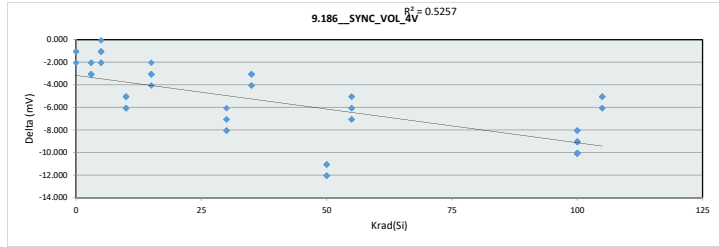
9.94_T_SYNC_FALL_2MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.482	8.386	8.304	8.393	8.383	8.398	8.443	8.386	8.396	8.395	8.386
Average	8.496	8.426	8.418	8.429	8.455	8.443	8.484	8.466	8.445	8.471	8.439
Max	8.505	8.464	8.470	8.463	8.523	8.529	8.514	8.510	8.495	8.549	8.496
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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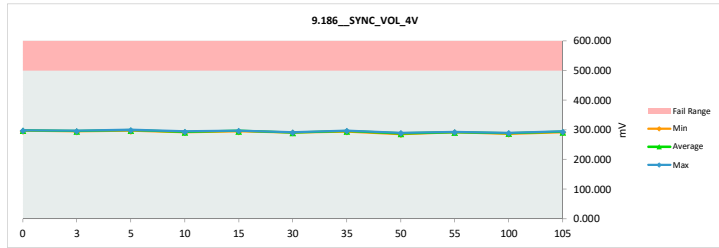
9.186_SYNC_VOL_4V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	117	300.000	299.000	-1.000
0	118	300.000	299.000	-1.000
0	122	299.000	297.000	-2.000
3	61	299.000	296.000	-3.000
3	62	300.000	297.000	-3.000
3	63	299.000	296.000	-3.000
3	64	299.000	296.000	-3.000
3	65	297.000	295.000	-2.000
3	66	299.000	297.000	-2.000
5	67	300.000	300.000	0.000
5	68	301.000	300.000	-1.000
5	69	299.000	298.000	-1.000
5	70	301.000	299.000	-2.000
5	71	298.000	297.000	-1.000
5	72	302.000	300.000	-2.000
10	73	299.000	293.000	-6.000
10	74	300.000	295.000	-5.000
10	75	299.000	293.000	-6.000
10	76	299.000	294.000	-5.000
10	77	297.000	292.000	-5.000
10	78	299.000	293.000	-6.000
15	79	300.000	298.000	-2.000
15	80	301.000	298.000	-3.000
15	81	299.000	296.000	-3.000
15	82	301.000	298.000	-3.000
15	83	298.000	295.000	-3.000
15	84	302.000	298.000	-4.000
30	86	299.000	291.000	-8.000
30	89	300.000	292.000	-8.000
30	91	299.000	292.000	-7.000
30	92	299.000	292.000	-7.000
30	94	297.000	291.000	-6.000
30	95	299.000	291.000	-8.000
35	100	300.000	297.000	-3.000
35	101	301.000	298.000	-3.000
35	102	299.000	296.000	-3.000
35	103	301.000	298.000	-3.000
35	104	298.000	294.000	-4.000
35	105	302.000	298.000	-4.000
50	106	298.000	286.000	-12.000
50	107	300.000	289.000	-11.000
50	108	301.000	290.000	-11.000
50	109	299.000	288.000	-11.000
50	110	301.000	289.000	-12.000
50	111	299.000	287.000	-12.000
55	113	299.000	293.000	-6.000
55	114	299.000	292.000	-7.000
55	115	298.000	292.000	-6.000
55	116	299.000	292.000	-7.000
55	126	297.000	292.000	-5.000
55	128	298.000	293.000	-5.000
100	129	296.000	287.000	-9.000
100	131	299.000	289.000	-10.000
100	132	299.000	290.000	-9.000
100	133	298.000	288.000	-10.000
100	134	299.000	290.000	-9.000
100	135	297.000	287.000	-10.000
100	138	297.000	288.000	-9.000
100	139	298.000	289.000	-9.000
100	140	299.000	289.000	-10.000
100	141	298.000	288.000	-10.000
100	142	298.000	288.000	-10.000
100	143	297.000	289.000	-8.000
100	144	297.000	289.000	-8.000
100	220	300.000	290.000	-10.000
100	221	298.000	288.000	-10.000
100	224	298.000	290.000	-8.000
100	225	299.000	290.000	-9.000
100	226	298.000	288.000	-10.000
100	228	298.000	288.000	-10.000
100	232	299.000	290.000	-9.000
100	233	300.000	290.000	-10.000
100	236	297.000	287.000	-10.000
100	238	298.000	289.000	-9.000
100	243	298.000	288.000	-10.000
105	244	299.000	293.000	-6.000
105	246	300.000	295.000	-5.000
105	248	299.000	294.000	-5.000
105	374	297.000	292.000	-5.000
105	376	301.000	295.000	-6.000
105	Max	302.000	300.000	0.000
105	Average	299.000	292.750	-6.250
105	Min	296.000	286.000	-12.000
105	Std Dev	1.331	3.957	3.320



9.186_SYNC_VOL	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

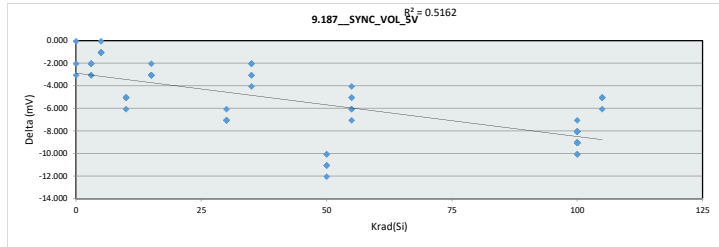
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	297.000	295.000	297.000	292.000	295.000	291.000	294.000	286.000	292.000	287.000	292.000
Average	298.333	296.167	299.000	293.333	297.167	291.500	296.833	288.167	292.333	288.708	293.800
Max	299.000	297.000	300.000	295.000	298.000	292.000	298.000	290.000	293.000	290.000	295.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000



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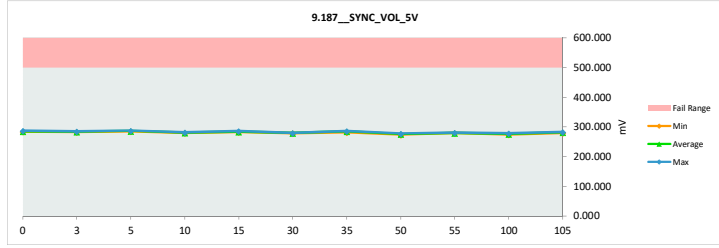
9.187_SYNC_VOL_5V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	117	288.000	286.000	-2.000
0	118	287.000	287.000	0.000
0	122	287.000	284.000	-3.000
3	61	286.000	284.000	-2.000
3	62	287.000	285.000	-2.000
3	63	287.000	284.000	-3.000
3	64	287.000	284.000	-3.000
3	65	285.000	283.000	-2.000
3	66	286.000	284.000	-2.000
5	67	288.000	287.000	-1.000
5	68	288.000	287.000	-1.000
5	69	286.000	286.000	0.000
5	70	288.000	287.000	-1.000
5	71	286.000	285.000	-1.000
5	72	289.000	288.000	-1.000
10	73	286.000	281.000	-5.000
10	74	287.000	282.000	-5.000
10	75	287.000	281.000	-6.000
10	76	287.000	282.000	-5.000
10	77	285.000	280.000	-5.000
10	78	286.000	281.000	-5.000
15	79	288.000	285.000	-3.000
15	80	288.000	285.000	-3.000
15	81	286.000	284.000	-2.000
15	82	288.000	285.000	-3.000
15	83	286.000	283.000	-3.000
15	84	289.000	286.000	-3.000
30	86	286.000	279.000	-7.000
30	89	287.000	280.000	-7.000
30	91	287.000	280.000	-7.000
30	92	287.000	280.000	-7.000
30	94	285.000	279.000	-6.000
30	95	286.000	279.000	-7.000
35	100	288.000	286.000	-2.000
35	101	288.000	285.000	-3.000
35	102	286.000	284.000	-2.000
35	103	288.000	286.000	-2.000
35	104	286.000	282.000	-4.000
35	105	289.000	286.000	-3.000
50	106	286.000	274.000	-12.000
50	107	288.000	278.000	-10.000
50	108	289.000	278.000	-11.000
50	109	287.000	276.000	-11.000
50	110	288.000	277.000	-11.000
50	111	286.000	276.000	-10.000
55	113	286.000	281.000	-5.000
55	114	286.000	280.000	-6.000
55	115	286.000	280.000	-6.000
55	116	287.000	280.000	-7.000
55	126	285.000	280.000	-5.000
55	128	285.000	281.000	-4.000
100	129	284.000	276.000	-8.000
100	131	286.000	278.000	-8.000
100	132	286.000	278.000	-8.000
100	133	285.000	276.000	-9.000
100	134	287.000	278.000	-9.000
100	135	284.000	275.000	-9.000
100	138	284.000	276.000	-8.000
100	139	286.000	278.000	-8.000
100	140	286.000	277.000	-9.000
100	141	286.000	277.000	-9.000
100	142	286.000	276.000	-10.000
100	143	285.000	277.000	-8.000
100	144	285.000	277.000	-8.000
100	220	288.000	279.000	-9.000
100	221	286.000	276.000	-10.000
100	224	286.000	279.000	-7.000
100	225	287.000	278.000	-9.000
100	226	285.000	276.000	-9.000
100	228	286.000	277.000	-9.000
100	232	287.000	278.000	-9.000
100	233	288.000	279.000	-9.000
100	236	285.000	275.000	-10.000
100	238	286.000	277.000	-9.000
100	243	286.000	277.000	-9.000
105	244	287.000	281.000	-6.000
105	246	288.000	283.000	-5.000
105	248	287.000	282.000	-5.000
105	374	285.000	280.000	-5.000
105	375	288.000	283.000	-5.000
105	Max	289.000	288.000	0.000
105	Average	286.563	280.775	-5.788
105	Min	284.000	274.000	-12.000
105	Std Dev	1.210	3.684	3.161



9.187_SYNC_VOL	
Test Site	
Tester	
Test Number	
Max Limit	500
Min Limit	mV

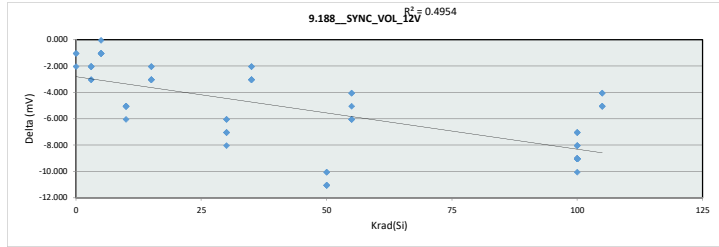
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	284.000	283.000	285.000	280.000	283.000	279.000	282.000	274.000	280.000	275.000	280.000
Average	285.667	284.000	286.667	281.167	284.667	279.500	284.833	276.500	280.333	277.083	281.800
Max	287.000	285.000	288.000	282.000	286.000	280.000	286.000	278.000	281.000	279.000	283.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000



LDR TID Report
TPS7H5001-SP

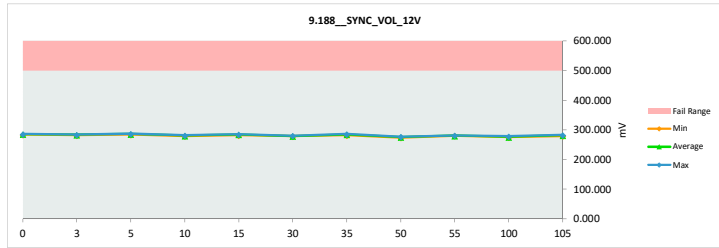
9.188_SYNC_VOL_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	117	287.000	286.000	-1.000
0	118	287.000	286.000	-1.000
0	122	286.000	284.000	-2.000
3	61	286.000	284.000	-2.000
3	62	287.000	284.000	-3.000
3	63	286.000	283.000	-3.000
3	64	286.000	284.000	-2.000
3	65	284.000	282.000	-2.000
3	66	286.000	284.000	-2.000
5	67	287.000	287.000	0.000
5	68	288.000	287.000	-1.000
5	69	286.000	285.000	-1.000
5	70	288.000	287.000	-1.000
5	71	285.000	284.000	-1.000
5	72	288.000	287.000	-1.000
10	73	286.000	280.000	-6.000
10	74	287.000	282.000	-5.000
10	75	286.000	281.000	-5.000
10	76	286.000	281.000	-5.000
10	77	284.000	279.000	-5.000
10	78	286.000	281.000	-5.000
15	79	287.000	285.000	-2.000
15	80	288.000	285.000	-3.000
15	81	286.000	284.000	-2.000
15	82	288.000	285.000	-3.000
15	83	285.000	282.000	-3.000
15	84	288.000	285.000	-3.000
30	86	286.000	279.000	-7.000
30	89	287.000	280.000	-7.000
30	91	286.000	280.000	-6.000
30	92	286.000	279.000	-7.000
30	94	284.000	278.000	-6.000
30	95	286.000	278.000	-8.000
35	100	287.000	285.000	-2.000
35	101	288.000	285.000	-3.000
35	102	286.000	283.000	-3.000
35	103	288.000	286.000	-2.000
35	104	285.000	282.000	-3.000
35	105	288.000	285.000	-3.000
50	106	285.000	274.000	-11.000
50	107	287.000	277.000	-10.000
50	108	288.000	277.000	-11.000
50	109	286.000	276.000	-10.000
50	110	288.000	277.000	-11.000
50	111	286.000	275.000	-11.000
55	113	285.000	280.000	-5.000
55	114	286.000	280.000	-6.000
55	115	286.000	280.000	-6.000
55	116	286.000	280.000	-6.000
55	126	284.000	280.000	-4.000
55	128	285.000	281.000	-4.000
100	129	283.000	275.000	-8.000
100	131	286.000	277.000	-9.000
100	132	286.000	277.000	-9.000
100	133	285.000	276.000	-9.000
100	134	286.000	277.000	-9.000
100	135	284.000	275.000	-9.000
100	138	284.000	276.000	-8.000
100	139	286.000	277.000	-9.000
100	140	286.000	277.000	-9.000
100	141	285.000	276.000	-9.000
100	142	286.000	276.000	-10.000
100	143	284.000	277.000	-7.000
100	144	284.000	277.000	-7.000
100	220	287.000	278.000	-9.000
100	221	285.000	276.000	-9.000
100	224	285.000	278.000	-7.000
100	225	286.000	277.000	-9.000
100	226	285.000	276.000	-9.000
100	228	285.000	276.000	-9.000
100	232	286.000	278.000	-8.000
100	233	287.000	278.000	-9.000
100	236	284.000	275.000	-9.000
100	238	286.000	277.000	-9.000
100	243	285.000	276.000	-9.000
105	244	286.000	281.000	-5.000
105	246	287.000	283.000	-4.000
105	248	286.000	282.000	-4.000
105	374	284.000	279.000	-5.000
105	375	287.000	282.000	-5.000
105	Max	288.000	287.000	0.000
105	Average	285.988	280.325	-5.663
105	Min	283.000	274.000	-11.000
105	Std Dev	1.227	3.697	3.150



9.188_SYNC_VOL	
Test Site	
Tester	
Test Number	
Max Limit	500
Min Limit	mV
Krad(Si)	0 3

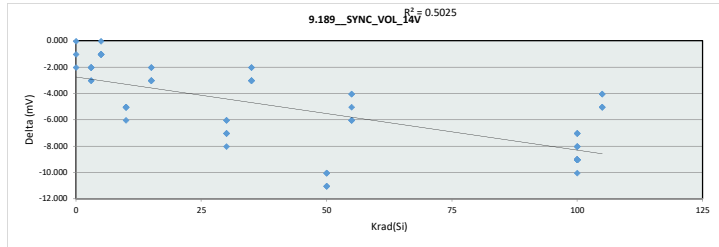
LL	5	10	15	30	35	50	55	100	105		
Min	284.000	282.000	284.000	279.000	282.000	278.000	282.000	274.000	280.000	275.000	279.000
Average	285.333	283.500	286.167	280.667	284.333	279.000	284.333	276.000	280.167	276.583	281.400
Max	286.000	284.000	287.000	282.000	285.000	280.000	286.000	277.000	281.000	278.000	283.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000



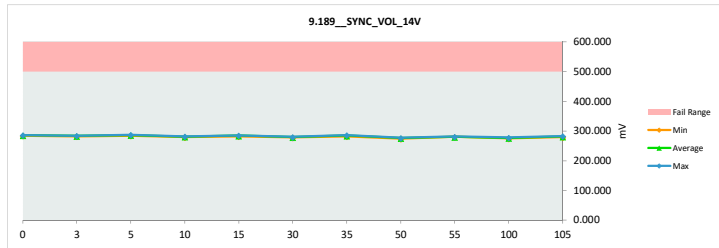
LDR TID Report
TPS7H5001-SP

9.189_SYNC_VOL_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	117	287.000	286.000	-1.000
0	118	286.000	286.000	0.000
0	122	286.000	284.000	-2.000
3	61	286.000	284.000	-2.000
3	62	287.000	284.000	-3.000
3	63	286.000	283.000	-3.000
3	64	286.000	284.000	-2.000
3	65	284.000	282.000	-2.000
3	66	286.000	284.000	-2.000
5	67	287.000	287.000	0.000
5	68	288.000	287.000	-1.000
5	69	286.000	285.000	-1.000
5	70	288.000	287.000	-1.000
5	71	285.000	284.000	-1.000
5	72	288.000	287.000	-1.000
10	73	286.000	280.000	-6.000
10	74	287.000	282.000	-5.000
10	75	286.000	281.000	-5.000
10	76	286.000	281.000	-5.000
10	77	284.000	279.000	-5.000
10	78	286.000	281.000	-5.000
15	79	287.000	285.000	-2.000
15	80	288.000	285.000	-3.000
15	81	286.000	284.000	-2.000
15	82	288.000	285.000	-3.000
15	83	285.000	282.000	-3.000
15	84	288.000	285.000	-3.000
30	86	286.000	279.000	-7.000
30	89	287.000	280.000	-7.000
30	91	286.000	280.000	-6.000
30	92	286.000	279.000	-7.000
30	94	284.000	278.000	-6.000
30	95	286.000	278.000	-8.000
35	100	287.000	285.000	-2.000
35	101	288.000	285.000	-3.000
35	102	286.000	283.000	-3.000
35	103	288.000	286.000	-2.000
35	104	285.000	282.000	-3.000
35	105	288.000	285.000	-3.000
50	106	285.000	274.000	-11.000
50	107	287.000	277.000	-10.000
50	108	288.000	277.000	-11.000
50	109	286.000	276.000	-10.000
50	110	287.000	277.000	-10.000
50	111	286.000	275.000	-11.000
55	113	285.000	280.000	-5.000
55	114	286.000	280.000	-6.000
55	115	286.000	280.000	-6.000
55	116	286.000	280.000	-6.000
55	126	284.000	280.000	-4.000
55	128	285.000	281.000	-4.000
100	129	283.000	275.000	-8.000
100	131	286.000	277.000	-9.000
100	132	286.000	277.000	-9.000
100	133	285.000	276.000	-9.000
100	134	286.000	277.000	-9.000
100	135	284.000	275.000	-9.000
100	138	284.000	276.000	-8.000
100	139	286.000	277.000	-9.000
100	140	286.000	277.000	-9.000
100	141	285.000	276.000	-9.000
100	142	286.000	276.000	-10.000
100	143	284.000	277.000	-7.000
100	144	284.000	277.000	-7.000
100	220	287.000	278.000	-9.000
100	221	285.000	276.000	-9.000
100	224	285.000	278.000	-7.000
100	225	286.000	277.000	-9.000
100	226	285.000	276.000	-9.000
100	228	285.000	276.000	-9.000
100	232	286.000	278.000	-8.000
100	233	287.000	278.000	-9.000
100	236	284.000	275.000	-9.000
100	238	286.000	277.000	-9.000
100	243	285.000	276.000	-9.000
105	244	286.000	281.000	-5.000
105	246	287.000	283.000	-4.000
105	248	286.000	282.000	-4.000
105	374	284.000	279.000	-5.000
105	375	287.000	282.000	-5.000
105	Max	288.000	287.000	0.000
105	Average	285.963	280.325	-5.638
105	Min	283.000	274.000	-11.000
105	Std Dev	1.206	3.697	3.151



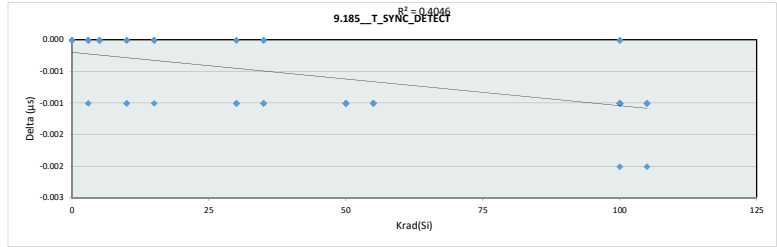
9.189_SYNC_VOL	
Test Site	
Tester	
Test Number	
Max Limit	500
Min Limit	500
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	284.000 282.000 284.000 279.000 282.000 278.000 282.000 274.000 280.000 275.000 279.000
Average	285.333 283.500 286.167 280.667 284.333 279.000 284.333 276.000 280.167 276.583 281.400
Max	286.000 284.000 287.000 282.000 285.000 280.000 286.000 277.000 281.000 278.000 283.000
UL	500.000 500.000 500.000 500.000 500.000 500.000 500.000 500.000 500.000 500.000 500.000



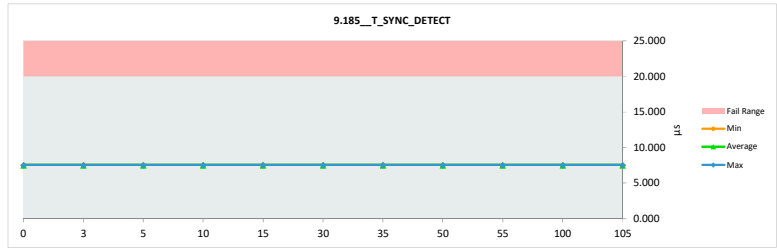
LDR TID Report
TPS7H5001-SP

9.185_T_SYNC_DETECT	
Test Site	
Tester	
Test Number	
Unit	µs
Max Limit	20
Min Limit	20

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.587	7.587	0.000
0	118	7.586	7.586	0.000
0	122	7.587	7.587	0.000
3	61	7.588	7.588	0.000
3	62	7.587	7.587	0.000
3	63	7.587	7.587	0.000
3	64	7.588	7.587	-0.001
3	65	7.587	7.587	0.000
3	66	7.588	7.588	0.000
5	67	7.587	7.587	0.000
5	68	7.586	7.586	0.000
5	69	7.587	7.587	0.000
5	70	7.587	7.587	0.000
5	71	7.588	7.588	0.000
5	72	7.588	7.588	0.000
10	73	7.588	7.588	0.000
10	74	7.587	7.587	0.000
10	75	7.587	7.587	0.000
10	76	7.588	7.587	-0.001
10	77	7.587	7.587	0.000
10	78	7.588	7.587	-0.001
15	79	7.587	7.587	0.000
15	80	7.586	7.586	0.000
15	81	7.587	7.587	0.000
15	82	7.587	7.587	0.000
15	83	7.588	7.588	0.000
15	84	7.588	7.587	-0.001
30	86	7.588	7.587	-0.001
30	89	7.587	7.587	0.000
30	91	7.587	7.586	-0.001
30	92	7.588	7.587	-0.001
30	94	7.587	7.586	-0.001
30	95	7.588	7.588	0.000
35	100	7.587	7.587	0.000
35	101	7.586	7.586	0.000
35	102	7.587	7.586	-0.001
35	103	7.587	7.587	0.000
35	104	7.588	7.587	-0.001
35	105	7.588	7.587	-0.001
50	106	7.588	7.587	-0.001
50	107	7.588	7.587	-0.001
50	108	7.586	7.585	-0.001
50	109	7.587	7.586	-0.001
50	110	7.587	7.586	-0.001
50	111	7.588	7.587	-0.001
55	113	7.588	7.587	-0.001
55	114	7.587	7.586	-0.001
55	115	7.588	7.587	-0.001
55	116	7.588	7.587	-0.001
55	126	7.586	7.585	-0.001
55	128	7.588	7.587	-0.001
100	129	7.587	7.587	0.000
100	131	7.586	7.585	-0.001
100	132	7.587	7.586	-0.001
100	133	7.586	7.585	-0.001
100	134	7.588	7.587	-0.001
100	135	7.587	7.586	-0.001
100	138	7.588	7.587	-0.001
100	139	7.585	7.584	-0.001
100	140	7.586	7.585	-0.001
100	141	7.588	7.587	-0.001
100	142	7.586	7.585	-0.001
100	143	7.587	7.585	-0.002
100	144	7.587	7.586	-0.001
100	220	7.587	7.586	-0.001
100	221	7.587	7.586	-0.001
100	224	7.586	7.585	-0.001
100	225	7.587	7.586	-0.001
100	226	7.588	7.587	-0.001
100	228	7.588	7.587	-0.001
100	232	7.586	7.586	0.000
100	233	7.587	7.586	-0.001
100	236	7.586	7.585	-0.001
100	238	7.588	7.588	0.000
100	243	7.587	7.586	-0.001
105	244	7.587	7.585	-0.002
105	246	7.586	7.585	-0.001
105	248	7.587	7.586	-0.001
105	374	7.586	7.585	-0.001
105	376	7.588	7.587	-0.001
105	Max	7.588	7.588	0.000
105	Average	7.587	7.587	-0.001
105	Min	7.585	7.584	-0.002
105	Std Dev	0.001	0.001	0.001



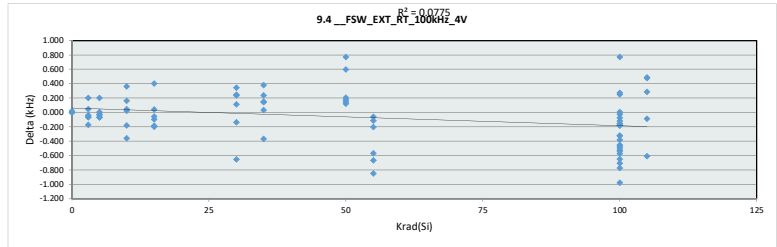
9.185_T_SYNC_D											
Test Site											
Tester											
Test Number											
Max Limit	20 µs										
Min Limit	µs										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.586	7.587	7.586	7.587	7.586	7.586	7.586	7.585	7.585	7.584	7.585
Average	7.587	7.587	7.587	7.587	7.587	7.587	7.587	7.586	7.587	7.586	7.586
Max	7.587	7.588	7.588	7.588	7.588	7.588	7.587	7.587	7.587	7.588	7.587
UL	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000



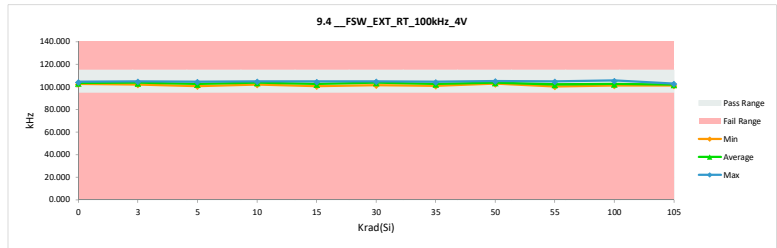
LDR TID Report
TPS7H5001-SP

9.4_FSW_EXT_RT_100kHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	115
Min Limit	95

Krad(Si)	Serial #	PRE	POST	Delta
0	117	104.640	104.650	0.010
0	118	103.382	103.390	0.008
0	122	102.764	102.792	0.028
3	61	104.230	104.175	-0.055
3	62	104.811	105.021	0.210
3	63	102.449	102.285	-0.164
3	64	103.076	103.047	-0.029
3	65	104.387	104.331	-0.056
3	66	103.906	103.960	0.054
5	67	102.778	102.792	0.014
5	68	102.320	102.304	-0.016
5	69	102.636	102.574	-0.062
5	70	104.512	104.725	0.213
5	71	102.929	102.904	-0.025
5	72	100.854	100.796	-0.058
10	73	104.230	104.287	0.057
10	74	104.811	105.182	0.371
10	75	102.449	102.277	-0.172
10	76	103.076	102.728	-0.348
10	77	104.387	104.556	0.169
10	78	103.906	103.940	0.034
15	79	102.778	102.831	0.053
15	80	102.320	102.132	-0.188
15	81	102.636	102.550	-0.086
15	82	104.512	104.925	0.413
15	83	102.929	102.752	-0.177
15	84	100.854	100.805	-0.049
30	86	104.230	104.475	0.245
30	89	104.811	105.066	0.255
30	91	102.449	101.804	-0.645
30	92	103.076	102.948	-0.128
30	94	104.387	104.740	0.353
30	95	103.906	104.030	0.124
35	100	102.778	102.931	0.153
35	101	102.320	101.963	-0.357
35	102	102.636	103.027	0.391
35	103	104.512	104.668	0.156
35	104	102.929	102.970	0.041
35	105	100.854	101.101	0.247
50	106	103.512	103.728	0.216
50	107	103.243	103.399	0.156
50	108	105.201	105.387	0.186
50	109	103.760	104.367	0.607
50	110	102.999	103.132	0.133
50	111	102.147	102.930	0.783
55	113	101.779	100.939	-0.840
55	114	102.137	101.576	-0.561
55	115	102.736	102.079	-0.657
55	116	100.590	100.483	-0.107
55	126	105.037	104.985	-0.052
55	128	103.920	103.727	-0.193
100	129	102.274	102.533	0.259
100	131	104.163	103.706	-0.457
100	132	104.663	104.022	-0.641
100	133	102.170	102.154	-0.016
100	134	102.823	102.713	-0.110
100	135	102.850	102.476	-0.374
100	138	101.988	102.768	0.780
100	139	102.710	102.012	-0.698
100	140	102.917	102.743	-0.174
100	141	104.554	103.792	-0.762
100	142	102.335	102.619	0.284
100	143	102.129	101.645	-0.484
100	144	101.800	101.487	-0.313
100	220	103.914	102.945	-0.969
100	221	103.711	103.644	-0.067
100	224	102.665	102.102	-0.563
100	225	104.684	104.170	-0.514
100	226	105.891	105.746	-0.145
100	228	102.169	102.182	0.013
100	232	102.740	102.570	-0.170
100	233	101.931	101.782	-0.149
100	236	102.864	102.551	-0.313
100	238	104.019	103.493	-0.526
100	243	103.319	102.874	-0.445
105	244	102.036	101.436	-0.600
105	246	102.537	102.458	-0.079
105	248	102.469	102.957	0.488
105	374	101.129	101.623	0.494
105	376	102.782	103.077	0.295
105	378	105.891	105.746	-0.145
Max		105.891	105.746	0.783
Average		103.134	103.068	-0.066
Min		100.590	100.483	-0.969
Std Dev		1.122	1.189	0.358



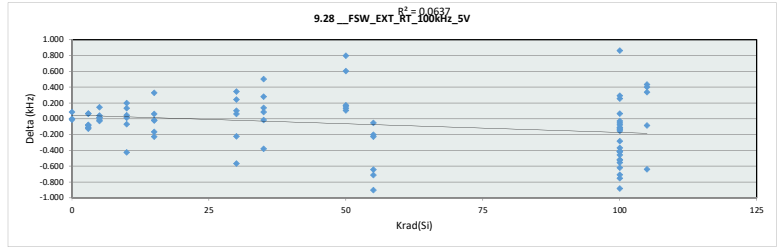
9.4_FSW_EXT_R												
Test Site												
Tester												
Test Number												
Max Limit	115	kHz										
Min Limit	95	kHz										
Krad(Si)	0	5	10	15	30	35	50	55	100	105		
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	
Min	102.792	102.285	100.796	102.277	100.805	101.804	101.101	102.930	100.483	101.487	101.436	
Average	103.611	103.803	102.683	103.828	102.666	103.844	102.777	103.824	102.298	102.864	102.310	
Max	104.650	105.021	104.725	105.182	104.925	105.066	104.668	105.387	104.985	105.746	103.077	
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	



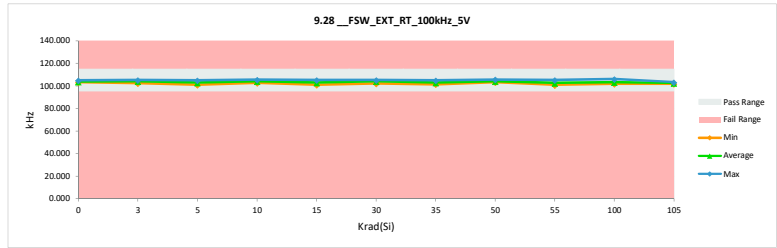
LDR TID Report
TPS7H5001-SP

9.28 FSW_EXT_RT_100kHz_5V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	115
Min Limit	95

Krad(Si)	Serial #	PRE	POST	Delta
0	117	105.035	105.031	-0.004
0	118	103.714	103.720	0.006
0	122	103.134	103.226	0.092
3	61	104.589	104.520	-0.069
3	62	105.327	105.400	0.073
3	63	102.728	102.610	-0.118
3	64	103.503	103.403	-0.100
3	65	104.779	104.702	-0.077
3	66	104.331	104.404	0.073
5	67	103.126	103.145	0.019
5	68	102.663	102.683	0.020
5	69	102.892	102.941	0.049
5	70	104.968	105.120	0.152
5	71	103.389	103.380	-0.009
5	72	101.198	101.178	-0.020
10	73	104.589	104.640	0.051
10	74	105.327	105.534	0.207
10	75	102.728	102.668	-0.060
10	76	103.503	103.086	-0.417
10	77	104.779	104.919	0.140
10	78	104.331	104.349	0.018
15	79	103.126	103.193	0.067
15	80	102.663	102.503	-0.160
15	81	102.892	102.880	-0.012
15	82	104.968	105.303	0.335
15	83	103.389	103.169	-0.220
15	84	101.198	101.184	-0.014
30	86	104.589	104.839	0.250
30	89	105.327	105.437	0.110
30	91	102.728	102.168	-0.560
30	92	103.503	103.289	-0.214
30	94	104.779	105.130	0.351
30	95	104.331	104.399	0.068
35	100	103.126	103.273	0.147
35	101	102.663	102.292	-0.371
35	102	102.892	103.403	0.511
35	103	104.968	105.060	0.092
35	104	103.389	103.380	-0.009
35	105	101.198	101.483	0.285
50	106	104.031	104.143	0.112
50	107	103.647	103.787	0.140
50	108	105.593	105.761	0.168
50	109	104.066	104.675	0.609
50	110	103.360	103.538	0.178
50	111	102.476	103.281	0.805
55	113	102.250	101.353	-0.897
55	114	102.543	101.906	-0.637
55	115	103.130	102.427	-0.703
55	116	101.027	100.834	-0.193
55	126	105.394	105.349	-0.045
55	128	104.320	104.100	-0.220
100	129	102.686	102.949	0.263
100	131	104.487	104.077	-0.410
100	132	104.993	104.383	-0.610
100	133	102.535	102.515	-0.020
100	134	103.186	103.117	-0.069
100	135	103.216	102.815	-0.401
100	138	102.297	103.165	0.868
100	139	103.081	102.382	-0.699
100	140	103.240	103.115	-0.125
100	141	104.932	104.188	-0.744
100	142	102.746	103.043	0.297
100	143	102.472	102.024	-0.448
100	144	102.165	101.890	-0.275
100	220	104.235	103.361	-0.874
100	221	104.091	104.046	-0.045
100	224	102.983	102.468	-0.515
100	225	105.071	104.523	-0.548
100	226	106.325	106.177	-0.148
100	228	102.540	102.612	0.072
100	232	103.117	103.009	-0.108
100	233	102.255	102.152	-0.103
100	236	103.291	102.931	-0.360
100	238	104.399	103.887	-0.512
100	243	103.672	103.264	-0.408
105	244	102.495	101.863	-0.632
105	246	102.852	102.775	-0.077
105	248	102.837	103.244	0.407
105	374	101.500	101.942	0.442
105	376	103.099	103.441	0.342
105	Max	106.325	106.177	0.868
105	Average	103.512	103.444	-0.068
105	Min	101.027	100.834	-0.897
105	Std Dev	1.143	1.191	0.354



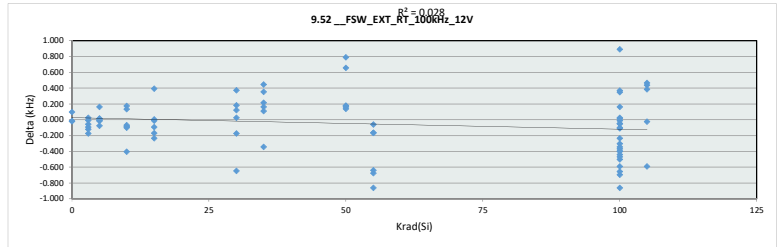
9.28 FSW_EXT		5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	103.226	102.610	101.178	102.668	101.184	102.168	101.483	103.281	100.834	101.890
Average	103.992	104.173	103.075	104.199	103.039	104.210	103.149	104.198	102.662	103.254
Max	105.031	105.400	105.120	105.534	105.303	105.437	105.060	105.761	105.349	106.177
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000



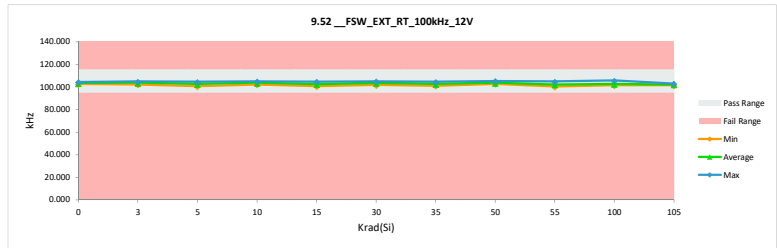
LDR TID Report
TPS7H5001-SP

9.52_FSW_EXT_RT_100kHz_12V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	115
Min Limit	95

Krad(Si)	Serial #	PRE	POST	Delta
0	117	104.472	104.461	-0.011
0	118	103.196	103.188	-0.008
0	122	102.578	102.681	0.103
3	61	104.152	104.038	-0.114
3	62	104.842	104.873	0.031
3	63	102.303	102.136	-0.167
3	64	103.005	102.919	-0.086
3	65	104.219	104.169	-0.050
3	66	103.858	103.856	-0.002
5	67	102.620	102.637	0.017
5	68	102.169	102.160	-0.009
5	69	102.449	102.381	-0.068
5	70	104.327	104.495	0.168
5	71	102.790	102.790	0.000
5	72	100.669	100.694	0.025
10	73	104.152	104.089	-0.063
10	74	104.842	105.024	0.182
10	75	102.303	102.211	-0.092
10	76	103.005	102.606	-0.399
10	77	104.219	104.361	0.142
10	78	103.858	103.781	-0.077
15	79	102.620	102.632	0.012
15	80	102.169	101.940	-0.229
15	81	102.449	102.365	-0.084
15	82	104.327	104.726	0.399
15	83	102.790	102.628	-0.162
15	84	100.669	100.663	-0.006
30	86	104.152	104.340	0.188
30	89	104.842	104.969	0.127
30	91	102.303	101.663	-0.640
30	92	103.005	102.840	-0.165
30	94	104.219	104.597	0.378
30	95	103.858	103.889	0.031
35	100	102.620	102.789	0.169
35	101	102.169	101.832	-0.337
35	102	102.449	102.903	0.454
35	103	104.327	104.548	0.221
35	104	102.790	102.906	0.116
35	105	100.669	101.030	0.361
50	106	103.432	103.603	0.171
50	107	103.111	103.298	0.187
50	108	105.073	105.254	0.181
50	109	103.530	104.193	0.663
50	110	102.882	103.029	0.147
50	111	101.992	102.788	0.796
55	113	101.731	100.875	-0.856
55	114	102.079	101.448	-0.631
55	115	102.578	101.908	-0.670
55	116	100.514	100.356	-0.158
55	126	104.874	104.819	-0.055
55	128	103.756	103.598	-0.158
100	129	102.130	102.487	0.357
100	131	103.957	103.595	-0.362
100	132	104.489	103.906	-0.583
100	133	102.005	102.038	0.033
100	134	102.627	102.621	-0.006
100	135	102.717	102.380	-0.337
100	138	101.808	102.708	0.900
100	139	102.561	101.911	-0.650
100	140	102.740	102.698	-0.042
100	141	104.401	103.711	-0.690
100	142	102.166	102.543	0.377
100	143	102.004	101.544	-0.460
100	144	101.686	101.459	-0.227
100	220	103.742	102.886	-0.856
100	221	103.558	103.579	0.021
100	224	102.436	102.002	-0.434
100	225	104.508	104.015	-0.493
100	226	105.824	105.729	-0.095
100	228	101.962	102.131	0.169
100	232	102.611	102.509	-0.102
100	233	101.724	101.679	-0.045
100	236	102.749	102.452	-0.297
100	238	103.803	103.411	-0.392
100	243	103.169	102.779	-0.390
105	244	101.976	101.394	-0.582
105	246	102.336	102.321	-0.015
105	248	102.354	102.826	0.472
105	374	101.013	101.457	0.444
105	376	102.592	102.985	0.393
105	375	105.824	105.729	-0.095
105	Average	102.996	102.947	-0.049
105	Min	100.514	100.356	-0.856
105	Std Dev	1.135	1.177	0.351



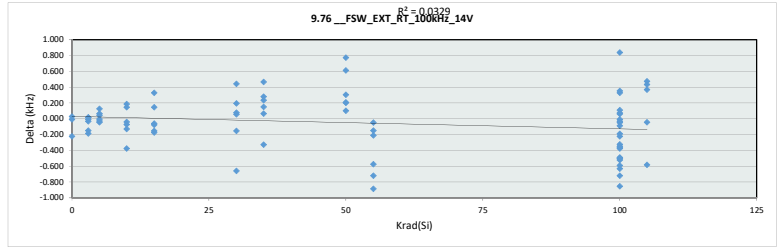
9.52_FSW_EXT												
Test Site												
Tester												
Test Number												
Max Limit	115	kHz										
Min Limit	95	kHz										
Krad(Si)	0	5	10	15	30	35	50	55	100	105		
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	
Min	102.681	102.136	100.694	102.211	100.663	101.663	101.030	102.788	100.356	101.459	101.394	
Average	103.443	103.665	102.526	103.679	102.492	103.716	102.668	103.694	102.167	102.782	102.197	
Max	104.461	104.873	104.495	105.024	104.726	104.969	104.548	105.254	104.819	105.729	102.985	
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	



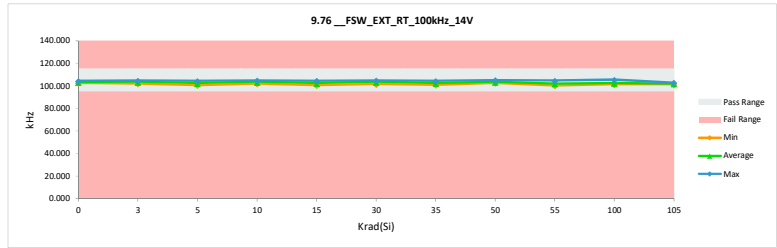
LDR TID Report
TPS7H5001-SP

9.76_FSW_EXT_RT_100kHz_14V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	115
Min Limit	95

Krad(Si)	Serial #	PRE	POST	Delta
0	117	104.456	104.490	0.034
0	118	103.396	103.180	-0.216
0	122	102.660	102.661	0.001
3	61	104.141	103.999	-0.142
3	62	104.859	104.860	0.001
3	63	102.293	102.115	-0.178
3	64	102.961	102.938	-0.023
3	65	104.157	104.182	0.025
3	66	103.812	103.835	0.023
5	67	102.543	102.617	0.074
5	68	102.110	102.112	0.002
5	69	102.412	102.390	-0.022
5	70	104.391	104.526	0.135
5	71	102.808	102.860	0.052
5	72	100.731	100.693	-0.038
10	73	104.141	104.077	-0.064
10	74	104.859	105.014	0.155
10	75	102.293	102.173	-0.120
10	76	102.961	102.594	-0.367
10	77	104.157	104.350	0.193
10	78	103.812	103.779	-0.033
15	79	102.543	102.696	0.153
15	80	102.110	101.965	-0.145
15	81	102.412	102.358	-0.054
15	82	104.391	104.727	0.336
15	83	102.808	102.643	-0.165
15	84	100.731	100.660	-0.071
30	86	104.141	104.343	0.202
30	89	104.859	104.922	0.063
30	91	102.293	101.642	-0.651
30	92	102.961	102.814	-0.147
30	94	104.157	104.605	0.448
30	95	103.812	103.895	0.083
35	100	102.543	102.786	0.243
35	101	102.110	101.788	-0.322
35	102	102.412	102.886	0.474
35	103	104.391	104.548	0.157
35	104	102.808	102.881	0.073
35	105	100.731	101.017	0.286
50	106	103.357	103.571	0.214
50	107	103.102	103.209	0.107
50	108	105.032	105.242	0.210
50	109	103.557	104.174	0.617
50	110	102.724	103.037	0.313
50	111	101.987	102.768	0.781
55	113	101.718	100.841	-0.877
55	114	102.047	101.478	-0.569
55	115	102.611	101.897	-0.714
55	116	100.554	100.351	-0.203
55	126	104.852	104.810	-0.042
55	128	103.748	103.604	-0.144
100	129	102.125	102.462	0.337
100	131	103.949	103.579	-0.370
100	132	104.460	103.878	-0.582
100	133	101.983	102.053	0.070
100	134	102.648	102.647	-0.001
100	135	102.709	102.367	-0.342
100	138	101.842	102.687	0.845
100	139	102.527	101.905	-0.622
100	140	102.708	102.683	-0.025
100	141	104.401	103.690	-0.711
100	142	102.162	102.521	0.359
100	143	102.028	101.515	-0.513
100	144	101.666	101.452	-0.214
100	220	103.735	102.887	-0.848
100	221	103.480	103.562	0.082
100	224	102.447	101.963	-0.484
100	225	104.509	104.012	-0.497
100	226	105.783	105.602	-0.181
100	228	102.016	102.131	0.115
100	232	102.589	102.508	-0.081
100	233	101.740	101.698	-0.042
100	236	102.754	102.438	-0.316
100	238	103.890	103.400	-0.490
100	243	103.141	102.789	-0.352
105	244	101.981	101.405	-0.576
105	246	102.330	102.295	-0.035
105	248	102.316	102.798	0.482
105	374	101.006	101.448	0.442
105	376	102.595	102.970	0.375
Max		105.783	105.602	0.845
Average		102.987	102.937	-0.050
Min		100.554	100.351	-0.877
Std Dev		1.129	1.173	0.352



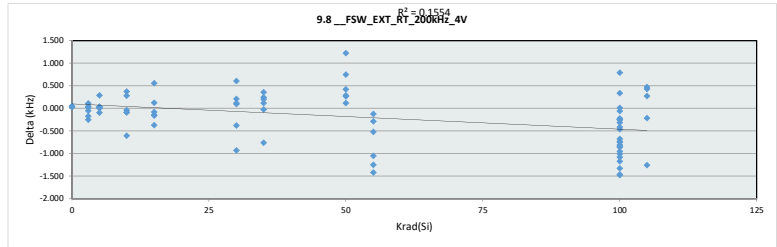
9.76_FSW_EXT										
Test Site										
Tester										
Test Number										
Max Limit	115 kHz									
Min Limit	95 kHz									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.661	102.115	100.693	102.173	100.660	101.642	101.017	102.768	100.351	101.452
Average	103.444	103.655	102.533	103.665	102.508	103.704	102.651	103.667	102.164	102.768
Max	104.490	104.860	104.526	105.014	104.727	104.922	104.548	105.242	104.810	105.602
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000



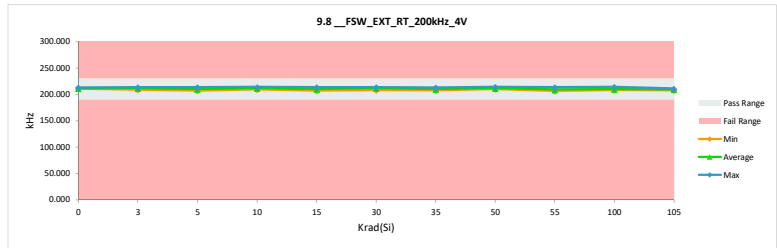
LDR TID Report
TPS7H5001-SP

9.8_FSW_EXT_RT_200kHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz kHz
Max Limit	230 230
Min Limit	190 190

Krad(Si)	Serial #	PRE	POST	Delta
0	117	213.421	213.463	0.042
0	118	211.339	211.402	0.063
0	122	210.901	210.964	0.063
3	61	212.744	212.588	-0.156
3	62	214.187	214.311	0.124
3	63	209.805	209.571	-0.234
3	64	211.436	211.395	-0.041
3	65	212.923	212.956	0.033
3	66	212.524	212.582	0.058
5	67	210.526	210.565	0.039
5	68	210.165	210.221	0.056
5	69	209.898	209.919	0.021
5	70	213.848	214.153	0.305
5	71	210.860	210.894	0.034
5	72	207.661	207.580	-0.081
10	73	212.744	212.718	-0.026
10	74	214.187	214.483	0.296
10	75	209.805	209.730	-0.075
10	76	211.436	210.842	-0.594
10	77	212.923	213.309	0.386
10	78	212.524	212.450	-0.074
15	79	210.526	210.668	0.142
15	80	210.165	209.811	-0.354
15	81	209.898	209.751	-0.147
15	82	213.848	214.416	0.568
15	83	210.860	210.730	-0.130
15	84	207.661	207.597	-0.064
30	86	212.744	212.966	0.222
30	89	214.187	214.315	0.128
30	91	209.805	208.885	-0.920
30	92	211.436	211.071	-0.365
30	94	212.923	213.542	0.619
30	95	212.524	212.631	0.107
35	100	210.526	210.744	0.218
35	101	210.165	209.415	-0.750
35	102	209.898	210.273	0.375
35	103	213.848	213.840	-0.008
35	104	210.860	210.994	0.134
35	105	207.661	207.919	0.258
50	106	211.872	212.307	0.435
50	107	211.244	211.523	0.279
50	108	214.334	214.637	0.303
50	109	212.645	213.410	0.765
50	110	211.133	211.263	0.130
50	111	209.385	210.625	1.240
55	113	209.110	207.702	-1.408
55	114	209.297	208.263	-1.034
55	115	210.432	209.198	-1.234
55	116	207.142	206.871	-0.271
55	126	214.090	213.979	-0.111
55	128	212.584	212.075	-0.509
100	129	209.878	209.901	0.023
100	131	212.831	212.033	-0.798
100	132	213.772	212.611	-1.161
100	133	209.795	209.555	-0.240
100	134	210.997	210.596	-0.401
100	135	211.036	210.320	-0.716
100	138	209.658	210.462	0.804
100	139	210.994	209.683	-1.311
100	140	211.072	210.626	-0.446
100	141	213.150	211.705	-1.445
100	142	210.387	210.738	0.351
100	143	209.516	208.694	-0.822
100	144	209.757	209.025	-0.732
100	220	212.146	210.682	-1.464
100	221	212.042	211.835	-0.207
100	224	210.437	209.497	-0.940
100	225	213.663	212.665	-0.998
100	226	214.902	214.664	-0.238
100	228	209.741	209.697	-0.044
100	232	210.375	210.076	-0.299
100	233	209.668	209.438	-0.230
100	236	210.633	209.973	-0.660
100	238	212.774	211.705	-1.069
100	243	211.106	210.262	-0.844
105	244	209.543	208.298	-1.245
105	246	210.424	210.223	-0.201
105	248	210.848	211.336	0.488
105	374	207.830	208.276	0.446
105	376	211.083	211.368	0.285
105	375	214.902	214.664	-0.238
Max				
Average		211.234	211.043	-0.191
Min		207.142	206.871	-1.464
Std Dev		1.773	1.892	0.568



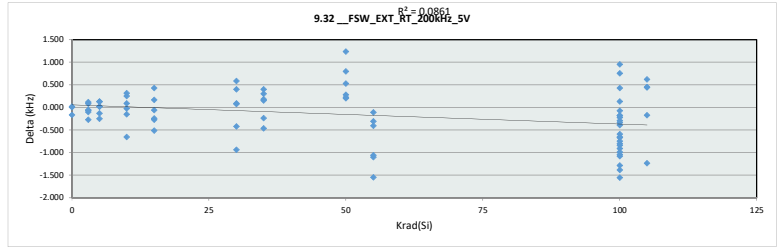
9.8_FSW_EXT_R										
Test Site										
Tester										
Test Number										
Max Limit	230 kHz									
Min Limit	190 kHz									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	210.964	209.571	207.580	209.730	207.597	208.885	207.919	210.625	206.871	208.694
Average	211.943	212.234	210.555	212.255	210.496	212.235	210.531	212.294	209.681	210.685
Max	213.463	214.311	214.153	214.483	214.416	214.315	213.840	214.637	213.979	214.664
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000



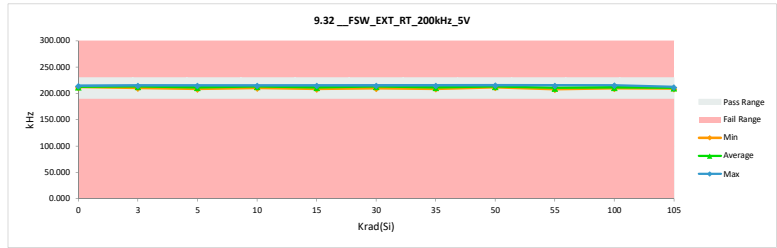
LDR TID Report
TPS7H5001-SP

9.32_FSW_EXT_RT_200kHz_5V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	230
Min Limit	190

Krad(Si)	Serial #	PRE	POST	Delta
0	117	214.455	214.472	0.017
0	118	212.473	212.321	-0.152
0	122	211.885	211.917	0.032
3	61	213.621	213.574	-0.047
3	62	215.194	215.322	0.128
3	63	210.776	210.516	-0.260
3	64	212.470	212.379	-0.091
3	65	214.025	213.981	-0.044
3	66	213.538	213.631	0.093
5	67	211.528	211.558	0.030
5	68	211.007	211.042	0.035
5	69	210.888	210.770	-0.118
5	70	215.030	215.178	0.148
5	71	212.178	211.944	-0.234
5	72	208.620	208.758	0.138
10	73	213.621	213.723	0.102
10	74	215.194	215.324	0.327
10	75	210.776	210.640	-0.136
10	76	212.470	211.827	-0.643
10	77	214.025	214.291	0.266
10	78	213.538	213.528	-0.010
15	79	211.528	211.707	0.179
15	80	211.007	210.752	-0.255
15	81	210.888	210.656	-0.232
15	82	215.030	215.473	0.443
15	83	212.178	211.679	-0.499
15	84	208.620	208.576	-0.044
30	86	213.621	214.035	0.414
30	89	215.194	215.299	0.105
30	91	210.776	209.854	-0.922
30	92	212.470	212.062	-0.408
30	94	214.025	214.626	0.601
30	95	213.538	213.624	0.086
35	100	211.528	211.722	0.194
35	101	211.007	210.555	-0.452
35	102	210.888	211.303	0.415
35	103	215.030	215.196	0.166
35	104	212.178	211.958	-0.220
35	105	208.620	208.939	0.319
50	106	213.012	213.556	0.544
50	107	212.328	212.546	0.218
50	108	215.417	215.714	0.297
50	109	213.535	214.350	0.815
50	110	212.063	212.304	0.241
50	111	210.406	211.658	1.252
55	113	210.256	208.722	-1.534
55	114	210.324	209.234	-1.090
55	115	211.427	210.380	-1.047
55	116	208.126	207.835	-0.291
55	126	215.130	215.032	-0.098
55	128	213.633	213.237	-0.396
100	129	210.654	211.099	0.445
100	131	213.770	213.038	-0.732
100	132	214.703	213.636	-1.067
100	133	210.810	210.602	-0.208
100	134	211.994	211.671	-0.323
100	135	212.002	211.346	-0.656
100	138	210.517	211.483	0.966
100	139	211.997	210.726	-1.271
100	140	212.060	211.681	-0.379
100	141	214.148	212.780	-1.368
100	142	211.376	212.144	0.768
100	143	210.435	209.609	-0.826
100	144	210.774	210.130	-0.644
100	220	213.166	211.628	-1.538
100	221	213.062	212.999	-0.063
100	224	211.411	210.516	-0.895
100	225	214.699	213.723	-0.976
100	226	215.937	215.770	-0.167
100	228	210.677	210.824	0.147
100	232	211.451	211.174	-0.277
100	233	210.604	210.437	-0.167
100	236	211.639	211.063	-0.576
100	238	213.791	212.764	-1.027
100	243	212.101	211.314	-0.787
105	244	210.578	209.358	-1.220
105	246	211.300	211.138	-0.162
105	248	211.711	212.342	0.631
105	374	208.731	209.198	0.467
105	376	211.964	212.420	0.456
105	Max	215.937	215.770	1.252
105	Average	212.239	212.076	-0.163
105	Min	208.126	207.835	-1.538
105	Std Dev	1.802	1.910	0.578



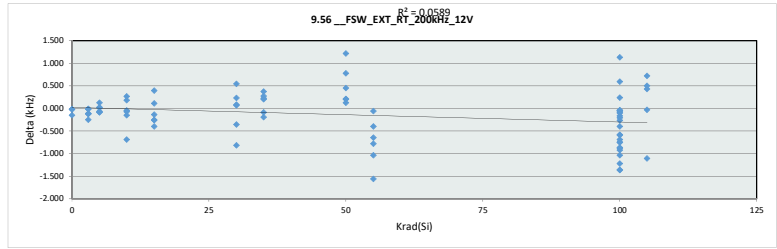
9.32_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	211.917	210.516	208.758	210.640	208.576	209.854	208.939	211.658	207.835	209.609	209.198
Average	212.903	213.234	211.542	213.255	211.474	213.250	211.612	213.355	210.740	211.757	210.891
Max	214.472	215.322	215.178	215.521	215.473	215.299	215.196	215.714	215.032	215.770	212.420
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000



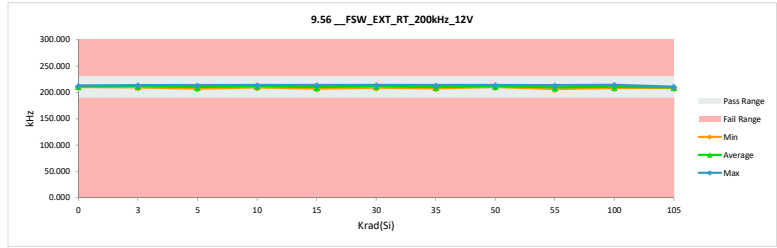
LDR TID Report
TPS7H5001-SP

9.56_FSW_EXT_RT_200kHz_12V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	230
Min Limit	190

Krad(S)	Serial #	PRE	POST	Delta
0	117	213.135	213.133	-0.002
0	118	211.168	211.155	-0.013
0	122	210.703	210.568	-0.135
3	61	212.508	212.400	-0.108
3	62	214.067	214.067	0.000
3	63	209.594	209.358	-0.236
3	64	211.123	211.019	-0.104
3	65	212.731	212.629	-0.102
3	66	212.295	212.300	0.005
5	67	210.260	210.301	0.041
5	68	209.634	209.776	0.142
5	69	209.701	209.634	-0.067
5	70	213.677	213.699	0.022
5	71	210.805	210.729	-0.076
5	72	207.495	207.441	-0.054
10	73	212.508	212.475	-0.033
10	74	214.067	214.263	0.196
10	75	209.594	209.458	-0.136
10	76	211.123	210.446	-0.677
10	77	212.731	213.008	0.277
10	78	212.295	212.236	-0.059
15	79	210.260	210.385	0.125
15	80	209.634	209.513	-0.121
15	81	209.701	209.459	-0.242
15	82	213.677	214.084	0.407
15	83	210.805	210.419	-0.386
15	84	207.495	207.251	-0.244
30	86	212.508	212.752	0.244
30	89	214.067	214.164	0.097
30	91	209.594	208.788	-0.806
30	92	211.123	210.781	-0.342
30	94	212.731	213.287	0.556
30	95	212.295	212.374	0.079
35	100	210.260	210.544	0.284
35	101	209.634	209.557	-0.077
35	102	209.701	210.089	0.388
35	103	213.677	213.896	0.219
35	104	210.805	210.624	-0.181
35	105	207.495	207.734	0.239
50	106	211.830	212.296	0.466
50	107	210.955	211.177	0.222
50	108	214.121	214.334	0.213
50	109	212.191	212.981	0.790
50	110	210.867	211.005	0.138
50	111	209.152	210.384	1.232
55	113	209.039	207.495	-1.544
55	114	209.122	208.102	-1.020
55	115	210.132	209.361	-0.771
55	116	206.964	206.579	-0.385
55	126	213.795	213.748	-0.047
55	128	212.721	212.086	-0.635
100	129	209.553	209.802	0.249
100	131	212.484	211.811	-0.673
100	132	213.423	212.397	-1.026
100	133	209.509	209.444	-0.065
100	134	210.695	210.545	-0.150
100	135	210.750	210.182	-0.568
100	138	209.251	210.398	1.147
100	139	210.741	209.533	-1.208
100	140	210.751	210.499	-0.252
100	141	212.897	211.551	-1.346
100	142	210.145	210.752	0.607
100	143	209.218	208.305	-0.913
100	144	209.435	209.049	-0.386
100	220	211.892	210.540	-1.352
100	221	211.778	211.751	-0.027
100	224	210.099	209.365	-0.734
100	225	213.321	212.471	-0.850
100	226	214.766	214.675	-0.091
100	228	209.464	209.426	-0.038
100	232	210.168	209.971	-0.197
100	233	209.312	209.272	-0.040
100	236	210.419	209.844	-0.575
100	238	212.556	211.691	-0.865
100	243	210.885	210.147	-0.738
105	244	209.382	208.289	-1.093
105	246	210.011	209.997	-0.014
105	248	210.485	211.222	0.737
105	374	207.704	208.216	0.512
105	376	210.740	211.183	0.443
105	375	214.766	214.675	-0.091
105	Max	210.992	210.846	-0.147
105	Average	206.964	206.579	-1.544
105	Min	206.964	206.579	-1.544
105	Std Dev	1.786	1.873	0.539



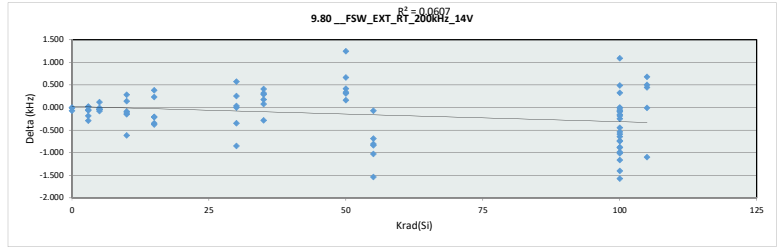
9.56_FSW_EXT										
Test Site										
Tester										
Test Number										
Max Limit	230 kHz									
Min Limit	190 kHz									
Krad(S)	0	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	211.568	209.358	207.441	209.458	207.251	208.788	207.734	210.384	206.579	208.305
Average	211.619	211.962	210.263	211.981	210.185	212.024	210.407	212.030	209.562	210.559
Max	213.133	214.067	213.699	214.263	214.084	214.164	213.896	214.334	213.748	214.675
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000



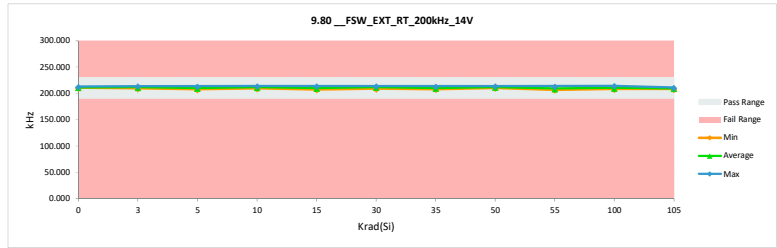
LDR TID Report
TPS7H5001-SP

9.80_FSW_EXT_RT_200kHz_14V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	230
Min Limit	190

Krad(Si)	Serial #	PRE	POST	Delta
0	117	213.132	213.134	0.002
0	118	211.047	211.059	0.012
0	122	210.668	210.607	-0.061
3	61	212.505	212.333	-0.172
3	62	214.048	214.071	0.023
3	63	209.588	209.307	-0.281
3	64	211.074	211.021	-0.053
3	65	212.690	212.649	-0.041
3	66	212.308	212.339	0.031
5	67	210.179	210.311	0.132
5	68	209.794	209.765	-0.029
5	69	209.651	209.628	-0.023
5	70	213.681	213.614	-0.067
5	71	210.755	210.721	-0.034
5	72	207.408	207.412	0.004
10	73	212.505	212.434	-0.071
10	74	214.048	214.202	0.154
10	75	209.588	209.449	-0.139
10	76	211.074	210.472	-0.602
10	77	212.690	212.986	0.296
10	78	212.308	212.196	-0.112
15	79	210.179	210.424	0.245
15	80	209.794	209.460	-0.334
15	81	209.651	209.448	-0.203
15	82	213.681	214.078	0.397
15	83	210.755	210.390	-0.365
15	84	207.408	207.217	-0.191
30	86	212.505	212.768	0.263
30	89	214.048	214.098	0.050
30	91	209.588	208.751	-0.837
30	92	211.074	210.737	-0.337
30	94	212.690	213.274	0.584
30	95	212.308	212.321	0.013
35	100	210.179	210.478	0.299
35	101	209.794	209.524	-0.270
35	102	209.651	210.072	0.421
35	103	213.681	213.770	0.089
35	104	210.755	210.945	0.190
35	105	207.408	207.735	0.327
50	106	211.825	212.252	0.427
50	107	210.826	211.175	0.349
50	108	214.049	214.371	0.322
50	109	212.311	212.987	0.676
50	110	210.836	211.008	0.172
50	111	209.136	210.396	1.260
55	113	209.001	207.477	-1.524
55	114	209.122	208.104	-1.018
55	115	210.026	209.230	-0.796
55	116	207.314	206.637	-0.677
55	126	213.766	213.706	-0.060
55	128	212.869	212.046	-0.823
100	129	209.320	209.660	0.340
100	131	212.448	211.818	-0.630
100	132	213.388	212.414	-0.974
100	133	209.508	209.425	-0.083
100	134	210.663	210.488	-0.175
100	135	210.722	210.145	-0.577
100	138	209.224	210.323	1.099
100	139	210.688	209.541	-1.147
100	140	210.696	210.456	-0.240
100	141	212.900	211.511	-1.389
100	142	210.133	210.632	0.499
100	143	209.236	208.237	-0.999
100	144	209.477	209.041	-0.436
100	220	211.880	210.318	-1.562
100	221	211.750	211.758	0.008
100	224	210.073	209.343	-0.730
100	225	213.306	212.442	-0.864
100	226	214.730	214.660	-0.070
100	228	209.436	209.437	0.001
100	232	210.120	209.978	-0.142
100	233	209.299	209.250	-0.049
100	236	210.416	209.892	-0.524
100	238	212.556	211.684	-0.872
100	243	210.897	210.174	-0.723
105	244	209.386	208.302	-1.084
105	246	209.997	210.001	0.004
105	248	210.497	211.189	0.692
105	374	207.640	208.156	0.516
105	376	210.678	211.137	0.459
105	Max	214.730	214.660	1.260
105	Average	210.975	210.825	-0.150
105	Min	207.314	206.637	-1.562
105	Std Dev	1.784	1.871	0.550



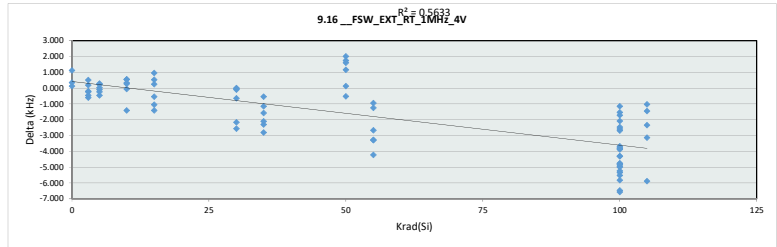
9.80_FSW_EXT		0	5	10	15	30	35	50	55	100	105
LL		190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min		210.607	209.307	207.412	209.449	207.217	208.751	207.735	210.396	206.637	208.237
Average		211.600	211.953	210.242	211.957	210.170	211.992	210.421	212.032	209.533	210.526
Max		213.134	214.071	213.614	214.202	214.078	214.098	213.770	214.371	213.706	214.660
UL		230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000



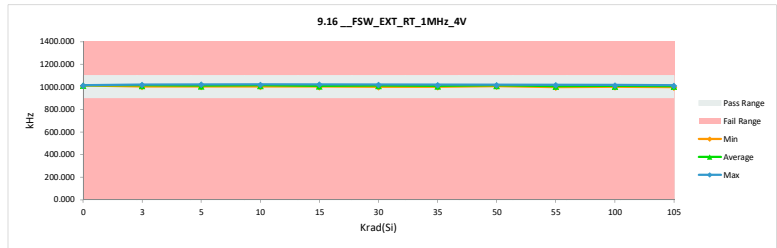
LDR TID Report
TPS7H5001-SP

9.16_FSW_EXT_RT_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz kHz
Max Limit	1100 1100
Min Limit	900 900

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1016.625	1017.001	0.376
0	118	1013.508	1013.677	0.169
0	122	1014.885	1016.047	1.162
3	61	1014.566	1014.006	-0.560
3	62	1021.207	1021.760	0.553
3	63	1005.657	1005.223	-0.434
3	64	1016.414	1016.219	-0.195
3	65	1014.372	1014.213	-0.159
3	66	1016.652	1016.883	0.231
5	67	1010.625	1010.194	-0.431
5	68	1013.291	1013.554	0.263
5	69	1005.295	1005.100	-0.195
5	70	1023.880	1024.197	0.317
5	71	1010.413	1010.344	-0.069
5	72	1007.923	1007.986	0.063
10	73	1014.566	1014.933	0.367
10	74	1021.207	1021.790	0.583
10	75	1005.657	1005.962	0.305
10	76	1016.414	1015.032	-1.382
10	77	1014.372	1014.959	0.587
10	78	1016.652	1016.634	-0.018
15	79	1010.625	1011.188	0.563
15	80	1013.291	1012.789	-0.502
15	81	1005.295	1003.927	-1.368
15	82	1023.880	1024.882	1.002
15	83	1010.413	1009.396	-1.017
15	84	1007.923	1008.198	0.275
30	86	1014.566	1014.497	-0.069
30	89	1021.207	1021.203	-0.004
30	91	1005.657	1003.123	-2.534
30	92	1016.414	1014.298	-2.116
30	94	1014.372	1014.408	0.036
30	95	1016.652	1016.052	-0.600
35	100	1010.625	1010.111	-0.514
35	101	1013.291	1011.221	-2.070
35	102	1005.295	1003.035	-2.260
35	103	1023.880	1021.109	-2.771
35	104	1010.413	1008.871	-1.542
35	105	1007.923	1006.807	-1.116
50	106	1015.360	1016.560	1.200
50	107	1013.268	1013.429	0.161
50	108	1017.042	1018.829	1.787
50	109	1018.953	1020.584	1.631
50	110	1014.793	1014.317	-0.476
50	111	1005.771	1007.808	2.037
55	113	1007.197	1002.999	-4.198
55	114	1002.511	999.264	-3.247
55	115	1009.986	1007.351	-2.635
55	116	1000.700	999.798	-0.902
55	126	1019.883	1018.665	-1.218
55	128	1017.985	1014.734	-3.251
100	129	1010.195	1006.548	-3.647
100	131	1018.490	1013.585	-4.905
100	132	1022.562	1017.759	-4.803
100	133	1012.650	1008.866	-3.784
100	134	1016.399	1012.134	-4.265
100	135	1011.502	1006.778	-4.724
100	138	1011.230	1009.551	-1.679
100	139	1015.098	1009.319	-5.779
100	140	1014.474	1010.639	-3.835
100	141	1013.734	1007.206	-6.528
100	142	1015.802	1014.301	-1.501
100	143	1007.293	1001.808	-5.485
100	144	1015.989	1010.685	-5.304
100	220	1014.570	1008.139	-6.431
100	221	1013.342	1011.303	-2.039
100	224	1011.951	1007.201	-4.750
100	225	1018.230	1013.269	-4.961
100	226	1015.057	1012.508	-2.549
100	228	1010.427	1009.322	-1.105
100	232	1011.534	1009.099	-2.435
100	233	1013.168	1010.525	-2.643
100	236	1005.189	1001.439	-3.750
100	238	1016.712	1011.511	-5.201
100	243	1007.856	1003.596	-4.260
105	244	1007.450	1001.602	-5.848
105	246	1012.746	1011.321	-1.425
105	248	1016.178	1015.196	-0.982
105	374	1002.257	999.959	-2.298
105	376	1016.223	1013.118	-3.105
Max		1023.880	1024.882	2.037
Average		1013.096	1011.418	-1.678
Min		1000.700	999.264	-6.528
Std Dev		5.180	5.796	2.167



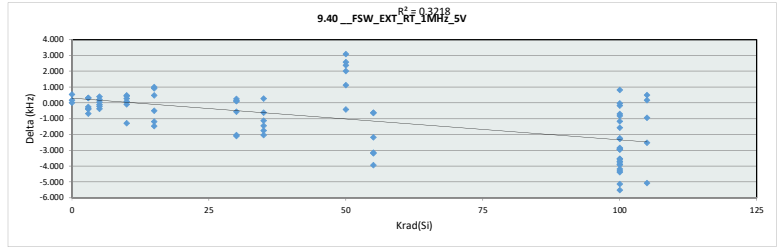
9.16_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	1100 kHz										
Min Limit	900 kHz										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1013.677	1005.223	1005.100	1005.962	1003.927	1003.123	1003.035	1007.808	999.264	1001.439	999.959
Average	1015.575	1014.717	1011.896	1014.885	1011.730	1013.930	1010.192	1015.255	1007.135	1009.462	1008.239
Max	1017.001	1021.760	1024.197	1021.790	1024.882	1021.203	1021.109	1020.584	1018.665	1017.759	1015.196
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000



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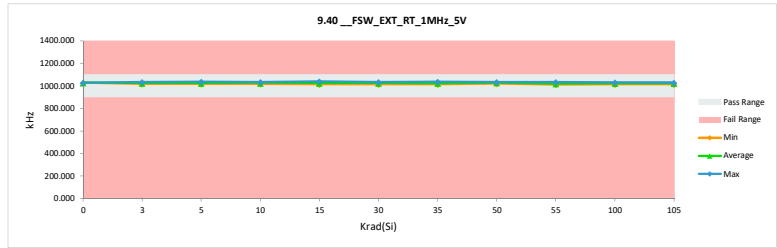
9.40_FSW_EXT_RT_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	1100
Min Limit	900

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1029.916	1030.121	0.205
0	118	1028.590	1028.630	0.040
0	122	1030.407	1030.987	0.580
3	61	1029.874	1029.227	-0.647
3	62	1036.045	1036.408	0.363
3	63	1018.789	1018.433	-0.356
3	64	1030.200	1029.993	-0.207
3	65	1028.048	1027.739	-0.309
3	66	1030.224	1030.579	0.355
5	67	1025.484	1025.145	-0.339
5	68	1027.840	1028.276	0.436
5	69	1018.230	1018.157	-0.073
5	70	1039.545	1039.790	0.245
5	71	1025.306	1025.141	-0.165
5	72	1022.378	1022.464	0.086
10	73	1029.874	1030.005	0.131
10	74	1036.045	1036.367	0.322
10	75	1018.789	1019.254	0.465
10	76	1030.200	1028.959	-1.241
10	77	1028.048	1028.557	0.509
10	78	1030.224	1030.177	-0.047
15	79	1025.484	1026.447	0.963
15	80	1027.840	1027.388	-0.452
15	81	1018.230	1016.804	-1.426
15	82	1039.545	1040.592	1.047
15	83	1025.306	1024.161	-1.145
15	84	1022.378	1022.884	0.506
30	86	1029.874	1030.044	0.170
30	89	1036.045	1036.335	0.290
30	91	1018.789	1016.729	-2.060
30	92	1030.200	1028.210	-1.990
30	94	1028.048	1028.205	0.157
30	95	1030.224	1029.715	-0.509
35	100	1025.484	1025.800	0.316
35	101	1027.840	1026.429	-1.411
35	102	1018.230	1016.513	-1.717
35	103	1039.545	1037.550	-1.995
35	104	1025.306	1024.215	-1.091
35	105	1022.378	1021.798	-0.580
50	106	1030.758	1032.802	2.044
50	107	1028.423	1029.585	1.162
50	108	1028.567	1032.691	3.124
50	109	1034.527	1036.949	2.422
50	110	1030.313	1029.933	-0.380
50	111	1019.126	1021.745	2.619
55	113	1021.368	1017.473	-3.895
55	114	1016.476	1013.368	-3.108
55	115	1024.572	1022.441	-2.131
55	116	1016.054	1015.451	-0.603
55	126	1035.414	1034.827	-0.587
55	128	1032.844	1029.697	-3.147
100	129	1025.279	1023.077	-2.202
100	131	1032.519	1028.261	-4.258
100	132	1037.206	1033.543	-3.663
100	133	1027.861	1025.065	-2.796
100	134	1031.544	1028.629	-2.915
100	135	1023.547	1019.701	-3.846
100	138	1025.798	1025.674	-0.124
100	139	1028.561	1024.225	-4.336
100	140	1028.938	1026.076	-2.862
100	141	1027.002	1021.904	-5.098
100	142	1030.534	1030.542	0.008
100	143	1021.890	1017.752	-4.138
100	144	1030.712	1027.019	-3.693
100	220	1028.749	1023.284	-5.465
100	221	1027.657	1027.005	-0.652
100	224	1026.219	1022.732	-3.487
100	225	1031.446	1027.924	-3.522
100	226	1029.761	1028.639	-1.122
100	228	1024.312	1025.174	0.862
100	232	1026.460	1025.677	-0.783
100	233	1026.759	1025.224	-1.535
100	236	1018.095	1015.850	-2.245
100	238	1031.033	1027.154	-3.879
100	243	1021.927	1019.030	-2.897
105	244	1022.117	1017.082	-5.035
105	246	1026.084	1026.298	0.214
105	248	1029.882	1030.424	0.542
105	374	1016.573	1015.670	-0.903
105	376	1029.804	1027.317	-2.487
105	Max	1039.545	1040.592	3.124
105	Average	1027.406	1026.339	-1.067
105	Min	1016.054	1013.368	-5.465
105	Std Dev	5.510	5.998	1.876



9.40_FSW_EXT	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	1100
Min Limit	900

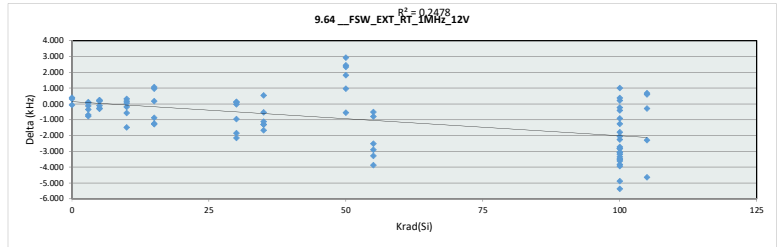
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1028.630	1018.433	1018.157	1019.254	1016.804	1016.729	1016.513	1021.745	1013.368	1015.850	1015.670
Average	1029.913	1028.730	1026.496	1028.887	1026.379	1028.206	1025.384	1030.618	1022.210	1024.965	1023.358
Max	1030.987	1036.408	1039.790	1036.367	1040.592	1036.335	1037.550	1036.949	1034.827	1033.543	1030.424
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000



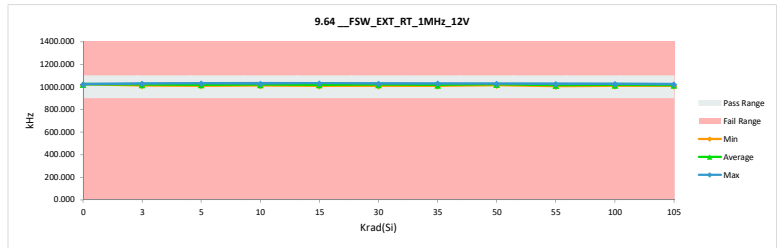
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		9.64_FSW_EXT_RT_1MHz_12V	
Test Site			
Tester			
Test Number			
Unit	kHz	kHz	
Max Limit	1100	1100	
Min Limit	900	900	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1024.335	1024.688	0.353
0	118	1023.558	1023.517	-0.041
0	122	1025.143	1025.587	0.444
3	61	1024.929	1024.194	-0.735
3	62	1031.106	1031.201	0.095
3	63	1013.840	1013.187	-0.653
3	64	1024.646	1024.558	-0.088
3	65	1022.912	1022.607	-0.305
3	66	1025.168	1025.310	0.142
5	67	1020.286	1020.206	-0.080
5	68	1022.943	1023.222	0.279
5	69	1012.705	1012.985	0.280
5	70	1034.288	1034.499	0.211
5	71	1020.404	1020.135	-0.269
5	72	1017.515	1017.279	-0.236
10	73	1024.929	1024.767	-0.162
10	74	1031.106	1031.196	0.090
10	75	1013.840	1014.048	0.208
10	76	1024.646	1023.203	-1.443
10	77	1022.912	1023.261	0.349
10	78	1025.168	1024.637	-0.531
15	79	1020.286	1021.392	1.106
15	80	1022.943	1022.108	-0.835
15	81	1012.705	1011.493	-1.212
15	82	1034.288	1035.285	0.997
15	83	1020.404	1019.154	-1.250
15	84	1017.515	1017.717	0.202
30	86	1024.929	1024.947	0.018
30	89	1031.106	1031.236	0.130
30	91	1013.840	1011.719	-2.121
30	92	1024.646	1022.841	-1.805
30	94	1022.912	1023.087	0.175
30	95	1025.168	1024.250	-0.918
35	100	1020.286	1020.852	0.566
35	101	1022.943	1021.668	-1.275
35	102	1012.705	1011.447	-1.258
35	103	1034.288	1032.661	-1.627
35	104	1020.404	1019.322	-1.082
35	105	1017.515	1017.017	-0.498
50	106	1025.569	1027.413	1.844
50	107	1022.929	1023.939	1.010
50	108	1024.224	1027.180	2.956
50	109	1028.888	1031.358	2.470
50	110	1025.526	1025.001	-0.525
50	111	1013.970	1016.355	2.385
55	113	1016.250	1012.416	-3.834
55	114	1011.376	1008.131	-3.245
55	115	1019.639	1017.160	-2.479
55	116	1011.238	1010.470	-0.768
55	126	1030.121	1029.650	-0.471
55	128	1027.253	1024.388	-2.865
100	129	1020.295	1018.541	-1.754
100	131	1026.917	1023.131	-3.786
100	132	1031.566	1028.528	-3.038
100	133	1022.548	1020.338	-2.210
100	134	1026.367	1023.587	-2.780
100	135	1018.068	1014.571	-3.497
100	138	1020.583	1020.827	0.244
100	139	1023.121	1019.229	-3.892
100	140	1023.679	1020.902	-2.777
100	141	1021.707	1016.867	-4.840
100	142	1025.232	1025.636	0.404
100	143	1016.769	1013.196	-3.573
100	144	1025.703	1022.269	-3.434
100	220	1023.530	1018.201	-5.329
100	221	1022.242	1021.868	-0.374
100	224	1020.964	1017.954	-3.010
100	225	1026.029	1022.712	-3.317
100	226	1024.782	1023.904	-0.878
100	228	1019.163	1020.211	1.048
100	232	1020.997	1020.806	-0.191
100	233	1021.469	1020.247	-1.222
100	236	1013.068	1011.059	-2.009
100	238	1025.352	1022.213	-3.139
100	243	1017.043	1014.355	-2.688
105	244	1017.237	1012.622	-4.615
105	246	1020.770	1021.416	0.646
105	248	1024.936	1025.645	0.709
105	374	1011.764	1011.506	-0.258
105	376	1024.516	1022.258	-2.258
105	Max	1034.288	1035.285	2.956
105	Average	1022.233	1021.257	-0.976
105	Min	1011.238	1008.131	-5.329
105	Std Dev	5.447	5.895	1.739



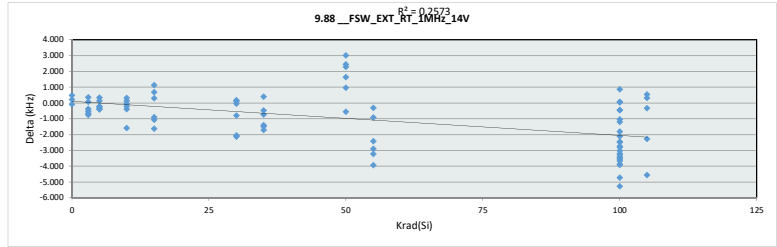
		9.64_FSW_EXT										
Test Site												
Tester												
Test Number												
Max Limit	1100	kHz										
Min Limit	900	kHz										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	
Min	1023.517	1013.187	1012.985	1014.048	1011.493	1011.719	1011.447	1016.355	1008.131	1011.059	1011.506	
Average	1024.597	1023.510	1021.388	1023.519	1021.192	1023.013	1020.495	1025.208	1017.036	1020.048	1018.689	
Max	1025.587	1031.201	1034.499	1031.196	1035.285	1031.236	1032.661	1031.358	1029.650	1028.528	1025.645	
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	



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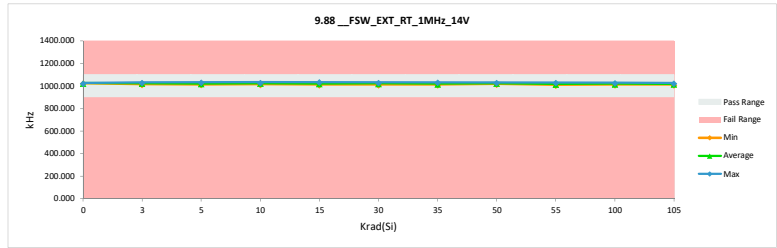
9.88_FSW_EXT_RT_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	1100
Min Limit	900

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1024.104	1024.354	0.250
0	118	1023.471	1023.429	-0.042
0	122	1025.067	1025.586	0.519
3	61	1024.771	1024.049	-0.722
3	62	1031.050	1031.164	0.114
3	63	1013.696	1013.085	-0.611
3	64	1024.752	1024.422	-0.330
3	65	1022.833	1022.363	-0.470
3	66	1024.908	1025.297	0.389
5	67	1020.381	1020.002	-0.379
5	68	1022.923	1023.096	0.173
5	69	1013.022	1012.725	-0.297
5	70	1033.903	1034.276	0.373
5	71	1020.063	1019.890	-0.173
5	72	1017.332	1017.117	-0.215
10	73	1024.771	1024.591	-0.180
10	74	1031.050	1031.032	-0.018
10	75	1013.696	1013.858	0.162
10	76	1024.752	1023.209	-1.543
10	77	1022.833	1023.181	0.348
10	78	1024.908	1024.544	-0.364
15	79	1020.381	1021.090	0.709
15	80	1022.923	1022.065	-0.858
15	81	1013.022	1011.425	-1.597
15	82	1033.903	1035.074	1.171
15	83	1020.063	1019.049	-1.014
15	84	1017.332	1017.674	0.342
30	86	1024.771	1024.916	0.145
30	89	1031.050	1031.041	-0.009
30	91	1013.696	1011.597	-2.099
30	92	1024.752	1022.736	-2.016
30	94	1022.833	1023.040	0.207
30	95	1024.908	1024.158	-0.750
35	100	1020.381	1020.807	0.426
35	101	1022.923	1021.485	-1.438
35	102	1013.022	1011.352	-1.670
35	103	1033.903	1032.531	-1.372
35	104	1020.063	1019.367	-0.696
35	105	1017.332	1016.890	-0.442
50	106	1025.488	1027.166	1.678
50	107	1023.029	1024.034	1.005
50	108	1024.036	1027.076	3.040
50	109	1028.759	1031.230	2.471
50	110	1025.228	1024.710	-0.518
50	111	1013.966	1016.292	2.326
55	113	1016.182	1012.290	-3.892
55	114	1011.301	1008.118	-3.183
55	115	1019.525	1017.140	-2.385
55	116	1011.108	1010.226	-0.882
55	126	1029.869	1029.597	-0.272
55	128	1027.167	1024.299	-2.868
100	129	1020.155	1018.383	-1.772
100	131	1026.874	1023.006	-3.868
100	132	1031.613	1028.450	-3.163
100	133	1022.526	1020.095	-2.431
100	134	1026.188	1023.490	-2.698
100	135	1017.974	1014.342	-3.632
100	138	1020.473	1020.537	0.064
100	139	1023.022	1019.178	-3.844
100	140	1023.600	1020.842	-2.758
100	141	1021.522	1016.841	-4.681
100	142	1025.215	1025.331	0.116
100	143	1016.637	1013.129	-3.508
100	144	1025.610	1022.180	-3.430
100	220	1023.406	1018.168	-5.238
100	221	1022.230	1021.823	-0.407
100	224	1020.772	1017.786	-2.986
100	225	1025.859	1022.604	-3.255
100	226	1024.774	1023.767	-1.007
100	228	1019.152	1020.061	0.909
100	232	1020.994	1020.583	-0.411
100	233	1021.294	1020.130	-1.164
100	236	1012.926	1010.841	-2.085
100	238	1025.336	1021.940	-3.396
100	243	1016.798	1014.376	-2.422
105	244	1017.013	1012.488	-4.525
105	246	1020.772	1021.134	0.362
105	248	1024.827	1025.404	0.577
105	374	1011.664	1011.362	-0.302
105	376	1024.416	1022.174	-2.242
105	Max	1033.903	1035.074	3.040
105	Average	1022.136	1021.127	-1.008
105	Min	1011.108	1008.118	-5.238
105	Std Dev	5.406	5.888	1.716



9.88_FSW_EXT	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	1100
Min Limit	900

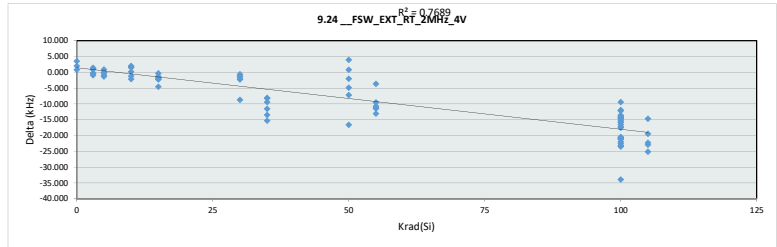
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1023.429	1013.085	1012.725	1013.858	1011.425	1011.597	1011.352	1016.292	1008.118	1010.841	1011.362
Average	1024.456	1023.397	1021.184	1023.403	1021.063	1022.915	1020.405	1025.085	1016.945	1019.912	1018.512
Max	1025.586	1031.164	1034.276	1031.032	1035.074	1031.041	1032.531	1031.230	1029.597	1028.450	1025.404
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000



LDR TID Report
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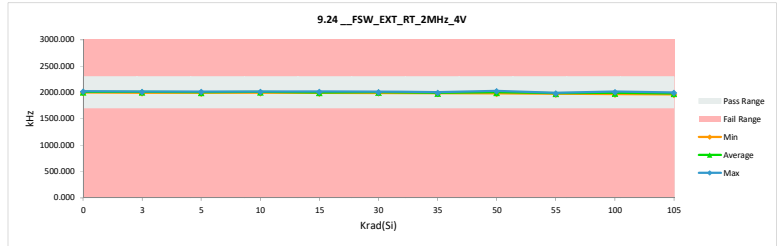
9.24_FSW_EXT_RT_2MHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	2300
Min Limit	1700

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2023.508	2025.746	2.238
0	118	2001.757	2002.727	0.970
0	122	2005.692	2009.369	3.677
3	61	1997.022	1996.747	-0.275
3	62	2013.080	2014.639	1.559
3	63	2010.961	2010.901	-0.060
3	64	2016.920	2018.351	1.431
3	65	2019.328	2020.417	1.089
3	66	2016.633	2015.897	-0.736
5	67	1995.582	1995.654	0.072
5	68	2004.039	2002.902	-1.137
5	69	2012.047	2011.293	-0.754
5	70	2019.587	2019.647	0.060
5	71	2003.940	2004.122	0.182
5	72	1991.611	1992.695	1.084
10	73	1997.022	1998.701	1.679
10	74	2013.080	2014.936	1.856
10	75	2010.961	2009.877	-1.084
10	76	2016.920	2014.992	-1.928
10	77	2019.328	2019.735	0.407
10	78	2016.633	2018.816	2.183
15	79	1995.582	1993.568	-2.014
15	80	2004.039	2003.892	-0.147
15	81	2012.047	2007.637	-4.410
15	82	2019.587	2018.466	-1.121
15	83	2003.940	2002.380	-1.560
15	84	1991.611	1989.642	-1.969
30	86	1997.022	1995.971	-1.051
30	89	2013.080	2011.052	-2.028
30	91	2010.961	2009.385	-1.576
30	92	2016.920	2014.966	-1.954
30	94	2019.328	2018.092	-1.236
30	95	2016.633	2016.217	-0.416
35	100	1995.582	1986.376	-9.206
35	101	2004.039	1992.634	-11.405
35	102	2012.047	1998.761	-13.286
35	103	2019.587	2004.438	-15.149
35	104	2003.940	1995.936	-8.004
35	105	1991.611	1983.649	-7.962
50	106	2002.471	2000.658	-1.813
50	107	1993.519	1986.541	-6.978
50	108	2048.079	2031.658	-16.421
50	109	2004.684	1999.961	-4.723
50	110	1996.240	1997.218	0.978
50	111	2012.861	2016.961	4.100
55	113	2005.858	1992.934	-12.924
55	114	1996.174	1986.794	-9.380
55	115	1992.387	1981.278	-11.109
55	116	1977.691	1974.263	-3.428
55	126	2002.051	1991.503	-10.548
55	128	2005.270	1993.957	-11.313
100	129	1992.369	1978.430	-13.939
100	131	2019.744	2007.774	-11.970
100	132	2031.054	2009.250	-21.804
100	133	1995.935	1981.008	-14.927
100	134	2008.763	1995.391	-13.372
100	135	2042.157	2008.398	-33.759
100	138	1993.894	1976.597	-17.297
100	139	2032.074	2008.725	-23.349
100	140	2013.620	2004.424	-9.196
100	141	2009.016	1988.297	-20.719
100	142	2005.849	1990.283	-15.566
100	143	1993.605	1973.296	-20.309
100	144	2012.828	1991.854	-20.974
100	220	2011.347	1994.343	-17.004
100	221	2005.736	1991.468	-14.268
100	224	2010.572	1994.287	-16.285
100	225	2037.975	2017.328	-20.647
100	226	2001.795	1987.793	-14.002
100	228	2000.855	1986.439	-14.416
100	232	2009.142	1993.478	-15.664
100	233	2019.858	2008.059	-11.799
100	236	2017.787	1995.362	-22.425
100	238	2022.068	1999.018	-23.050
100	243	1993.908	1973.359	-20.549
105	244	1992.496	1969.833	-22.663
105	246	2013.902	1991.842	-22.060
105	248	2018.358	1999.112	-19.246
105	374	1991.605	1966.720	-24.885
105	375	2015.153	2000.609	-14.544
105	Max	2048.079	2031.658	4.100
105	Average	2008.549	1999.884	-8.665
105	Min	1977.691	1966.720	-33.759
105	Std Dev	12.518	13.926	8.952



9.24_FSW_EXT	
Test Site	
Tester	
Test Number	
Max Limit	2300 kHz
Min Limit	1700 kHz

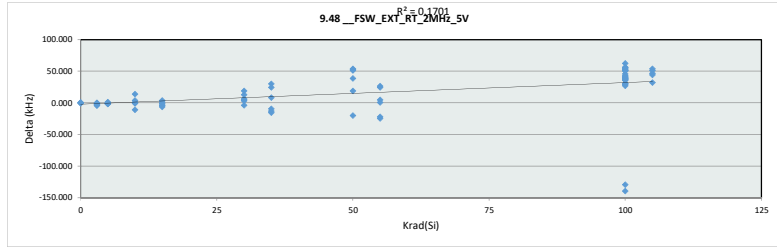
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2002.727	1996.747	1992.695	1998.701	1989.642	1995.971	1983.649	1986.541	1974.263	1973.296
Average	2012.614	2012.825	2004.386	2012.843	2002.598	2009.781	1993.632	2005.500	1986.788	1993.944
Max	2025.746	2020.417	2019.647	2019.735	2018.466	2018.092	2004.438	2031.658	1993.957	2017.328
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



LDR TID Report
TPS7H5001-SP

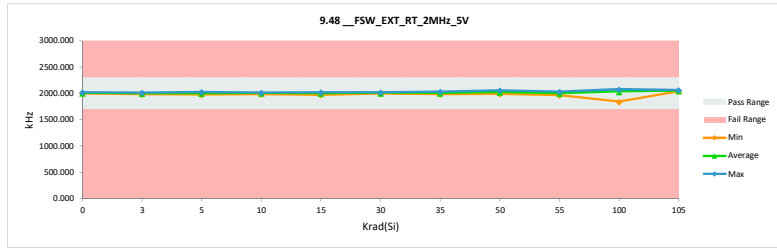
9.48_FSW_EXT_RT_2MHz_5V	
Test Site	
Tester	
Test Number	
Unit	kHz kHz
Max Limit	2300 2300
Min Limit	1700 1700

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2001.204	2002.346	1.142
0	118	2007.489	2007.550	0.061
0	122	2020.877	2022.769	1.892
3	61	2010.382	2007.273	-3.109
3	62	2015.456	2012.991	-2.465
3	63	1988.663	1987.751	-0.912
3	64	2017.967	2018.290	0.323
3	65	2003.998	2002.594	-1.404
3	66	2012.483	2013.544	1.061
5	67	2006.492	2008.817	2.325
5	68	2007.270	2006.816	-0.454
5	69	1979.653	1979.388	-0.265
5	70	2030.783	2031.872	1.089
5	71	2009.942	2009.148	-0.794
5	72	2002.110	2002.872	0.762
10	73	2010.382	2000.543	-9.839
10	74	2015.456	2017.434	1.978
10	75	1988.663	1989.950	1.287
10	76	2017.967	2018.155	0.188
10	77	2003.998	2019.107	15.109
10	78	2012.483	2017.217	4.734
15	79	2006.492	2011.449	4.957
15	80	2007.270	2007.280	0.010
15	81	1979.653	1976.148	-3.505
15	82	2030.783	2025.672	-5.111
15	83	2009.942	2012.020	2.078
15	84	2002.110	2000.011	-2.099
30	86	2010.382	2007.488	-2.894
30	89	2015.456	2021.360	5.904
30	91	1988.663	2022.414	13.751
30	92	2017.967	2022.343	4.376
30	94	2003.998	2023.847	19.849
30	95	2012.483	2019.848	7.365
35	100	2006.492	2037.831	31.339
35	101	2007.270	1998.648	-8.622
35	102	1979.653	2005.197	25.544
35	103	2030.783	2018.958	-11.825
35	104	2009.942	2019.213	9.271
35	105	2002.110	1987.463	-14.647
50	106	2008.332	2060.823	52.491
50	107	1990.873	2010.659	19.786
50	108	2007.606	2052.268	54.662
50	109	2015.657	1996.780	-18.877
50	110	2005.850	2059.907	54.057
50	111	1981.006	2020.625	39.619
55	113	2009.724	2035.006	25.282
55	114	1998.642	2026.052	27.410
55	115	2001.947	2003.939	1.992
55	116	1989.818	1968.767	-21.051
55	126	2007.673	1984.087	-23.586
55	128	2008.646	2014.259	5.613
100	129	2008.291	2060.763	52.472
100	131	2026.050	2065.102	39.052
100	132	2040.527	2080.941	40.414
100	133	2008.754	2066.027	57.273
100	134	2022.495	2074.495	52.000
100	135	1982.894	1844.614	-138.280
100	138	2002.880	2059.730	56.850
100	139	2011.298	2047.868	36.570
100	140	2017.494	2061.103	43.609
100	141	2001.669	2033.442	31.773
100	142	2018.753	1890.593	-128.160
100	143	1996.672	2043.197	46.525
100	144	2015.989	2067.735	51.746
100	220	2023.464	2053.778	30.314
100	221	2010.524	2048.897	38.373
100	224	2007.539	2050.753	43.214
100	225	2007.875	2047.913	40.038
100	226	2004.278	2057.932	53.654
100	228	2008.405	2071.860	63.455
100	232	2018.803	2074.394	55.591
100	233	2021.978	2053.737	31.759
100	236	1978.872	2007.011	28.139
100	238	2024.573	2065.979	41.406
100	243	2001.873	2039.521	37.648
105	244	1993.588	2041.423	47.835
105	246	2009.189	2063.965	54.776
105	248	2020.812	2066.145	45.333
105	374	1992.688	2045.139	52.451
105	376	2016.309	2049.182	32.873
105	Max	2040.527	2080.941	63.455
105	Average	2007.443	2023.000	15.557
105	Min	1978.872	1844.614	-138.280
105	Std Dev	12.705	36.868	33.580



9.48_FSW_EXT	
Test Site	
Tester	
Test Number	
Max Limit	2300 kHz
Min Limit	1700 kHz

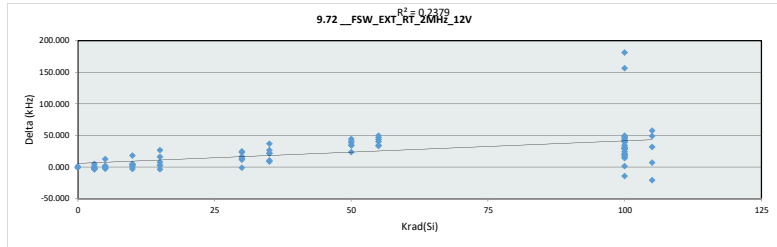
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2002.346	1987.751	1979.388	1989.950	1976.148	2002.414	1987.463	1996.780	1968.767	1844.614
Average	2010.888	2007.074	2006.486	2010.401	2005.430	2016.217	2011.218	2033.510	2005.352	2040.308
Max	2022.769	2018.290	2031.872	2019.107	2025.672	2023.847	2037.831	2060.823	2035.006	2080.941
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



LDR TID Report
TPS7H5001-SP

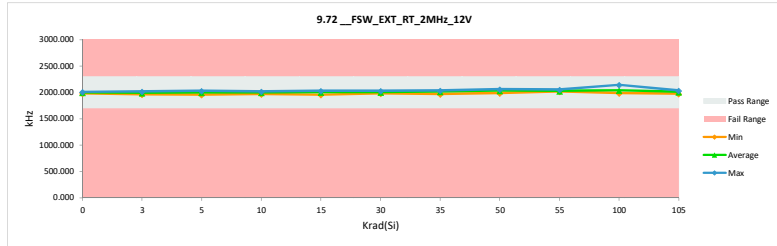
9.72_FSW_EXT_RT_2MHz_12V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	2300
Min Limit	1700

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1982.972	1983.936	0.964
0	118	2012.898	2013.201	0.303
0	122	2009.199	2011.075	1.876
3	61	2016.957	2017.047	0.090
3	62	2018.609	2020.975	2.366
3	63	1969.194	1968.078	-1.116
3	64	1993.494	1991.170	-2.324
3	65	1988.109	1986.109	-2.000
3	66	1984.093	1990.191	6.098
5	67	2016.338	2017.737	1.399
5	68	2005.131	2004.018	-1.113
5	69	1961.563	1962.149	0.586
5	70	2017.945	2031.567	13.622
5	71	1994.290	1992.924	-1.366
5	72	2007.119	2010.362	3.243
10	73	2016.957	2021.209	4.252
10	74	2018.609	2023.868	5.259
10	75	1969.194	1970.733	1.539
10	76	1993.494	2012.656	19.162
10	77	1988.109	1986.278	-1.831
10	78	1984.093	1990.308	6.215
15	79	2016.338	2025.148	8.810
15	80	2005.131	2009.045	3.914
15	81	1961.563	1958.882	-2.681
15	82	2017.945	2035.169	17.224
15	83	1994.290	2021.934	27.644
15	84	2007.119	2010.504	3.385
30	86	2016.957	2031.595	14.638
30	89	2018.609	2031.243	12.634
30	91	1969.194	1987.003	18.009
30	92	1993.494	2017.649	24.155
30	94	1988.109	1988.056	-0.053
30	95	1984.093	2009.972	25.879
35	100	2016.338	2038.931	22.593
35	101	2005.131	2014.623	9.492
35	102	1961.563	1973.280	11.717
35	103	2017.945	2040.540	22.595
35	104	1994.290	2032.140	37.850
35	105	2007.119	2034.998	27.879
50	106	2018.758	2054.307	35.549
50	107	2002.646	2041.934	39.288
50	108	1980.379	2023.524	42.145
50	109	2017.433	2062.966	45.533
50	110	2017.314	2052.605	35.291
50	111	1963.476	1988.531	25.055
55	113	1984.658	2032.242	47.584
55	114	1970.891	2021.534	50.643
55	115	2000.666	2035.190	34.524
55	116	1995.892	2036.713	40.821
55	126	2011.177	2055.867	44.690
55	128	2000.420	2035.838	35.418
100	129	2013.590	2045.331	31.741
100	131	2008.828	2040.530	31.702
100	132	2023.213	2070.709	47.496
100	133	2012.165	2047.537	35.372
100	134	2017.629	2058.166	40.537
100	135	1966.018	2123.195	157.177
100	138	1999.090	2044.697	45.607
100	139	1992.131	2022.298	30.167
100	140	2001.269	2043.002	41.733
100	141	1973.350	1993.066	19.716
100	142	2014.042	2063.237	49.195
100	143	2004.535	2021.567	17.032
100	144	2021.518	2042.424	20.906
100	220	2033.885	2020.960	-12.925
100	221	2013.798	2016.355	2.557
100	224	1991.006	2034.479	43.473
100	225	1990.429	2020.017	29.588
100	226	2008.332	2031.310	22.978
100	228	2018.925	2050.271	31.346
100	232	2031.257	2056.421	25.164
100	233	1990.509	2021.887	31.378
100	236	1964.552	2146.612	182.060
100	238	1997.122	2047.768	50.646
100	243	1996.059	2011.087	15.028
105	244	2000.347	2008.708	8.361
105	246	1983.611	2042.111	58.500
105	248	1994.498	2027.238	32.740
105	374	1977.431	2027.464	50.033
105	376	2001.693	1981.810	-19.883
105	Max	2033.885	2146.612	182.060
105	Average	1999.102	2023.338	24.236
105	Min	1961.563	1958.882	-19.883
105	Std Dev	18.120	30.893	29.635



9.72_FSW_EXT	
Test Site	
Tester	
Test Number	
Max Limit	2300 kHz
Min Limit	1700 kHz

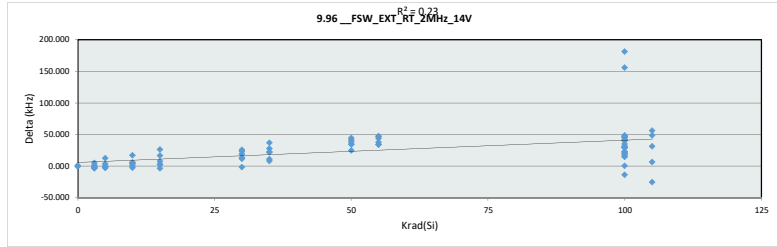
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	1983.936	1968.078	1962.149	1970.733	1958.882	1987.203	1973.280	1988.531	2021.534	1993.066	1981.810
Average	2002.737	1995.595	2003.126	2000.842	2010.114	2010.953	2022.419	2037.145	2036.231	2044.705	2017.466
Max	2013.201	2020.975	2031.567	2023.868	2035.169	2031.595	2040.540	2062.966	2055.867	2146.612	2042.111
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



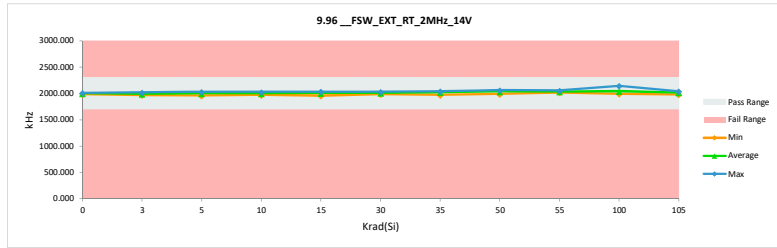
LDR TID Report
TPS7H5001-SP

9.96_FSW_EXT_RT_2MHz_14V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	2300
Min Limit	1700

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1982.915	1983.758	0.843
0	118	2012.703	2013.065	0.362
0	122	2008.769	2010.867	2.098
3	61	2016.400	2016.911	0.511
3	62	2018.336	2020.942	2.606
3	63	1968.601	1967.894	-0.707
3	64	1993.417	1990.784	-2.633
3	65	1987.840	1986.178	-1.662
3	66	1983.859	1990.128	6.269
5	67	2016.495	2017.640	1.145
5	68	2004.941	2003.702	-1.239
5	69	1961.210	1961.465	0.255
5	70	2017.511	2031.112	13.601
5	71	1994.259	1992.658	-1.601
5	72	2005.896	2010.342	4.446
10	73	2016.400	2020.934	4.534
10	74	2018.336	2024.191	5.855
10	75	1968.601	1969.966	1.365
10	76	1993.417	2011.901	18.484
10	77	1987.840	1986.262	-1.578
10	78	1983.859	1990.940	7.081
15	79	2016.495	2025.165	8.670
15	80	2004.941	2008.771	3.830
15	81	1961.210	1958.626	-2.584
15	82	2017.511	2035.030	17.519
15	83	1994.259	2021.622	27.363
15	84	2005.896	2010.244	4.348
30	86	2016.400	2031.131	14.731
30	89	2018.336	2031.018	12.682
30	91	1968.601	1988.839	20.238
30	92	1993.417	2017.909	24.492
30	94	1987.840	1987.523	-0.317
30	95	1983.859	2010.561	26.702
35	100	2016.495	2039.613	23.118
35	101	2004.941	2014.262	9.321
35	102	1961.210	1973.660	12.450
35	103	2017.511	2040.823	23.312
35	104	1994.259	2032.102	37.843
35	105	2005.896	2034.843	28.947
50	106	2018.246	2053.996	35.750
50	107	2002.005	2041.689	39.684
50	108	1980.000	2022.495	42.495
50	109	2017.093	2062.471	45.378
50	110	2016.963	2052.620	35.657
50	111	1963.190	1988.857	25.667
55	113	1984.471	2032.220	47.749
55	114	1970.548	2019.123	48.575
55	115	2000.165	2034.944	34.779
55	116	1997.635	2036.696	39.061
55	126	2010.903	2055.873	44.970
55	128	2000.000	2035.353	35.353
100	129	2013.097	2044.789	31.692
100	131	2008.324	2039.623	31.299
100	132	2023.025	2070.243	47.218
100	133	2011.535	2047.279	35.744
100	134	2016.881	2058.132	41.251
100	135	1965.645	2122.393	156.748
100	138	1998.038	2044.642	46.604
100	139	1991.795	2022.204	30.409
100	140	2000.955	2042.900	41.945
100	141	1973.312	1992.532	19.220
100	142	2013.510	2063.287	49.777
100	143	2004.422	2020.986	16.564
100	144	2021.420	2041.882	20.462
100	220	2033.181	2020.658	-12.523
100	221	2014.090	2015.660	1.570
100	224	1990.482	2033.903	43.421
100	225	1990.083	2019.742	29.659
100	226	2009.138	2031.444	22.306
100	228	2018.378	2050.173	31.795
100	232	2031.208	2055.711	24.503
100	233	1990.180	2022.159	31.979
100	236	1964.300	2146.471	182.171
100	238	1996.626	2044.182	47.556
100	243	1996.188	2011.719	15.531
105	244	2000.241	2007.634	7.393
105	246	1983.747	2040.881	57.134
105	248	1994.573	2026.946	32.373
105	374	1977.505	2027.338	49.833
105	376	2004.640	1980.654	-23.986
105	Max	2033.181	2146.471	182.171
105	Average	1998.855	2023.074	24.218
105	Min	1961.210	1958.626	-23.986
105	Std Dev	18.107	30.817	29.615



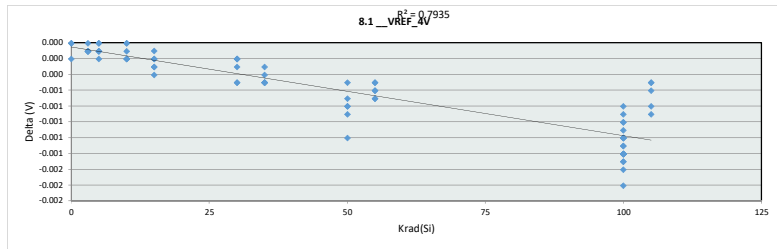
9.96_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	2300 kHz										
Min Limit	1700 kHz										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	1983.758	1967.894	1961.465	1969.966	1958.626	1987.523	1973.660	1988.857	2019.123	1992.532	1980.654
Average	2002.563	1995.473	2002.820	2000.699	2009.910	2011.164	2022.551	2037.021	2035.702	2044.280	2016.691
Max	2013.065	2020.942	2031.112	2024.191	2035.030	2031.131	2040.823	2062.471	2055.873	2146.471	2040.881
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



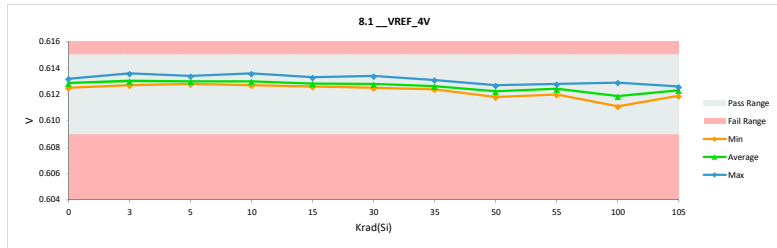
LDR TID Report
TPS7H5001-SP

		8.1_VREF_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.615	0.615	
Min Limit	0.609	0.609	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	0.000
30	89	0.613	0.613	0.000
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	0.000
35	101	0.613	0.612	-0.001
35	102	0.613	0.612	-0.001
35	103	0.613	0.613	0.000
35	104	0.613	0.613	0.000
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	-0.001
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	-0.001
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.001
100	133	0.613	0.612	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.001
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.611	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.611	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.613	0.612	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	0.000
105	248	0.613	0.613	-0.001
105	374	0.613	0.612	-0.001
105	376	0.613	0.612	-0.001
	Max	0.614	0.614	0.000
	Average	0.613	0.612	-0.001
	Min	0.613	0.611	-0.002
	Std Dev	0.000	0.001	0.001



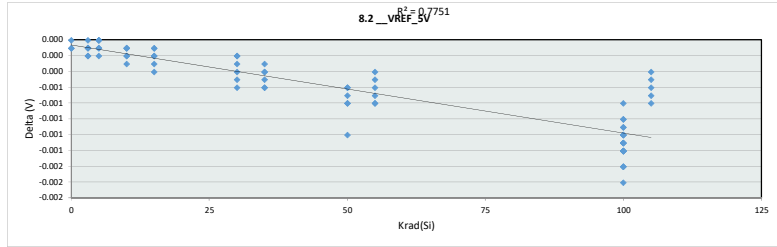
		8.1_VREF_4V	
Test Site			
Tester			
Test Number			
Max Limit	0.615	V	
Min Limit	0.609	V	
Krad(Si)	0	3	5
LL	0.609	0.609	0.609
Min	0.613	0.613	0.613
Average	0.613	0.613	0.613
Max	0.613	0.614	0.614
UL	0.615	0.615	0.615



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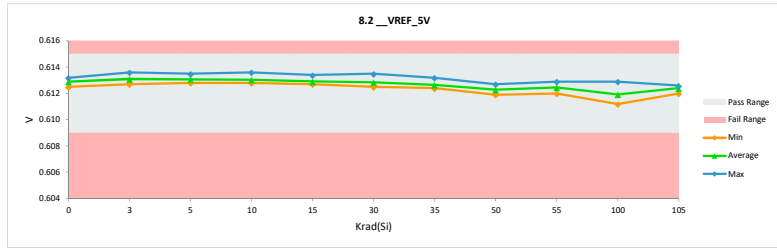
8.2_VREF_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.615
Min Limit	0.609

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	-0.001
30	89	0.613	0.613	0.000
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	-0.001
35	101	0.613	0.612	-0.001
35	102	0.613	0.613	0.000
35	103	0.613	0.613	0.000
35	104	0.613	0.613	0.000
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	-0.001
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	0.000
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.002
100	133	0.614	0.613	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.001
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.612	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.612	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.614	0.612	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	-0.001
105	248	0.613	0.613	0.000
105	374	0.613	0.613	0.000
105	375	0.613	0.612	-0.001
	Max	0.614	0.614	0.000
	Average	0.613	0.613	-0.001
	Min	0.613	0.611	-0.002
	Std Dev	0.000	0.001	0.001



8.2_VREF_5V	
Test Site	
Tester	
Test Number	
Max Limit	0.615 V
Min Limit	0.609 V

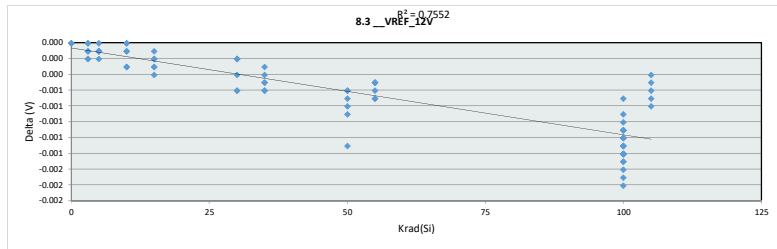
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.611	0.612
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.614	0.614	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615



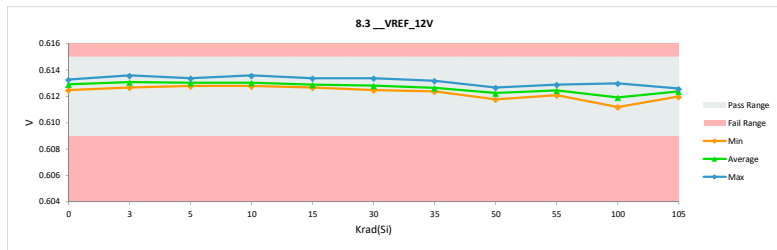
LDR TID Report
TPS7H5001-SP

8.3_VREF_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.615
Min Limit	0.609

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	-0.001
30	89	0.613	0.613	-0.001
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	-0.001
35	101	0.613	0.612	-0.001
35	102	0.613	0.613	0.000
35	103	0.613	0.613	-0.001
35	104	0.613	0.613	-0.001
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	0.000
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	0.000
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.002
100	133	0.614	0.613	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.002
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.612	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.611	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.614	0.613	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	-0.001
105	248	0.613	0.613	0.000
105	374	0.613	0.613	0.000
105	375	0.613	0.612	-0.001
105	Max	0.614	0.614	0.000
105	Average	0.613	0.613	-0.001
105	Min	0.613	0.611	-0.002
105	Std Dev	0.000	0.001	0.001



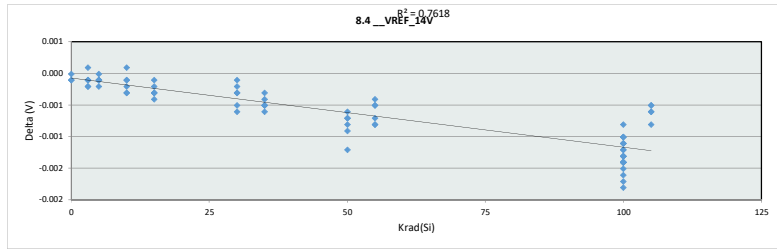
8.3_VREF_12V											
Test Site											
Tester											
Test Number											
Max Limit	0.615 V										
Min Limit	0.609 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.611	0.612
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613	0.613	0.613
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615



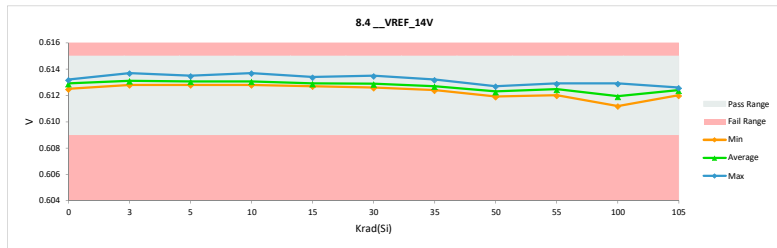
LDR TID Report
TPS7H5001-SP

8.4_VREF_14V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	0.615	0.615
Min Limit	0.609	0.609

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	-0.001
30	89	0.613	0.613	-0.001
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	0.000
35	101	0.613	0.612	-0.001
35	102	0.613	0.613	0.000
35	103	0.613	0.613	-0.001
35	104	0.613	0.613	-0.001
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	0.000
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	0.000
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.002
100	133	0.614	0.613	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.001
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.612	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.611	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.614	0.613	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	-0.001
105	248	0.613	0.613	0.000
105	374	0.613	0.613	0.000
105	375	0.613	0.612	-0.001
	Max	0.614	0.614	0.000
	Average	0.613	0.613	-0.001
	Min	0.613	0.611	-0.002
	Std Dev	0.000	0.001	0.001



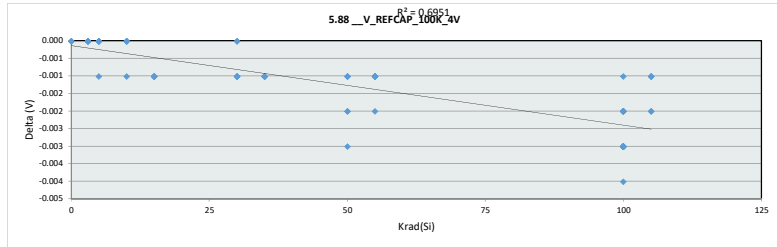
8.4_VREF_14V											
Test Site											
Tester											
Test Number											
Max Limit	0.615	V									
Min Limit	0.609	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.611	0.612
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.614	0.614	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615



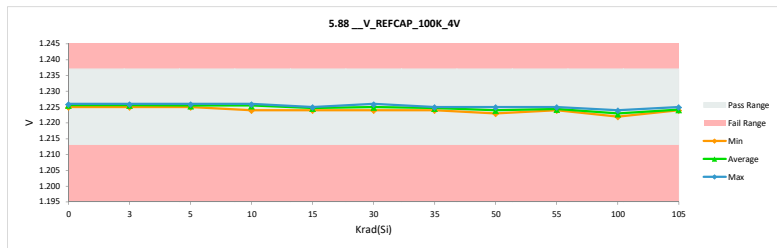
LDR TID Report
TPS7H5001-SP

5.88_V_REFCAP_100K_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.225	-0.001
10	73	1.225	1.224	-0.001
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.224	-0.001
15	80	1.225	1.224	-0.001
15	81	1.226	1.225	-0.001
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.226	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.225	1.224	-0.001
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.225	1.223	-0.002
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.222	-0.003
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



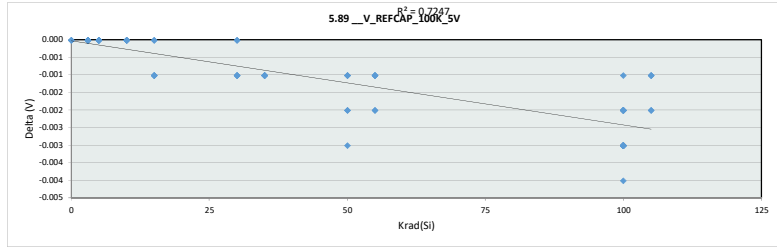
5.88_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.224	1.222
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



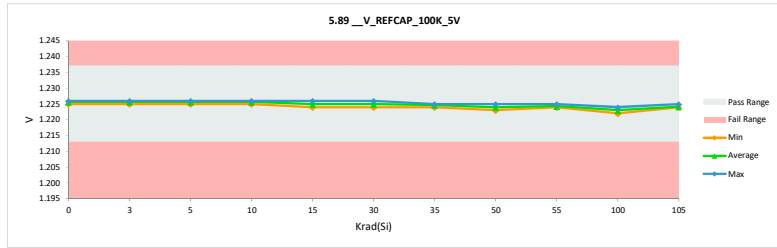
LDR TID Report
TPS7H5001-SP

5.89_V_REFCAP_100K_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.223	-0.002
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



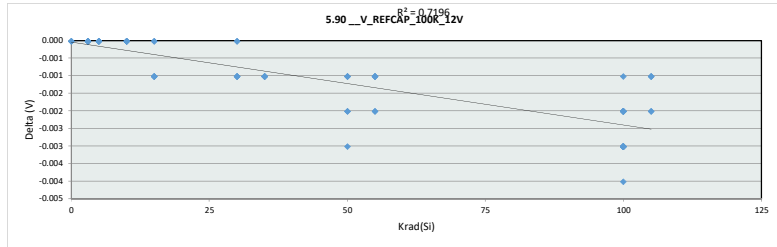
5.89_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



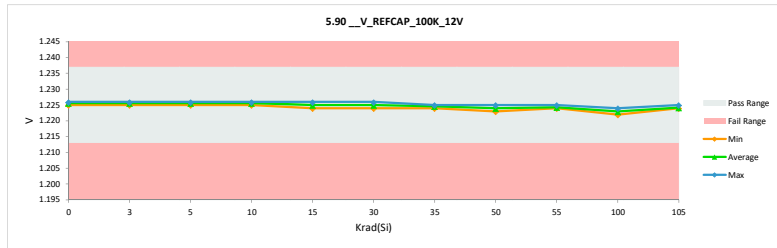
LDR TID Report
TPS7H5001-SP

5.90_V_REFCAP_100K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.225	1.223	-0.002
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.223	-0.002
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



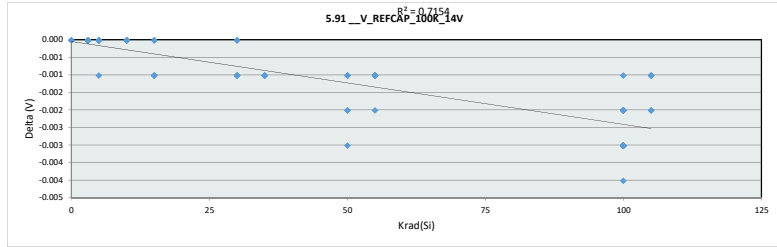
5.90_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.222
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



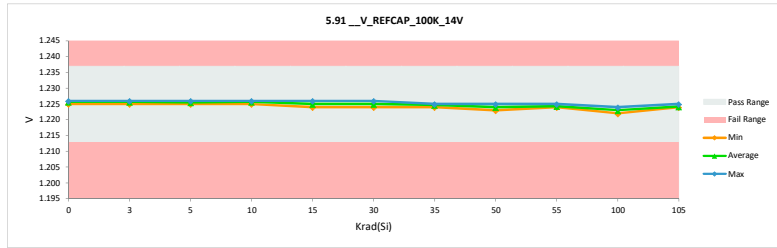
LDR TID Report
TPS7H5001-SP

5.91_V_REFCAP_100K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.225	-0.001
10	73	1.225	1.225	0.000
10	74	1.226	1.225	-0.001
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.225	1.224	-0.001
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.223	-0.002
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



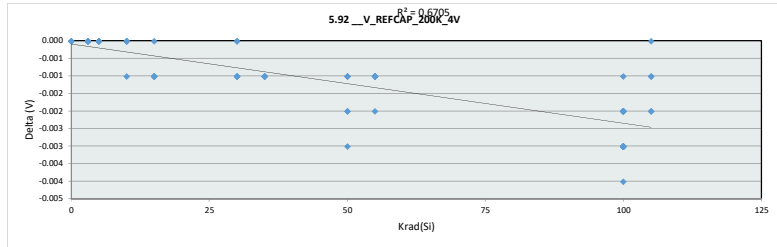
5.91_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237 V										
Min Limit	1.213 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



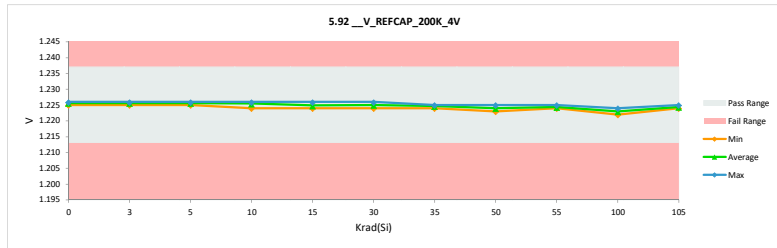
LDR TID Report
TPS7H5001-SP

5.92_V_REFCAP_200K_4V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.224	-0.001
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.224	-0.001
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.225	1.224	-0.001
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.225	1.223	-0.002
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.223	-0.002
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.225	0.000
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001

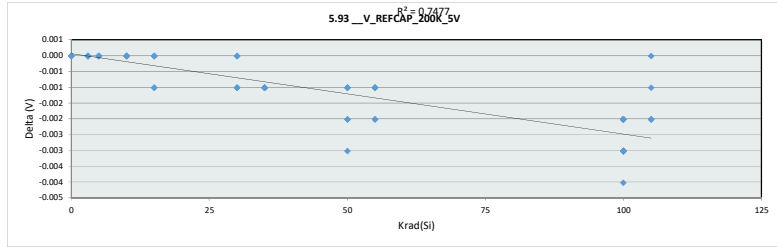


5.92_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.224	1.222
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

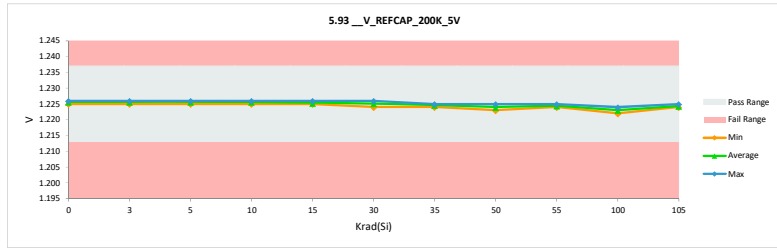


LDR TID Report
TPS7H5001-SP

5.93_V_REFCAP_200K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.225	0.000
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.226	0.000
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.226	0.000
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.226	1.224	-0.002
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.226	1.224	-0.002
105	374	1.225	1.225	0.000
105	375	1.226	1.224	-0.002
	Max	1.226	1.226	0.000
	Average	1.226	1.224	-0.001
	Min	1.225	1.222	-0.004
	Std Dev	0.000	0.001	0.001

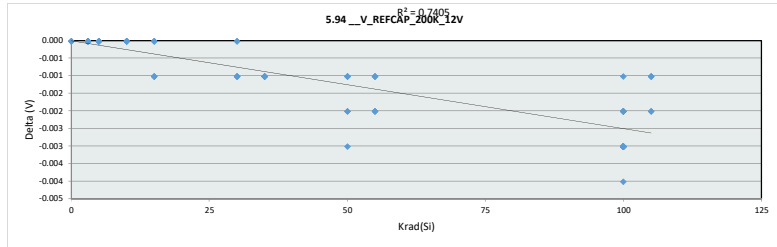


5.93_V_REFCAP												
Test Site												
Tester												
Test Number												
Max Limit	1.237	V										
Min Limit	1.213	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.222	1.224	
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225	1.224	
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225	
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	

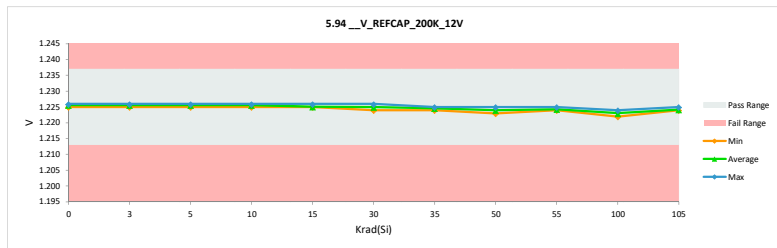


LDR TID Report
TPS7H5001-SP

5.94_V_REFCAP_200K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.225	0.000
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.226	1.223	-0.003
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



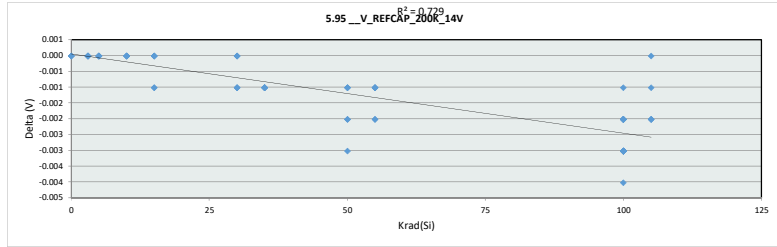
5.94_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



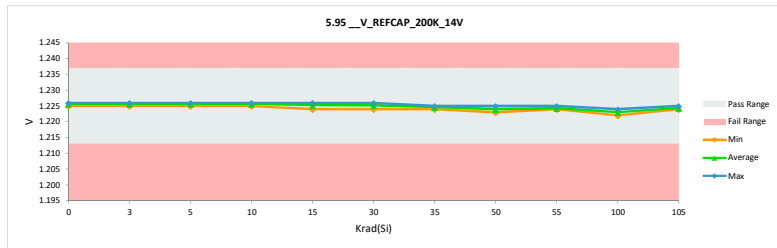
LDR TID Report
TPS7H5001-SP

5.95_V_REFCAP_200K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.226	0.000
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.226	1.224	-0.002
105	374	1.225	1.225	0.000
105	376	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001

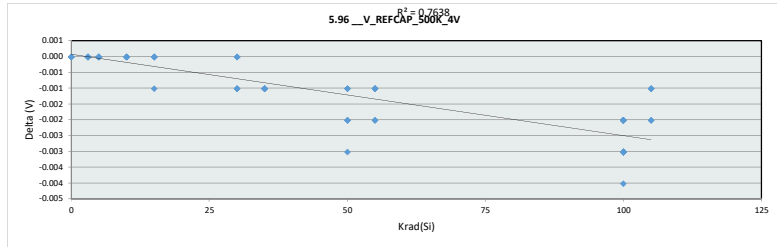


5.95_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237 V										
Min Limit	1.213 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

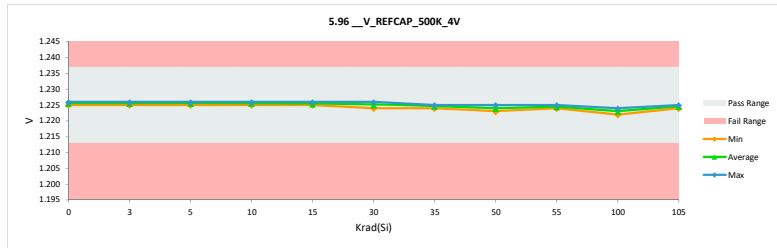


LDR TID Report
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5.96_V_REFCAP_500K_4V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	117	1.225	1.225	0.000	
0	118	1.226	1.226	0.000	
0	122	1.226	1.226	0.000	
3	61	1.225	1.225	0.000	
3	62	1.226	1.226	0.000	
3	63	1.226	1.226	0.000	
3	64	1.225	1.225	0.000	
3	65	1.226	1.226	0.000	
3	66	1.226	1.226	0.000	
5	67	1.225	1.225	0.000	
5	68	1.225	1.225	0.000	
5	69	1.226	1.226	0.000	
5	70	1.226	1.226	0.000	
5	71	1.226	1.226	0.000	
5	72	1.226	1.226	0.000	
10	73	1.225	1.225	0.000	
10	74	1.226	1.226	0.000	
10	75	1.226	1.226	0.000	
10	76	1.225	1.225	0.000	
10	77	1.226	1.226	0.000	
10	78	1.226	1.226	0.000	
15	79	1.225	1.225	0.000	
15	80	1.225	1.225	0.000	
15	81	1.226	1.226	0.000	
15	82	1.226	1.225	-0.001	
15	83	1.226	1.226	0.000	
15	84	1.226	1.226	0.000	
30	86	1.225	1.224	-0.001	
30	89	1.226	1.225	-0.001	
30	91	1.226	1.226	0.000	
30	92	1.225	1.225	0.000	
30	94	1.226	1.225	-0.001	
30	95	1.226	1.226	0.000	
35	100	1.225	1.224	-0.001	
35	101	1.225	1.224	-0.001	
35	102	1.226	1.225	-0.001	
35	103	1.226	1.225	-0.001	
35	104	1.226	1.225	-0.001	
35	105	1.226	1.225	-0.001	
50	106	1.226	1.224	-0.002	
50	107	1.226	1.224	-0.002	
50	108	1.226	1.223	-0.003	
50	109	1.226	1.224	-0.002	
50	110	1.226	1.225	-0.001	
50	111	1.225	1.224	-0.001	
55	113	1.225	1.224	-0.001	
55	114	1.226	1.224	-0.002	
55	115	1.226	1.224	-0.002	
55	116	1.226	1.225	-0.001	
55	126	1.226	1.225	-0.001	
55	128	1.226	1.225	-0.001	
100	129	1.225	1.222	-0.003	
100	131	1.226	1.223	-0.003	
100	132	1.226	1.223	-0.003	
100	133	1.226	1.224	-0.002	
100	134	1.226	1.223	-0.003	
100	135	1.226	1.222	-0.004	
100	138	1.226	1.223	-0.003	
100	139	1.226	1.224	-0.002	
100	140	1.226	1.223	-0.003	
100	141	1.225	1.222	-0.003	
100	142	1.226	1.223	-0.003	
100	143	1.225	1.223	-0.002	
100	144	1.226	1.224	-0.002	
100	220	1.225	1.222	-0.003	
100	221	1.226	1.223	-0.003	
100	224	1.225	1.222	-0.003	
100	225	1.226	1.224	-0.002	
100	226	1.226	1.224	-0.002	
100	228	1.226	1.223	-0.003	
100	232	1.226	1.223	-0.003	
100	233	1.226	1.224	-0.002	
100	236	1.226	1.224	-0.002	
100	238	1.226	1.223	-0.003	
100	243	1.226	1.223	-0.003	
105	244	1.226	1.225	-0.001	
105	246	1.226	1.225	-0.001	
105	248	1.226	1.224	-0.002	
105	374	1.226	1.225	-0.001	
105	375	1.226	1.224	-0.002	
105	Max	1.226	1.226	0.000	
105	Average	1.226	1.224	-0.001	
105	Min	1.225	1.222	-0.004	
105	Std Dev	0.000	0.001	0.001	



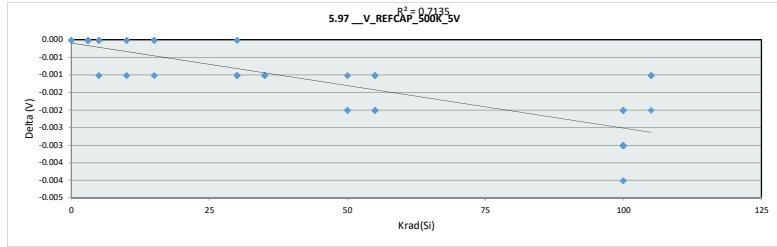
5.96_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225	1.223	1.225
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



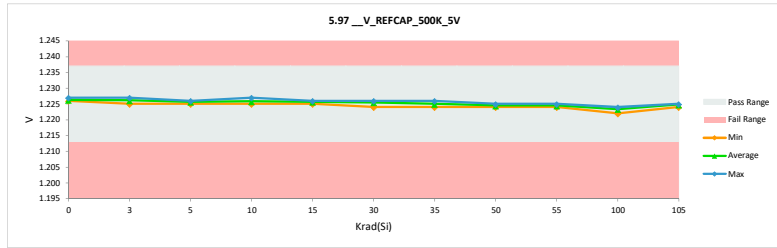
LDR TID Report
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5.97_V_REFCAP_500K_5V			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.226	1.225	-0.001
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.224	-0.002
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.224	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.226	1.225	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.001	0.001	0.001



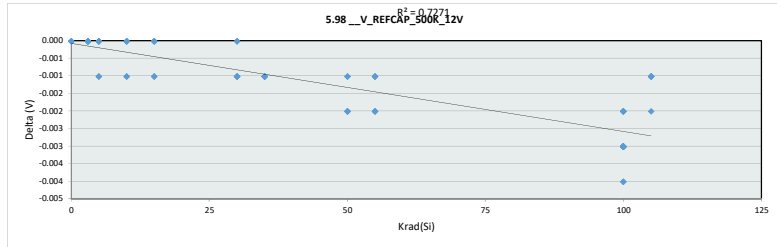
5.97_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237	V								
Min Limit	1.213	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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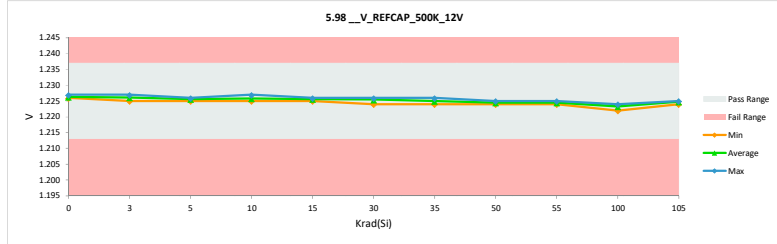
5.98_V_REFCAP_500K_12V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.226	1.225	-0.001
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.224	-0.003
100	226	1.226	1.224	-0.002
100	228	1.227	1.224	-0.003
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.226	1.225	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.001	0.001	0.001



5.98_V_REFCAP	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

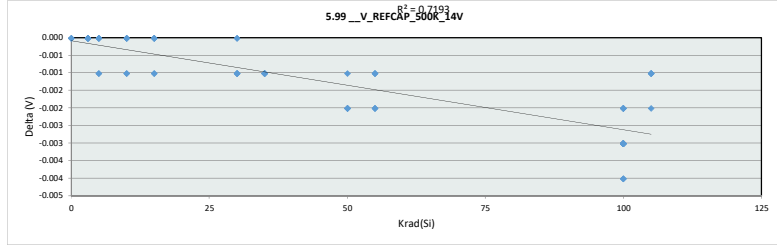
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.223	1.225
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.226	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



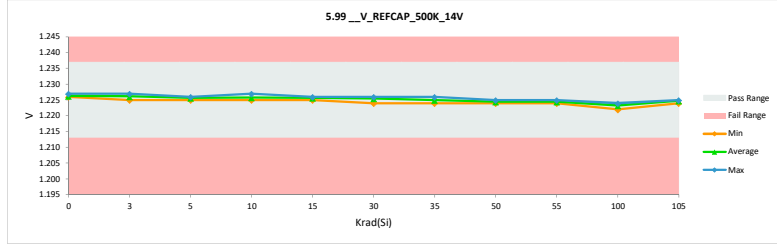
LDR TID Report
TPS7H5001-SP

5.99_V_REFCAP_500K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.226	1.225	-0.002
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.224	-0.003
100	226	1.226	1.224	-0.002
100	228	1.227	1.224	-0.003
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.224	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.226	1.225	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.001	0.001	0.001



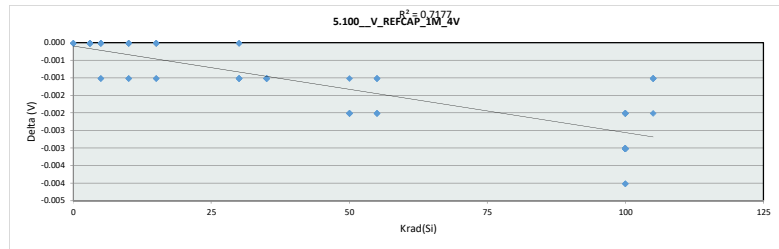
5.99_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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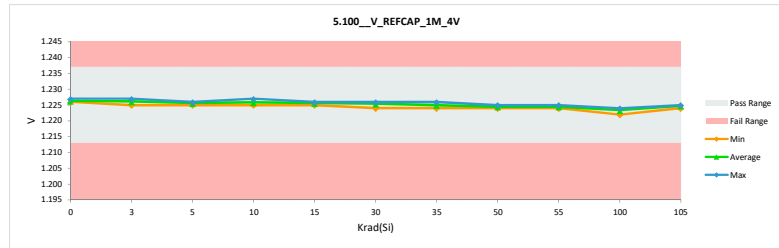
5.100_V_REFCAP_1M_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.226	1.223	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.224	-0.003
100	226	1.226	1.224	-0.002
100	228	1.226	1.224	-0.002
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.226	1.225	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.001	0.001	0.001



5.100_V_REFCAP	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

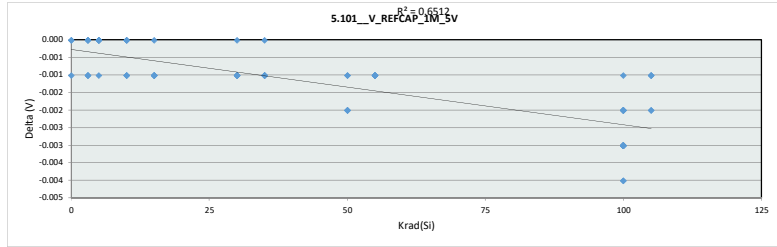
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



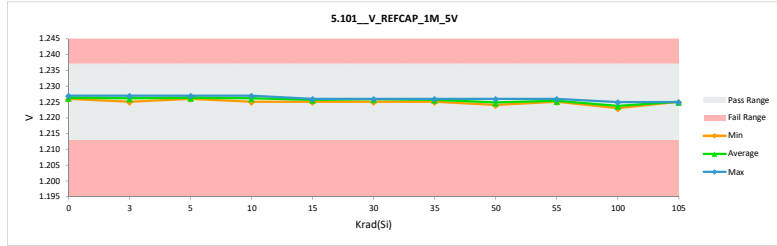
LDR TID Report TPS7H5001-SP

		5.101_V_REFCAP_1M_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.226	-0.001
3	61	1.226	1.225	-0.001
3	62	1.227	1.227	0.000
3	63	1.227	1.226	-0.001
3	64	1.226	1.226	0.000
3	65	1.227	1.226	-0.001
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.226	-0.001
5	72	1.226	1.226	0.000
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.226	-0.001
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.226	-0.001
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.226	1.226	0.000
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.226	1.226	0.000
50	106	1.226	1.225	-0.001
50	107	1.227	1.225	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.226	1.225	-0.001
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.223	-0.004
100	135	1.227	1.223	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.226	1.225	-0.001
100	220	1.226	1.223	-0.003
100	221	1.226	1.224	-0.002
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.226	1.224	-0.002
100	233	1.227	1.224	-0.003
100	236	1.227	1.225	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.225	-0.001
105	Max	1.227	1.227	0.000
105	Average	1.227	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.001	0.001	0.001



		5.101_V_REFCAP								
Test Site										
Tester										
Test Number										
Max Limit	1.237	V								
Min Limit	1.213	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.226	1.225	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.225
Max	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

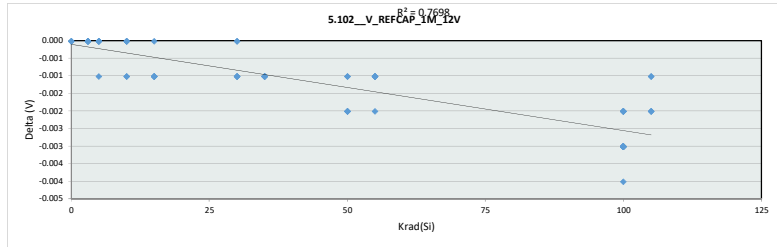


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5.102_V_REFCAP_1M_12V

Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	1.237	1.237
Min Limit	1.213	1.213

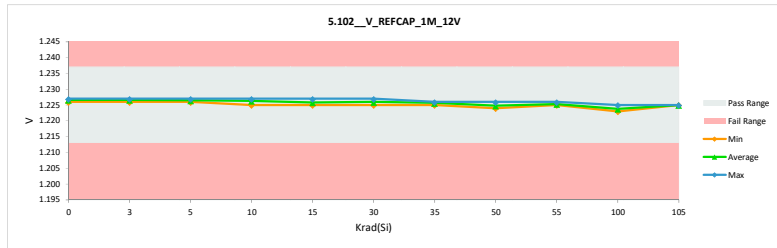
Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.227	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.226	-0.001
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.227	1.226	-0.001
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.226	1.225	-0.001
50	107	1.227	1.225	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.225	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.227	1.225	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.227	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001



5.102_V_REFCAP

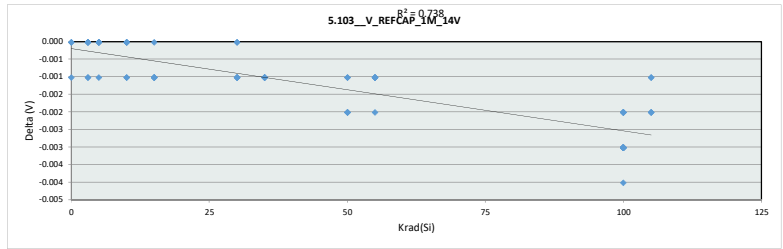
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.225
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

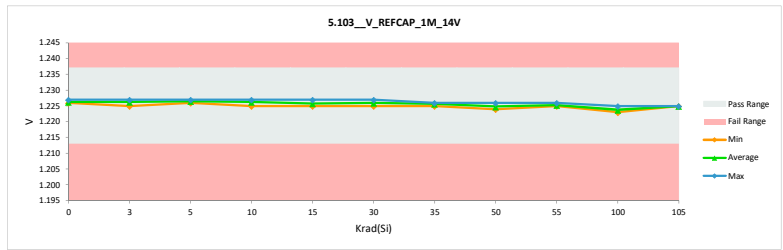


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5.103_V_REFCAP_1M_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.226	-0.001
3	61	1.226	1.225	-0.001
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.226	-0.001
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.226	-0.001
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.227	1.226	-0.001
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.226	1.225	-0.001
50	107	1.227	1.225	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.225	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.227	1.225	-0.002
	Max	1.227	1.227	0.000
	Average	1.227	1.225	-0.001
	Min	1.226	1.223	-0.004
	Std Dev	0.000	0.001	0.001



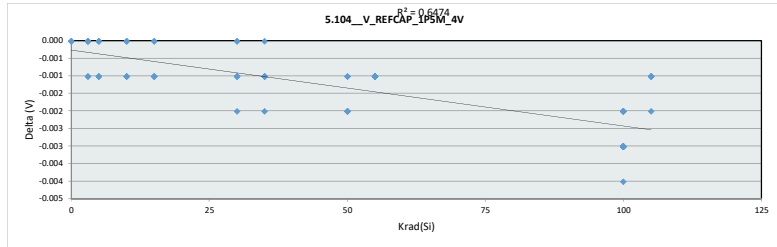
5.103_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.226	1.226	1.227	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



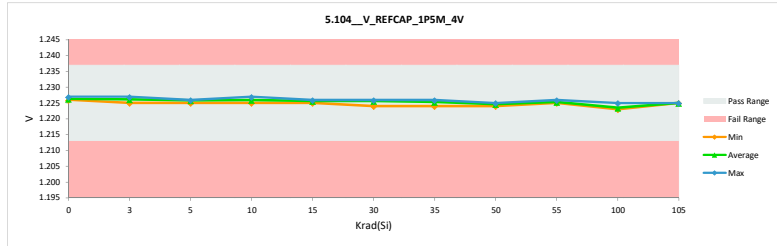
LDR TID Report
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		5.104_V_REFCAP_1P5M_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.226	1.225	-0.001
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.226	-0.001
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.225	-0.001
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.227	1.226	-0.001
5	72	1.226	1.226	0.000
10	73	1.226	1.225	-0.001
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.227	1.226	-0.001
15	84	1.226	1.226	0.000
30	86	1.226	1.224	-0.002
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.226	1.224	-0.002
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.227	1.226	-0.001
35	105	1.226	1.226	0.000
50	106	1.226	1.225	-0.001
50	107	1.226	1.225	-0.001
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.226	1.225	-0.001
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.225	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.226	1.224	-0.002
100	220	1.226	1.223	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	375	1.226	1.225	-0.001
105		1.227	1.227	0.000
	Max			
	Average	1.226	1.225	-0.001
	Min	1.226	1.223	-0.004
	Std Dev	0.000	0.001	0.001



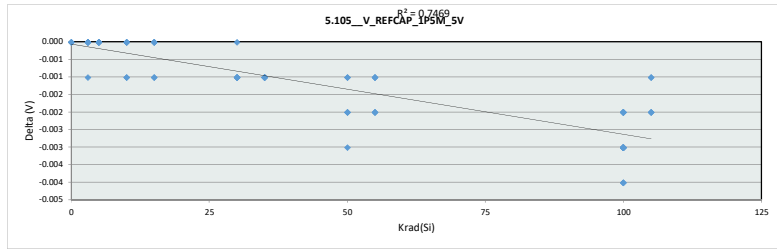
		5.104_V_REFCAP								
Test Site										
Tester										
Test Number										
Max Limit	1.237	V								
Min Limit	1.213	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.225	1.223
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



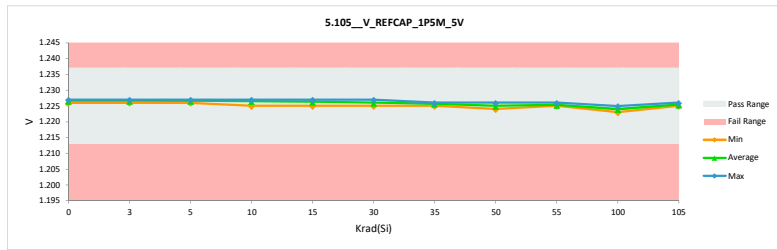
LDR TID Report
TPS7H5001-SP

		5.105_V_REFCAP_1P5M_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.227	1.226	-0.001
3	65	1.227	1.227	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	0.000
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.227	1.226	-0.001
10	77	1.227	1.227	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.226	0.000
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.227	0.000
15	84	1.227	1.227	0.000
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.227	1.226	-0.001
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.225	-0.002
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.225	-0.001
55	113	1.226	1.225	-0.001
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.224	-0.003
100	140	1.227	1.224	-0.003
100	141	1.227	1.223	-0.004
100	142	1.227	1.224	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.227	1.223	-0.004
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.225	-0.002
100	236	1.227	1.225	-0.002
100	238	1.227	1.224	-0.003
100	243	1.226	1.224	-0.002
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.225	-0.002
105	374	1.227	1.226	-0.001
105	375	1.227	1.225	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.227	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001

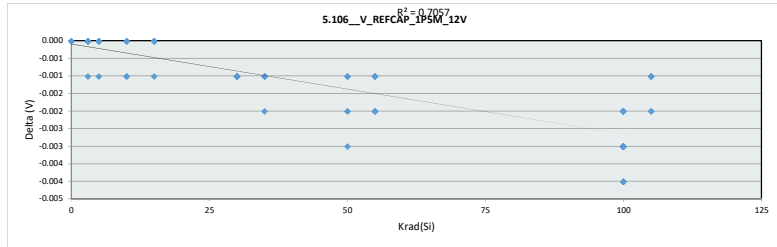


		5.105_V_REFCAP								
Test Site										
Tester										
Test Number										
Max Limit	1.237	V								
Min Limit	1.213	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223
Average	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.225	1.224
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

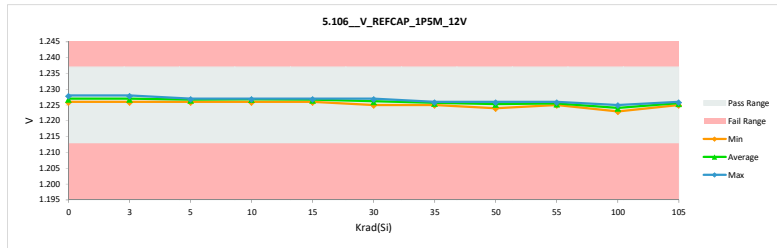


LDR TID Report
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5.106_V_REFCAP_1P5M_12V					
Test Site					
Tester					
Test Number					
Unit	V		V		
Max Limit	1.237		1.237		
Min Limit	1.213		1.213		
Krad(Si)	Serial #	PRE	POST	Delta	
0	117	1.226	1.226	0.000	
0	118	1.228	1.228	0.000	
0	122	1.227	1.227	0.000	
3	61	1.226	1.226	0.000	
3	62	1.228	1.228	0.000	
3	63	1.227	1.227	0.000	
3	64	1.227	1.227	0.000	
3	65	1.227	1.227	0.000	
3	66	1.228	1.227	-0.001	
5	67	1.227	1.226	-0.001	
5	68	1.226	1.226	0.000	
5	69	1.227	1.227	0.000	
5	70	1.227	1.227	0.000	
5	71	1.227	1.227	0.000	
5	72	1.227	1.227	0.000	
10	73	1.226	1.226	0.000	
10	74	1.228	1.227	-0.001	
10	75	1.227	1.227	0.000	
10	76	1.227	1.227	0.000	
10	77	1.227	1.227	0.000	
10	78	1.228	1.227	-0.001	
15	79	1.227	1.226	-0.001	
15	80	1.226	1.226	0.000	
15	81	1.227	1.227	0.000	
15	82	1.227	1.227	0.000	
15	83	1.227	1.227	0.000	
15	84	1.227	1.227	0.000	
30	86	1.226	1.225	-0.001	
30	89	1.228	1.227	-0.001	
30	91	1.227	1.226	-0.001	
30	92	1.227	1.226	-0.001	
30	94	1.227	1.226	-0.001	
30	95	1.228	1.227	-0.001	
35	100	1.227	1.225	-0.002	
35	101	1.226	1.225	-0.001	
35	102	1.227	1.226	-0.001	
35	103	1.227	1.226	-0.001	
35	104	1.227	1.226	-0.001	
35	105	1.227	1.226	-0.001	
50	106	1.227	1.226	-0.001	
50	107	1.227	1.225	-0.002	
50	108	1.227	1.224	-0.003	
50	109	1.227	1.226	-0.001	
50	110	1.228	1.226	-0.002	
50	111	1.226	1.225	-0.001	
55	113	1.227	1.225	-0.002	
55	114	1.227	1.225	-0.002	
55	115	1.227	1.225	-0.002	
55	116	1.227	1.226	-0.001	
55	126	1.227	1.226	-0.001	
55	128	1.227	1.226	-0.001	
100	129	1.227	1.224	-0.003	
100	131	1.227	1.224	-0.003	
100	132	1.228	1.224	-0.004	
100	133	1.228	1.225	-0.003	
100	134	1.227	1.224	-0.003	
100	135	1.227	1.223	-0.004	
100	138	1.227	1.224	-0.003	
100	139	1.227	1.225	-0.002	
100	140	1.227	1.224	-0.003	
100	141	1.227	1.223	-0.004	
100	142	1.227	1.224	-0.003	
100	143	1.226	1.224	-0.002	
100	144	1.227	1.225	-0.002	
100	220	1.227	1.223	-0.004	
100	221	1.227	1.224	-0.003	
100	224	1.227	1.223	-0.004	
100	225	1.227	1.225	-0.002	
100	226	1.227	1.225	-0.002	
100	228	1.227	1.224	-0.003	
100	232	1.227	1.224	-0.003	
100	233	1.227	1.225	-0.002	
100	236	1.227	1.225	-0.002	
100	238	1.227	1.224	-0.003	
100	243	1.227	1.224	-0.003	
105	244	1.227	1.225	-0.002	
105	246	1.227	1.226	-0.001	
105	248	1.227	1.226	-0.001	
105	374	1.227	1.226	-0.001	
105	376	1.227	1.225	-0.002	
	Max	1.228	1.228	0.000	
	Average	1.227	1.226	-0.001	
	Min	1.226	1.223	-0.004	
	Std Dev	0.000	0.001	0.001	



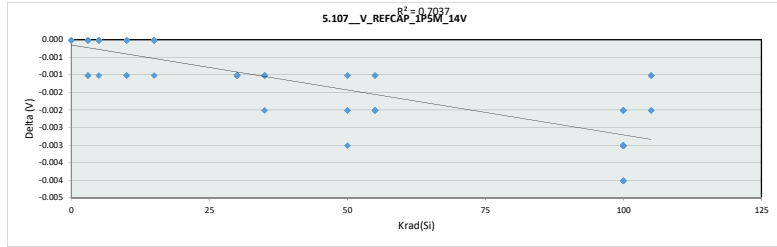
5.106_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.224	1.225
Max	1.228	1.228	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



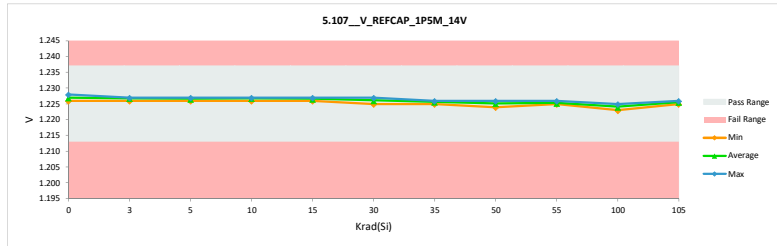
LDR TID Report TPS7H5001-SP

5.107_V_REFCAP_1P5M_14V			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.228	1.228	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.228	1.227	-0.001
3	63	1.227	1.227	0.000
3	64	1.227	1.227	0.000
3	65	1.227	1.227	0.000
3	66	1.228	1.227	-0.001
5	67	1.227	1.226	-0.001
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	0.000
10	73	1.226	1.226	0.000
10	74	1.228	1.227	-0.001
10	75	1.227	1.227	0.000
10	76	1.227	1.227	0.000
10	77	1.227	1.227	0.000
10	78	1.228	1.227	-0.001
15	79	1.227	1.226	-0.001
15	80	1.226	1.226	0.000
15	81	1.227	1.227	0.000
15	82	1.227	1.227	0.000
15	83	1.227	1.227	0.000
15	84	1.227	1.227	0.000
30	86	1.226	1.225	-0.001
30	89	1.228	1.227	-0.001
30	91	1.227	1.226	-0.001
30	92	1.227	1.226	-0.001
30	94	1.227	1.226	-0.001
30	95	1.228	1.227	-0.001
35	100	1.227	1.225	-0.002
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.226	-0.001
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.225	-0.002
50	110	1.228	1.226	-0.002
50	111	1.226	1.225	-0.001
55	113	1.227	1.225	-0.002
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.227	1.224	-0.003
100	131	1.227	1.224	-0.003
100	132	1.228	1.224	-0.004
100	133	1.228	1.225	-0.003
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.225	-0.002
100	140	1.227	1.224	-0.003
100	141	1.227	1.223	-0.004
100	142	1.227	1.224	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.227	1.223	-0.004
100	221	1.227	1.224	-0.003
100	224	1.227	1.223	-0.004
100	225	1.227	1.225	-0.002
100	226	1.227	1.225	-0.002
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.228	1.225	-0.003
100	236	1.227	1.225	-0.002
100	238	1.227	1.224	-0.003
100	243	1.227	1.224	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.226	-0.001
105	374	1.227	1.226	-0.001
105	375	1.227	1.225	-0.002
105	Max	1.228	1.228	0.000
105	Average	1.227	1.226	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.001	0.001	0.001



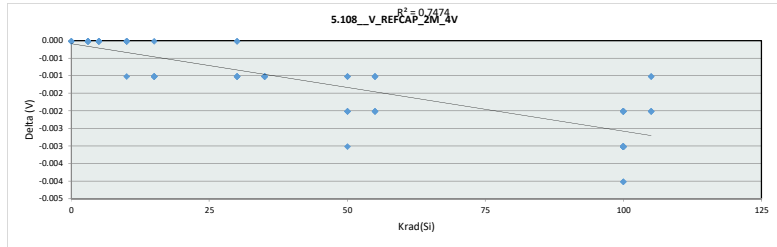
5.107_V_REFCAP										
Test Site										
Tester										
Test Number	1.237	V								
Min Limit	1.213	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225	1.223
Average	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.225	1.225	1.224
Max	1.228	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



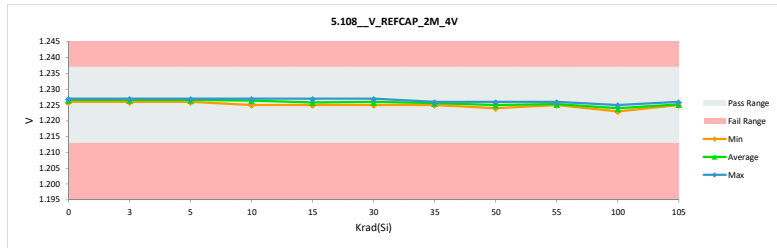
LDR TID Report
TPS7H5001-SP

		5.108_V_REFCAP_2M_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.227	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	-0.001
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.227	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.227	1.226	-0.001
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.225	-0.002
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.225	-0.001
55	113	1.226	1.225	-0.001
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.224	-0.003
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.224	-0.002
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.225	-0.002
100	236	1.227	1.225	-0.002
100	238	1.227	1.223	-0.004
100	243	1.226	1.224	-0.002
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.225	-0.002
105	374	1.226	1.225	-0.001
105	375	1.227	1.225	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.227	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001



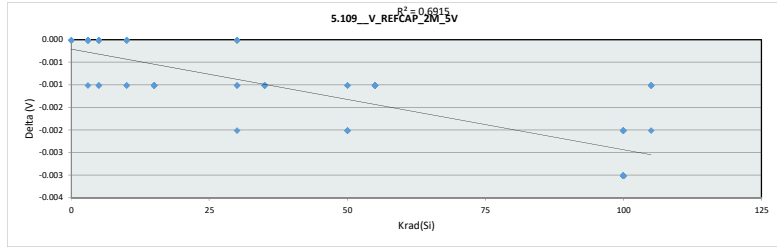
		5.108_V_REFCAP								
Test Site										
Tester										
Test Number										
Max Limit	1.237	V								
Min Limit	1.213	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223
Average	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.224	1.225
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



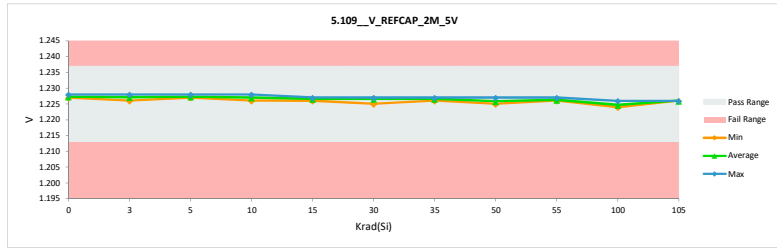
LDR TID Report
TPS7H5001-SP

5.109_V_REFCAP_2M_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.228	0.000
0	122	1.227	1.227	0.000
3	61	1.227	1.226	-0.001
3	62	1.228	1.228	0.000
3	63	1.227	1.227	0.000
3	64	1.227	1.227	0.000
3	65	1.227	1.227	0.000
3	66	1.228	1.228	0.000
5	67	1.227	1.227	0.000
5	68	1.227	1.227	0.000
5	69	1.228	1.228	0.000
5	70	1.228	1.228	0.000
5	71	1.228	1.227	-0.001
5	72	1.228	1.227	-0.001
10	73	1.227	1.226	-0.001
10	74	1.228	1.227	-0.001
10	75	1.227	1.227	0.000
10	76	1.227	1.227	0.000
10	77	1.227	1.227	0.000
10	78	1.228	1.228	0.000
15	79	1.227	1.226	-0.001
15	80	1.227	1.226	-0.001
15	81	1.228	1.227	-0.001
15	82	1.228	1.227	-0.001
15	83	1.228	1.227	-0.001
15	84	1.228	1.227	-0.001
30	86	1.227	1.225	-0.002
30	89	1.228	1.227	-0.001
30	91	1.227	1.227	0.000
30	92	1.227	1.227	0.000
30	94	1.227	1.227	0.000
30	95	1.228	1.227	-0.001
35	100	1.227	1.226	-0.001
35	101	1.227	1.226	-0.001
35	102	1.228	1.227	-0.001
35	103	1.228	1.227	-0.001
35	104	1.228	1.227	-0.001
35	105	1.228	1.227	-0.001
50	106	1.227	1.226	-0.001
50	107	1.228	1.226	-0.002
50	108	1.227	1.225	-0.002
50	109	1.228	1.226	-0.002
50	110	1.228	1.227	-0.001
50	111	1.227	1.225	-0.002
55	113	1.227	1.226	-0.001
55	114	1.227	1.226	-0.001
55	115	1.227	1.226	-0.001
55	116	1.227	1.226	-0.001
55	126	1.228	1.227	-0.001
55	128	1.228	1.227	-0.001
100	129	1.227	1.224	-0.003
100	131	1.227	1.225	-0.002
100	132	1.228	1.225	-0.003
100	133	1.228	1.226	-0.002
100	134	1.228	1.225	-0.003
100	135	1.227	1.224	-0.003
100	138	1.227	1.224	-0.003
100	139	1.227	1.225	-0.002
100	140	1.228	1.225	-0.003
100	141	1.227	1.224	-0.003
100	142	1.227	1.224	-0.003
100	143	1.227	1.225	-0.002
100	144	1.228	1.226	-0.002
100	220	1.227	1.224	-0.003
100	221	1.227	1.225	-0.002
100	224	1.227	1.224	-0.003
100	225	1.228	1.225	-0.003
100	226	1.228	1.225	-0.003
100	228	1.228	1.225	-0.003
100	232	1.228	1.225	-0.003
100	233	1.228	1.225	-0.003
100	236	1.228	1.225	-0.003
100	238	1.227	1.224	-0.003
100	243	1.227	1.224	-0.003
105	244	1.227	1.226	-0.001
105	246	1.228	1.226	-0.002
105	248	1.227	1.226	-0.001
105	374	1.227	1.226	-0.001
105	376	1.227	1.226	-0.001
105	Max	1.228	1.228	0.000
105	Average	1.227	1.226	-0.001
105	Min	1.227	1.224	-0.003
105	Std Dev	0.001	0.001	0.001



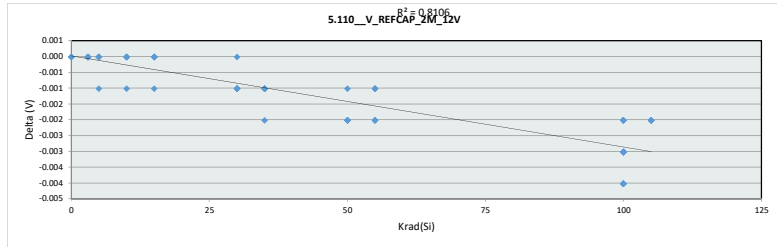
5.109_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.227	1.226	1.227	1.226	1.226	1.225	1.226	1.225	1.226	1.224
Average	1.227	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.225
Max	1.228	1.228	1.228	1.228	1.227	1.227	1.227	1.227	1.227	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



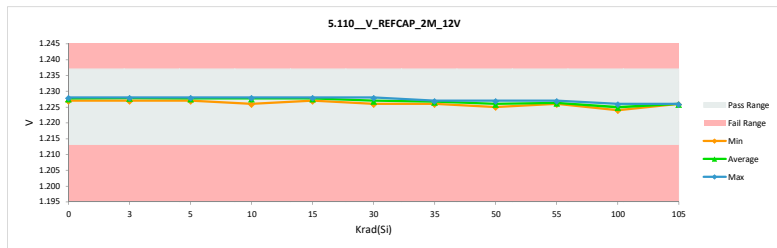
LDR TID Report
TPS7H5001-SP

5.110_V_REFCAP_2M_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.228	0.000
0	122	1.228	1.228	0.000
3	61	1.227	1.227	0.000
3	62	1.228	1.228	0.000
3	63	1.228	1.228	0.000
3	64	1.228	1.228	0.000
3	65	1.228	1.228	0.000
3	66	1.228	1.228	0.000
5	67	1.228	1.227	-0.001
5	68	1.227	1.227	0.000
5	69	1.228	1.228	0.000
5	70	1.228	1.228	0.000
5	71	1.228	1.228	0.000
5	72	1.228	1.228	0.000
10	73	1.227	1.226	-0.001
10	74	1.228	1.228	0.000
10	75	1.228	1.228	0.000
10	76	1.228	1.228	0.000
10	77	1.228	1.228	0.000
10	78	1.228	1.228	0.000
15	79	1.228	1.227	-0.001
15	80	1.227	1.227	0.000
15	81	1.228	1.228	0.000
15	82	1.228	1.228	0.000
15	83	1.228	1.228	0.000
15	84	1.228	1.228	0.000
30	86	1.227	1.226	-0.001
30	89	1.228	1.227	-0.001
30	91	1.228	1.227	-0.001
30	92	1.228	1.227	-0.001
30	94	1.228	1.227	-0.001
30	95	1.228	1.228	0.000
35	100	1.228	1.226	-0.002
35	101	1.227	1.226	-0.001
35	102	1.228	1.227	-0.001
35	103	1.228	1.227	-0.001
35	104	1.228	1.227	-0.001
35	105	1.228	1.227	-0.001
50	106	1.228	1.226	-0.002
50	107	1.228	1.226	-0.002
50	108	1.227	1.225	-0.002
50	109	1.228	1.226	-0.002
50	110	1.229	1.227	-0.002
50	111	1.227	1.226	-0.001
55	113	1.227	1.226	-0.001
55	114	1.228	1.226	-0.002
55	115	1.228	1.226	-0.002
55	116	1.228	1.226	-0.002
55	126	1.228	1.227	-0.001
55	128	1.228	1.227	-0.001
100	129	1.228	1.225	-0.003
100	131	1.228	1.225	-0.003
100	132	1.228	1.225	-0.003
100	133	1.229	1.226	-0.003
100	134	1.228	1.225	-0.003
100	135	1.228	1.224	-0.004
100	138	1.228	1.225	-0.003
100	139	1.228	1.225	-0.003
100	140	1.228	1.225	-0.003
100	141	1.228	1.224	-0.004
100	142	1.228	1.225	-0.003
100	143	1.227	1.225	-0.002
100	144	1.228	1.226	-0.002
100	220	1.228	1.224	-0.004
100	221	1.228	1.225	-0.003
100	224	1.228	1.224	-0.004
100	225	1.228	1.226	-0.002
100	226	1.228	1.225	-0.003
100	228	1.228	1.225	-0.003
100	232	1.228	1.225	-0.003
100	233	1.228	1.226	-0.002
100	236	1.228	1.226	-0.002
100	238	1.228	1.224	-0.004
100	243	1.227	1.224	-0.003
105	244	1.228	1.226	-0.002
105	246	1.228	1.226	-0.002
105	248	1.228	1.226	-0.002
105	374	1.228	1.226	-0.002
105	375	1.228	1.226	-0.002
105	Max	1.229	1.228	0.000
105	Average	1.228	1.226	-0.001
105	Min	1.227	1.224	-0.004
105	Std Dev	0.000	0.001	0.001



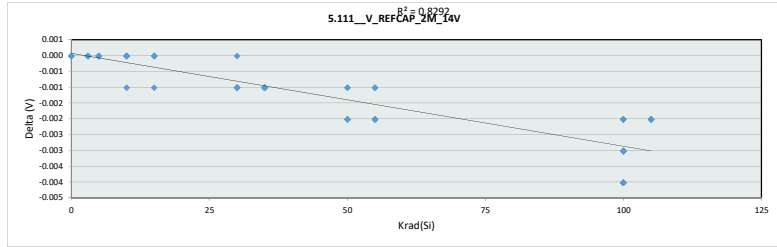
5.110_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.227	1.227	1.227	1.226	1.227	1.226	1.226	1.225	1.226	1.224
Average	1.228	1.228	1.228	1.228	1.228	1.227	1.227	1.226	1.226	1.225
Max	1.228	1.228	1.228	1.228	1.228	1.228	1.227	1.227	1.227	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



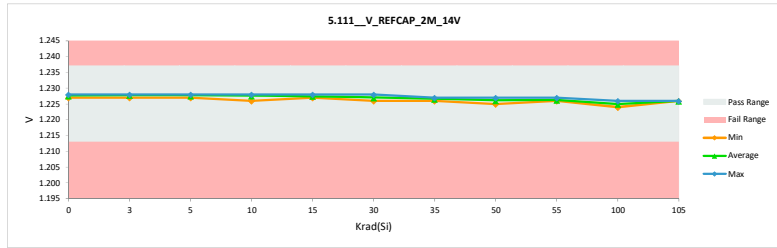
LDR TID Report
TPS7H5001-SP

5.111_V_REFCAP_2M_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.228	0.000
0	122	1.228	1.228	0.000
3	61	1.227	1.227	0.000
3	62	1.228	1.228	0.000
3	63	1.228	1.228	0.000
3	64	1.228	1.228	0.000
3	65	1.228	1.228	0.000
3	66	1.228	1.228	0.000
5	67	1.227	1.227	0.000
5	68	1.227	1.227	0.000
5	69	1.228	1.228	0.000
5	70	1.228	1.228	0.000
5	71	1.228	1.228	0.000
5	72	1.228	1.228	0.000
10	73	1.227	1.226	-0.001
10	74	1.228	1.228	0.000
10	75	1.228	1.228	0.000
10	76	1.228	1.228	0.000
10	77	1.228	1.228	0.000
10	78	1.228	1.228	0.000
15	79	1.227	1.227	0.000
15	80	1.227	1.227	0.000
15	81	1.228	1.228	0.000
15	82	1.228	1.228	0.000
15	83	1.228	1.227	-0.001
15	84	1.228	1.228	0.000
30	86	1.227	1.226	-0.001
30	89	1.228	1.227	-0.001
30	91	1.228	1.227	-0.001
30	92	1.228	1.227	-0.001
30	94	1.228	1.227	-0.001
30	95	1.228	1.228	0.000
35	100	1.227	1.226	-0.001
35	101	1.227	1.226	-0.001
35	102	1.228	1.227	-0.001
35	103	1.228	1.227	-0.001
35	104	1.228	1.227	-0.001
35	105	1.228	1.227	-0.001
50	106	1.228	1.227	-0.001
50	107	1.228	1.226	-0.002
50	108	1.227	1.225	-0.002
50	109	1.228	1.226	-0.002
50	110	1.229	1.227	-0.002
50	111	1.227	1.226	-0.001
55	113	1.228	1.226	-0.002
55	114	1.228	1.226	-0.002
55	115	1.228	1.226	-0.002
55	116	1.228	1.226	-0.002
55	126	1.228	1.227	-0.001
55	128	1.228	1.227	-0.001
100	129	1.228	1.225	-0.003
100	131	1.228	1.225	-0.003
100	132	1.229	1.225	-0.004
100	133	1.229	1.226	-0.003
100	134	1.228	1.225	-0.003
100	135	1.228	1.224	-0.004
100	138	1.228	1.225	-0.003
100	139	1.228	1.225	-0.003
100	140	1.228	1.225	-0.003
100	141	1.228	1.224	-0.004
100	142	1.228	1.225	-0.003
100	143	1.227	1.225	-0.002
100	144	1.228	1.226	-0.002
100	220	1.228	1.224	-0.004
100	221	1.228	1.225	-0.003
100	224	1.228	1.224	-0.004
100	225	1.228	1.226	-0.002
100	226	1.228	1.225	-0.003
100	228	1.228	1.225	-0.003
100	232	1.228	1.225	-0.003
100	233	1.228	1.226	-0.002
100	236	1.228	1.226	-0.002
100	238	1.228	1.224	-0.004
100	243	1.227	1.224	-0.003
105	244	1.228	1.226	-0.002
105	246	1.228	1.226	-0.002
105	248	1.228	1.226	-0.002
105	374	1.228	1.226	-0.002
105	376	1.228	1.226	-0.002
105	Max	1.229	1.228	0.000
105	Average	1.228	1.226	-0.001
105	Min	1.227	1.224	-0.004
105	Std Dev	0.000	0.001	0.001



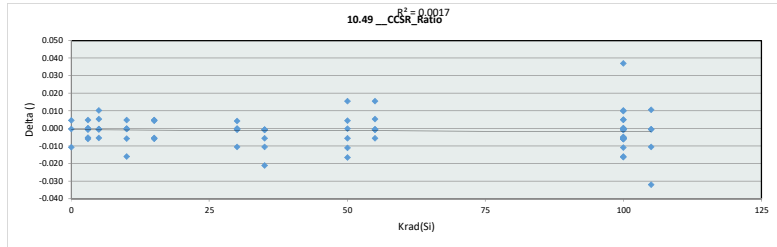
5.111_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237 V										
Min Limit	1.213 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.227	1.227	1.227	1.226	1.227	1.226	1.226	1.225	1.226	1.224	1.226
Average	1.228	1.228	1.228	1.228	1.228	1.227	1.227	1.226	1.226	1.225	1.226
Max	1.228	1.228	1.228	1.228	1.228	1.228	1.227	1.227	1.227	1.226	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



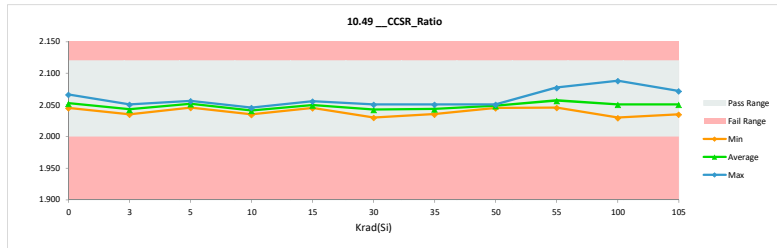
LDR TID Report
TPS7H5001-SP

		10.49_CCSR_Ratio	
Test Site			
Tester			
Test Number			
Unit			
Max Limit	2.12	2.12	
Min Limit	2	2	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.041	2.046	0.005
0	118	2.067	2.067	0.000
0	122	2.056	2.046	-0.010
3	61	2.041	2.046	0.005
3	62	2.046	2.041	-0.006
3	63	2.046	2.046	0.000
3	64	2.051	2.051	0.000
3	65	2.040	2.036	-0.005
3	66	2.041	2.041	0.000
5	67	2.041	2.046	0.006
5	68	2.056	2.056	0.000
5	69	2.056	2.051	-0.005
5	70	2.046	2.057	0.011
5	71	2.051	2.051	0.000
5	72	2.051	2.051	0.000
10	73	2.041	2.046	0.005
10	74	2.046	2.041	-0.005
10	75	2.046	2.046	0.000
10	76	2.051	2.035	-0.016
10	77	2.040	2.041	0.000
10	78	2.041	2.041	0.000
15	79	2.041	2.046	0.005
15	80	2.056	2.051	-0.005
15	81	2.056	2.051	-0.005
15	82	2.046	2.051	0.005
15	83	2.051	2.046	-0.005
15	84	2.051	2.056	0.005
30	86	2.041	2.041	0.000
30	89	2.046	2.046	0.000
30	91	2.046	2.046	0.000
30	92	2.051	2.051	0.000
30	94	2.040	2.030	-0.010
30	95	2.041	2.046	0.005
35	100	2.041	2.040	0.000
35	101	2.056	2.036	-0.021
35	102	2.056	2.046	-0.010
35	103	2.046	2.046	0.000
35	104	2.051	2.046	-0.005
35	105	2.051	2.051	0.000
50	106	2.062	2.051	-0.011
50	107	2.041	2.046	0.005
50	108	2.051	2.046	-0.005
50	109	2.035	2.051	0.016
50	110	2.067	2.051	-0.016
50	111	2.051	2.051	0.000
55	113	2.051	2.051	0.000
55	114	2.062	2.062	0.000
55	115	2.051	2.046	-0.005
55	116	2.051	2.051	0.000
55	126	2.062	2.078	0.016
55	128	2.051	2.057	0.006
100	129	2.035	2.046	0.010
100	131	2.062	2.056	-0.006
100	132	2.051	2.046	-0.005
100	133	2.041	2.046	0.005
100	134	2.041	2.041	0.000
100	135	2.046	2.030	-0.016
100	138	2.056	2.057	0.000
100	139	2.062	2.046	-0.016
100	140	2.041	2.040	0.000
100	141	2.072	2.067	-0.005
100	142	2.067	2.061	-0.006
100	143	2.041	2.041	0.000
100	144	2.088	2.078	-0.010
100	220	2.046	2.046	0.000
100	221	2.051	2.046	-0.005
100	224	2.056	2.057	0.001
100	225	2.035	2.041	0.005
100	226	2.067	2.062	-0.005
100	228	2.051	2.046	-0.004
100	232	2.051	2.089	0.037
100	233	2.046	2.041	-0.005
100	236	2.046	2.041	-0.005
100	238	2.051	2.062	0.011
100	243	2.046	2.046	0.000
105	244	2.072	2.072	0.000
105	246	2.078	2.046	-0.032
105	248	2.046	2.036	-0.010
105	374	2.051	2.062	0.011
105	376	2.041	2.041	0.000
105	Max	2.088	2.089	0.037
105	Average	2.050	2.049	-0.001
105	Min	2.035	2.030	-0.032
105	Std Dev	0.010	0.010	0.009



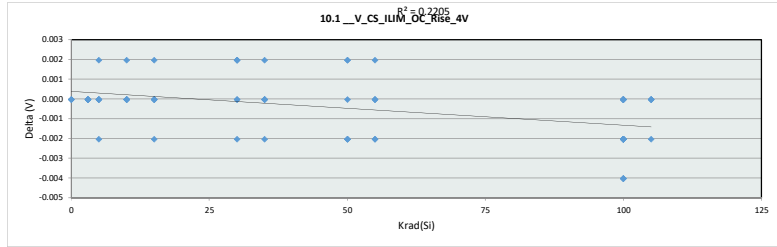
		10.49_CCSR_Ratio											
Test Site													
Tester													
Test Number													
Max Limit	2.12												
Min Limit	2												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000		
Min	2.046	2.036	2.046	2.035	2.046	2.030	2.036	2.046	2.046	2.030	2.036		
Average	2.053	2.043	2.052	2.042	2.050	2.043	2.044	2.049	2.057	2.051	2.051		
Max	2.067	2.051	2.057	2.046	2.056	2.051	2.051	2.051	2.078	2.089	2.072		
UL	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120		



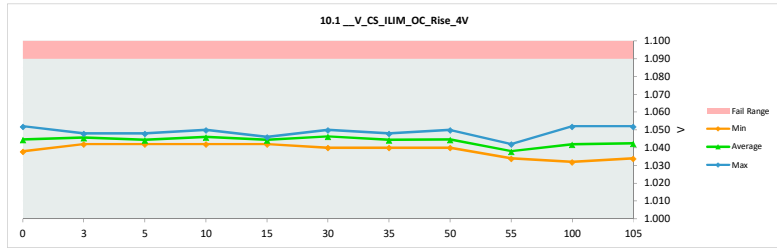
LDR TID Report
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10.1 V_CS_ILIM_OC_Rise_4V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	1.09	1.09
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.052	1.052	0.000
0	118	1.038	1.038	0.000
0	122	1.044	1.044	0.000
3	61	1.048	1.048	0.000
3	62	1.046	1.046	0.000
3	63	1.046	1.046	0.000
3	64	1.044	1.044	0.000
3	65	1.042	1.042	0.000
3	66	1.048	1.048	0.000
5	67	1.048	1.048	0.000
5	68	1.046	1.046	0.000
5	69	1.042	1.044	0.002
5	70	1.044	1.044	0.000
5	71	1.042	1.042	0.000
5	72	1.044	1.042	-0.002
10	73	1.048	1.050	0.002
10	74	1.046	1.046	0.000
10	75	1.046	1.046	0.000
10	76	1.044	1.044	0.000
10	77	1.042	1.042	0.000
10	78	1.048	1.048	0.000
15	79	1.048	1.046	-0.002
15	80	1.046	1.046	0.000
15	81	1.042	1.044	0.002
15	82	1.044	1.044	0.000
15	83	1.042	1.042	0.000
15	84	1.044	1.044	0.000
30	86	1.048	1.050	0.002
30	89	1.046	1.046	0.000
30	91	1.046	1.046	0.000
30	92	1.044	1.046	0.002
30	94	1.042	1.040	-0.002
30	95	1.048	1.050	0.002
35	100	1.048	1.048	0.000
35	101	1.046	1.046	0.000
35	102	1.042	1.044	0.002
35	103	1.044	1.044	0.000
35	104	1.042	1.040	-0.002
35	105	1.044	1.044	0.000
50	106	1.050	1.050	0.000
50	107	1.046	1.044	-0.002
50	108	1.046	1.044	-0.002
50	109	1.044	1.042	-0.002
50	110	1.046	1.048	0.002
50	111	1.038	1.040	0.002
55	113	1.036	1.034	-0.002
55	114	1.036	1.036	0.000
55	115	1.038	1.036	-0.002
55	116	1.040	1.040	0.000
55	126	1.042	1.042	0.000
55	128	1.038	1.040	0.002
100	129	1.046	1.044	-0.002
100	131	1.034	1.034	0.000
100	132	1.038	1.036	-0.002
100	133	1.038	1.038	0.000
100	134	1.044	1.044	0.000
100	135	1.046	1.042	-0.004
100	138	1.052	1.050	-0.002
100	139	1.040	1.036	-0.004
100	140	1.040	1.040	0.000
100	141	1.044	1.040	-0.004
100	142	1.034	1.032	-0.002
100	143	1.046	1.044	-0.002
100	144	1.052	1.052	0.000
100	220	1.048	1.046	-0.002
100	221	1.050	1.048	-0.002
100	224	1.042	1.042	0.000
100	225	1.042	1.040	-0.002
100	226	1.042	1.040	-0.002
100	228	1.048	1.044	-0.004
100	232	1.042	1.042	0.000
100	233	1.050	1.048	-0.002
100	236	1.040	1.038	-0.002
100	238	1.042	1.042	0.000
100	243	1.046	1.044	-0.002
105	244	1.038	1.038	0.000
105	246	1.034	1.034	0.000
105	248	1.052	1.052	0.000
105	374	1.040	1.040	0.000
105	376	1.050	1.048	-0.002
105	Max	1.052	1.052	0.002
105	Average	1.044	1.043	-0.001
105	Min	1.034	1.032	-0.004
105	Std Dev	0.004	0.004	0.001



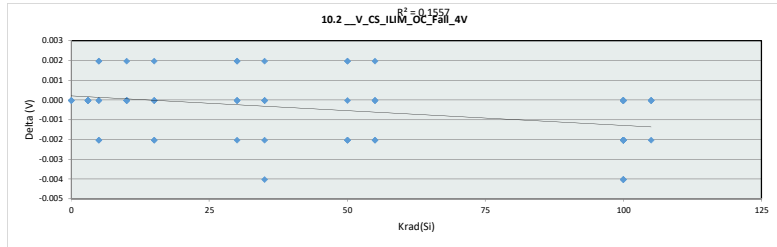
10.1 V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.038	1.042	1.042	1.042	1.042	1.040	1.040	1.040	1.034	1.032	1.034
Average	1.045	1.046	1.044	1.044	1.044	1.046	1.044	1.044	1.045	1.038	1.042
Max	1.052	1.048	1.048	1.050	1.046	1.050	1.048	1.050	1.042	1.052	1.052
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



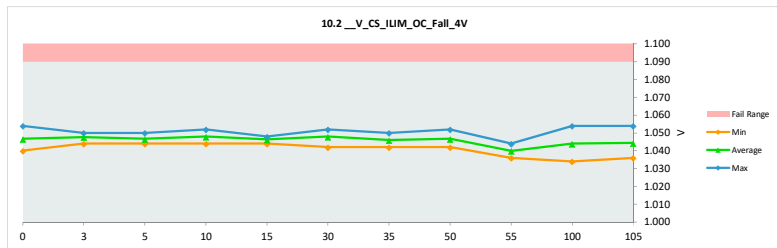
LDR TID Report
TPS7H5001-SP

10.2 V_CS_ILIM_OC_Fail_4V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.09 1.09
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.054	1.054	0.000
0	118	1.040	1.040	0.000
0	122	1.046	1.046	0.000
3	61	1.050	1.050	0.000
3	62	1.048	1.048	0.000
3	63	1.048	1.048	0.000
3	64	1.046	1.046	0.000
3	65	1.044	1.044	0.000
3	66	1.050	1.050	0.000
5	67	1.050	1.050	0.000
5	68	1.050	1.048	-0.002
5	69	1.044	1.046	0.002
5	70	1.046	1.048	0.002
5	71	1.044	1.044	0.000
5	72	1.046	1.044	-0.002
10	73	1.050	1.052	0.002
10	74	1.048	1.048	0.000
10	75	1.048	1.048	0.000
10	76	1.046	1.046	0.000
10	77	1.044	1.044	0.000
10	78	1.050	1.050	0.000
15	79	1.050	1.048	-0.002
15	80	1.050	1.048	-0.002
15	81	1.044	1.046	0.002
15	82	1.046	1.046	0.000
15	83	1.044	1.044	0.000
15	84	1.046	1.046	0.000
30	86	1.050	1.052	0.002
30	89	1.048	1.048	0.000
30	91	1.048	1.048	0.000
30	92	1.046	1.048	0.002
30	94	1.044	1.042	-0.002
30	95	1.050	1.050	0.000
35	100	1.050	1.050	0.000
35	101	1.050	1.046	-0.004
35	102	1.044	1.046	0.002
35	103	1.046	1.046	0.000
35	104	1.044	1.042	-0.002
35	105	1.046	1.046	0.000
50	106	1.052	1.052	0.000
50	107	1.048	1.046	-0.002
50	108	1.048	1.046	-0.002
50	109	1.046	1.044	-0.002
50	110	1.048	1.050	0.002
50	111	1.040	1.042	0.002
55	113	1.038	1.036	-0.002
55	114	1.038	1.038	0.000
55	115	1.040	1.038	-0.002
55	116	1.042	1.042	0.000
55	126	1.044	1.044	0.000
55	128	1.040	1.042	0.002
100	129	1.048	1.046	-0.002
100	131	1.036	1.036	0.000
100	132	1.038	1.038	0.000
100	133	1.040	1.040	0.000
100	134	1.046	1.046	0.000
100	135	1.048	1.044	-0.004
100	138	1.054	1.052	-0.002
100	139	1.042	1.040	-0.002
100	140	1.042	1.040	-0.002
100	141	1.046	1.042	-0.004
100	142	1.038	1.034	-0.004
100	143	1.048	1.046	-0.002
100	144	1.054	1.054	0.000
100	220	1.050	1.050	0.000
100	221	1.052	1.050	-0.002
100	224	1.044	1.044	0.000
100	225	1.044	1.042	-0.002
100	226	1.044	1.042	-0.002
100	228	1.050	1.046	-0.004
100	232	1.044	1.044	0.000
100	233	1.052	1.050	-0.002
100	236	1.042	1.040	-0.002
100	238	1.044	1.044	0.000
100	243	1.048	1.046	-0.002
105	244	1.040	1.040	0.000
105	246	1.036	1.036	0.000
105	248	1.054	1.054	0.000
105	374	1.042	1.042	0.000
105	376	1.052	1.050	-0.002
105	Max	1.054	1.054	0.002
105	Average	1.046	1.045	-0.001
105	Min	1.036	1.034	-0.004
105	Std Dev	0.004	0.004	0.002



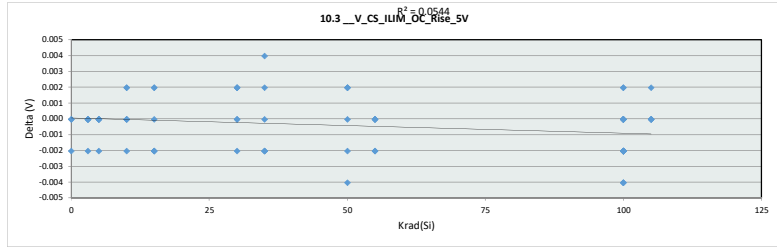
10.2 V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09 V										
Min Limit	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.040	1.044	1.044	1.044	1.044	1.042	1.042	1.042	1.036	1.034	1.036
Average	1.047	1.048	1.047	1.048	1.046	1.048	1.046	1.047	1.040	1.044	1.044
Max	1.054	1.050	1.050	1.052	1.048	1.052	1.050	1.052	1.044	1.054	1.054
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



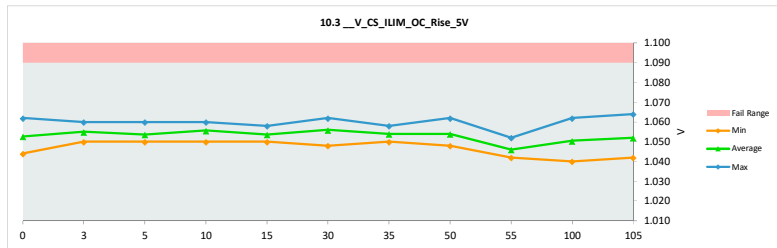
LDR TID Report
TPS7H5001-SP

10.3 V_CS_ILIM_OC_Rise_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.062	1.062	0.000
0	118	1.046	1.044	-0.002
0	122	1.052	1.052	0.000
3	61	1.058	1.058	0.000
3	62	1.056	1.054	-0.002
3	63	1.054	1.054	0.000
3	64	1.054	1.054	0.000
3	65	1.050	1.050	0.000
3	66	1.060	1.060	0.000
5	67	1.060	1.060	0.000
5	68	1.054	1.054	0.000
5	69	1.052	1.052	0.000
5	70	1.056	1.056	0.000
5	71	1.052	1.050	-0.002
5	72	1.050	1.050	0.000
10	73	1.058	1.060	0.002
10	74	1.056	1.054	-0.002
10	75	1.054	1.054	0.000
10	76	1.054	1.056	0.002
10	77	1.050	1.050	0.000
10	78	1.060	1.060	0.000
15	79	1.060	1.058	-0.002
15	80	1.054	1.052	-0.002
15	81	1.052	1.054	0.002
15	82	1.056	1.058	0.002
15	83	1.052	1.050	-0.002
15	84	1.050	1.050	0.000
30	86	1.058	1.060	0.002
30	89	1.056	1.056	0.000
30	91	1.054	1.054	0.000
30	92	1.054	1.056	0.002
30	94	1.050	1.048	-0.002
30	95	1.060	1.062	0.002
35	100	1.060	1.058	-0.002
35	101	1.054	1.052	-0.002
35	102	1.052	1.054	0.002
35	103	1.056	1.056	0.000
35	104	1.052	1.050	-0.002
35	105	1.050	1.054	0.004
50	106	1.060	1.062	0.002
50	107	1.054	1.054	0.000
50	108	1.054	1.044	-0.010
50	109	1.056	1.052	-0.004
50	110	1.054	1.056	0.002
50	111	1.046	1.048	0.002
55	113	1.044	1.042	-0.002
55	114	1.042	1.042	0.000
55	115	1.048	1.046	-0.002
55	116	1.048	1.048	0.000
55	126	1.052	1.052	0.000
55	128	1.046	1.046	0.000
100	129	1.054	1.052	-0.002
100	131	1.044	1.042	-0.002
100	132	1.044	1.042	-0.002
100	133	1.046	1.046	0.000
100	134	1.050	1.052	0.002
100	135	1.054	1.052	-0.002
100	138	1.060	1.058	-0.002
100	139	1.048	1.044	-0.004
100	140	1.050	1.048	-0.002
100	141	1.050	1.048	-0.002
100	142	1.044	1.040	-0.004
100	143	1.054	1.054	0.000
100	144	1.062	1.062	0.000
100	220	1.058	1.058	0.000
100	221	1.062	1.060	-0.002
100	224	1.048	1.048	0.000
100	225	1.050	1.048	-0.002
100	226	1.050	1.048	-0.002
100	228	1.058	1.054	-0.004
100	232	1.050	1.052	0.002
100	233	1.058	1.056	-0.002
100	236	1.046	1.046	0.000
100	238	1.050	1.050	0.000
100	243	1.054	1.052	-0.002
105	244	1.046	1.048	0.002
105	246	1.042	1.042	0.000
105	248	1.064	1.064	0.000
105	374	1.048	1.048	0.000
105	376	1.058	1.058	0.000
105	Max	1.064	1.064	0.004
105	Average	1.053	1.052	0.000
105	Min	1.042	1.040	-0.004
105	Std Dev	0.005	0.006	0.002



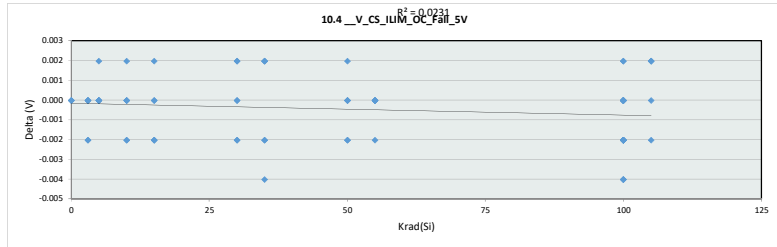
10.3 V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09 V										
Min Limit	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.050	1.050	1.050	1.050	1.048	1.050	1.048	1.042	1.040	1.042
Average	1.053	1.055	1.054	1.054	1.054	1.056	1.054	1.054	1.046	1.051	1.052
Max	1.062	1.060	1.060	1.060	1.058	1.062	1.058	1.062	1.052	1.062	1.064
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



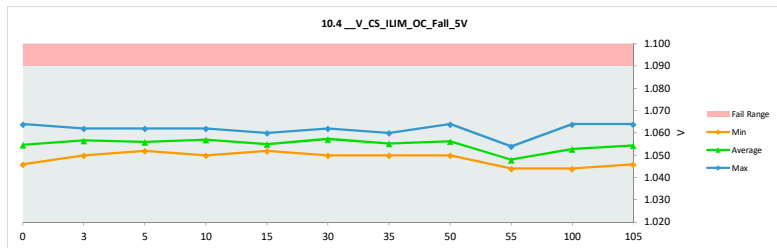
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10.4 V_CS_ILIM_OC_Fail_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.064	1.064	0.000
0	118	1.046	1.046	0.000
0	122	1.054	1.054	0.000
3	61	1.060	1.060	0.000
3	62	1.058	1.056	-0.002
3	63	1.056	1.056	0.000
3	64	1.056	1.056	0.000
3	65	1.052	1.050	-0.002
3	66	1.062	1.062	0.000
5	67	1.062	1.062	0.000
5	68	1.056	1.056	0.000
5	69	1.054	1.054	0.000
5	70	1.056	1.058	0.002
5	71	1.054	1.054	0.000
5	72	1.052	1.052	0.000
10	73	1.060	1.060	0.000
10	74	1.058	1.056	-0.002
10	75	1.056	1.056	0.000
10	76	1.056	1.058	0.002
10	77	1.052	1.050	-0.002
10	78	1.062	1.062	0.000
15	79	1.062	1.060	-0.002
15	80	1.056	1.054	-0.002
15	81	1.054	1.054	0.000
15	82	1.056	1.058	0.002
15	83	1.054	1.052	-0.002
15	84	1.052	1.052	0.000
30	86	1.060	1.062	0.002
30	89	1.058	1.056	-0.002
30	91	1.056	1.056	0.000
30	92	1.056	1.058	0.002
30	94	1.052	1.050	-0.002
30	95	1.062	1.062	0.000
35	100	1.062	1.060	-0.002
35	101	1.056	1.054	-0.002
35	102	1.054	1.056	0.002
35	103	1.056	1.058	0.002
35	104	1.054	1.050	-0.004
35	105	1.052	1.054	0.002
50	106	1.064	1.064	0.000
50	107	1.064	1.056	-0.002
50	108	1.056	1.046	-0.000
50	109	1.056	1.054	-0.002
50	110	1.058	1.058	0.000
50	111	1.048	1.050	0.002
55	113	1.046	1.044	-0.002
55	114	1.044	1.044	0.000
55	115	1.048	1.048	0.000
55	116	1.050	1.050	0.000
55	126	1.054	1.054	0.000
55	128	1.048	1.048	0.000
100	129	1.056	1.056	0.000
100	131	1.044	1.044	0.000
100	132	1.046	1.046	0.000
100	133	1.048	1.046	-0.002
100	134	1.054	1.054	0.000
100	135	1.056	1.054	-0.002
100	138	1.064	1.060	-0.004
100	139	1.050	1.046	-0.004
100	140	1.052	1.050	-0.002
100	141	1.052	1.050	-0.002
100	142	1.046	1.044	-0.002
100	143	1.056	1.056	0.000
100	144	1.064	1.064	0.000
100	220	1.058	1.060	0.002
100	221	1.064	1.062	-0.002
100	224	1.052	1.050	-0.002
100	225	1.050	1.052	0.002
100	226	1.052	1.050	-0.002
100	228	1.060	1.056	-0.004
100	232	1.054	1.054	0.000
100	233	1.060	1.058	-0.002
100	236	1.050	1.048	-0.002
100	238	1.052	1.052	0.000
100	243	1.056	1.056	0.000
105	244	1.048	1.050	0.002
105	246	1.044	1.046	0.002
105	248	1.066	1.064	-0.002
105	374	1.050	1.052	0.002
105	375	1.060	1.060	0.000
105	376	1.066	1.064	0.002
	Max	1.055	1.054	0.000
	Average	1.055	1.054	0.000
	Min	1.044	1.044	-0.004
	Std Dev	0.005	0.005	0.002



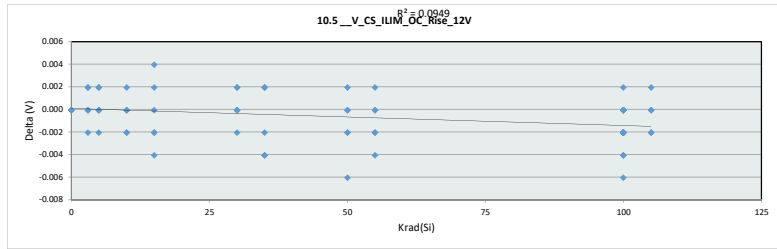
10.4 V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09
Min Limit	V
Krad(Si)	0
LL	0
Min	1.046
Average	1.055
Max	1.064
UL	1.090



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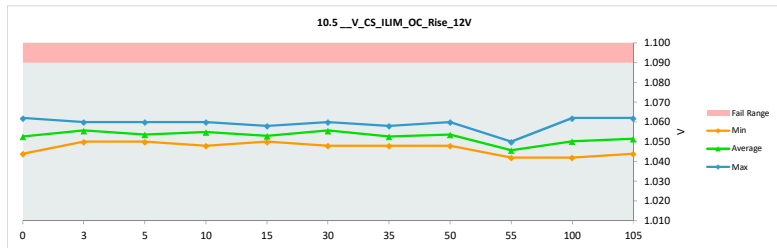
10.5 V_CS_ILIM_OC_Rise_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.062	1.062	0.000
0	118	1.044	1.044	0.000
0	122	1.052	1.052	0.000
3	61	1.058	1.060	0.002
3	62	1.056	1.054	-0.002
3	63	1.054	1.054	0.000
3	64	1.056	1.056	0.000
3	65	1.048	1.050	0.002
3	66	1.060	1.060	0.000
5	67	1.060	1.060	0.000
5	68	1.054	1.052	-0.002
5	69	1.050	1.052	0.002
5	70	1.054	1.056	0.002
5	71	1.052	1.052	0.000
5	72	1.050	1.050	0.000
10	73	1.058	1.060	0.002
10	74	1.056	1.054	-0.002
10	75	1.054	1.054	0.000
10	76	1.056	1.054	-0.002
10	77	1.048	1.048	0.000
10	78	1.060	1.060	0.000
15	79	1.060	1.056	-0.004
15	80	1.054	1.052	-0.002
15	81	1.050	1.052	0.002
15	82	1.054	1.058	0.004
15	83	1.052	1.050	-0.002
15	84	1.050	1.050	0.000
30	86	1.058	1.060	0.002
30	89	1.056	1.054	-0.002
30	91	1.054	1.054	0.000
30	92	1.056	1.058	0.002
30	94	1.048	1.048	0.000
30	95	1.060	1.060	0.000
35	100	1.060	1.058	-0.002
35	101	1.054	1.050	-0.004
35	102	1.050	1.052	0.002
35	103	1.054	1.056	0.002
35	104	1.052	1.048	-0.004
35	105	1.050	1.052	0.002
50	106	1.062	1.060	-0.002
50	107	1.056	1.054	-0.002
50	108	1.054	1.054	0.000
50	109	1.056	1.050	-0.006
50	110	1.056	1.056	0.000
50	111	1.046	1.048	0.002
55	113	1.046	1.042	-0.004
55	114	1.040	1.042	0.002
55	115	1.048	1.046	-0.002
55	116	1.048	1.048	0.000
55	126	1.052	1.050	-0.002
55	128	1.046	1.046	0.000
100	129	1.054	1.054	0.000
100	131	1.042	1.042	0.000
100	132	1.044	1.042	-0.002
100	133	1.046	1.044	-0.002
100	134	1.052	1.050	-0.002
100	135	1.054	1.052	-0.002
100	138	1.060	1.058	-0.002
100	139	1.048	1.044	-0.004
100	140	1.050	1.048	-0.002
100	141	1.052	1.048	-0.004
100	142	1.042	1.042	0.000
100	143	1.056	1.054	-0.002
100	144	1.060	1.062	0.002
100	220	1.056	1.056	0.000
100	221	1.060	1.058	-0.002
100	224	1.048	1.046	-0.002
100	225	1.050	1.050	0.000
100	226	1.050	1.048	-0.002
100	228	1.058	1.052	-0.006
100	232	1.052	1.052	0.000
100	233	1.058	1.056	-0.002
100	236	1.048	1.046	-0.002
100	238	1.050	1.048	-0.002
100	243	1.054	1.054	0.000
105	244	1.048	1.046	-0.002
105	246	1.042	1.044	0.002
105	248	1.064	1.062	-0.002
105	374	1.048	1.048	0.000
105	376	1.058	1.058	0.000
105	Max	1.064	1.062	0.004
105	Average	1.053	1.052	-0.001
105	Min	1.040	1.042	-0.006
105	Std Dev	0.005	0.005	0.002



10.5 V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09 V
Min Limit	V

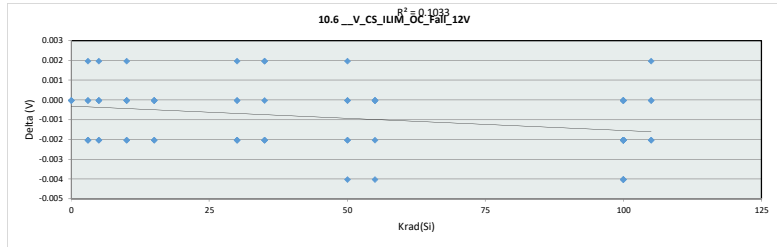
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.050	1.050	1.048	1.050	1.048	1.048	1.048	1.042	1.042	1.044
Average	1.053	1.056	1.054	1.055	1.053	1.056	1.053	1.054	1.046	1.050	1.052
Max	1.062	1.060	1.060	1.060	1.058	1.060	1.058	1.060	1.050	1.062	1.062
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



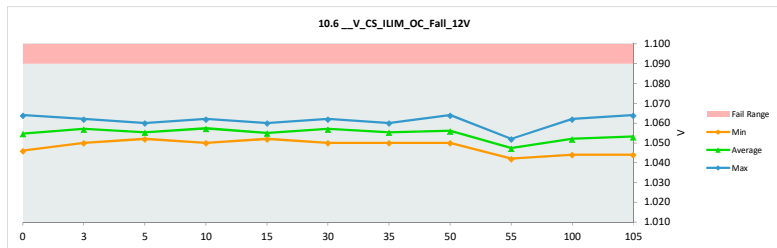
LDR TID Report
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10.6 V_CS_ILIM_OC_Fall_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.064	1.064	0.000
0	118	1.046	1.046	0.000
0	122	1.054	1.054	0.000
3	61	1.060	1.062	0.002
3	62	1.058	1.056	-0.002
3	63	1.056	1.056	0.000
3	64	1.058	1.056	-0.002
3	65	1.052	1.050	-0.002
3	66	1.062	1.062	0.000
5	67	1.062	1.060	-0.002
5	68	1.056	1.054	-0.002
5	69	1.054	1.054	0.000
5	70	1.058	1.058	0.000
5	71	1.052	1.054	0.002
5	72	1.052	1.052	0.000
10	73	1.060	1.062	0.002
10	74	1.058	1.056	-0.002
10	75	1.056	1.056	0.000
10	76	1.058	1.058	0.000
10	77	1.052	1.050	-0.002
10	78	1.062	1.062	0.000
15	79	1.062	1.060	-0.002
15	80	1.056	1.054	-0.002
15	81	1.054	1.054	0.000
15	82	1.058	1.058	0.000
15	83	1.052	1.052	0.000
15	84	1.052	1.052	0.000
30	86	1.060	1.062	0.002
30	89	1.058	1.056	-0.002
30	91	1.056	1.054	-0.002
30	92	1.058	1.058	0.000
30	94	1.052	1.050	-0.002
30	95	1.062	1.062	0.000
35	100	1.062	1.060	-0.002
35	101	1.056	1.054	-0.002
35	102	1.054	1.056	0.002
35	103	1.058	1.058	0.000
35	104	1.052	1.050	-0.002
35	105	1.052	1.054	0.002
50	106	1.064	1.064	0.000
50	107	1.058	1.056	-0.002
50	108	1.056	1.054	-0.002
50	109	1.058	1.054	-0.004
50	110	1.058	1.058	0.000
50	111	1.048	1.050	0.002
55	113	1.048	1.044	-0.004
55	114	1.042	1.042	0.000
55	115	1.048	1.048	0.000
55	116	1.050	1.050	0.000
55	126	1.054	1.052	-0.002
55	128	1.048	1.048	0.000
100	129	1.056	1.054	-0.002
100	131	1.044	1.044	0.000
100	132	1.046	1.044	-0.002
100	133	1.048	1.046	-0.002
100	134	1.054	1.054	0.000
100	135	1.056	1.054	-0.002
100	138	1.062	1.060	-0.002
100	139	1.050	1.046	-0.004
100	140	1.052	1.050	-0.002
100	141	1.052	1.048	-0.004
100	142	1.046	1.044	-0.002
100	143	1.056	1.056	0.000
100	144	1.064	1.062	-0.002
100	220	1.058	1.058	0.000
100	221	1.064	1.060	-0.004
100	224	1.050	1.048	-0.002
100	225	1.052	1.050	-0.002
100	226	1.052	1.050	-0.002
100	228	1.060	1.056	-0.004
100	232	1.054	1.054	0.000
100	233	1.060	1.058	-0.002
100	236	1.048	1.046	-0.002
100	238	1.052	1.052	0.000
100	243	1.056	1.054	-0.002
105	244	1.048	1.048	0.000
105	246	1.044	1.044	0.000
105	248	1.066	1.064	-0.002
105	374	1.050	1.052	0.002
105	376	1.066	1.058	-0.002
105	Max	1.066	1.064	0.002
105	Average	1.055	1.054	-0.001
105	Min	1.042	1.042	-0.004
105	Std Dev	0.005	0.005	0.002



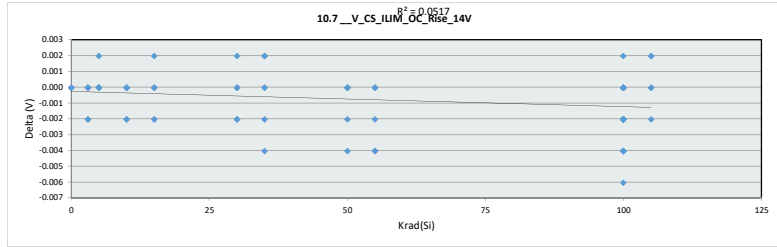
10.6 V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09
Min Limit	V
Krad(Si)	0
LL	0
Min	1.046
Average	1.055
Max	1.064
UL	1.090



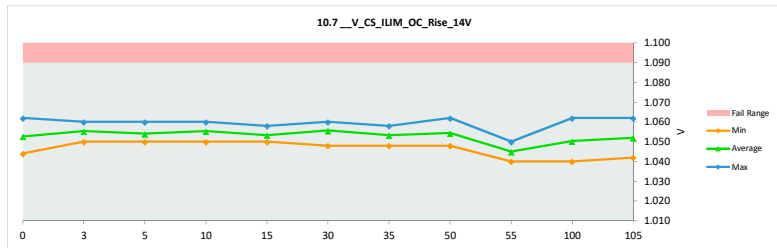
LDR TID Report
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10.7_V_CS_ILIM_OC_Rise_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.062	1.062	0.000
0	118	1.044	1.044	0.000
0	122	1.052	1.052	0.000
3	61	1.060	1.060	0.000
3	62	1.056	1.054	-0.002
3	63	1.054	1.054	0.000
3	64	1.056	1.054	-0.002
3	65	1.050	1.050	0.000
3	66	1.060	1.060	0.000
5	67	1.060	1.060	0.000
5	68	1.052	1.054	0.002
5	69	1.052	1.052	0.000
5	70	1.056	1.056	0.000
5	71	1.052	1.052	0.000
5	72	1.050	1.050	0.000
10	73	1.060	1.060	0.000
10	74	1.056	1.054	-0.002
10	75	1.054	1.054	0.000
10	76	1.056	1.054	-0.002
10	77	1.050	1.050	0.000
10	78	1.060	1.060	0.000
15	79	1.060	1.058	-0.002
15	80	1.052	1.052	0.000
15	81	1.052	1.052	0.000
15	82	1.056	1.056	0.000
15	83	1.052	1.050	-0.002
15	84	1.050	1.052	0.002
30	86	1.060	1.060	0.000
30	89	1.056	1.054	-0.002
30	91	1.054	1.054	0.000
30	92	1.056	1.058	0.002
30	94	1.050	1.048	-0.002
30	95	1.060	1.060	0.000
35	100	1.060	1.058	-0.002
35	101	1.052	1.052	0.000
35	102	1.052	1.054	0.002
35	103	1.056	1.056	0.000
35	104	1.052	1.048	-0.004
35	105	1.050	1.052	0.002
50	106	1.062	1.062	0.000
50	107	1.056	1.054	-0.002
50	108	1.054	1.054	0.000
50	109	1.056	1.052	-0.004
50	110	1.056	1.056	0.000
50	111	1.048	1.048	0.000
55	113	1.046	1.042	-0.004
55	114	1.040	1.040	0.000
55	115	1.048	1.044	-0.004
55	116	1.048	1.048	0.000
55	126	1.052	1.050	-0.002
55	128	1.046	1.046	0.000
100	129	1.054	1.052	-0.002
100	131	1.042	1.042	0.000
100	132	1.044	1.044	0.000
100	133	1.044	1.044	0.000
100	134	1.052	1.050	-0.002
100	135	1.054	1.052	-0.002
100	138	1.060	1.058	-0.002
100	139	1.048	1.044	-0.004
100	140	1.048	1.048	0.000
100	141	1.052	1.046	-0.006
100	142	1.044	1.040	-0.004
100	143	1.054	1.054	0.000
100	144	1.062	1.062	0.000
100	220	1.056	1.056	0.000
100	221	1.062	1.060	-0.002
100	224	1.048	1.048	0.000
100	225	1.050	1.050	0.000
100	226	1.050	1.048	-0.002
100	228	1.058	1.054	-0.004
100	232	1.052	1.052	0.000
100	233	1.058	1.056	-0.002
100	236	1.048	1.044	-0.004
100	238	1.048	1.050	0.002
100	243	1.056	1.054	-0.002
105	244	1.046	1.048	0.002
105	246	1.042	1.042	0.000
105	248	1.064	1.062	-0.002
105	374	1.048	1.050	0.002
105	376	1.058	1.058	0.000
105	Max	1.064	1.062	0.002
105	Average	1.053	1.052	-0.001
105	Min	1.040	1.040	-0.006
105	Std Dev	0.005	0.006	0.002



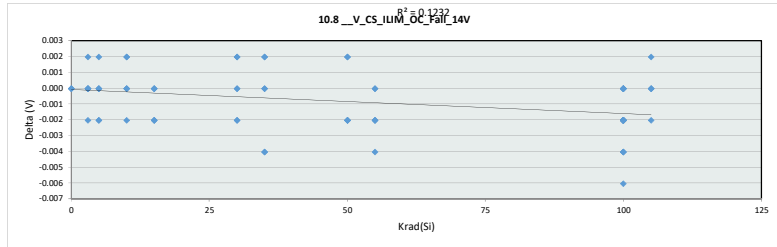
10.7_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09 V										
Min Limit	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.050	1.050	1.050	1.050	1.048	1.048	1.048	1.040	1.040	1.042
Average	1.053	1.055	1.054	1.054	1.053	1.056	1.053	1.054	1.045	1.050	1.052
Max	1.062	1.060	1.060	1.060	1.058	1.060	1.058	1.062	1.050	1.062	1.062
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



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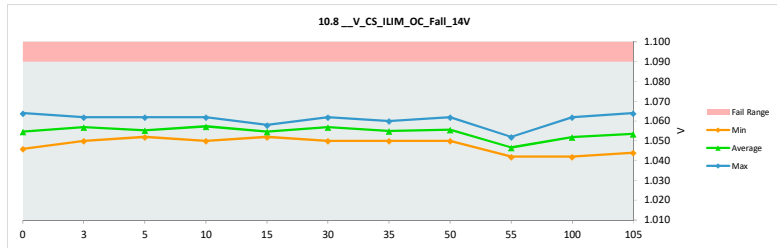
10.8 V_CS_ILIM_OC_Fall_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.064	1.064	0.000
0	118	1.046	1.046	0.000
0	122	1.054	1.054	0.000
3	61	1.060	1.062	0.002
3	62	1.056	1.056	0.000
3	63	1.056	1.056	0.000
3	64	1.056	1.056	0.000
3	65	1.052	1.050	-0.002
3	66	1.062	1.062	0.000
5	67	1.060	1.062	0.002
5	68	1.056	1.054	-0.002
5	69	1.054	1.054	0.000
5	70	1.058	1.058	0.000
5	71	1.054	1.052	-0.002
5	72	1.052	1.052	0.000
10	73	1.060	1.062	0.002
10	74	1.056	1.056	0.000
10	75	1.056	1.056	0.000
10	76	1.056	1.058	0.002
10	77	1.052	1.050	-0.002
10	78	1.062	1.062	0.000
15	79	1.060	1.058	-0.002
15	80	1.056	1.054	-0.002
15	81	1.054	1.054	0.000
15	82	1.058	1.058	0.000
15	83	1.054	1.052	-0.002
15	84	1.052	1.052	0.000
30	86	1.060	1.062	0.002
30	89	1.056	1.056	0.000
30	91	1.056	1.054	-0.002
30	92	1.056	1.058	0.002
30	94	1.052	1.050	-0.002
30	95	1.062	1.062	0.000
35	100	1.060	1.060	0.000
35	101	1.056	1.052	-0.004
35	102	1.054	1.056	0.002
35	103	1.058	1.058	0.000
35	104	1.054	1.050	-0.004
35	105	1.052	1.054	0.002
50	106	1.064	1.062	-0.002
50	107	1.058	1.056	-0.002
50	108	1.056	1.054	-0.002
50	109	1.056	1.054	-0.002
50	110	1.056	1.058	0.002
50	111	1.048	1.050	0.002
55	113	1.048	1.044	-0.004
55	114	1.042	1.042	0.000
55	115	1.048	1.046	-0.002
55	116	1.050	1.048	-0.002
55	126	1.054	1.052	-0.002
55	128	1.048	1.048	0.000
100	129	1.056	1.054	-0.002
100	131	1.044	1.044	0.000
100	132	1.046	1.044	-0.002
100	133	1.048	1.046	-0.002
100	134	1.054	1.054	0.000
100	135	1.056	1.054	-0.002
100	138	1.062	1.060	-0.002
100	139	1.050	1.046	-0.004
100	140	1.052	1.050	-0.002
100	141	1.054	1.048	-0.006
100	142	1.044	1.042	-0.002
100	143	1.058	1.056	-0.002
100	144	1.064	1.062	-0.002
100	220	1.058	1.058	0.000
100	221	1.064	1.062	-0.002
100	224	1.050	1.048	-0.002
100	225	1.052	1.050	-0.002
100	226	1.052	1.048	-0.004
100	228	1.060	1.056	-0.004
100	232	1.054	1.054	0.000
100	233	1.060	1.058	-0.002
100	236	1.048	1.046	-0.002
100	238	1.052	1.052	0.000
100	243	1.056	1.056	0.000
105	244	1.048	1.048	0.000
105	246	1.044	1.044	0.000
105	248	1.066	1.064	-0.002
105	374	1.050	1.052	0.002
105	375	1.060	1.060	0.000
105	376	1.066	1.064	-0.002
	Max	1.055	1.054	-0.001
	Average	1.055	1.054	-0.001
	Min	1.042	1.042	-0.006
	Std Dev	0.005	0.006	0.002



10.8 V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09
Min Limit	V
Krad(Si)	0
LL	0
Min	1.046
Average	1.055
Max	1.064
UL	1.090

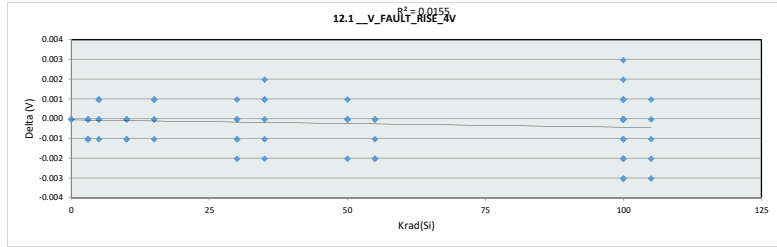
	3	5	10	15	30	35	50	55	100	105
LL										
Min	1.046	1.050	1.052	1.050	1.052	1.050	1.050	1.050	1.042	1.044
Average	1.055	1.057	1.055	1.057	1.055	1.057	1.055	1.056	1.047	1.052
Max	1.064	1.062	1.062	1.062	1.058	1.062	1.060	1.062	1.052	1.062
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



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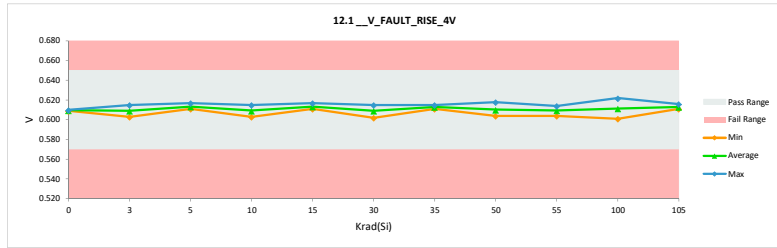
12.1_V_FAULT_RISE_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.609	0.609	0.000
0	118	0.610	0.610	0.000
0	122	0.610	0.610	0.000
3	61	0.605	0.604	-0.001
3	62	0.614	0.614	0.000
3	63	0.603	0.603	0.000
3	64	0.615	0.614	-0.001
3	65	0.606	0.605	-0.001
3	66	0.615	0.615	0.000
5	67	0.616	0.615	-0.001
5	68	0.610	0.611	0.001
5	69	0.610	0.611	0.001
5	70	0.616	0.617	0.001
5	71	0.611	0.611	0.000
5	72	0.615	0.615	0.000
10	73	0.605	0.605	0.000
10	74	0.614	0.614	0.000
10	75	0.603	0.603	0.000
10	76	0.615	0.614	-0.001
10	77	0.606	0.605	-0.001
10	78	0.615	0.615	0.000
15	79	0.616	0.615	-0.001
15	80	0.610	0.611	0.001
15	81	0.610	0.611	0.001
15	82	0.616	0.617	0.001
15	83	0.611	0.611	0.000
15	84	0.615	0.615	0.000
30	86	0.605	0.604	-0.001
30	89	0.614	0.615	0.001
30	91	0.603	0.602	-0.001
30	92	0.615	0.613	-0.002
30	94	0.606	0.606	0.000
30	95	0.615	0.615	0.000
35	100	0.616	0.614	-0.002
35	101	0.610	0.612	0.002
35	102	0.610	0.611	0.001
35	103	0.616	0.615	-0.001
35	104	0.611	0.612	0.001
35	105	0.615	0.615	0.000
50	106	0.614	0.614	0.000
50	107	0.612	0.613	0.001
50	108	0.606	0.606	0.000
50	109	0.618	0.618	0.000
50	110	0.606	0.604	-0.002
50	111	0.607	0.607	0.000
55	113	0.606	0.606	0.000
55	114	0.606	0.604	-0.002
55	115	0.612	0.611	-0.001
55	116	0.616	0.614	-0.002
55	126	0.613	0.613	0.000
55	128	0.609	0.609	0.000
100	129	0.610	0.611	0.001
100	131	0.604	0.603	-0.001
100	132	0.606	0.606	0.000
100	133	0.611	0.611	0.000
100	134	0.609	0.606	-0.003
100	135	0.614	0.613	-0.001
100	138	0.611	0.611	0.000
100	139	0.603	0.601	-0.002
100	140	0.607	0.608	0.001
100	141	0.617	0.614	-0.003
100	142	0.616	0.616	0.000
100	143	0.609	0.609	0.000
100	144	0.616	0.614	-0.002
100	220	0.613	0.613	0.000
100	221	0.616	0.616	0.000
100	224	0.602	0.605	0.003
100	225	0.610	0.610	0.000
100	226	0.611	0.610	-0.001
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.614	0.615	0.001
100	238	0.612	0.610	-0.002
100	243	0.617	0.617	0.000
105	244	0.612	0.611	-0.001
105	246	0.610	0.611	0.001
105	248	0.617	0.615	-0.002
105	374	0.613	0.613	0.000
105	376	0.619	0.616	-0.003
105	Max	0.622	0.622	0.003
105	Average	0.611	0.611	0.000
105	Min	0.602	0.601	-0.003
105	Std Dev	0.005	0.005	0.001



12.1_V_FAULT_R	
Test Site	
Tester	
Test Number	
Max Limit	0.65 V
Min Limit	0.57 V

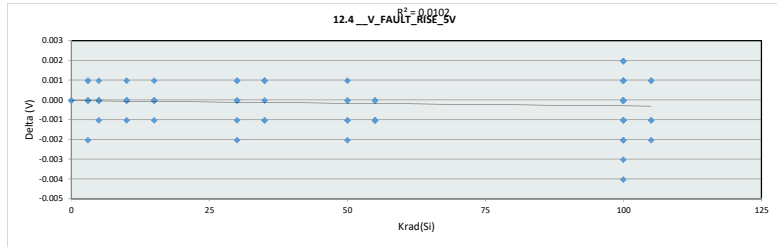
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.609	0.603	0.611	0.603	0.611	0.602	0.611	0.604	0.604	0.601	0.611
Average	0.610	0.609	0.613	0.609	0.613	0.609	0.613	0.610	0.610	0.612	0.613
Max	0.610	0.615	0.617	0.615	0.617	0.615	0.615	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



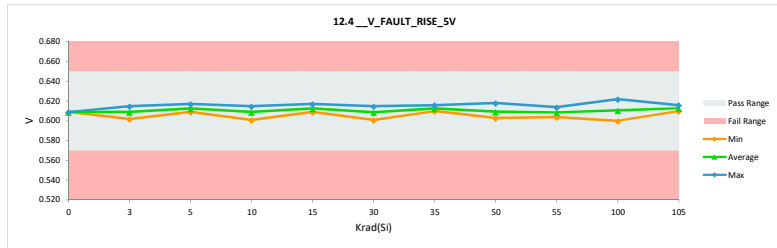
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12.4_V_FAULT_RISE_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.609	0.609	0.000
0	118	0.609	0.609	0.000
0	122	0.609	0.609	0.000
3	61	0.606	0.604	-0.002
3	62	0.613	0.613	0.000
3	63	0.601	0.602	0.001
3	64	0.615	0.615	0.000
3	65	0.605	0.605	0.000
3	66	0.614	0.615	0.001
5	67	0.616	0.615	-0.001
5	68	0.609	0.609	0.000
5	69	0.609	0.610	0.001
5	70	0.617	0.617	0.000
5	71	0.610	0.610	0.000
5	72	0.614	0.614	-0.000
10	73	0.606	0.605	-0.001
10	74	0.613	0.613	0.000
10	75	0.601	0.601	0.000
10	76	0.615	0.615	0.000
10	77	0.605	0.605	0.000
10	78	0.614	0.615	0.001
15	79	0.616	0.616	0.000
15	80	0.609	0.609	0.000
15	81	0.609	0.610	0.001
15	82	0.617	0.617	0.000
15	83	0.610	0.610	0.000
15	84	0.614	0.613	-0.001
30	86	0.606	0.604	-0.002
30	89	0.613	0.614	0.001
30	91	0.601	0.601	0.000
30	92	0.615	0.614	-0.001
30	94	0.605	0.605	0.000
30	95	0.614	0.615	0.001
35	100	0.616	0.615	-0.001
35	101	0.609	0.610	0.001
35	102	0.609	0.610	0.001
35	103	0.617	0.616	-0.001
35	104	0.610	0.611	0.001
35	105	0.614	0.614	0.000
50	106	0.615	0.614	-0.001
50	107	0.612	0.613	0.001
50	108	0.604	0.604	0.000
50	109	0.619	0.618	-0.001
50	110	0.605	0.603	-0.002
50	111	0.605	0.605	0.000
55	113	0.605	0.604	-0.001
55	114	0.605	0.604	-0.001
55	115	0.611	0.610	-0.001
55	116	0.615	0.614	-0.001
55	126	0.612	0.612	0.000
55	128	0.608	0.608	0.000
100	129	0.609	0.610	0.001
100	131	0.601	0.600	-0.001
100	132	0.604	0.604	0.000
100	133	0.611	0.611	0.000
100	134	0.608	0.605	-0.003
100	135	0.613	0.612	-0.001
100	138	0.610	0.610	0.000
100	139	0.602	0.600	-0.002
100	140	0.605	0.606	0.001
100	141	0.617	0.613	-0.004
100	142	0.614	0.614	0.000
100	143	0.608	0.608	0.000
100	144	0.615	0.613	-0.002
100	220	0.613	0.614	0.001
100	221	0.615	0.615	0.000
100	224	0.601	0.603	0.002
100	225	0.610	0.609	-0.001
100	226	0.610	0.610	0.000
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.612	0.614	0.002
100	238	0.611	0.610	-0.001
100	243	0.616	0.616	0.000
105	244	0.611	0.610	-0.001
105	246	0.609	0.610	0.001
105	248	0.616	0.615	-0.001
105	374	0.612	0.613	0.001
105	376	0.618	0.616	-0.002
105	Max	0.622	0.622	0.000
105	Average	0.611	0.611	0.000
105	Min	0.601	0.600	-0.004
105	Std Dev	0.005	0.005	0.001



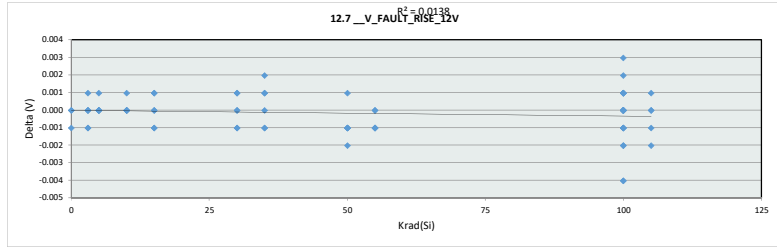
12.4_V_FAULT_R											
Test Site											
Tester											
Test Number											
Max Limit	0.65 V										
Min Limit	0.57 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.609	0.602	0.609	0.601	0.609	0.601	0.610	0.603	0.604	0.600	0.610
Average	0.609	0.609	0.613	0.609	0.613	0.609	0.613	0.610	0.609	0.611	0.613
Max	0.609	0.615	0.617	0.615	0.617	0.615	0.616	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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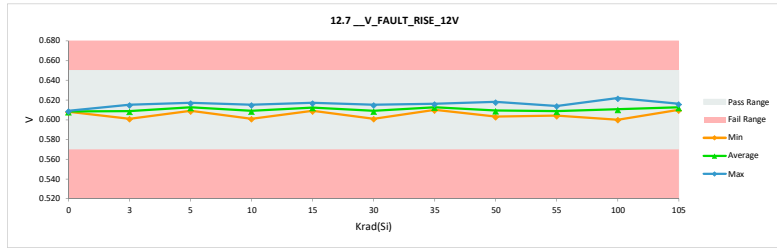
12.7_V_FAULT_RISE_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.609	0.609	0.000
0	118	0.609	0.608	-0.001
0	122	0.608	0.608	0.000
3	61	0.605	0.604	-0.001
3	62	0.613	0.613	0.000
3	63	0.601	0.601	0.000
3	64	0.615	0.615	0.000
3	65	0.605	0.604	-0.001
3	66	0.614	0.615	0.001
5	67	0.616	0.616	0.000
5	68	0.608	0.609	0.001
5	69	0.609	0.609	0.000
5	70	0.617	0.617	0.000
5	71	0.610	0.610	0.000
5	72	0.614	0.614	0.000
10	73	0.605	0.605	0.000
10	74	0.613	0.613	0.000
10	75	0.601	0.601	0.000
10	76	0.615	0.615	0.000
10	77	0.605	0.605	0.000
10	78	0.614	0.615	0.001
15	79	0.616	0.615	-0.001
15	80	0.608	0.609	0.001
15	81	0.609	0.610	0.001
15	82	0.617	0.617	0.000
15	83	0.610	0.610	0.000
15	84	0.614	0.613	-0.001
30	86	0.605	0.604	-0.001
30	89	0.613	0.614	0.001
30	91	0.601	0.601	0.000
30	92	0.615	0.614	-0.001
30	94	0.605	0.605	0.000
30	95	0.614	0.615	0.001
35	100	0.616	0.615	-0.001
35	101	0.608	0.610	0.002
35	102	0.609	0.610	0.001
35	103	0.617	0.616	-0.001
35	104	0.610	0.611	0.001
35	105	0.614	0.614	0.000
50	106	0.615	0.614	-0.001
50	107	0.612	0.613	0.001
50	108	0.604	0.603	-0.001
50	109	0.619	0.618	-0.001
50	110	0.605	0.603	-0.002
50	111	0.605	0.604	-0.001
55	113	0.605	0.604	-0.001
55	114	0.605	0.604	-0.001
55	115	0.611	0.610	-0.001
55	116	0.615	0.614	-0.001
55	126	0.612	0.612	0.000
55	128	0.608	0.608	0.000
100	129	0.609	0.610	0.001
100	131	0.601	0.600	-0.001
100	132	0.604	0.604	0.000
100	133	0.611	0.611	0.000
100	134	0.608	0.604	-0.004
100	135	0.613	0.612	-0.001
100	138	0.610	0.609	-0.001
100	139	0.601	0.600	-0.001
100	140	0.605	0.606	0.001
100	141	0.617	0.613	-0.004
100	142	0.614	0.614	0.000
100	143	0.608	0.608	0.000
100	144	0.615	0.613	-0.002
100	220	0.614	0.615	0.001
100	221	0.615	0.615	0.000
100	224	0.600	0.603	0.003
100	225	0.609	0.609	0.000
100	226	0.610	0.610	0.000
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.612	0.613	0.001
100	238	0.611	0.609	-0.002
100	243	0.616	0.616	0.000
105	244	0.610	0.610	0.000
105	246	0.609	0.610	0.001
105	248	0.616	0.615	-0.001
105	374	0.612	0.612	0.000
105	376	0.618	0.616	-0.002
105	Max	0.622	0.622	0.003
105	Average	0.611	0.610	0.000
105	Min	0.600	0.600	-0.004
105	Std Dev	0.005	0.005	0.001



12.7_V_FAULT_R	
Test Site	
Tester	
Test Number	
Max Limit	0.65 V
Min Limit	0.57 V

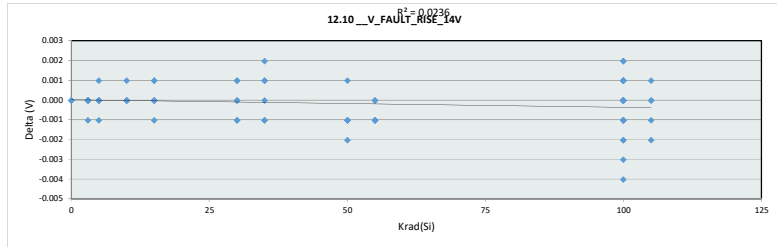
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.608	0.601	0.609	0.601	0.609	0.601	0.610	0.603	0.604	0.600	0.610
Average	0.608	0.609	0.613	0.609	0.612	0.609	0.613	0.609	0.609	0.611	0.613
Max	0.609	0.615	0.617	0.615	0.617	0.615	0.616	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



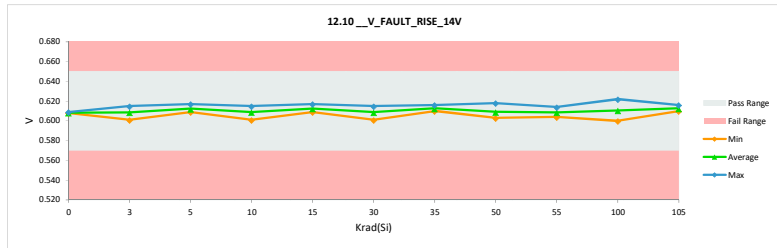
LDR TID Report
TPS7H5001-SP

12.10_V_FAULT_RISE_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.609	0.609	0.000
0	118	0.608	0.608	0.000
0	122	0.608	0.608	0.000
3	61	0.605	0.604	-0.001
3	62	0.613	0.613	0.000
3	63	0.601	0.601	0.000
3	64	0.615	0.615	0.000
3	65	0.605	0.605	0.000
3	66	0.614	0.614	0.000
5	67	0.616	0.615	-0.001
5	68	0.608	0.609	0.001
5	69	0.609	0.609	0.000
5	70	0.617	0.617	0.000
5	71	0.610	0.610	0.000
5	72	0.614	0.614	0.000
10	73	0.605	0.605	0.000
10	74	0.613	0.613	0.000
10	75	0.601	0.601	0.000
10	76	0.615	0.615	0.000
10	77	0.605	0.605	0.000
10	78	0.614	0.615	0.001
15	79	0.616	0.616	0.000
15	80	0.608	0.609	0.001
15	81	0.609	0.610	0.001
15	82	0.617	0.617	0.000
15	83	0.610	0.610	0.000
15	84	0.614	0.613	-0.001
30	86	0.605	0.604	-0.001
30	89	0.613	0.614	0.001
30	91	0.601	0.601	0.000
30	92	0.615	0.614	-0.001
30	94	0.605	0.605	0.000
30	95	0.614	0.615	0.001
35	100	0.616	0.615	-0.001
35	101	0.608	0.610	0.002
35	102	0.609	0.610	0.001
35	103	0.617	0.616	-0.001
35	104	0.610	0.611	0.001
35	105	0.614	0.614	0.000
50	106	0.615	0.614	-0.001
50	107	0.612	0.613	0.001
50	108	0.604	0.603	-0.001
50	109	0.619	0.618	-0.001
50	110	0.605	0.603	-0.002
50	111	0.605	0.604	-0.001
55	113	0.605	0.604	-0.001
55	114	0.605	0.604	-0.001
55	115	0.611	0.610	-0.001
55	116	0.615	0.614	-0.001
55	126	0.612	0.612	0.000
55	128	0.608	0.608	0.000
100	129	0.609	0.610	0.001
100	131	0.601	0.600	-0.001
100	132	0.604	0.604	0.000
100	133	0.611	0.611	0.000
100	134	0.608	0.604	-0.004
100	135	0.613	0.612	-0.001
100	138	0.610	0.609	-0.001
100	139	0.602	0.600	-0.002
100	140	0.605	0.606	0.001
100	141	0.617	0.614	-0.003
100	142	0.614	0.614	0.000
100	143	0.608	0.608	0.000
100	144	0.615	0.613	-0.002
100	220	0.614	0.615	0.001
100	221	0.615	0.615	0.000
100	224	0.601	0.603	0.002
100	225	0.609	0.608	-0.001
100	226	0.610	0.609	-0.001
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.612	0.613	0.001
100	238	0.610	0.610	0.000
100	243	0.616	0.616	0.000
105	244	0.610	0.610	0.000
105	246	0.609	0.610	0.001
105	248	0.616	0.615	-0.001
105	374	0.612	0.612	0.000
105	376	0.618	0.616	-0.002
105	Max	0.622	0.622	0.000
105	Average	0.611	0.610	0.000
105	Min	0.601	0.600	-0.004
105	Std Dev	0.005	0.005	0.001



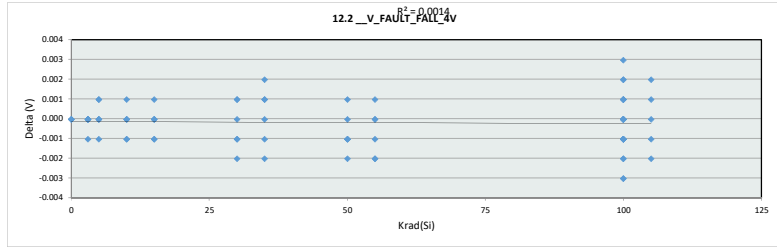
12.10_V_FAULT											
Test Site											
Tester											
Test Number											
Max Limit	0.65 V										
Min Limit	0.57 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.608	0.601	0.609	0.601	0.609	0.601	0.610	0.603	0.604	0.600	0.610
Average	0.608	0.609	0.612	0.609	0.613	0.609	0.613	0.609	0.609	0.611	0.613
Max	0.609	0.615	0.617	0.615	0.617	0.615	0.616	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



LDR TID Report
TPS7H5001-SP

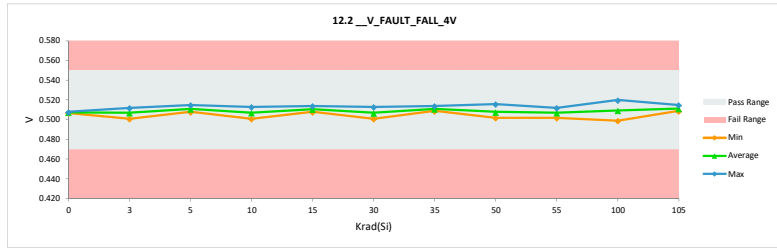
12.2_V_FAULT_FALL_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.507	0.507	0.000
0	118	0.508	0.508	0.000
0	122	0.508	0.508	0.000
3	61	0.503	0.503	0.000
3	62	0.512	0.512	0.000
3	63	0.501	0.501	0.000
3	64	0.512	0.512	0.000
3	65	0.504	0.503	-0.001
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.508	0.509	0.001
5	69	0.508	0.508	0.000
5	70	0.514	0.515	0.001
5	71	0.509	0.509	0.000
5	72	0.513	0.513	0.000
10	73	0.503	0.503	0.000
10	74	0.512	0.511	-0.001
10	75	0.501	0.501	0.000
10	76	0.512	0.512	0.000
10	77	0.504	0.503	-0.001
10	78	0.512	0.513	0.001
15	79	0.514	0.513	-0.001
15	80	0.508	0.509	0.001
15	81	0.508	0.508	0.000
15	82	0.514	0.514	0.000
15	83	0.509	0.509	0.000
15	84	0.513	0.512	-0.001
30	86	0.503	0.501	-0.002
30	89	0.512	0.513	0.001
30	91	0.501	0.501	0.000
30	92	0.512	0.511	-0.001
30	94	0.504	0.503	-0.001
30	95	0.512	0.513	0.001
35	100	0.514	0.512	-0.002
35	101	0.508	0.510	0.002
35	102	0.508	0.509	0.001
35	103	0.514	0.514	0.000
35	104	0.509	0.510	0.001
35	105	0.513	0.512	-0.001
50	106	0.512	0.512	0.000
50	107	0.510	0.511	0.001
50	108	0.504	0.503	-0.001
50	109	0.517	0.516	-0.001
50	110	0.504	0.502	-0.002
50	111	0.505	0.504	-0.001
55	113	0.504	0.503	-0.001
55	114	0.504	0.502	-0.002
55	115	0.509	0.509	0.000
55	116	0.514	0.512	-0.002
55	126	0.511	0.511	0.000
55	128	0.506	0.507	0.001
100	129	0.507	0.509	0.002
100	131	0.502	0.501	-0.001
100	132	0.504	0.504	0.000
100	133	0.509	0.509	0.000
100	134	0.507	0.504	-0.003
100	135	0.512	0.511	-0.001
100	138	0.510	0.509	-0.001
100	139	0.501	0.499	-0.002
100	140	0.504	0.505	0.001
100	141	0.515	0.512	-0.003
100	142	0.514	0.513	-0.001
100	143	0.507	0.507	0.000
100	144	0.514	0.512	-0.002
100	220	0.511	0.512	0.001
100	221	0.514	0.514	0.000
100	224	0.500	0.503	0.003
100	225	0.508	0.507	-0.001
100	226	0.508	0.508	0.000
100	228	0.512	0.513	0.001
100	232	0.517	0.519	0.002
100	233	0.520	0.520	0.000
100	236	0.512	0.513	0.001
100	238	0.510	0.509	-0.001
100	243	0.515	0.515	0.000
105	244	0.509	0.509	0.000
105	246	0.507	0.509	0.002
105	248	0.514	0.513	-0.001
105	374	0.510	0.511	0.001
105	376	0.517	0.515	-0.002
105	Max	0.520	0.520	0.003
105	Average	0.509	0.509	0.000
105	Min	0.500	0.499	-0.003
105	Std Dev	0.005	0.005	0.001



12.2_V_FAULT_F	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

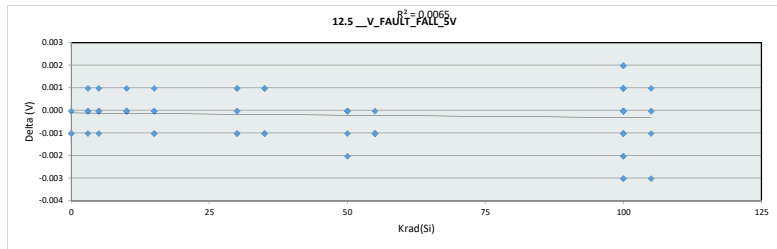
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.507	0.501	0.508	0.501	0.508	0.501	0.509	0.502	0.502	0.499	0.509
Average	0.508	0.507	0.511	0.507	0.511	0.507	0.511	0.508	0.507	0.510	0.511
Max	0.508	0.512	0.515	0.513	0.514	0.513	0.514	0.516	0.512	0.520	0.515
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



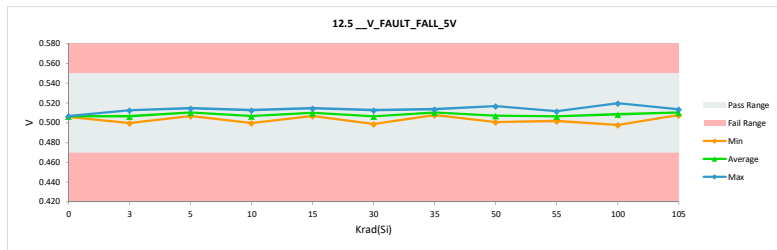
LDR TID Report
TPS7H5001-SP

12.5_V_FAULT_FALL_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.508	0.507	-0.001
0	118	0.507	0.506	-0.001
0	122	0.507	0.507	0.000
3	61	0.503	0.502	-0.001
3	62	0.511	0.511	0.000
3	63	0.499	0.500	0.001
3	64	0.513	0.513	0.000
3	65	0.503	0.503	0.000
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.507	0.507	0.000
5	69	0.507	0.508	0.001
5	70	0.515	0.515	0.000
5	71	0.508	0.508	0.000
5	72	0.512	0.512	0.000
10	73	0.503	0.503	0.000
10	74	0.511	0.511	0.000
10	75	0.499	0.500	0.001
10	76	0.513	0.513	0.000
10	77	0.503	0.503	0.000
10	78	0.512	0.512	0.000
15	79	0.514	0.513	-0.001
15	80	0.507	0.507	0.000
15	81	0.507	0.507	0.000
15	82	0.515	0.515	0.000
15	83	0.508	0.509	0.001
15	84	0.512	0.511	-0.001
30	86	0.503	0.502	-0.001
30	89	0.511	0.512	0.001
30	91	0.499	0.499	0.000
30	92	0.513	0.512	-0.001
30	94	0.503	0.503	0.000
30	95	0.512	0.513	0.001
35	100	0.514	0.513	-0.001
35	101	0.507	0.508	0.001
35	102	0.507	0.508	0.001
35	103	0.515	0.514	-0.001
35	104	0.508	0.509	0.001
35	105	0.512	0.511	-0.001
50	106	0.512	0.512	0.000
50	107	0.511	0.511	0.000
50	108	0.502	0.502	0.000
50	109	0.517	0.517	0.000
50	110	0.503	0.501	-0.002
50	111	0.503	0.502	-0.001
55	113	0.503	0.502	-0.001
55	114	0.503	0.502	-0.001
55	115	0.509	0.508	-0.001
55	116	0.513	0.512	-0.001
55	126	0.511	0.510	-0.001
55	128	0.506	0.506	0.000
100	129	0.508	0.509	0.001
100	131	0.499	0.498	-0.001
100	132	0.502	0.502	0.000
100	133	0.509	0.509	0.000
100	134	0.506	0.503	-0.003
100	135	0.511	0.510	-0.001
100	138	0.508	0.508	0.000
100	139	0.500	0.498	-0.002
100	140	0.503	0.504	0.001
100	141	0.515	0.512	-0.003
100	142	0.512	0.512	0.000
100	143	0.507	0.506	-0.001
100	144	0.513	0.511	-0.002
100	220	0.512	0.513	0.001
100	221	0.513	0.513	0.000
100	224	0.499	0.501	0.002
100	225	0.507	0.507	0.000
100	226	0.508	0.508	0.000
100	228	0.512	0.514	0.002
100	232	0.517	0.519	0.002
100	233	0.520	0.520	0.000
100	236	0.510	0.511	0.001
100	238	0.509	0.508	-0.001
100	243	0.514	0.514	0.000
105	244	0.509	0.508	-0.001
105	246	0.507	0.508	0.001
105	248	0.513	0.513	0.000
105	374	0.510	0.510	0.000
105	376	0.517	0.514	-0.003
105	Max	0.520	0.520	0.002
105	Average	0.509	0.509	0.000
105	Min	0.499	0.498	-0.003
105	Std Dev	0.005	0.005	0.001



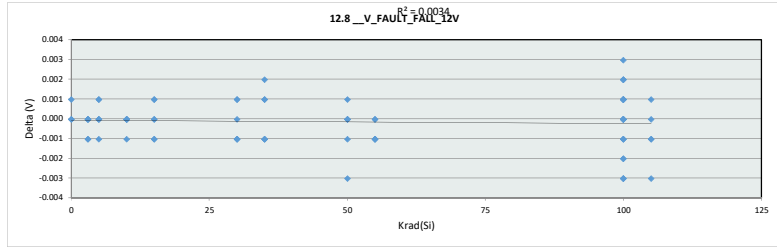
12.5_V_FAULT_F											
Test Site											
Tester											
Test Number											
Max Limit	0.55 V										
Min Limit	0.47 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.506	0.500	0.507	0.500	0.507	0.499	0.508	0.501	0.502	0.498	0.508
Average	0.507	0.507	0.511	0.507	0.510	0.507	0.511	0.508	0.507	0.509	0.511
Max	0.507	0.513	0.515	0.513	0.515	0.513	0.514	0.517	0.512	0.520	0.514
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



LDR TID Report
TPS7H5001-SP

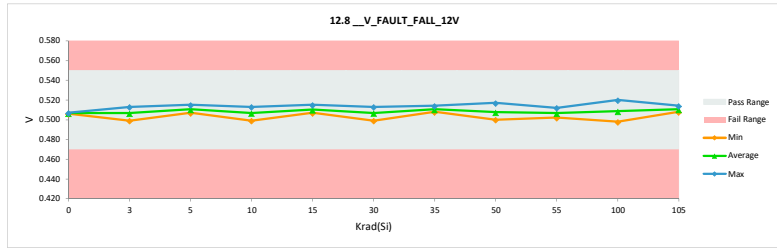
12.8_V_FAULT_FALL_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.507	0.507	0.000
0	118	0.506	0.506	0.000
0	122	0.506	0.507	0.001
3	61	0.503	0.502	-0.001
3	62	0.511	0.511	0.000
3	63	0.500	0.499	-0.001
3	64	0.513	0.513	0.000
3	65	0.503	0.503	0.000
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.506	0.507	0.001
5	69	0.507	0.508	0.001
5	70	0.515	0.515	0.000
5	71	0.508	0.508	0.000
5	72	0.512	0.512	0.000
10	73	0.503	0.503	0.000
10	74	0.511	0.511	0.000
10	75	0.500	0.499	-0.001
10	76	0.513	0.513	0.000
10	77	0.503	0.503	0.000
10	78	0.512	0.512	0.000
15	79	0.514	0.513	-0.001
15	80	0.506	0.507	0.001
15	81	0.507	0.508	0.001
15	82	0.515	0.515	0.000
15	83	0.508	0.508	0.000
15	84	0.512	0.511	-0.001
30	86	0.503	0.502	-0.001
30	89	0.511	0.512	0.001
30	91	0.500	0.499	-0.001
30	92	0.513	0.512	-0.001
30	94	0.503	0.503	0.000
30	95	0.512	0.513	0.001
35	100	0.514	0.513	-0.001
35	101	0.506	0.508	0.002
35	102	0.507	0.508	0.001
35	103	0.515	0.514	-0.001
35	104	0.508	0.509	0.001
35	105	0.512	0.511	-0.001
50	106	0.512	0.512	0.000
50	107	0.511	0.512	0.001
50	108	0.502	0.501	-0.001
50	109	0.517	0.517	0.000
50	110	0.503	0.500	-0.003
50	111	0.503	0.503	0.000
55	113	0.503	0.502	-0.001
55	114	0.503	0.502	-0.001
55	115	0.509	0.508	-0.001
55	116	0.513	0.512	-0.001
55	126	0.510	0.510	0.000
55	128	0.506	0.506	0.000
100	129	0.507	0.509	0.002
100	131	0.499	0.498	-0.001
100	132	0.502	0.502	0.000
100	133	0.509	0.510	0.001
100	134	0.506	0.503	-0.003
100	135	0.511	0.510	-0.001
100	138	0.508	0.508	0.000
100	139	0.500	0.498	-0.002
100	140	0.503	0.504	0.001
100	141	0.515	0.512	-0.003
100	142	0.511	0.512	0.001
100	143	0.507	0.506	-0.001
100	144	0.513	0.510	-0.003
100	220	0.512	0.513	0.001
100	221	0.513	0.513	0.000
100	224	0.498	0.501	0.003
100	225	0.508	0.506	-0.002
100	226	0.508	0.508	0.000
100	228	0.512	0.513	0.001
100	232	0.517	0.519	0.002
100	233	0.519	0.520	0.001
100	236	0.509	0.511	0.002
100	238	0.508	0.508	0.000
100	243	0.514	0.514	0.000
105	244	0.508	0.508	0.000
105	246	0.507	0.508	0.001
105	248	0.514	0.513	-0.001
105	374	0.511	0.510	-0.001
105	376	0.517	0.514	-0.003
105	Max	0.519	0.520	0.003
105	Average	0.509	0.508	0.000
105	Min	0.498	0.498	-0.003
105	Std Dev	0.005	0.005	0.001



12.8_V_FAULT_F	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

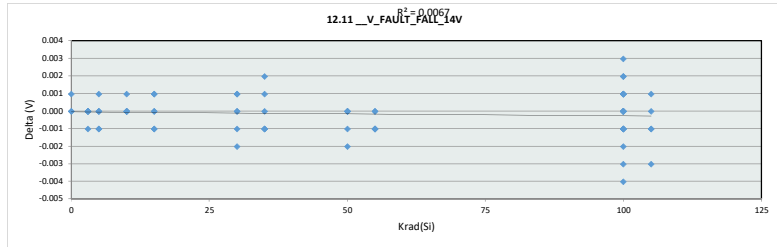
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.506	0.499	0.507	0.499	0.507	0.499	0.508	0.500	0.502	0.498	0.508
Average	0.507	0.507	0.511	0.507	0.510	0.507	0.511	0.508	0.507	0.509	0.511
Max	0.507	0.513	0.515	0.513	0.515	0.513	0.514	0.517	0.512	0.520	0.514
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



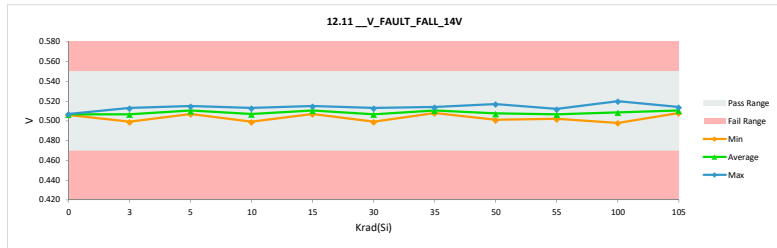
LDR TID Report
TPS7H5001-SP

12.11_V_FAULT_FALL_14V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	0.55 0.55
Min Limit	0.47 0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.507	0.507	0.000
0	118	0.506	0.507	0.001
0	122	0.506	0.506	0.000
3	61	0.503	0.502	-0.001
3	62	0.511	0.511	0.000
3	63	0.499	0.499	0.000
3	64	0.513	0.513	0.000
3	65	0.503	0.503	0.000
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.506	0.507	0.001
5	69	0.507	0.507	0.000
5	70	0.515	0.515	0.000
5	71	0.509	0.508	-0.001
5	72	0.512	0.512	0.000
10	73	0.503	0.503	0.000
10	74	0.511	0.511	0.000
10	75	0.499	0.499	0.000
10	76	0.513	0.513	0.000
10	77	0.503	0.503	0.000
10	78	0.512	0.513	0.001
15	79	0.514	0.513	-0.001
15	80	0.506	0.507	0.001
15	81	0.507	0.508	0.001
15	82	0.515	0.515	0.000
15	83	0.509	0.509	0.000
15	84	0.512	0.511	-0.001
30	86	0.503	0.501	-0.002
30	89	0.511	0.512	0.001
30	91	0.499	0.499	0.000
30	92	0.513	0.512	-0.001
30	94	0.503	0.503	0.000
30	95	0.512	0.513	0.001
35	100	0.514	0.513	-0.001
35	101	0.506	0.508	0.002
35	102	0.507	0.508	0.001
35	103	0.515	0.514	-0.001
35	104	0.509	0.509	0.000
35	105	0.512	0.511	-0.001
50	106	0.512	0.512	0.000
50	107	0.511	0.511	0.000
50	108	0.502	0.502	0.000
50	109	0.517	0.517	0.000
50	110	0.503	0.501	-0.002
50	111	0.503	0.502	-0.001
55	113	0.503	0.502	-0.001
55	114	0.503	0.502	-0.001
55	115	0.509	0.508	-0.001
55	116	0.513	0.512	-0.001
55	126	0.510	0.510	0.000
55	128	0.506	0.506	0.000
100	129	0.507	0.508	0.001
100	131	0.499	0.498	-0.001
100	132	0.502	0.501	-0.001
100	133	0.508	0.509	0.001
100	134	0.506	0.502	-0.004
100	135	0.511	0.510	-0.001
100	138	0.508	0.508	0.000
100	139	0.500	0.498	-0.002
100	140	0.503	0.504	0.001
100	141	0.515	0.512	-0.003
100	142	0.512	0.512	0.000
100	143	0.506	0.506	0.000
100	144	0.512	0.511	-0.001
100	220	0.512	0.513	0.001
100	221	0.513	0.513	0.000
100	224	0.498	0.501	0.003
100	225	0.507	0.507	0.000
100	226	0.508	0.508	0.000
100	228	0.512	0.514	0.002
100	232	0.517	0.519	0.002
100	233	0.520	0.520	0.000
100	236	0.510	0.511	0.001
100	238	0.509	0.508	-0.001
100	243	0.514	0.514	0.000
105	244	0.509	0.508	-0.001
105	246	0.507	0.508	0.001
105	248	0.514	0.513	-0.001
105	374	0.510	0.510	0.000
105	376	0.517	0.514	-0.003
105	Max	0.520	0.520	0.003
105	Average	0.509	0.508	0.000
105	Min	0.498	0.498	-0.004
105	Std Dev	0.005	0.005	0.001



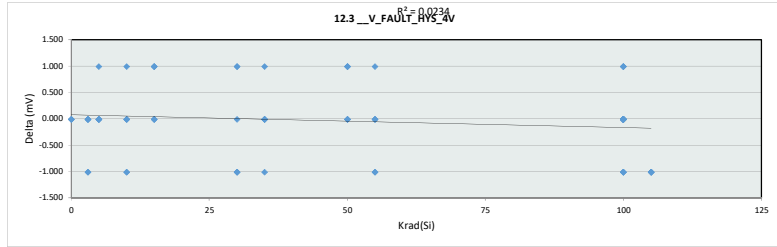
12.11_V_FAULT										
Test Site										
Tester										
Test Number										
Max Limit	0.55 V									
Min Limit	0.47 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.506	0.499	0.507	0.499	0.507	0.499	0.508	0.501	0.502	0.498
Average	0.507	0.507	0.510	0.507	0.511	0.507	0.511	0.508	0.507	0.509
Max	0.507	0.513	0.515	0.513	0.515	0.513	0.514	0.517	0.512	0.520
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



LDR TID Report
TPS7H5001-SP

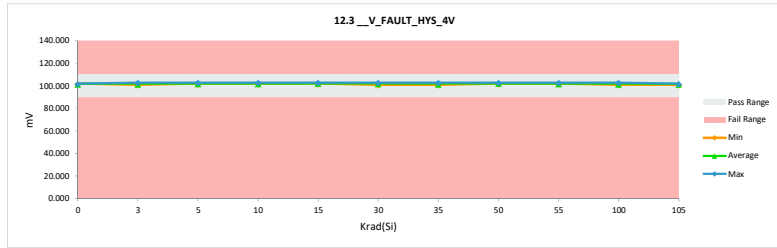
12.3_V_FAULT_HYS_4V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	110
Min Limit	90

Krad(Si)	Serial #	PRE	POST	Delta
0	117	102.000	102.000	0.000
0	118	102.000	102.000	0.000
0	122	102.000	102.000	0.000
3	61	102.000	101.000	-1.000
3	62	102.000	102.000	0.000
3	63	102.000	102.000	0.000
3	64	103.000	102.000	-1.000
3	65	102.000	102.000	0.000
3	66	103.000	103.000	0.000
5	67	102.000	102.000	0.000
5	68	102.000	102.000	0.000
5	69	102.000	103.000	1.000
5	70	102.000	102.000	0.000
5	71	102.000	102.000	0.000
5	72	102.000	102.000	0.000
10	73	102.000	102.000	0.000
10	74	102.000	103.000	1.000
10	75	102.000	102.000	0.000
10	76	103.000	102.000	-1.000
10	77	102.000	102.000	0.000
10	78	103.000	102.000	-1.000
15	79	102.000	102.000	0.000
15	80	102.000	102.000	0.000
15	81	102.000	103.000	1.000
15	82	102.000	103.000	1.000
15	83	102.000	102.000	0.000
15	84	102.000	103.000	1.000
30	86	102.000	103.000	1.000
30	89	102.000	102.000	0.000
30	91	102.000	101.000	-1.000
30	92	103.000	102.000	-1.000
30	94	102.000	103.000	1.000
30	95	103.000	102.000	-1.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	101.000	-1.000
35	104	102.000	102.000	0.000
35	105	102.000	103.000	1.000
50	106	102.000	102.000	0.000
50	107	102.000	102.000	0.000
50	108	102.000	103.000	1.000
50	109	101.000	102.000	1.000
50	110	102.000	102.000	0.000
50	111	102.000	103.000	1.000
55	113	102.000	103.000	1.000
55	114	102.000	102.000	0.000
55	115	103.000	102.000	-1.000
55	116	102.000	102.000	0.000
55	126	102.000	102.000	0.000
55	128	103.000	102.000	-1.000
100	129	103.000	102.000	-1.000
100	131	102.000	102.000	0.000
100	132	102.000	102.000	0.000
100	133	102.000	102.000	0.000
100	134	102.000	102.000	0.000
100	135	102.000	102.000	0.000
100	138	101.000	102.000	1.000
100	139	102.000	102.000	0.000
100	140	103.000	103.000	0.000
100	141	102.000	102.000	0.000
100	142	102.000	103.000	1.000
100	143	102.000	102.000	0.000
100	144	102.000	102.000	0.000
100	220	102.000	101.000	-1.000
100	221	102.000	102.000	0.000
100	224	102.000	102.000	0.000
100	225	102.000	103.000	1.000
100	226	103.000	102.000	-1.000
100	228	102.000	102.000	0.000
100	232	102.000	102.000	0.000
100	233	102.000	102.000	0.000
100	236	102.000	102.000	0.000
100	238	102.000	101.000	-1.000
100	243	102.000	102.000	0.000
105	244	103.000	102.000	-1.000
105	246	103.000	102.000	-1.000
105	248	103.000	102.000	-1.000
105	374	103.000	102.000	-1.000
105	376	102.000	101.000	-1.000
105	Max	103.000	103.000	1.000
105	Average	102.163	102.113	-0.050
105	Min	101.000	101.000	-1.000
105	Std Dev	0.434	0.503	0.654



12.3_V_FAULT_HYS_4V	
Test Site	
Tester	
Test Number	
Max Limit	110 mV
Min Limit	90 mV

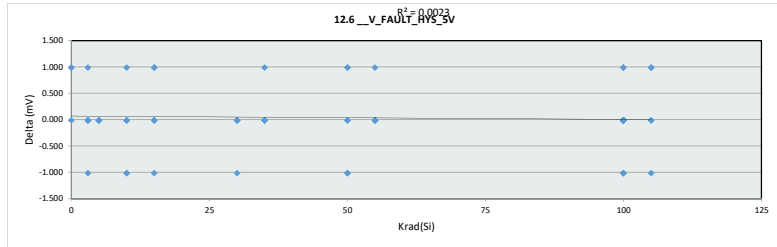
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	101.000	102.000	102.000	102.000	101.000	101.000	102.000	102.000	101.000	101.000
Average	102.000	102.000	102.167	102.167	102.500	102.167	102.000	102.333	102.167	102.042	101.800
Max	102.000	103.000	103.000	103.000	103.000	103.000	103.000	103.000	103.000	103.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



LDR TID Report
TPS7H5001-SP

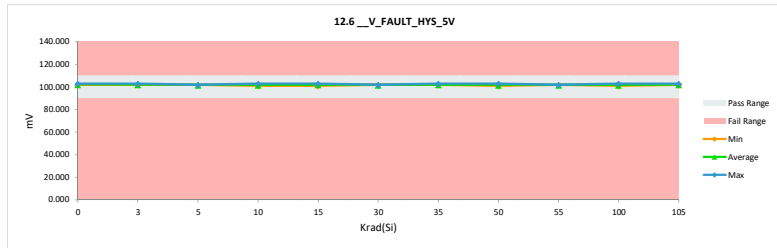
12.6_V_FAULT_HYS_5V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	110
Min Limit	90

Krad(Si)	Serial #	PRE	POST	Delta
0	117	101.000	102.000	1.000
0	118	102.000	103.000	1.000
0	122	102.000	102.000	0.000
3	61	103.000	102.000	-1.000
3	62	102.000	102.000	0.000
3	63	102.000	102.000	0.000
3	64	102.000	102.000	0.000
3	65	102.000	102.000	0.000
3	66	102.000	103.000	1.000
5	67	102.000	102.000	0.000
5	68	102.000	102.000	0.000
5	69	102.000	102.000	0.000
5	70	102.000	102.000	0.000
5	71	102.000	102.000	0.000
5	72	102.000	102.000	0.000
10	73	103.000	102.000	-1.000
10	74	102.000	102.000	0.000
10	75	102.000	101.000	-1.000
10	76	102.000	102.000	0.000
10	77	102.000	102.000	0.000
10	78	102.000	103.000	1.000
15	79	102.000	103.000	1.000
15	80	102.000	102.000	0.000
15	81	102.000	103.000	1.000
15	82	102.000	102.000	0.000
15	83	102.000	101.000	-1.000
15	84	102.000	102.000	0.000
30	86	103.000	102.000	-1.000
30	89	102.000	102.000	0.000
30	91	102.000	102.000	0.000
30	92	102.000	102.000	0.000
30	94	102.000	102.000	0.000
30	95	102.000	102.000	0.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	102.000	0.000
35	104	102.000	102.000	0.000
35	105	102.000	103.000	1.000
50	106	103.000	102.000	-1.000
50	107	101.000	102.000	1.000
50	108	102.000	102.000	0.000
50	109	102.000	101.000	-1.000
50	110	102.000	102.000	0.000
50	111	102.000	103.000	1.000
55	113	102.000	102.000	0.000
55	114	102.000	102.000	0.000
55	115	102.000	102.000	0.000
55	116	102.000	102.000	0.000
55	126	101.000	102.000	1.000
55	128	102.000	102.000	0.000
100	129	101.000	101.000	0.000
100	131	102.000	102.000	0.000
100	132	102.000	102.000	0.000
100	133	102.000	102.000	0.000
100	134	102.000	102.000	0.000
100	135	102.000	102.000	0.000
100	138	102.000	102.000	0.000
100	139	102.000	102.000	0.000
100	140	102.000	102.000	0.000
100	141	102.000	101.000	-1.000
100	142	102.000	102.000	0.000
100	143	101.000	102.000	1.000
100	144	102.000	102.000	0.000
100	220	101.000	101.000	0.000
100	221	102.000	102.000	0.000
100	224	102.000	102.000	0.000
100	225	103.000	102.000	-1.000
100	226	102.000	102.000	0.000
100	228	102.000	101.000	-1.000
100	232	102.000	102.000	0.000
100	233	102.000	102.000	0.000
100	236	102.000	103.000	1.000
100	238	102.000	102.000	0.000
100	243	102.000	102.000	0.000
105	244	102.000	102.000	0.000
105	246	102.000	102.000	0.000
105	248	103.000	102.000	-1.000
105	374	102.000	103.000	1.000
105	375	101.000	102.000	1.000
105	Max	103.000	103.000	1.000
105	Average	101.988	102.025	0.038
105	Min	101.000	101.000	-1.000
105	Std Dev	0.405	0.449	0.561



12.6_V_FAULT_HYS_5V	
Test Site	
Tester	
Test Number	
Max Limit	110 mV
Min Limit	90 mV

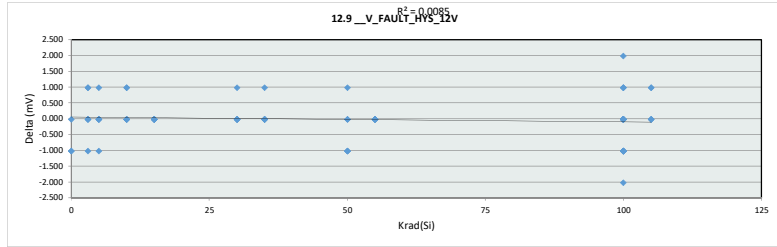
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	102.000	102.000	101.000	101.000	102.000	102.000	101.000	102.000	101.000	102.000
Average	102.333	102.167	102.000	102.000	102.167	102.000	102.167	102.000	102.000	101.875	102.200
Max	103.000	103.000	102.000	103.000	103.000	102.000	103.000	103.000	102.000	103.000	103.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



LDR TID Report
TPS7H5001-SP

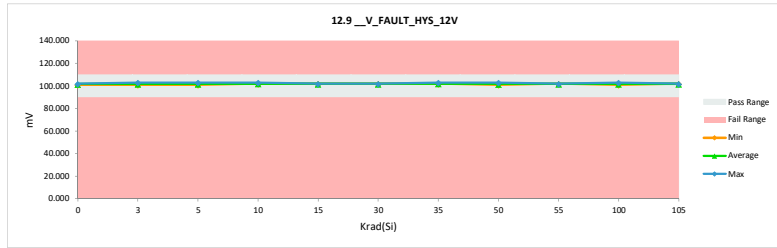
12.9_V_FAULT_HYS_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	110
Min Limit	90

Krad(Si)	Serial #	PRE	POST	Delta
0	117	102.000	102.000	0.000
0	118	103.000	102.000	-1.000
0	122	102.000	101.000	-1.000
3	61	102.000	102.000	0.000
3	62	102.000	102.000	0.000
3	63	101.000	102.000	1.000
3	64	102.000	102.000	0.000
3	65	102.000	101.000	-1.000
3	66	102.000	103.000	1.000
5	67	102.000	103.000	1.000
5	68	102.000	102.000	0.000
5	69	102.000	101.000	-1.000
5	70	102.000	102.000	0.000
5	71	102.000	102.000	0.000
5	72	102.000	102.000	0.000
10	73	102.000	102.000	0.000
10	74	102.000	102.000	0.000
10	75	101.000	102.000	1.000
10	76	102.000	102.000	0.000
10	77	102.000	102.000	0.000
10	78	102.000	103.000	1.000
15	79	102.000	102.000	0.000
15	80	102.000	102.000	0.000
15	81	102.000	102.000	0.000
15	82	102.000	102.000	0.000
15	83	102.000	102.000	0.000
15	84	102.000	102.000	0.000
30	86	102.000	102.000	0.000
30	89	102.000	102.000	0.000
30	91	101.000	102.000	1.000
30	92	102.000	102.000	0.000
30	94	102.000	102.000	0.000
30	95	102.000	102.000	0.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	102.000	0.000
35	104	102.000	102.000	0.000
35	105	102.000	103.000	1.000
50	106	103.000	102.000	-1.000
50	107	101.000	101.000	0.000
50	108	102.000	102.000	0.000
50	109	102.000	101.000	-1.000
50	110	102.000	103.000	1.000
50	111	102.000	101.000	-1.000
55	113	102.000	102.000	0.000
55	114	102.000	102.000	0.000
55	115	102.000	102.000	0.000
55	116	102.000	102.000	0.000
55	126	102.000	102.000	0.000
55	128	102.000	102.000	0.000
100	129	102.000	101.000	-1.000
100	131	102.000	102.000	0.000
100	132	102.000	102.000	0.000
100	133	102.000	101.000	-1.000
100	134	102.000	101.000	-1.000
100	135	102.000	102.000	0.000
100	138	102.000	101.000	-1.000
100	139	101.000	102.000	1.000
100	140	102.000	102.000	0.000
100	141	102.000	101.000	-1.000
100	142	103.000	102.000	-1.000
100	143	101.000	102.000	1.000
100	144	102.000	103.000	1.000
100	220	102.000	102.000	0.000
100	221	102.000	102.000	0.000
100	224	102.000	102.000	0.000
100	225	101.000	103.000	2.000
100	226	102.000	102.000	0.000
100	228	102.000	102.000	0.000
100	232	102.000	102.000	0.000
100	233	103.000	102.000	-1.000
100	236	103.000	102.000	-1.000
100	238	103.000	101.000	-2.000
100	243	102.000	102.000	0.000
105	244	102.000	102.000	0.000
105	246	102.000	102.000	0.000
105	248	102.000	102.000	0.000
105	374	101.000	102.000	1.000
105	376	101.000	102.000	1.000
105	Max	103.000	103.000	2.000
105	Average	101.963	101.938	-0.025
105	Min	101.000	101.000	-2.000
105	Std Dev	0.434	0.486	0.675



12.9_V_FAULT_HYS_12V	
Test Site	
Tester	
Test Number	
Max Limit	110 mV
Min Limit	90 mV

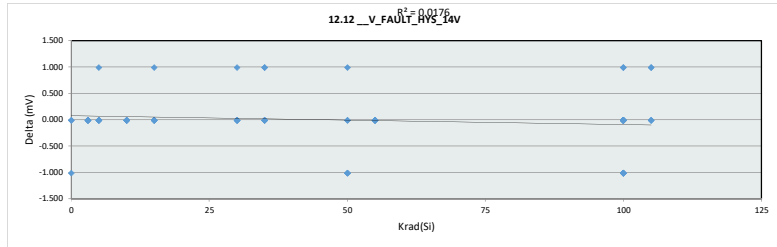
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	101.000	101.000	101.000	102.000	102.000	102.000	102.000	101.000	102.000	101.000	102.000
Average	101.667	102.000	102.000	102.167	102.000	102.000	102.167	101.667	102.000	101.833	102.000
Max	102.000	103.000	103.000	103.000	102.000	102.000	103.000	103.000	102.000	103.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



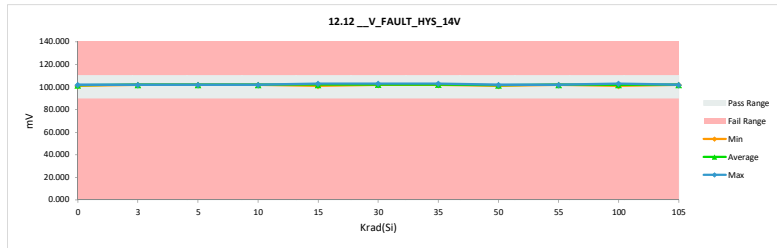
LDR TID Report
TPS7H5001-SP

12.12_V_FAULT_HYS_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	110
Min Limit	90

Krad(Si)	Serial #	PRE	POST	Delta
0	117	102.000	102.000	0.000
0	118	102.000	101.000	-1.000
0	122	102.000	102.000	0.000
3	61	102.000	102.000	0.000
3	62	102.000	102.000	0.000
3	63	102.000	102.000	0.000
3	64	102.000	102.000	0.000
3	65	102.000	102.000	0.000
3	66	102.000	102.000	0.000
5	67	102.000	102.000	0.000
5	68	102.000	102.000	0.000
5	69	102.000	102.000	0.000
5	70	102.000	102.000	0.000
5	71	101.000	102.000	1.000
5	72	102.000	102.000	0.000
10	73	102.000	102.000	0.000
10	74	102.000	102.000	0.000
10	75	102.000	102.000	0.000
10	76	102.000	102.000	0.000
10	77	102.000	102.000	0.000
10	78	102.000	102.000	0.000
15	79	102.000	103.000	1.000
15	80	102.000	102.000	0.000
15	81	102.000	102.000	0.000
15	82	102.000	102.000	0.000
15	83	101.000	101.000	0.000
15	84	102.000	102.000	0.000
30	86	102.000	103.000	1.000
30	89	102.000	102.000	0.000
30	91	102.000	102.000	0.000
30	92	102.000	102.000	0.000
30	94	102.000	102.000	0.000
30	95	102.000	102.000	0.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	102.000	0.000
35	104	101.000	102.000	1.000
35	105	102.000	103.000	1.000
50	106	103.000	102.000	-1.000
50	107	101.000	102.000	1.000
50	108	102.000	101.000	-1.000
50	109	102.000	101.000	-1.000
50	110	102.000	102.000	0.000
50	111	102.000	102.000	0.000
55	113	102.000	102.000	0.000
55	114	102.000	102.000	0.000
55	115	102.000	102.000	0.000
55	116	102.000	102.000	0.000
55	126	102.000	102.000	0.000
55	128	102.000	102.000	0.000
100	129	102.000	102.000	0.000
100	131	102.000	102.000	0.000
100	132	102.000	103.000	1.000
100	133	103.000	102.000	-1.000
100	134	102.000	102.000	0.000
100	135	102.000	102.000	0.000
100	138	102.000	101.000	-1.000
100	139	102.000	102.000	0.000
100	140	102.000	102.000	0.000
100	141	102.000	102.000	0.000
100	142	102.000	102.000	0.000
100	143	102.000	102.000	0.000
100	144	103.000	102.000	-1.000
100	220	102.000	102.000	0.000
100	221	102.000	102.000	0.000
100	224	103.000	102.000	-1.000
100	225	102.000	101.000	-1.000
100	226	102.000	101.000	-1.000
100	228	102.000	101.000	-1.000
100	232	102.000	102.000	0.000
100	233	102.000	102.000	0.000
100	236	102.000	102.000	0.000
100	238	101.000	102.000	1.000
100	243	102.000	102.000	0.000
105	244	101.000	102.000	1.000
105	246	102.000	102.000	0.000
105	248	102.000	102.000	0.000
105	374	102.000	102.000	0.000
105	376	101.000	102.000	1.000
105	Max	103.000	103.000	1.000
105	Average	101.963	101.950	-0.013
105	Min	101.000	101.000	-1.000
105	Std Dev	0.371	0.386	0.515



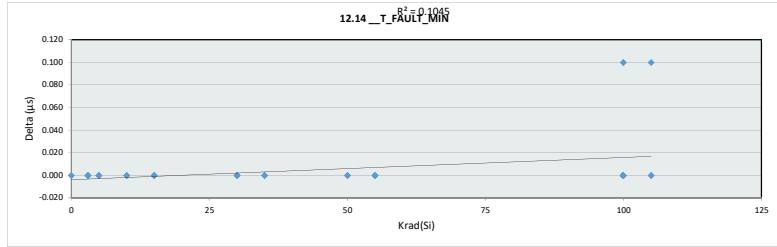
12.12_V_FAULT											
Test Site											
Tester											
Test Number											
Max Limit	110 mV										
Min Limit	90 mV										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	101.000	102.000	102.000	102.000	101.000	102.000	102.000	101.000	102.000	101.000	102.000
Average	101.667	102.000	102.000	102.000	102.000	102.167	102.167	101.667	102.000	101.875	102.000
Max	102.000	102.000	102.000	102.000	103.000	103.000	103.000	102.000	102.000	103.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



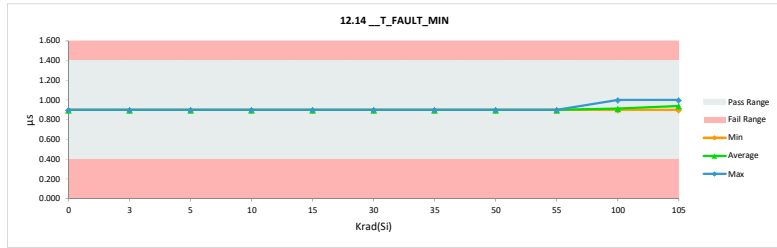
LDR TID Report
TPS7H5001-SP

12.14 T_FAULT_MIN	
Test Site	
Tester	
Test Number	
Unit	µs
Max Limit	1.4
Min Limit	0.4

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.900	0.900	0.000
0	118	0.900	0.900	0.000
0	122	0.900	0.900	0.000
3	61	0.900	0.900	0.000
3	62	0.900	0.900	0.000
3	63	0.900	0.900	0.000
3	64	0.900	0.900	0.000
3	65	0.900	0.900	0.000
3	66	0.900	0.900	0.000
5	67	0.900	0.900	0.000
5	68	0.900	0.900	0.000
5	69	0.900	0.900	0.000
5	70	0.900	0.900	0.000
5	71	0.900	0.900	0.000
5	72	0.900	0.900	0.000
10	73	0.900	0.900	0.000
10	74	0.900	0.900	0.000
10	75	0.900	0.900	0.000
10	76	0.900	0.900	0.000
10	77	0.900	0.900	0.000
10	78	0.900	0.900	0.000
15	79	0.900	0.900	0.000
15	80	0.900	0.900	0.000
15	81	0.900	0.900	0.000
15	82	0.900	0.900	0.000
15	83	0.900	0.900	0.000
15	84	0.900	0.900	0.000
30	86	0.900	0.900	0.000
30	89	0.900	0.900	0.000
30	91	0.900	0.900	0.000
30	92	0.900	0.900	0.000
30	94	0.900	0.900	0.000
30	95	0.900	0.900	0.000
35	100	0.900	0.900	0.000
35	101	0.900	0.900	0.000
35	102	0.900	0.900	0.000
35	103	0.900	0.900	0.000
35	104	0.900	0.900	0.000
35	105	0.900	0.900	0.000
50	106	0.900	0.900	0.000
50	107	0.900	0.900	0.000
50	108	0.900	0.900	0.000
50	109	0.900	0.900	0.000
50	110	0.900	0.900	0.000
50	111	0.900	0.900	0.000
55	113	0.900	0.900	0.000
55	114	0.900	0.900	0.000
55	115	0.900	0.900	0.000
55	116	0.900	0.900	0.000
55	126	0.900	0.900	0.000
55	128	0.900	0.900	0.000
100	129	0.900	0.900	0.000
100	131	0.900	0.900	0.000
100	132	0.900	0.900	0.000
100	133	0.900	1.000	0.100
100	134	0.900	0.900	0.000
100	135	0.900	0.900	0.000
100	138	0.900	1.000	0.100
100	139	0.900	0.900	0.000
100	140	0.900	0.900	0.000
100	141	0.900	0.900	0.000
100	142	0.900	0.900	0.000
100	143	0.900	0.900	0.000
100	144	0.900	0.900	0.000
100	220	0.900	0.900	0.000
100	221	0.900	0.900	0.000
100	224	0.900	0.900	0.000
100	225	0.900	1.000	0.100
100	226	0.900	0.900	0.000
100	228	0.900	0.900	0.000
100	232	0.900	0.900	0.000
100	233	0.900	0.900	0.000
100	236	0.900	0.900	0.000
100	238	0.900	0.900	0.000
100	243	0.900	0.900	0.000
105	244	0.900	0.900	0.000
105	246	0.900	1.000	0.100
105	248	0.900	1.000	0.100
105	374	0.900	0.900	0.000
105	376	0.900	0.900	0.000
105	Max	0.900	1.000	0.100
	Average	0.900	0.906	0.006
	Min	0.900	0.900	0.000
	Std Dev	0.000	0.024	0.024



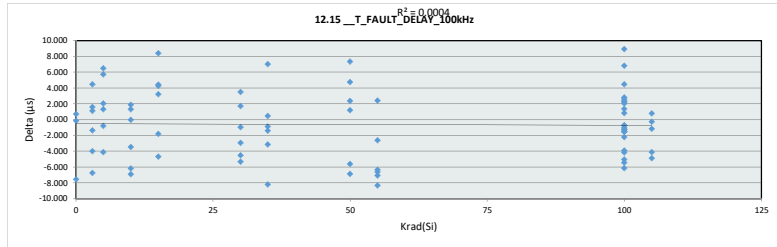
12.14 T_FAULT											
Test Site											
Tester											
Test Number											
Max Limit	1.4 µs										
Min Limit	0.4 µs										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400
Min	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900
Average	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.913	0.940
Max	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	1.000	1.000
UL	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400



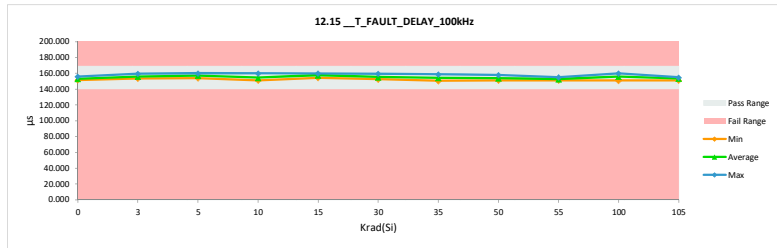
LDR TID Report
TPS7H5001-SP

		12.15 T_FAULT_DELAY_100kHz	
Test Site			
Tester			
Test Number			
Unit	µs	µs	
Max Limit	169	169	
Min Limit	140	140	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	156.143	156.056	-0.087
0	118	159.074	151.573	-7.501
0	122	151.484	152.229	0.745
3	61	156.932	155.615	-1.317
3	62	158.789	159.947	1.158
3	63	157.986	154.051	-3.935
3	64	151.448	155.983	4.535
3	65	156.182	157.851	1.669
3	66	160.282	153.618	-6.664
5	67	151.792	158.349	6.557
5	68	154.968	154.237	-0.731
5	69	158.714	160.092	1.378
5	70	154.510	160.288	5.778
5	71	159.183	155.113	-4.070
5	72	153.705	155.822	2.117
10	73	156.932	156.955	0.023
10	74	158.789	160.172	1.383
10	75	157.986	151.153	-6.833
10	76	151.448	153.366	1.918
10	77	156.182	152.786	-3.396
10	78	160.282	154.164	-6.118
15	79	151.792	160.258	8.466
15	80	154.968	159.295	4.327
15	81	158.714	156.952	-1.762
15	82	154.510	157.764	3.254
15	83	159.183	154.551	-4.632
15	84	153.705	158.177	4.472
30	86	156.932	154.070	-2.862
30	89	158.789	154.344	-4.445
30	91	157.986	152.738	-5.248
30	92	151.448	153.204	1.756
30	94	156.182	159.721	3.539
30	95	160.282	159.368	-0.914
35	100	151.792	158.873	7.081
35	101	154.968	155.493	0.525
35	102	158.714	157.898	-0.816
35	103	154.510	153.189	-1.321
35	104	159.183	151.038	-8.145
35	105	153.705	150.633	-3.072
50	106	158.671	151.863	-6.808
50	107	150.844	155.662	4.818
50	108	156.731	151.185	-5.546
50	109	151.456	153.882	2.426
50	110	150.688	158.091	7.403
50	111	152.919	154.157	1.238
55	113	157.832	151.572	-6.260
55	114	158.852	151.858	-6.994
55	115	157.847	155.276	-2.571
55	116	152.416	154.863	2.447
55	126	159.618	153.048	-6.570
55	128	159.685	151.431	-8.254
100	129	159.530	158.555	-0.975
100	131	158.720	154.857	-3.863
100	132	156.479	155.007	-1.472
100	133	152.354	151.192	-1.162
100	134	153.883	152.446	-1.437
100	135	153.011	157.528	4.517
100	138	151.345	158.222	6.877
100	139	157.626	156.962	-0.664
100	140	155.256	157.550	2.294
100	141	153.392	154.290	0.898
100	142	151.035	160.028	8.993
100	143	155.673	154.489	-1.184
100	144	153.909	152.443	-1.466
100	220	160.301	154.222	-6.079
100	221	158.395	153.020	-5.375
100	224	157.600	160.016	2.416
100	225	158.006	160.123	2.117
100	226	156.717	159.604	2.887
100	228	159.064	156.919	-2.145
100	232	150.810	153.462	2.652
100	233	158.394	159.793	1.399
100	236	156.087	151.104	-4.983
100	238	158.605	157.912	-0.693
100	243	159.534	155.424	-4.110
105	244	154.325	155.179	0.854
105	246	156.249	151.434	-4.815
105	248	154.956	154.761	-0.195
105	374	159.035	154.997	-4.038
105	375	154.127	153.017	-1.110
105	Max	160.301	160.288	8.993
105	Average	156.027	155.430	-0.597
105	Min	150.688	150.633	-8.254
105	Std Dev	2.934	2.885	4.207



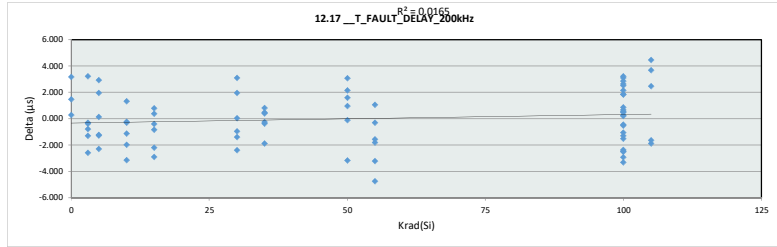
		12.15 T_FAULT										
Test Site												
Tester												
Test Number												
Max Limit	169	µs										
Min Limit	140	µs										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	
Min	151.573	153.618	154.237	151.153	154.551	152.738	150.633	151.185	151.431	151.104	151.434	
Average	153.286	156.178	157.317	154.766	157.833	155.574	154.521	154.140	153.008	156.049	153.878	
Max	156.056	159.947	160.288	160.172	160.258	159.721	158.873	158.091	155.276	160.123	155.179	
UL	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	



LDR TID Report
TPS7H5001-SP

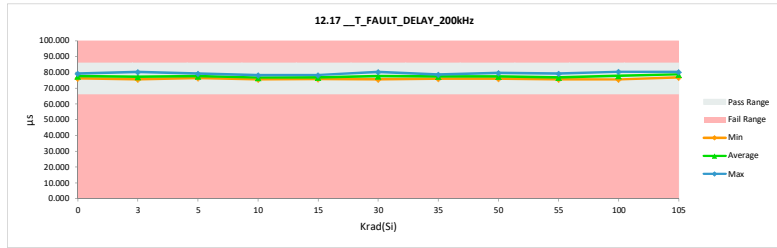
12.17 T_FAULT_DELAY_200kHz	
Test Site	
Tester	
Test Number	
Unit	µs
Max Limit	86
Min Limit	66

Krad(Si)	Serial #	PRE	POST	Delta
0	117	76.024	79.236	3.212
0	118	75.738	77.243	1.505
0	122	75.955	76.263	0.308
3	61	77.634	76.356	-1.278
3	62	78.253	75.702	-2.551
3	63	76.991	80.247	3.256
3	64	76.031	75.684	-0.347
3	65	76.345	75.597	-0.748
3	66	80.256	79.979	-0.277
5	67	76.314	79.272	2.958
5	68	77.920	78.091	0.171
5	69	76.563	78.563	2.000
5	70	79.071	76.803	-2.268
5	71	77.758	76.512	-1.246
5	72	77.981	76.787	-1.194
10	73	77.634	75.681	-1.953
10	74	78.253	77.150	-1.103
10	75	76.991	78.357	1.366
10	76	76.031	75.760	-0.271
10	77	76.345	76.143	-0.202
10	78	80.256	77.139	-3.117
15	79	76.314	77.136	0.822
15	80	77.920	78.333	0.413
15	81	76.563	76.186	-0.377
15	82	79.071	76.213	-2.858
15	83	77.758	76.956	-0.802
15	84	77.981	75.813	-2.168
30	86	77.634	76.696	-0.938
30	89	78.253	80.229	1.976
30	91	76.991	75.628	-1.363
30	92	76.031	79.157	3.126
30	94	76.345	76.422	0.077
30	95	80.256	77.898	-2.358
35	100	76.314	76.103	-0.211
35	101	77.920	76.067	-1.853
35	102	76.563	77.412	0.849
35	103	79.071	78.736	-0.335
35	104	77.758	78.275	0.517
35	105	77.981	78.407	0.426
50	106	76.126	78.320	2.194
50	107	76.178	76.112	-0.066
50	108	75.739	76.721	0.982
50	109	76.659	79.763	3.104
50	110	75.674	77.292	1.618
50	111	79.271	76.130	-3.141
55	113	78.894	75.700	-3.194
55	114	79.548	79.285	-0.263
55	115	78.432	76.903	-1.529
55	116	78.081	76.295	-1.786
55	126	80.422	75.700	-4.722
55	128	76.158	77.250	1.092
100	129	79.056	77.581	-1.475
100	131	76.518	79.780	3.262
100	132	77.120	75.847	-1.273
100	133	75.773	76.469	0.696
100	134	78.090	78.372	0.282
100	135	76.534	78.726	2.192
100	138	78.036	77.535	-0.501
100	139	76.750	76.330	-0.420
100	140	78.159	80.060	1.901
100	141	80.463	77.176	-3.287
100	142	76.838	75.807	-1.031
100	143	77.498	78.078	0.580
100	144	79.988	77.558	-2.430
100	220	78.921	79.423	0.502
100	221	77.287	79.146	1.859
100	224	77.185	79.720	2.535
100	225	79.691	77.212	-2.479
100	226	76.238	77.144	0.906
100	228	77.318	80.453	3.135
100	232	75.636	75.884	0.248
100	233	78.075	75.746	-2.329
100	236	78.451	75.569	-2.882
100	238	76.571	79.237	2.666
100	243	75.494	78.388	2.894
105	244	77.119	79.611	2.492
105	246	78.618	77.010	-1.608
105	248	76.175	79.889	3.714
105	374	75.807	80.286	4.479
105	376	78.693	76.840	-1.853
105	Max	80.463	80.453	4.479
105	Average	77.504	77.507	0.003
105	Min	75.494	75.569	-4.722
105	Std Dev	1.335	1.475	2.020



12.17 T_FAULT	
Test Site	
Tester	
Test Number	
Max Limit	86 µs
Min Limit	66 µs

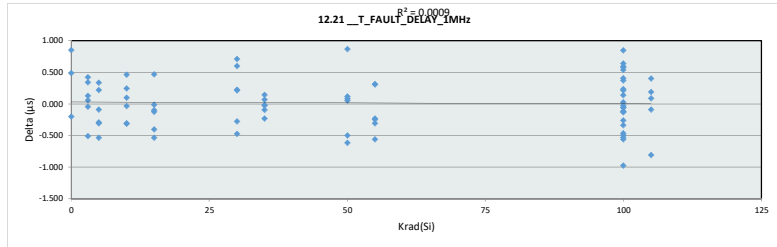
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
Min	76.263	75.597	76.512	75.681	75.813	75.628	76.067	76.112	75.700	75.569	76.840
Average	77.581	77.261	77.671	76.705	76.773	77.672	77.500	77.390	76.856	77.802	78.727
Max	79.236	80.247	79.272	78.357	78.333	80.229	78.736	79.763	79.285	80.453	80.286
UL	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000



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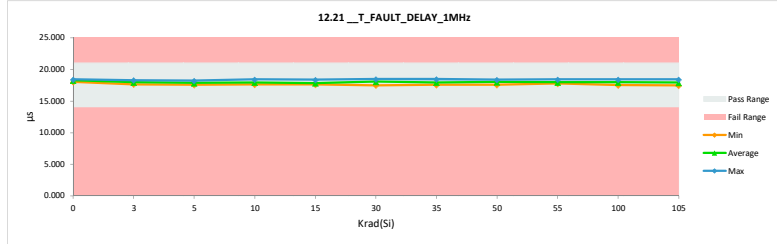
		12.21 T_FAULT_DELAY_1MHz	
Test Site			
Tester			
Test Number			
Unit		µs	µs
Max Limit		21	21
Min Limit		14	14

Krad(Si)	Serial #	PRE	POST	Delta
0	117	17.570	18.429	0.859
0	118	17.881	18.375	0.494
0	122	18.212	18.019	-0.193
3	61	17.751	17.713	-0.038
3	62	17.738	18.166	0.428
3	63	17.654	18.002	0.348
3	64	17.996	18.061	0.065
3	65	18.145	17.640	-0.505
3	66	18.164	18.297	0.133
5	67	17.579	17.921	0.342
5	68	17.825	17.739	-0.086
5	69	17.998	18.223	0.225
5	70	18.392	18.106	-0.286
5	71	18.123	17.593	-0.530
5	72	17.856	17.557	-0.299
10	73	17.738	17.857	0.106
10	74	17.738	18.205	0.467
10	75	17.654	17.628	-0.026
10	76	17.996	17.688	-0.308
10	77	18.145	17.843	-0.302
10	78	18.164	18.416	0.252
15	79	17.579	18.054	0.475
15	80	17.825	17.729	-0.096
15	81	17.998	17.599	-0.399
15	82	18.392	18.384	-0.008
15	83	18.123	17.595	-0.528
15	84	17.856	17.739	-0.117
30	86	17.751	17.481	-0.270
30	89	17.738	18.454	0.716
30	91	17.654	18.257	0.603
30	92	17.996	18.217	0.221
30	94	18.145	17.675	-0.470
30	95	18.164	18.393	0.229
35	100	17.579	17.565	-0.014
35	101	17.825	17.734	-0.091
35	102	17.998	18.146	0.148
35	103	18.392	18.463	0.071
35	104	18.123	18.105	-0.018
35	105	17.856	17.629	-0.227
50	106	18.470	17.859	-0.611
50	107	18.134	18.224	0.090
50	108	18.057	18.179	0.122
50	109	17.500	18.370	0.870
50	110	17.508	17.564	0.056
50	111	18.435	17.943	-0.492
55	113	18.317	17.762	-0.555
55	114	18.096	18.411	0.315
55	115	18.127	17.884	-0.243
55	116	18.319	18.092	-0.227
55	126	18.170	17.868	-0.302
55	128	17.556	17.873	0.317
100	129	18.391	18.423	0.032
100	131	17.500	18.351	0.851
100	132	17.567	18.152	0.585
100	133	18.306	17.780	-0.526
100	134	18.439	18.328	-0.111
100	135	18.111	17.988	-0.123
100	138	17.550	17.792	0.242
100	139	18.393	18.062	-0.331
100	140	18.322	17.767	-0.555
100	141	17.554	17.499	-0.055
100	142	18.088	17.831	-0.257
100	143	17.568	17.982	0.414
100	144	17.745	17.726	-0.019
100	220	18.369	18.323	-0.046
100	221	17.642	18.187	0.545
100	224	17.564	17.708	0.144
100	225	17.507	18.154	0.647
100	226	18.195	17.739	-0.456
100	228	17.810	18.408	0.598
100	232	18.209	17.719	-0.490
100	233	17.746	18.125	0.379
100	236	18.464	17.494	-0.970
100	238	17.821	17.703	-0.118
100	243	17.658	17.877	0.219
105	244	17.861	17.776	-0.085
105	246	17.844	18.038	0.194
105	248	18.276	17.475	-0.801
105	374	17.625	17.919	0.094
105	376	18.001	18.409	0.408
105	Max	18.470	18.463	0.870
105	Average	17.954	17.968	0.014
105	Min	17.500	17.475	-0.970
105	Std Dev	0.292	0.293	0.398



		12.21 T_FAULT	
Test Site			
Tester			
Test Number			
Max Limit		21	µs
Min Limit		14	µs

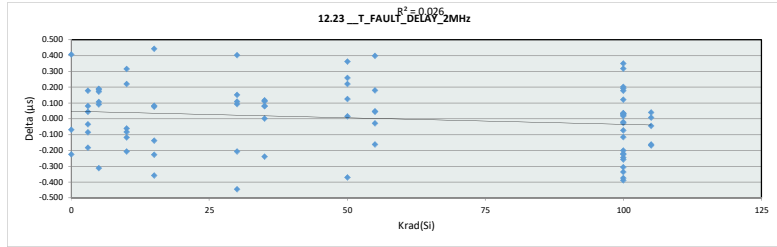
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000
Min	18.019	17.640	17.557	17.628	17.595	17.481	17.565	17.564	17.762	17.494	17.475
Average	18.274	17.980	17.857	17.940	17.850	18.080	17.940	18.023	17.982	17.963	17.923
Max	18.429	18.297	18.223	18.416	18.384	18.454	18.463	18.370	18.411	18.423	18.409
UL	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000



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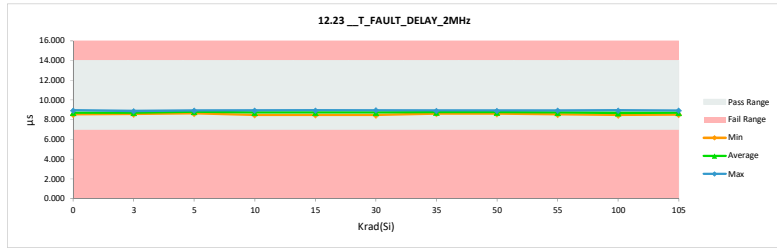
12.23 T_FAULT_DELAY_2MHz	
Test Site	
Tester	
Test Number	
Unit	µs
Max Limit	14
Min Limit	7

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.722	8.654	-0.068
0	118	8.560	8.969	0.409
0	122	8.566	8.543	-0.223
3	61	8.587	8.634	0.047
3	62	8.569	8.652	0.083
3	63	8.923	8.891	-0.032
3	64	8.916	8.832	-0.084
3	65	8.565	8.745	0.180
3	66	8.746	8.566	-0.180
5	67	8.511	8.703	0.192
5	68	8.939	8.630	-0.309
5	69	8.740	8.913	0.173
5	70	8.677	8.863	0.186
5	71	8.853	8.945	0.092
5	72	8.758	8.866	0.108
10	73	8.587	8.528	-0.059
10	74	8.569	8.487	-0.082
10	75	8.923	8.808	-0.115
10	76	8.916	8.711	-0.205
10	77	8.565	8.882	0.317
10	78	8.746	8.969	0.223
15	79	8.511	8.955	0.444
15	80	8.939	8.714	-0.225
15	81	8.740	8.604	-0.136
15	82	8.677	8.762	0.085
15	83	8.853	8.497	-0.356
15	84	8.758	8.837	0.079
30	86	8.587	8.699	0.112
30	89	8.569	8.974	0.405
30	91	8.923	8.718	-0.205
30	92	8.916	8.473	-0.443
30	94	8.565	8.660	0.095
30	95	8.746	8.899	0.153
35	100	8.511	8.631	0.120
35	101	8.939	8.702	-0.237
35	102	8.740	8.854	0.114
35	103	8.677	8.680	0.003
35	104	8.853	8.936	0.083
35	105	8.758	8.840	0.082
50	106	8.529	8.894	0.365
50	107	8.971	8.602	-0.369
50	108	8.701	8.923	0.222
50	109	8.496	8.756	0.260
50	110	8.725	8.742	0.017
50	111	8.513	8.641	0.128
55	113	8.801	8.641	-0.160
55	114	8.561	8.962	0.401
55	115	8.485	8.534	0.049
55	116	8.548	8.596	0.048
55	126	8.779	8.753	-0.026
55	128	8.708	8.890	0.182
100	129	8.872	8.759	-0.113
100	131	8.891	8.693	-0.198
100	132	8.910	8.885	-0.025
100	133	8.619	8.652	0.033
100	134	8.833	8.498	-0.335
100	135	8.902	8.646	-0.256
100	138	8.934	8.631	-0.303
100	139	8.964	8.745	-0.219
100	140	8.737	8.775	0.038
100	141	8.599	8.918	0.319
100	142	8.865	8.621	-0.244
100	143	8.564	8.601	0.037
100	144	8.534	8.560	0.026
100	220	8.577	8.756	0.179
100	221	8.558	8.762	0.204
100	224	8.846	8.474	-0.372
100	225	8.492	8.844	0.352
100	226	8.706	8.634	-0.072
100	228	8.976	8.958	-0.018
100	232	8.480	8.603	0.123
100	233	8.830	8.607	-0.223
100	236	8.687	8.707	0.020
100	238	8.942	8.556	-0.386
100	243	8.674	8.867	0.193
105	244	8.953	8.786	-0.167
105	246	8.681	8.639	-0.042
105	248	8.882	8.722	-0.160
105	374	8.897	8.940	0.043
105	376	8.517	8.527	0.010
	Max	8.976	8.974	0.444
	Average	8.727	8.732	0.005
	Min	8.480	8.473	-0.443
	Std Dev	0.156	0.143	0.210



12.23 T_FAULT	
Test Site	
Tester	
Test Number	
Max Limit	14 µs
Min Limit	7 µs

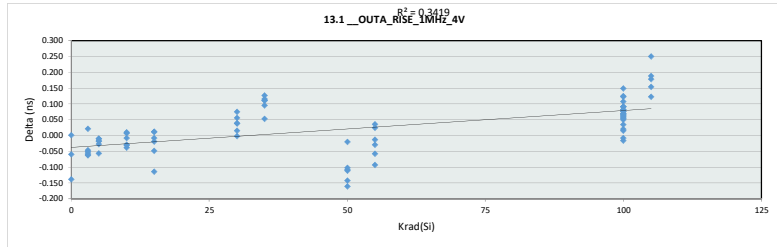
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000
Min	8.543	8.566	8.630	8.487	8.497	8.473	8.631	8.602	8.534	8.474	8.527
Average	8.722	8.720	8.820	8.731	8.728	8.737	8.774	8.760	8.729	8.698	8.723
Max	8.969	8.891	8.945	8.969	8.955	8.974	8.936	8.923	8.962	8.958	8.940
UL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000



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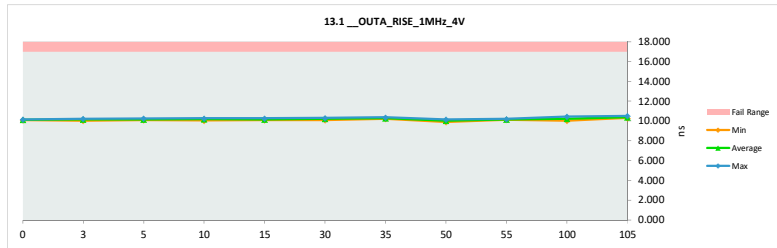
13.1 OUTA_RISE_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.240	10.103	-0.137
0	118	10.173	10.175	0.002
0	122	10.176	10.118	-0.058
3	61	10.269	10.224	-0.045
3	62	10.279	10.230	-0.049
3	63	10.245	10.184	-0.061
3	64	10.098	10.039	-0.059
3	65	10.220	10.166	-0.054
3	66	10.139	10.162	0.023
5	67	10.162	10.145	-0.017
5	68	10.274	10.248	-0.026
5	69	10.210	10.194	-0.016
5	70	10.183	10.128	-0.055
5	71	10.280	10.271	-0.009
5	72	10.109	10.100	-0.009
10	73	10.269	10.280	0.011
10	74	10.279	10.287	0.008
10	75	10.245	10.216	-0.029
10	76	10.098	10.061	-0.037
10	77	10.220	10.192	-0.028
10	78	10.139	10.132	-0.007
15	79	10.162	10.155	-0.007
15	80	10.274	10.287	0.013
15	81	10.210	10.097	-0.113
15	82	10.183	10.136	-0.047
15	83	10.280	10.262	-0.018
15	84	10.109	10.121	0.012
30	86	10.269	10.309	0.040
30	89	10.279	10.278	-0.001
30	91	10.245	10.321	0.076
30	92	10.098	10.114	0.016
30	94	10.220	10.260	0.040
30	95	10.139	10.196	0.057
35	100	10.162	10.290	0.128
35	101	10.274	10.386	0.112
35	102	10.210	10.323	0.113
35	103	10.183	10.237	0.054
35	104	10.280	10.376	0.096
35	105	10.109	10.226	0.117
50	106	10.152	10.011	-0.141
50	107	10.014	9.904	-0.110
50	108	10.197	10.178	-0.019
50	109	10.195	10.035	-0.160
50	110	10.230	10.129	-0.101
50	111	10.142	10.035	-0.107
55	113	10.188	10.176	-0.012
55	114	10.275	10.219	-0.056
55	115	10.229	10.201	-0.028
55	116	10.233	10.141	-0.092
55	126	10.155	10.181	0.026
55	128	10.140	10.177	0.037
100	129	10.101	10.184	0.083
100	131	10.120	10.197	0.077
100	132	10.098	10.175	0.077
100	133	10.179	10.261	0.082
100	134	10.068	10.053	-0.015
100	135	10.200	10.236	0.036
100	138	10.108	10.175	0.067
100	139	10.087	10.179	0.092
100	140	10.218	10.269	0.051
100	141	10.223	10.216	-0.007
100	142	10.242	10.303	0.061
100	143	10.313	10.463	0.150
100	144	10.115	10.241	0.126
100	220	10.183	10.292	0.109
100	221	10.151	10.221	0.070
100	224	10.207	10.331	0.124
100	225	10.157	10.173	0.016
100	226	10.262	10.318	0.056
100	228	10.227	10.249	0.022
100	232	10.057	10.149	0.092
100	233	10.209	10.273	0.064
100	236	10.262	10.319	0.057
100	238	10.114	10.184	0.070
100	243	10.195	10.287	0.092
105	244	10.297	10.476	0.179
105	246	10.159	10.314	0.155
105	248	10.190	10.380	0.190
105	374	10.270	10.521	0.251
105	376	10.203	10.327	0.124
105	375	10.313	10.521	0.251
Max				
Average		10.192	10.215	0.023
Min		10.014	9.904	-0.160
Std Dev		0.066	0.106	0.081



13.1 OUTA_RISE	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns
Krad(Si)	0
LL	3
Min	10.103
Average	10.132
Max	10.175
UL	17.000

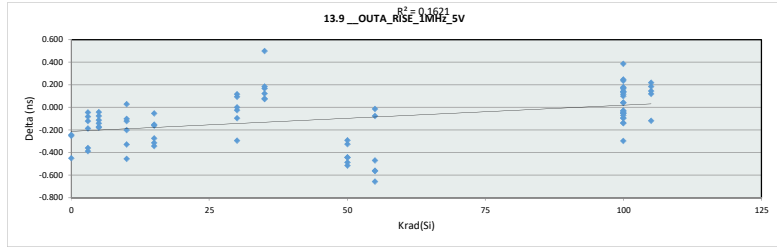
	3	5	10	15	30	35	50	55	100	105	
Min	10.103	10.039	10.100	10.061	10.097	10.114	10.226	9.904	10.141	10.053	10.314
Average	10.132	10.168	10.181	10.195	10.176	10.246	10.306	10.049	10.183	10.240	10.404
Max	10.175	10.230	10.271	10.287	10.287	10.321	10.386	10.178	10.219	10.463	10.521
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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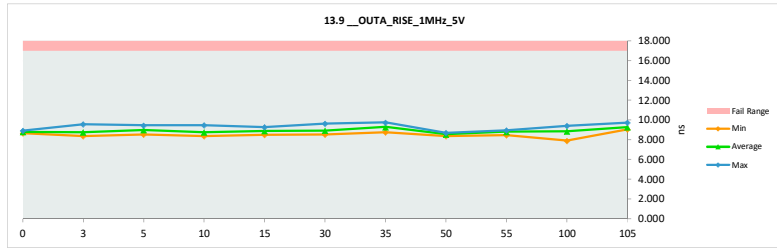
13.9_OUTA_RISE_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.283	8.837	-0.446
0	118	8.909	8.660	-0.249
0	122	9.163	8.923	-0.240
3	61	9.934	9.580	-0.354
3	62	8.427	8.385	-0.042
3	63	8.843	8.660	-0.183
3	64	8.801	8.683	-0.118
3	65	8.463	8.386	-0.077
3	66	9.369	8.986	-0.383
5	67	8.772	8.664	-0.108
5	68	9.638	9.465	-0.173
5	69	9.347	9.276	-0.071
5	70	8.585	8.546	-0.039
5	71	9.246	9.109	-0.137
5	72	9.069	8.902	-0.167
10	73	9.934	9.482	-0.452
10	74	8.427	8.459	0.032
10	75	8.843	8.645	-0.198
10	76	8.801	8.685	-0.116
10	77	8.463	8.365	-0.098
10	78	9.369	9.044	-0.325
15	79	8.772	8.501	-0.271
15	80	9.638	9.300	-0.338
15	81	9.347	9.198	-0.149
15	82	8.585	8.537	-0.048
15	83	9.246	8.935	-0.311
15	84	9.069	8.912	-0.157
30	86	9.934	9.645	-0.289
30	89	8.427	8.547	0.120
30	91	8.843	8.821	-0.022
30	92	8.801	8.805	0.004
30	94	8.463	8.560	0.097
30	95	9.369	9.277	-0.092
35	100	8.772	8.853	0.081
35	101	9.638	9.715	0.077
35	102	9.347	9.474	0.127
35	103	8.585	8.758	0.173
35	104	9.246	9.748	0.502
35	105	9.069	9.259	0.190
50	106	9.213	8.703	-0.510
50	107	8.664	8.376	-0.288
50	108	9.095	8.612	-0.483
50	109	8.945	8.624	-0.321
50	110	8.829	8.390	-0.439
50	111	9.037	8.600	-0.437
55	113	9.250	8.784	-0.466
55	114	9.565	8.912	-0.653
55	115	9.386	8.830	-0.556
55	116	9.505	8.945	-0.560
55	126	8.482	8.473	-0.009
55	128	9.036	8.965	-0.071
100	129	8.367	8.617	0.250
100	131	8.990	8.897	-0.093
100	132	8.790	8.765	-0.025
100	133	9.114	8.980	-0.134
100	134	8.798	8.752	-0.046
100	135	9.175	9.127	-0.048
100	138	8.850	8.955	0.105
100	139	8.010	8.249	0.239
100	140	9.030	8.964	-0.066
100	141	9.452	9.158	-0.294
100	142	8.807	8.989	0.182
100	143	9.060	9.228	0.168
100	144	7.856	7.900	0.044
100	220	8.953	9.075	0.122
100	221	8.794	8.705	-0.089
100	224	8.576	8.965	0.389
100	225	9.199	9.156	-0.043
100	226	9.282	9.424	0.142
100	228	9.113	9.252	0.139
100	232	8.589	8.636	0.047
100	233	8.901	8.872	-0.029
100	236	8.882	9.027	0.145
100	238	9.132	8.997	-0.135
100	243	8.216	8.392	0.176
105	244	9.545	9.735	0.190
105	246	9.038	9.187	0.149
105	248	8.833	9.057	0.224
105	374	9.229	9.115	-0.114
105	376	9.249	9.372	0.123
105	Max	9.934	9.748	0.502
105	Average	8.996	8.904	-0.092
105	Min	7.856	7.900	-0.653
105	Std Dev	0.415	0.372	0.234



13.9_OUTA_RISE	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

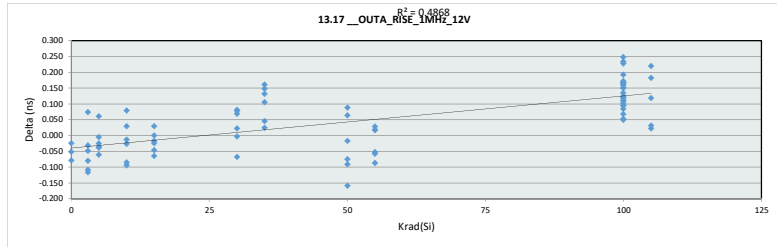
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.660	8.385	8.546	8.365	8.501	8.547	8.758	8.376	8.473	7.900	9.057
Average	8.807	8.780	8.994	8.780	8.897	8.943	9.301	8.551	8.818	8.878	9.293
Max	8.923	9.580	9.465	9.482	9.300	9.645	9.748	8.703	8.965	9.424	9.735
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



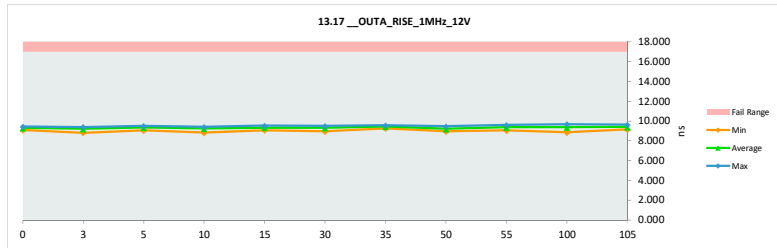
LDR TID Report
TPS7H5001-SP

13.17_OUTA_RISE_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.494	9.444	-0.050
0	118	9.091	9.068	-0.023
0	122	9.464	9.386	-0.078
3	61	9.508	9.393	-0.115
3	62	9.106	9.181	0.075
3	63	9.372	9.342	-0.030
3	64	9.253	9.174	-0.079
3	65	8.858	8.811	-0.047
3	66	9.501	9.394	-0.107
5	67	9.183	9.245	0.062
5	68	9.489	9.454	-0.035
5	69	9.544	9.507	-0.037
5	70	9.109	9.050	-0.059
5	71	9.257	9.233	-0.024
5	72	9.471	9.467	-0.004
10	73	9.508	9.415	-0.093
10	74	9.106	9.186	0.080
10	75	9.372	9.360	-0.012
10	76	9.253	9.284	0.031
10	77	8.858	8.833	-0.025
10	78	9.501	9.417	-0.084
15	79	9.183	9.214	0.031
15	80	9.489	9.473	-0.016
15	81	9.544	9.521	-0.023
15	82	9.109	9.064	-0.045
15	83	9.257	9.194	-0.063
15	84	9.471	9.472	0.001
30	86	9.508	9.442	-0.066
30	89	9.106	9.185	0.079
30	91	9.372	9.396	0.024
30	92	9.253	9.323	0.070
30	94	8.858	8.940	0.082
30	95	9.501	9.499	-0.002
35	100	9.183	9.316	0.133
35	101	9.489	9.536	0.047
35	102	9.544	9.570	0.026
35	103	9.109	9.258	0.149
35	104	9.257	9.419	0.162
35	105	9.471	9.578	0.107
50	106	9.433	9.343	-0.090
50	107	9.175	9.101	-0.074
50	108	9.122	9.187	0.065
50	109	9.375	9.464	0.089
50	110	9.108	8.950	-0.158
50	111	9.300	9.284	-0.016
55	113	9.236	9.185	-0.051
55	114	9.539	9.483	-0.056
55	115	9.487	9.401	-0.086
55	116	9.417	9.435	0.018
55	126	9.032	9.052	0.020
55	128	9.569	9.599	0.030
100	129	8.840	9.089	0.249
100	131	9.486	9.622	0.136
100	132	9.399	9.495	0.096
100	133	9.159	9.263	0.104
100	134	9.336	9.448	0.112
100	135	9.466	9.660	0.194
100	138	9.433	9.487	0.054
100	139	8.827	9.000	0.173
100	140	9.341	9.392	0.051
100	141	9.466	9.630	0.164
100	142	9.197	9.365	0.168
100	143	9.304	9.424	0.120
100	144	8.764	8.859	0.095
100	220	9.322	9.551	0.229
100	221	9.314	9.399	0.085
100	224	9.111	9.279	0.168
100	225	9.165	9.234	0.069
100	226	9.192	9.318	0.126
100	228	9.221	9.374	0.153
100	232	9.148	9.309	0.161
100	233	9.136	9.287	0.151
100	236	9.133	9.368	0.235
100	238	9.556	9.679	0.123
100	243	8.945	9.057	0.112
105	244	9.438	9.462	0.024
105	246	9.436	9.619	0.183
105	248	9.127	9.247	0.120
105	374	9.103	9.136	0.033
105	376	9.396	9.617	0.221
105	Max	9.569	9.679	0.249
105	Average	9.282	9.327	0.046
105	Min	8.764	8.811	-0.158
105	Std Dev	0.204	0.199	0.096



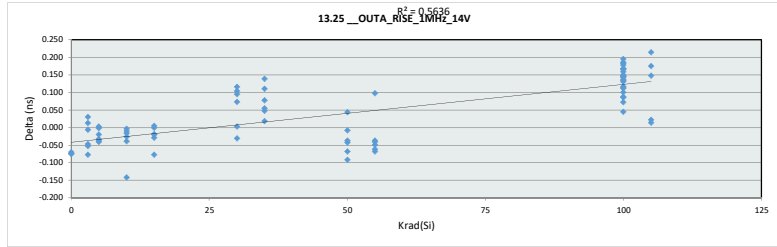
13.17_OUTA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.068	8.811	9.050	8.833	9.064	8.940	9.258	8.950	9.052	8.859	9.136
Average	9.299	9.216	9.326	9.249	9.323	9.298	9.446	9.222	9.359	9.358	9.416
Max	9.444	9.394	9.507	9.417	9.521	9.499	9.578	9.464	9.599	9.679	9.619
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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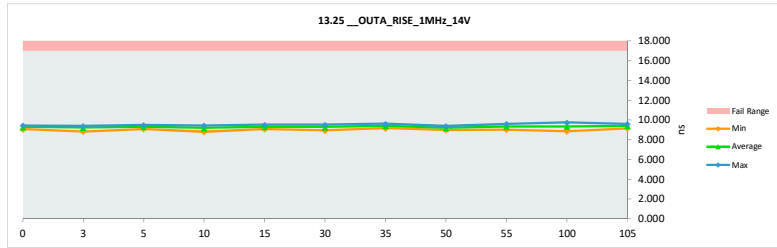
13.25_OUTA_RISE_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.513	9.440	-0.073
0	118	9.146	9.072	-0.074
0	122	9.462	9.393	-0.069
3	61	9.446	9.394	-0.052
3	62	9.160	9.191	0.031
3	63	9.397	9.321	-0.076
3	64	9.203	9.217	0.014
3	65	8.831	8.826	-0.005
3	66	9.417	9.371	-0.046
5	67	9.200	9.199	-0.001
5	68	9.495	9.455	-0.040
5	69	9.516	9.497	-0.019
5	70	9.117	9.085	-0.032
5	71	9.243	9.247	0.004
5	72	9.448	9.411	-0.037
5	73	9.446	9.444	-0.002
10	74	9.160	9.145	-0.015
10	75	9.397	9.256	-0.141
10	76	9.203	9.194	-0.009
10	77	8.831	8.806	-0.025
10	78	9.417	9.379	-0.038
15	79	9.200	9.200	0.000
15	80	9.495	9.419	-0.076
15	81	9.516	9.522	0.006
15	82	9.117	9.089	-0.028
15	83	9.243	9.224	-0.019
15	84	9.448	9.430	-0.018
30	86	9.446	9.416	-0.030
30	89	9.160	9.265	0.105
30	91	9.397	9.401	0.004
30	92	9.203	9.277	0.074
30	94	8.831	8.948	0.117
30	95	9.417	9.513	0.096
35	100	9.200	9.256	0.056
35	101	9.495	9.514	0.019
35	102	9.516	9.627	0.111
35	103	9.117	9.166	0.049
35	104	9.243	9.383	0.140
35	105	9.448	9.527	0.079
50	106	9.368	9.301	-0.067
50	107	9.179	9.088	-0.091
50	108	9.175	9.133	-0.042
50	109	9.345	9.390	0.045
50	110	9.018	8.982	-0.036
50	111	9.292	9.285	-0.007
55	113	9.206	9.139	-0.067
55	114	9.516	9.468	-0.048
55	115	9.446	9.410	-0.036
55	116	9.476	9.436	-0.040
55	126	9.074	9.013	-0.061
55	128	9.499	9.598	0.099
100	129	8.896	9.043	0.147
100	131	9.493	9.566	0.073
100	132	9.464	9.510	0.046
100	133	9.129	9.298	0.169
100	134	9.303	9.416	0.113
100	135	9.532	9.681	0.149
100	138	9.383	9.472	0.089
100	139	8.787	8.934	0.147
100	140	9.262	9.380	0.118
100	141	9.467	9.618	0.151
100	142	9.199	9.365	0.166
100	143	9.217	9.348	0.131
100	144	8.776	8.863	0.087
100	220	9.335	9.518	0.183
100	221	9.290	9.391	0.101
100	224	9.045	9.224	0.179
100	225	9.100	9.243	0.143
100	226	9.201	9.338	0.137
100	228	9.208	9.344	0.136
100	232	9.110	9.297	0.187
100	233	9.105	9.219	0.114
100	236	9.125	9.294	0.169
100	238	9.558	9.754	0.196
100	243	8.881	9.041	0.160
105	244	9.413	9.436	0.023
105	246	9.424	9.600	0.176
105	248	9.089	9.238	0.149
105	374	9.130	9.145	0.015
105	376	9.376	9.591	0.215
105	Max	9.558	9.754	0.215
105	Average	9.268	9.312	0.044
105	Min	8.776	8.806	-0.141
105	Std Dev	0.200	0.200	0.089



13.25_OUTA_RISE	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

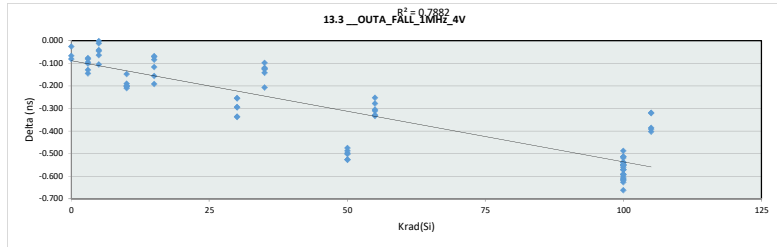
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.072	8.826	9.085	8.806	9.089	8.948	9.166	8.982	9.013	8.863	9.145
Average	9.302	9.220	9.316	9.204	9.314	9.303	9.412	9.197	9.344	9.340	9.402
Max	9.440	9.394	9.497	9.444	9.522	9.513	9.627	9.390	9.598	9.754	9.600
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



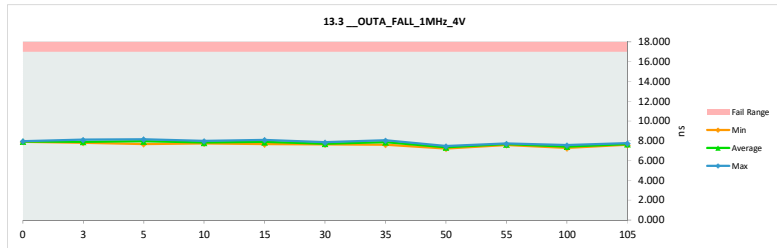
LDR TID Report
TPS7H5001-SP

13.3 OUTA_FALL_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.042	7.977	-0.065
0	118	7.930	7.905	-0.025
0	122	8.023	7.943	-0.080
3	61	8.004	7.925	-0.079
3	62	8.194	8.119	-0.075
3	63	7.950	7.850	-0.100
3	64	7.904	7.810	-0.094
3	65	7.934	7.806	-0.128
3	66	8.103	7.960	-0.143
5	67	7.728	7.684	-0.044
5	68	8.174	8.164	-0.010
5	69	7.995	7.932	-0.063
5	70	7.878	7.838	-0.040
5	71	7.968	7.968	0.000
5	72	8.256	8.152	-0.104
10	73	8.004	7.796	-0.208
10	74	8.194	7.993	-0.201
10	75	7.950	7.751	-0.199
10	76	7.904	7.758	-0.146
10	77	7.934	7.746	-0.188
10	78	8.103	7.905	-0.198
15	79	7.728	7.661	-0.067
15	80	8.174	8.105	-0.069
15	81	7.995	7.805	-0.190
15	82	7.878	7.763	-0.115
15	83	7.968	7.886	-0.082
15	84	8.256	8.101	-0.155
30	86	8.004	7.710	-0.294
30	89	8.194	7.860	-0.334
30	91	7.950	7.696	-0.254
30	92	7.904	7.651	-0.253
30	94	7.934	7.642	-0.292
30	95	8.103	7.767	-0.336
35	100	7.728	7.609	-0.119
35	101	8.174	8.049	-0.125
35	102	7.995	7.898	-0.097
35	103	7.878	7.737	-0.141
35	104	7.968	7.847	-0.121
35	105	8.256	8.050	-0.206
50	106	7.888	7.388	-0.500
50	107	7.783	7.287	-0.496
50	108	7.944	7.470	-0.474
50	109	7.723	7.237	-0.486
50	110	7.955	7.429	-0.526
50	111	7.889	7.365	-0.524
55	113	8.039	7.707	-0.332
55	114	7.933	7.624	-0.309
55	115	7.949	7.621	-0.328
55	116	8.032	7.729	-0.303
55	126	7.864	7.588	-0.276
55	128	7.934	7.683	-0.251
100	129	7.963	7.404	-0.559
100	131	8.051	7.515	-0.536
100	132	8.135	7.565	-0.570
100	133	8.110	7.562	-0.548
100	134	8.038	7.429	-0.609
100	135	8.099	7.507	-0.592
100	138	7.927	7.382	-0.545
100	139	7.845	7.295	-0.550
100	140	8.058	7.488	-0.570
100	141	7.934	7.397	-0.537
100	142	8.046	7.457	-0.589
100	143	7.886	7.375	-0.511
100	144	7.792	7.275	-0.517
100	220	8.039	7.414	-0.625
100	221	7.861	7.314	-0.547
100	224	7.985	7.499	-0.486
100	225	7.989	7.420	-0.569
100	226	7.931	7.380	-0.551
100	228	8.170	7.557	-0.613
100	232	7.890	7.378	-0.512
100	233	7.977	7.387	-0.590
100	236	7.920	7.317	-0.603
100	238	8.062	7.445	-0.617
100	243	7.997	7.336	-0.661
105	244	8.084	7.683	-0.401
105	246	8.038	7.721	-0.317
105	248	7.974	7.655	-0.319
105	374	7.983	7.599	-0.384
105	376	8.155	7.765	-0.390
	Max	8.256	8.164	0.000
	Average	7.989	7.668	-0.321
	Min	7.723	7.237	-0.661
	Std Dev	0.123	0.243	0.204



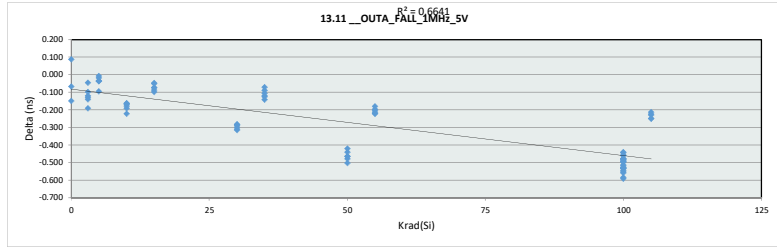
13.3 OUTA_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.905	7.806	7.684	7.746	7.661	7.642	7.609	7.237	7.588	7.275	7.599
Average	7.942	7.912	7.956	7.825	7.887	7.721	7.865	7.363	7.659	7.421	7.685
Max	7.977	8.119	8.164	7.993	8.105	7.860	8.050	7.470	7.729	7.565	7.765
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



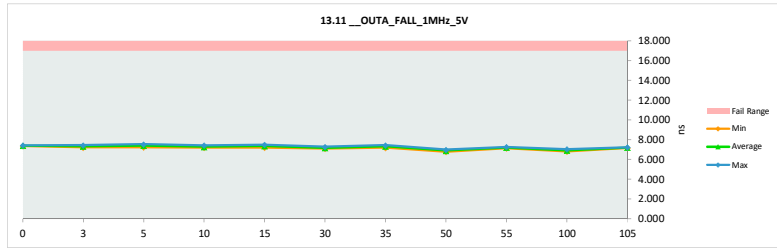
LDR TID Report
TPS7H5001-SP

13.11_OUTA_FALL_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.498	7.434	-0.064
0	118	7.334	7.425	-0.091
0	122	7.508	7.361	-0.147
3	61	7.468	7.341	-0.127
3	62	7.591	7.453	-0.138
3	63	7.432	7.314	-0.118
3	64	7.417	7.229	-0.188
3	65	7.404	7.361	-0.043
3	66	7.514	7.419	-0.095
5	67	7.285	7.251	-0.034
5	68	7.562	7.471	-0.091
5	69	7.432	7.401	-0.031
5	70	7.343	7.327	-0.016
5	71	7.442	7.409	-0.033
5	72	7.558	7.553	-0.005
10	73	7.468	7.308	-0.160
10	74	7.591	7.428	-0.163
10	75	7.432	7.212	-0.220
10	76	7.417	7.229	-0.188
10	77	7.404	7.240	-0.164
10	78	7.514	7.339	-0.175
15	79	7.285	7.215	-0.070
15	80	7.562	7.466	-0.096
15	81	7.432	7.387	-0.045
15	82	7.343	7.258	-0.085
15	83	7.442	7.371	-0.071
15	84	7.558	7.510	-0.048
30	86	7.468	7.156	-0.312
30	89	7.591	7.295	-0.296
30	91	7.432	7.147	-0.285
30	92	7.417	7.111	-0.306
30	94	7.404	7.122	-0.282
30	95	7.514	7.235	-0.279
35	100	7.285	7.198	-0.087
35	101	7.562	7.445	-0.117
35	102	7.432	7.309	-0.123
35	103	7.343	7.204	-0.139
35	104	7.442	7.374	-0.068
35	105	7.558	7.455	-0.103
50	106	7.379	6.903	-0.476
50	107	7.362	6.862	-0.500
50	108	7.419	7.002	-0.417
50	109	7.555	6.794	-0.461
50	110	7.369	6.931	-0.438
50	111	7.374	6.913	-0.461
55	113	7.426	7.207	-0.219
55	114	7.376	7.199	-0.177
55	115	7.406	7.189	-0.217
55	116	7.438	7.239	-0.199
55	126	7.358	7.148	-0.210
55	128	7.459	7.266	-0.193
100	129	7.419	6.926	-0.493
100	131	7.453	6.999	-0.454
100	132	7.533	7.059	-0.474
100	133	7.539	6.983	-0.556
100	134	7.412	6.954	-0.458
100	135	7.480	6.951	-0.529
100	138	7.402	6.907	-0.495
100	139	7.316	6.874	-0.442
100	140	7.454	6.976	-0.478
100	141	7.477	6.950	-0.527
100	142	7.515	6.926	-0.589
100	143	7.430	6.904	-0.526
100	144	7.348	6.856	-0.492
100	220	7.426	6.910	-0.516
100	221	7.302	6.827	-0.475
100	224	7.420	6.983	-0.437
100	225	7.542	6.961	-0.581
100	226	7.387	6.855	-0.532
100	228	7.528	6.999	-0.529
100	232	7.344	6.861	-0.483
100	233	7.395	6.885	-0.510
100	236	7.387	6.844	-0.543
100	238	7.454	6.973	-0.481
100	243	7.485	6.935	-0.550
105	244	7.461	7.235	-0.226
105	246	7.445	7.223	-0.222
105	248	7.425	7.215	-0.210
105	374	7.424	7.178	-0.246
105	376	7.479	7.232	-0.247
105	Max	7.591	7.553	0.038
105	Average	7.437	7.160	-0.277
105	Min	7.255	6.794	-0.589
105	Std Dev	0.078	0.208	0.187



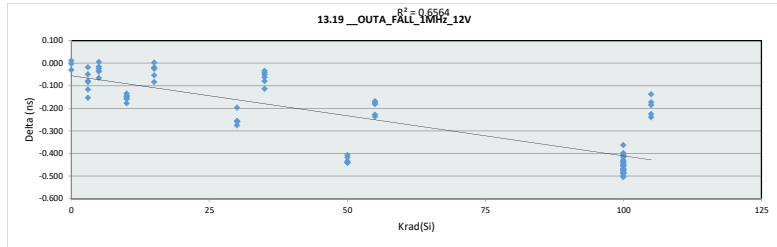
13.11_OUTA_FALL_1MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.361	7.229	7.251	7.212	7.215	7.111	7.198	6.794	7.148	6.827	7.178
Average	7.407	7.353	7.402	7.293	7.368	7.178	7.331	6.901	7.208	6.929	7.217
Max	7.434	7.453	7.553	7.428	7.510	7.295	7.455	7.002	7.266	7.059	7.235
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



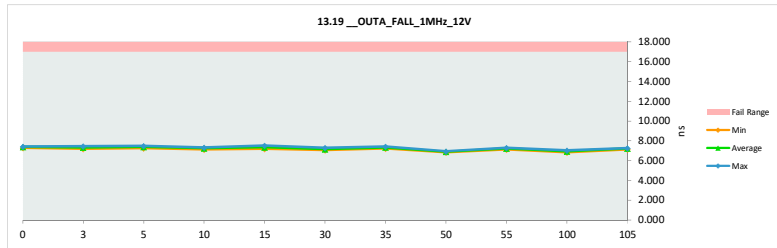
LDR TID Report
TPS7H5001-SP

13.19_OUTA_FALL_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.428	7.427	-0.001
0	118	7.312	7.284	-0.028
0	122	7.420	7.433	0.013
3	61	7.372	7.295	-0.077
3	62	7.486	7.469	-0.017
3	63	7.446	7.365	-0.081
3	64	7.343	7.296	-0.047
3	65	7.325	7.211	-0.114
3	66	7.478	7.327	-0.151
5	67	7.289	7.254	-0.035
5	68	7.502	7.489	-0.013
5	69	7.385	7.365	-0.020
5	70	7.341	7.309	-0.032
5	71	7.393	7.400	0.007
5	72	7.543	7.480	-0.063
10	73	7.372	7.239	-0.133
10	74	7.486	7.334	-0.152
10	75	7.446	7.302	-0.144
10	76	7.343	7.187	-0.156
10	77	7.325	7.150	-0.175
10	78	7.478	7.334	-0.144
15	79	7.289	7.207	-0.082
15	80	7.502	7.450	-0.052
15	81	7.385	7.390	0.005
15	82	7.341	7.319	-0.022
15	83	7.393	7.372	-0.021
15	84	7.543	7.526	-0.017
30	86	7.372	7.116	-0.256
30	89	7.486	7.291	-0.195
30	91	7.446	7.173	-0.273
30	92	7.343	7.088	-0.255
30	94	7.325	7.069	-0.256
30	95	7.478	7.224	-0.254
35	100	7.289	7.240	-0.049
35	101	7.502	7.425	-0.077
35	102	7.385	7.353	-0.032
35	103	7.341	7.281	-0.060
35	104	7.393	7.352	-0.041
35	105	7.543	7.431	-0.112
50	106	7.322	6.918	-0.404
50	107	7.270	6.833	-0.437
50	108	7.390	6.958	-0.432
50	109	7.309	6.894	-0.415
50	110	7.332	6.894	-0.438
50	111	7.376	6.937	-0.439
55	113	7.411	7.185	-0.226
55	114	7.419	7.244	-0.175
55	115	7.415	7.245	-0.170
55	116	7.448	7.214	-0.234
55	126	7.301	7.135	-0.166
55	128	7.476	7.297	-0.179
100	129	7.377	6.912	-0.465
100	131	7.404	6.990	-0.414
100	132	7.453	7.048	-0.405
100	133	7.398	6.932	-0.466
100	134	7.432	6.949	-0.483
100	135	7.384	7.024	-0.360
100	138	7.415	6.930	-0.485
100	139	7.298	6.892	-0.406
100	140	7.383	6.937	-0.446
100	141	7.432	6.951	-0.481
100	142	7.430	6.934	-0.496
100	143	7.322	6.926	-0.396
100	144	7.297	6.860	-0.437
100	220	7.413	6.962	-0.451
100	221	7.344	6.889	-0.455
100	224	7.369	6.924	-0.445
100	225	7.378	6.906	-0.472
100	226	7.367	6.865	-0.502
100	228	7.440	6.966	-0.474
100	232	7.338	6.905	-0.433
100	233	7.341	6.914	-0.427
100	236	7.352	6.870	-0.482
100	238	7.453	6.967	-0.486
100	243	7.404	6.934	-0.470
105	244	7.456	7.219	-0.237
105	246	7.415	7.279	-0.136
105	248	7.396	7.212	-0.184
105	374	7.362	7.139	-0.223
105	376	7.444	7.273	-0.171
105	Max	7.543	7.526	0.013
105	Average	7.395	7.156	-0.239
105	Min	7.270	6.833	-0.502
105	Std Dev	0.066	0.200	0.177



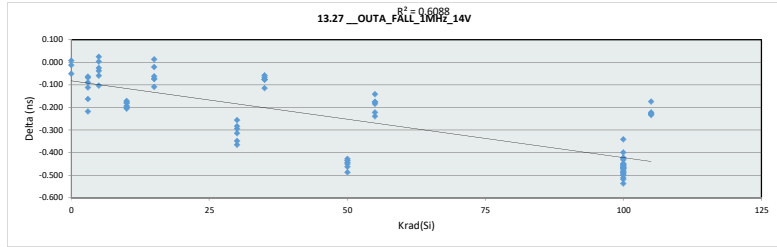
13.19_OUTA_FALL_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.284	7.211	7.254	7.150	7.207	7.069	7.240	6.833	7.135	6.860	7.139
Average	7.381	7.327	7.383	7.258	7.377	7.160	7.347	6.906	7.220	6.933	7.224
Max	7.433	7.469	7.489	7.334	7.526	7.291	7.431	6.958	7.297	7.048	7.279
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



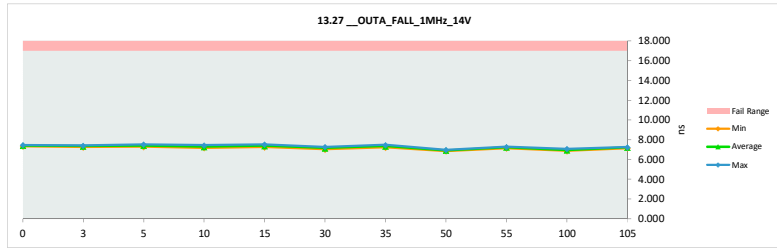
LDR TID Report
TPS7H5001-SP

13.27 OUTA_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.454	7.463	0.009
0	118	7.340	7.329	-0.011
0	122	7.436	7.387	-0.049
3	61	7.450	7.388	-0.062
3	62	7.630	7.414	-0.216
3	63	7.447	7.360	-0.087
3	64	7.412	7.251	-0.161
3	65	7.346	7.280	-0.066
3	66	7.508	7.398	-0.110
5	67	7.271	7.276	0.005
5	68	7.533	7.497	-0.036
5	69	7.441	7.340	-0.101
5	70	7.350	7.326	-0.024
5	71	7.448	7.390	-0.058
5	72	7.509	7.535	0.026
10	73	7.450	7.246	-0.204
10	74	7.630	7.438	-0.192
10	75	7.447	7.278	-0.169
10	76	7.412	7.216	-0.196
10	77	7.346	7.171	-0.175
10	78	7.508	7.328	-0.180
15	79	7.271	7.252	-0.019
15	80	7.533	7.473	-0.060
15	81	7.441	7.370	-0.071
15	82	7.350	7.279	-0.071
15	83	7.448	7.341	-0.107
15	84	7.509	7.524	0.015
30	86	7.450	7.158	-0.292
30	89	7.630	7.266	-0.364
30	91	7.447	7.193	-0.254
30	92	7.412	7.131	-0.281
30	94	7.346	7.033	-0.313
30	95	7.508	7.161	-0.347
35	100	7.271	7.215	-0.056
35	101	7.533	7.468	-0.065
35	102	7.441	7.366	-0.075
35	103	7.350	7.275	-0.075
35	104	7.448	7.335	-0.113
35	105	7.509	7.435	-0.074
50	106	7.381	6.920	-0.461
50	107	7.333	6.847	-0.486
50	108	7.406	6.973	-0.433
50	109	7.315	6.867	-0.448
50	110	7.323	6.897	-0.426
50	111	7.390	6.948	-0.442
55	113	7.382	7.208	-0.174
55	114	7.437	7.255	-0.182
55	115	7.404	7.264	-0.140
55	116	7.447	7.273	-0.174
55	126	7.375	7.138	-0.237
55	128	7.498	7.278	-0.220
100	129	7.352	6.905	-0.447
100	131	7.466	7.036	-0.430
100	132	7.464	7.042	-0.422
100	133	7.437	6.962	-0.475
100	134	7.435	6.968	-0.467
100	135	7.411	7.071	-0.340
100	138	7.362	6.941	-0.421
100	139	7.360	6.875	-0.485
100	140	7.435	6.926	-0.509
100	141	7.433	6.978	-0.455
100	142	7.478	6.943	-0.535
100	143	7.414	6.946	-0.468
100	144	7.329	6.871	-0.458
100	220	7.456	6.940	-0.516
100	221	7.376	6.878	-0.498
100	224	7.388	6.922	-0.466
100	225	7.405	6.917	-0.488
100	226	7.390	6.896	-0.494
100	228	7.475	7.013	-0.462
100	232	7.323	6.925	-0.398
100	233	7.334	6.881	-0.453
100	236	7.394	6.929	-0.465
100	238	7.460	7.012	-0.448
100	243	7.400	6.917	-0.483
105	244	7.444	7.216	-0.228
105	246	7.486	7.262	-0.224
105	248	7.411	7.239	-0.172
105	374	7.372	7.140	-0.232
105	376	7.452	7.232	-0.220
105	Max	7.630	7.535	0.026
105	Average	7.424	7.165	-0.258
105	Min	7.271	6.847	-0.535
105	Std Dev	0.074	0.200	0.176



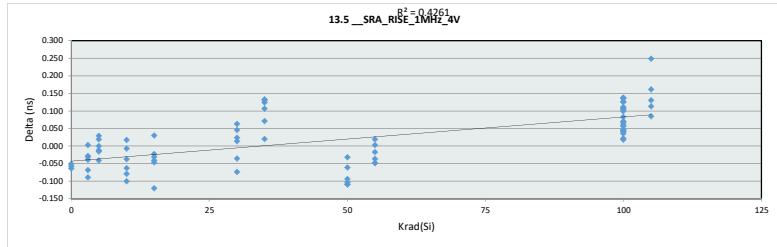
13.27 OUTA_FALL_1MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.329	7.251	7.276	7.171	7.252	7.033	7.215	6.847	7.138	6.871	7.140
Average	7.393	7.349	7.394	7.280	7.373	7.157	7.349	6.909	7.236	6.946	7.218
Max	7.463	7.414	7.535	7.438	7.524	7.266	7.468	6.973	7.278	7.071	7.262
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



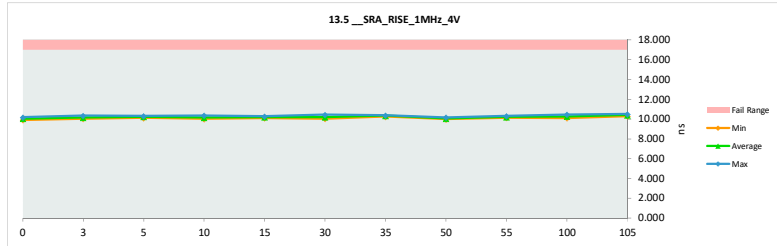
LDR TID Report
TPS7H5001-SP

13.5_SRA_RISE_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.090	10.034	-0.056
0	118	9.973	9.923	-0.050
0	122	10.245	10.182	-0.063
3	61	10.370	10.340	-0.030
3	62	10.067	10.040	-0.027
3	63	10.107	10.040	-0.067
3	64	10.160	10.122	-0.038
3	65	10.224	10.228	0.004
3	66	10.226	10.138	-0.088
5	67	10.212	10.242	0.030
5	68	10.148	10.168	0.020
5	69	10.359	10.319	-0.040
5	70	10.256	10.257	0.001
5	71	10.309	10.295	-0.014
5	72	10.139	10.128	-0.011
10	73	10.370	10.364	-0.006
10	74	10.067	10.031	-0.036
10	75	10.107	10.045	-0.062
10	76	10.160	10.082	-0.078
10	77	10.224	10.242	0.018
10	78	10.226	10.127	-0.099
15	79	10.212	10.167	-0.045
15	80	10.148	10.118	-0.030
15	81	10.359	10.240	-0.119
15	82	10.256	10.287	0.031
15	83	10.309	10.287	-0.022
15	84	10.139	10.099	-0.040
30	86	10.370	10.434	0.064
30	89	10.067	10.082	0.015
30	91	10.107	10.034	-0.073
30	92	10.160	10.185	0.025
30	94	10.224	10.271	0.047
30	95	10.226	10.192	-0.034
35	100	10.212	10.346	0.134
35	101	10.148	10.279	0.131
35	102	10.359	10.380	0.021
35	103	10.256	10.381	0.125
35	104	10.309	10.381	0.072
35	105	10.139	10.247	0.108
50	106	10.134	10.026	-0.108
50	107	10.203	10.096	-0.107
50	108	10.932	10.001	-0.931
50	109	10.229	10.169	-0.060
50	110	10.121	10.019	-0.102
50	111	10.073	9.980	-0.093
55	113	10.180	10.133	-0.047
55	114	10.308	10.261	-0.047
55	115	10.180	10.164	-0.016
55	116	10.219	10.184	-0.035
55	126	10.174	10.194	0.020
55	128	10.305	10.309	0.004
100	129	10.204	10.263	0.059
100	131	10.100	10.229	0.129
100	132	10.109	10.179	0.070
100	133	10.150	10.260	0.110
100	134	10.044	10.182	0.138
100	135	10.276	10.334	0.058
100	138	10.185	10.297	0.112
100	139	9.968	10.106	0.138
100	140	10.146	10.217	0.071
100	141	10.260	10.308	0.048
100	142	10.131	10.257	0.126
100	143	10.355	10.461	0.106
100	144	10.091	10.133	0.042
100	220	10.326	10.370	0.044
100	221	10.121	10.169	0.048
100	224	10.163	10.228	0.065
100	225	10.235	10.258	0.023
100	226	10.111	10.250	0.139
100	228	10.298	10.398	0.100
100	232	10.343	10.385	0.042
100	233	10.257	10.276	0.019
100	236	10.259	10.296	0.037
100	238	10.248	10.355	0.107
100	243	10.099	10.183	0.084
105	244	10.246	10.408	0.162
105	246	10.047	10.297	0.250
105	248	10.400	10.514	0.114
105	374	10.308	10.439	0.131
105	375	10.311	10.397	0.086
105	Max	10.400	10.514	0.250
105	Average	10.200	10.222	0.022
105	Min	9.968	9.923	-0.119
105	Std Dev	0.100	0.126	0.078



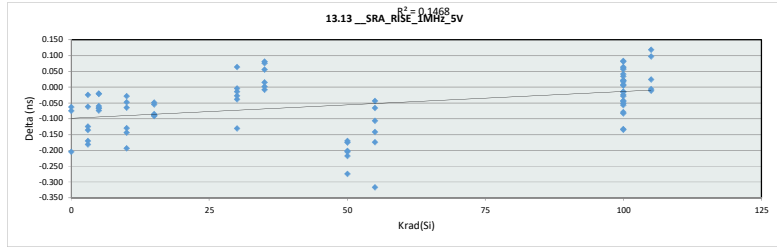
13.5_SRA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.923	10.040	10.128	10.031	10.099	10.034	10.247	9.980	10.133	10.106	10.297
Average	10.046	10.151	10.235	10.149	10.200	10.200	10.336	10.049	10.208	10.266	10.411
Max	10.182	10.340	10.319	10.364	10.287	10.434	10.381	10.169	10.309	10.461	10.514
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



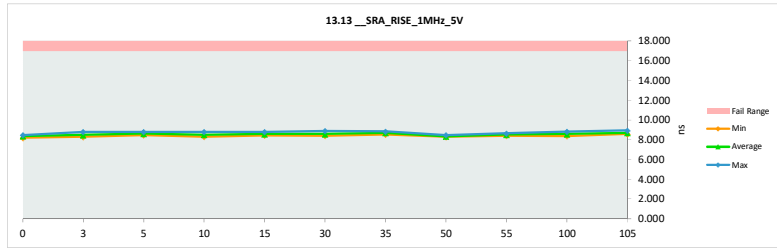
LDR TID Report
TPS7H5001-SP

13.13_SRA_RISE_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.524	8.462	-0.062
0	118	8.271	8.197	-0.074
0	122	8.680	8.477	-0.203
3	61	8.923	8.788	-0.135
3	62	8.336	8.313	-0.023
3	63	8.421	8.298	-0.123
3	64	8.679	8.499	-0.180
3	65	8.666	8.605	-0.061
3	66	8.601	8.432	-0.169
5	67	8.648	8.629	-0.019
5	68	8.540	8.520	-0.020
5	69	8.850	8.791	-0.059
5	70	8.723	8.650	-0.073
5	71	8.671	8.605	-0.066
5	72	8.528	8.463	-0.065
10	73	8.923	8.795	-0.128
10	74	8.336	8.309	-0.027
10	75	8.421	8.357	-0.064
10	76	8.679	8.487	-0.192
10	77	8.666	8.523	-0.143
10	78	8.601	8.554	-0.047
15	79	8.648	8.600	-0.048
15	80	8.540	8.452	-0.088
15	81	8.850	8.796	-0.054
15	82	8.723	8.633	-0.090
15	83	8.671	8.587	-0.084
15	84	8.528	8.440	-0.088
30	86	8.923	8.897	-0.026
30	89	8.336	8.401	0.065
30	91	8.421	8.418	-0.003
30	92	8.679	8.550	-0.129
30	94	8.666	8.629	-0.037
30	95	8.601	8.588	-0.013
35	100	8.648	8.725	0.077
35	101	8.540	8.543	0.003
35	102	8.850	8.843	-0.007
35	103	8.723	8.805	0.082
35	104	8.671	8.687	0.016
35	105	8.528	8.585	0.057
50	106	8.490	8.316	-0.174
50	107	8.675	8.402	-0.273
50	108	8.512	8.296	-0.216
50	109	8.677	8.474	-0.203
50	110	8.514	8.314	-0.200
50	111	8.486	8.317	-0.169
55	113	8.551	8.410	-0.141
55	114	8.970	8.654	-0.316
55	115	8.550	8.508	-0.042
55	116	8.607	8.434	-0.173
55	126	8.511	8.446	-0.065
55	128	8.749	8.644	-0.105
100	129	8.582	8.541	-0.041
100	131	8.488	8.510	0.022
100	132	8.480	8.538	0.058
100	133	8.484	8.470	-0.014
100	134	8.423	8.432	0.009
100	135	8.736	8.713	-0.023
100	138	8.678	8.696	0.018
100	139	8.311	8.373	0.062
100	140	8.527	8.514	-0.013
100	141	8.683	8.632	-0.051
100	142	8.536	8.620	0.084
100	143	8.757	8.822	0.065
100	144	8.292	8.374	0.082
100	220	8.791	8.834	0.043
100	221	8.551	8.469	-0.082
100	224	8.544	8.550	0.006
100	225	8.709	8.653	-0.056
100	226	8.523	8.558	0.035
100	228	8.716	8.739	0.023
100	232	8.736	8.605	-0.131
100	233	8.645	8.567	-0.078
100	236	8.633	8.606	-0.027
100	238	8.857	8.723	-0.134
100	243	8.498	8.454	-0.044
105	244	8.596	8.586	-0.010
105	246	8.498	8.617	0.119
105	248	8.839	8.937	0.098
105	374	8.567	8.592	0.025
105	376	8.747	8.742	-0.005
105	Max	8.970	8.937	0.119
105	Average	8.612	8.558	-0.054
105	Min	8.271	8.197	-0.316
105	Std Dev	0.153	0.157	0.089



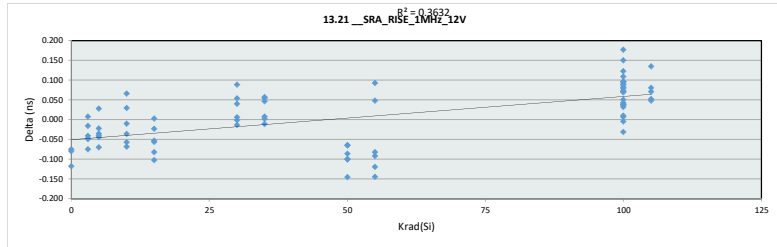
13.13_SRA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.197	8.298	8.463	8.309	8.440	8.401	8.543	8.296	8.410	8.373	8.586
Average	8.379	8.489	8.610	8.504	8.585	8.581	8.698	8.353	8.516	8.583	8.695
Max	8.477	8.788	8.791	8.795	8.796	8.897	8.843	8.474	8.654	8.834	8.937
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



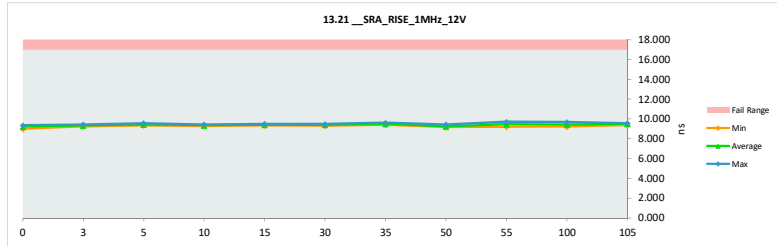
LDR TID Report
TPS7H5001-SP

		13.21_SRA_RISE_1MHz_12V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.450	9.333	-0.117
0	118	9.100	9.026	-0.074
0	122	9.454	9.375	-0.079
3	61	9.520	9.446	-0.074
3	62	9.303	9.288	-0.015
3	63	9.368	9.377	0.009
3	64	9.370	9.322	-0.048
3	65	9.294	9.248	-0.046
3	66	9.492	9.452	-0.040
5	67	9.511	9.540	0.029
5	68	9.429	9.408	-0.021
5	69	9.632	9.593	-0.039
5	70	9.444	9.375	-0.069
5	71	9.507	9.464	-0.043
5	72	9.434	9.400	-0.034
10	73	9.520	9.464	-0.056
10	74	9.303	9.334	0.031
10	75	9.368	9.435	0.067
10	76	9.370	9.335	-0.035
10	77	9.294	9.285	-0.009
10	78	9.492	9.425	-0.067
15	79	9.511	9.515	0.004
15	80	9.429	9.407	-0.022
15	81	9.632	9.531	-0.101
15	82	9.444	9.363	-0.081
15	83	9.507	9.455	-0.052
15	84	9.434	9.379	-0.055
30	86	9.520	9.508	-0.012
30	89	9.303	9.393	0.090
30	91	9.368	9.423	0.055
30	92	9.370	9.377	0.007
30	94	9.294	9.335	0.041
30	95	9.492	9.492	0.000
35	100	9.511	9.559	0.048
35	101	9.429	9.419	-0.010
35	102	9.632	9.641	0.009
35	103	9.444	9.502	0.058
35	104	9.507	9.510	0.003
35	105	9.434	9.490	0.056
50	106	9.342	9.244	-0.098
50	107	9.329	9.244	-0.085
50	108	9.288	9.188	-0.100
50	109	9.605	9.461	-0.144
50	110	9.253	9.190	-0.063
50	111	9.398	9.334	-0.064
55	113	9.379	9.236	-0.143
55	114	9.716	9.598	-0.118
55	115	9.591	9.500	-0.091
55	116	9.528	9.447	-0.081
55	126	9.385	9.434	0.049
55	128	9.635	9.729	0.094
100	129	9.325	9.365	0.040
100	131	9.433	9.503	0.070
100	132	9.420	9.544	0.124
100	133	9.176	9.249	0.073
100	134	9.337	9.370	0.033
100	135	9.547	9.657	0.110
100	138	9.475	9.513	0.038
100	139	9.244	9.283	0.039
100	140	9.391	9.402	0.011
100	141	9.512	9.556	0.044
100	142	9.441	9.532	0.091
100	143	9.553	9.650	0.097
100	144	9.304	9.393	0.089
100	220	9.578	9.630	0.052
100	221	9.515	9.485	-0.030
100	224	9.355	9.452	0.097
100	225	9.269	9.343	0.074
100	226	9.249	9.427	0.178
100	228	9.425	9.522	0.097
100	232	9.317	9.398	0.081
100	233	9.395	9.403	0.008
100	236	9.442	9.439	-0.003
100	238	9.556	9.707	0.151
100	243	9.326	9.409	0.083
105	244	9.388	9.460	0.072
105	246	9.455	9.591	0.136
105	248	9.542	9.591	0.049
105	374	9.332	9.385	0.053
105	376	9.504	9.586	0.082
105	Max	9.716	9.729	0.178
105	Average	9.427	9.433	0.006
105	Min	9.100	9.026	-0.144
105	Std Dev	0.114	0.125	0.073



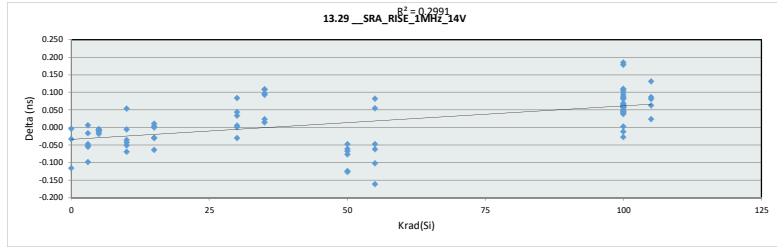
		13.21_SRA_RISE	
Test Site			
Tester			
Test Number			
Max Limit	17	ns	
Min Limit		ns	
Krad(Si)	0	3	5
LL			
Min	9.026	9.248	9.375
Average	9.245	9.356	9.463
Max	9.375	9.452	9.593
UL	17.000	17.000	17.000



LDR TID Report
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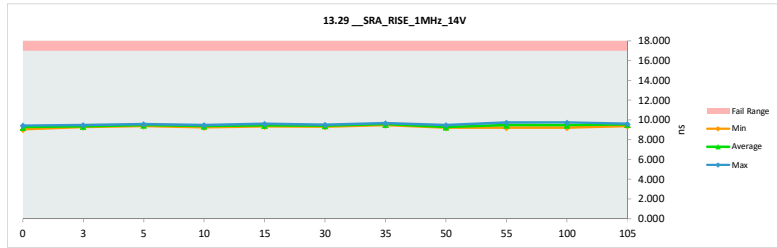
13.29_SRA_RISE_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.444	9.330	-0.114
0	118	9.071	9.039	-0.032
0	122	9.441	9.439	-0.002
3	61	9.520	9.467	-0.053
3	62	9.293	9.278	-0.015
3	63	9.408	9.356	-0.052
3	64	9.374	9.328	-0.046
3	65	9.281	9.289	0.008
3	66	9.512	9.415	-0.097
5	67	9.521	9.515	-0.006
5	68	9.461	9.455	-0.006
5	69	9.593	9.581	-0.012
5	70	9.397	9.394	-0.003
5	71	9.511	9.494	-0.017
5	72	9.396	9.388	-0.008
10	73	9.520	9.470	-0.050
10	74	9.293	9.348	0.055
10	75	9.408	9.404	-0.004
10	76	9.374	9.332	-0.042
10	77	9.281	9.247	-0.034
10	78	9.512	9.444	-0.068
15	79	9.521	9.533	0.012
15	80	9.461	9.398	-0.063
15	81	9.593	9.598	0.005
15	82	9.397	9.398	0.001
15	83	9.511	9.482	-0.029
15	84	9.396	9.368	-0.028
30	86	9.520	9.522	0.002
30	89	9.293	9.328	0.035
30	91	9.408	9.453	0.045
30	92	9.374	9.345	-0.029
30	94	9.281	9.366	0.085
30	95	9.512	9.519	0.007
35	100	9.521	9.631	0.110
35	101	9.461	9.486	0.025
35	102	9.593	9.687	0.094
35	103	9.397	9.507	0.110
35	104	9.511	9.527	0.016
35	105	9.396	9.494	0.098
50	106	9.337	9.271	-0.066
50	107	9.310	9.235	-0.075
50	108	9.353	9.230	-0.123
50	109	9.602	9.477	-0.125
50	110	9.292	9.232	-0.060
50	111	9.389	9.343	-0.046
55	113	9.335	9.234	-0.101
55	114	9.730	9.570	-0.160
55	115	9.565	9.519	-0.046
55	116	9.533	9.472	-0.061
55	126	9.287	9.343	0.056
55	128	9.659	9.742	0.083
100	129	9.293	9.353	0.060
100	131	9.403	9.504	0.101
100	132	9.387	9.475	0.088
100	133	9.249	9.238	-0.011
100	134	9.324	9.406	0.082
100	135	9.563	9.749	0.186
100	138	9.510	9.549	0.039
100	139	9.278	9.344	0.066
100	140	9.369	9.412	0.043
100	141	9.543	9.606	0.063
100	142	9.464	9.513	0.049
100	143	9.595	9.641	0.046
100	144	9.389	9.363	-0.026
100	220	9.576	9.635	0.059
100	221	9.457	9.543	0.086
100	224	9.375	9.425	0.050
100	225	9.244	9.355	0.111
100	226	9.261	9.441	0.180
100	228	9.464	9.534	0.070
100	232	9.355	9.414	0.059
100	233	9.388	9.392	0.004
100	236	9.397	9.491	0.094
100	238	9.630	9.738	0.108
100	243	9.375	9.434	0.059
105	244	9.418	9.500	0.082
105	246	9.451	9.583	0.132
105	248	9.520	9.608	0.088
105	374	9.356	9.381	0.025
105	376	9.523	9.587	0.064
105	Max	9.730	9.749	0.186
105	Average	9.429	9.444	0.015
105	Min	9.071	9.039	-0.160
105	Std Dev	0.115	0.131	0.070



13.29_SRA_RISE	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

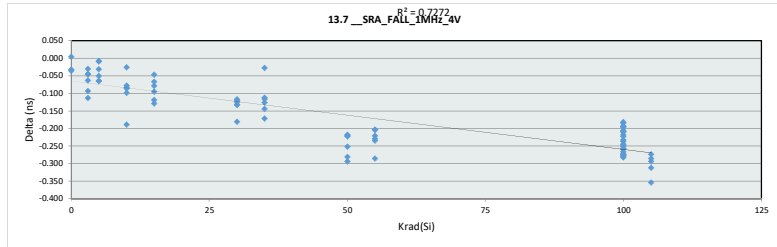
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.039	9.278	9.388	9.247	9.368	9.328	9.486	9.230	9.234	9.238	9.381
Average	9.269	9.356	9.471	9.374	9.463	9.422	9.555	9.298	9.480	9.481	9.532
Max	9.439	9.467	9.581	9.470	9.598	9.522	9.687	9.477	9.742	9.749	9.608
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



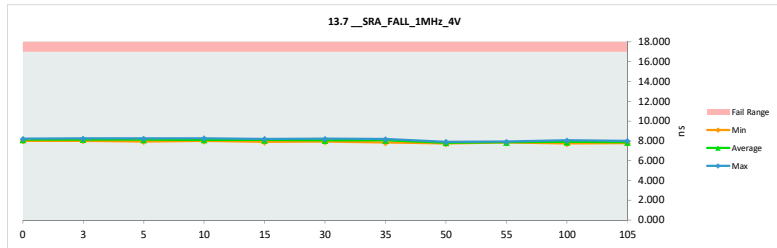
LDR TID Report
TPS7H5001-SP

13.7_SRA_FALL_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.199	8.204	0.005
0	118	8.025	7.994	-0.031
0	122	8.190	8.155	-0.035
3	61	8.158	8.129	-0.029
3	62	8.339	8.247	-0.092
3	63	8.075	8.030	-0.045
3	64	8.160	8.098	-0.062
3	65	8.051	8.007	-0.044
3	66	8.353	8.241	-0.112
5	67	7.942	7.934	-0.008
5	68	8.287	8.223	-0.064
5	69	8.125	8.095	-0.030
5	70	8.115	8.107	-0.008
5	71	8.187	8.124	-0.063
5	72	8.297	8.248	-0.049
10	73	8.158	8.076	-0.082
10	74	8.339	8.254	-0.085
10	75	8.075	7.977	-0.098
10	76	8.160	8.135	-0.025
10	77	8.051	7.974	-0.077
10	78	8.353	8.165	-0.188
15	79	7.942	7.896	-0.046
15	80	8.287	8.193	-0.094
15	81	8.125	8.047	-0.078
15	82	8.115	8.049	-0.066
15	83	8.187	8.069	-0.118
15	84	8.297	8.169	-0.128
30	86	8.158	8.034	-0.124
30	89	8.339	8.208	-0.131
30	91	8.075	7.956	-0.119
30	92	8.160	8.044	-0.116
30	94	8.051	7.920	-0.131
30	95	8.353	8.173	-0.180
35	100	7.942	7.831	-0.111
35	101	8.287	8.172	-0.115
35	102	8.125	8.098	-0.027
35	103	8.115	7.990	-0.125
35	104	8.187	8.016	-0.171
35	105	8.297	8.154	-0.143
50	106	8.083	7.803	-0.280
50	107	8.053	7.831	-0.222
50	108	8.129	7.912	-0.217
50	109	7.947	7.729	-0.218
50	110	8.134	7.841	-0.293
50	111	8.022	7.771	-0.251
55	113	8.050	7.830	-0.220
55	114	8.044	7.811	-0.233
55	115	8.042	7.814	-0.228
55	116	8.197	7.913	-0.284
55	126	8.069	7.867	-0.202
55	128	8.084	7.882	-0.202
100	129	8.087	7.852	-0.235
100	131	8.154	7.903	-0.251
100	132	8.245	8.036	-0.209
100	133	8.214	8.006	-0.208
100	134	8.027	7.835	-0.192
100	135	8.210	7.938	-0.272
100	138	8.073	7.816	-0.257
100	139	7.944	7.727	-0.217
100	140	8.162	7.887	-0.275
100	141	8.116	7.844	-0.272
100	142	8.135	7.931	-0.204
100	143	8.084	7.892	-0.192
100	144	7.954	7.759	-0.195
100	220	8.156	7.934	-0.222
100	221	8.057	7.825	-0.232
100	224	8.104	7.830	-0.274
100	225	8.139	7.874	-0.265
100	226	8.077	7.821	-0.256
100	228	8.279	8.036	-0.243
100	232	8.084	7.903	-0.181
100	233	8.182	7.999	-0.183
100	236	8.069	7.788	-0.281
100	238	8.150	7.904	-0.246
100	243	8.129	7.849	-0.280
105	244	8.199	7.846	-0.353
105	246	8.069	7.796	-0.273
105	248	8.145	7.834	-0.311
105	374	8.052	7.768	-0.284
105	376	8.288	7.995	-0.293
	Max	8.353	8.254	0.005
	Average	8.139	7.973	-0.166
	Min	7.942	7.727	-0.353
	Std Dev	0.105	0.146	0.092



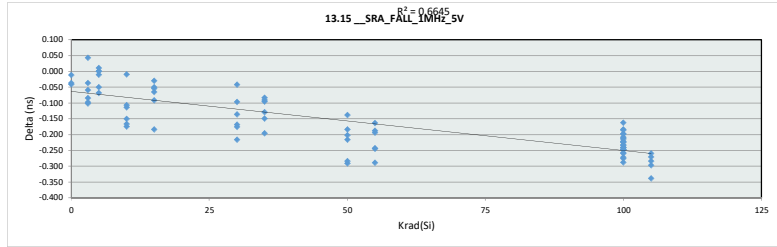
13.7_SRA_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.994	8.007	7.934	7.974	7.896	7.920	7.831	7.729	7.811	7.727	7.768
Average	8.118	8.125	8.122	8.097	8.071	8.056	8.044	7.815	7.853	7.883	7.848
Max	8.204	8.247	8.248	8.254	8.193	8.208	8.172	7.912	7.913	8.036	7.995
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



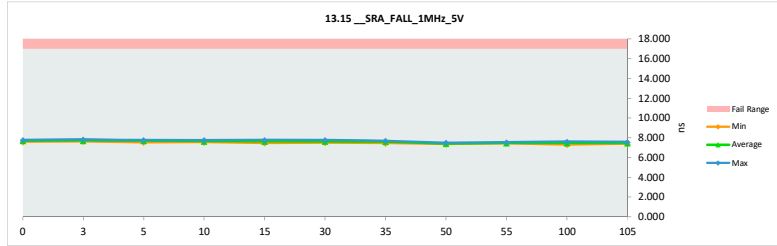
LDR TID Report
TPS7H5001-SP

13.15_SRA_FALL_1MHz_5V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	17	17
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.795	7.755	-0.040
0	118	7.609	7.573	-0.036
0	122	7.775	7.765	-0.010
3	61	7.802	7.701	-0.101
3	62	7.893	7.835	-0.058
3	63	7.720	7.623	-0.097
3	64	7.642	7.686	0.044
3	65	7.657	7.621	-0.036
3	66	7.873	7.790	-0.083
5	67	7.524	7.526	0.002
5	68	7.754	7.756	0.002
5	69	7.738	7.671	-0.067
5	70	7.672	7.684	0.012
5	71	7.755	7.746	-0.009
5	72	7.818	7.769	-0.049
10	73	7.802	7.629	-0.173
10	74	7.893	7.728	-0.165
10	75	7.720	7.571	-0.149
10	76	7.642	7.634	-0.008
10	77	7.657	7.551	-0.106
10	78	7.873	7.760	-0.113
15	79	7.524	7.460	-0.064
15	80	7.754	7.725	-0.029
15	81	7.738	7.555	-0.183
15	82	7.672	7.623	-0.049
15	83	7.755	7.665	-0.090
15	84	7.818	7.765	-0.053
30	86	7.802	7.667	-0.135
30	89	7.893	7.719	-0.174
30	91	7.720	7.505	-0.215
30	92	7.642	7.601	-0.041
30	94	7.657	7.490	-0.167
30	95	7.873	7.777	-0.096
35	100	7.524	7.442	-0.082
35	101	7.754	7.660	-0.094
35	102	7.738	7.543	-0.195
35	103	7.672	7.583	-0.089
35	104	7.755	7.607	-0.148
35	105	7.818	7.690	-0.128
50	106	7.567	7.384	-0.183
50	107	7.663	7.380	-0.283
50	108	7.694	7.493	-0.201
50	109	7.563	7.348	-0.215
50	110	7.608	7.471	-0.137
50	111	7.650	7.360	-0.290
55	113	7.680	7.438	-0.242
55	114	7.618	7.425	-0.193
55	115	7.638	7.451	-0.187
55	116	7.770	7.482	-0.288
55	126	7.696	7.453	-0.243
55	128	7.705	7.543	-0.162
100	129	7.714	7.475	-0.239
100	131	7.687	7.465	-0.222
100	132	7.790	7.605	-0.185
100	133	7.788	7.548	-0.240
100	134	7.660	7.415	-0.245
100	135	7.738	7.505	-0.233
100	138	7.656	7.473	-0.183
100	139	7.578	7.303	-0.275
100	140	7.690	7.483	-0.207
100	141	7.702	7.456	-0.246
100	142	7.670	7.509	-0.161
100	143	7.700	7.469	-0.231
100	144	7.570	7.347	-0.223
100	220	7.719	7.499	-0.220
100	221	7.662	7.404	-0.258
100	224	7.628	7.431	-0.197
100	225	7.717	7.507	-0.210
100	226	7.726	7.439	-0.287
100	228	7.853	7.582	-0.271
100	232	7.636	7.439	-0.197
100	233	7.773	7.533	-0.240
100	236	7.656	7.410	-0.246
100	238	7.735	7.479	-0.256
100	243	7.675	7.462	-0.213
105	244	7.795	7.458	-0.337
105	246	7.647	7.377	-0.270
105	248	7.785	7.489	-0.296
105	374	7.721	7.438	-0.283
105	376	7.840	7.582	-0.258
	Max	7.893	7.835	0.044
	Average	7.714	7.553	-0.160
	Min	7.524	7.303	-0.337
	Std Dev	0.089	0.128	0.092



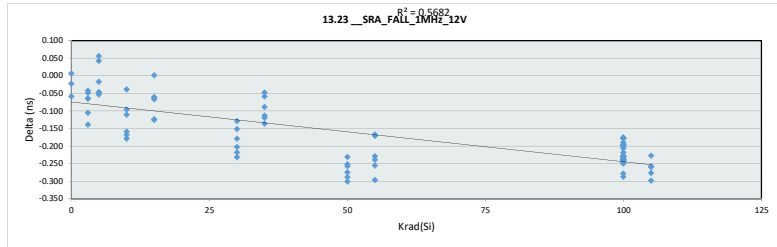
13.15_SRA_FALL												
Test Site												
Tester												
Test Number												
Max Limit	17	ns										
Min Limit	ns											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	7.573	7.621	7.526	7.551	7.460	7.490	7.442	7.348	7.425	7.303	7.377	
Average	7.698	7.709	7.692	7.646	7.632	7.627	7.588	7.406	7.465	7.468	7.469	
Max	7.765	7.835	7.769	7.760	7.765	7.777	7.690	7.493	7.543	7.605	7.582	
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	



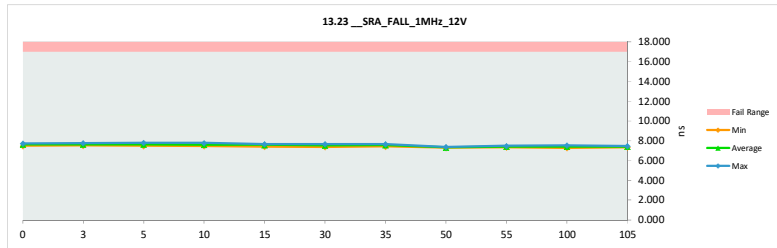
LDR TID Report
TPS7H5001-SP

13.23_SRA_FALL_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.776	7.755	-0.021
0	118	7.495	7.503	0.008
0	122	7.749	7.691	-0.058
3	61	7.741	7.603	-0.138
3	62	7.830	7.767	-0.063
3	63	7.670	7.629	-0.041
3	64	7.739	7.635	-0.104
3	65	7.598	7.534	-0.064
3	66	7.823	7.775	-0.048
5	67	7.554	7.508	-0.046
5	68	7.725	7.673	-0.052
5	69	7.649	7.692	0.043
5	70	7.607	7.591	-0.016
5	71	7.724	7.678	-0.046
5	72	7.748	7.805	0.057
10	73	7.741	7.574	-0.167
10	74	7.830	7.792	-0.038
10	75	7.670	7.575	-0.095
10	76	7.739	7.561	-0.178
10	77	7.598	7.488	-0.110
10	78	7.823	7.665	-0.158
15	79	7.554	7.430	-0.124
15	80	7.725	7.665	-0.060
15	81	7.649	7.651	0.002
15	82	7.607	7.546	-0.061
15	83	7.724	7.602	-0.122
15	84	7.748	7.682	-0.066
30	86	7.741	7.510	-0.231
30	89	7.830	7.679	-0.151
30	91	7.670	7.542	-0.128
30	92	7.739	7.561	-0.178
30	94	7.598	7.381	-0.217
30	95	7.823	7.621	-0.202
35	100	7.554	7.442	-0.112
35	101	7.725	7.590	-0.135
35	102	7.649	7.602	-0.047
35	103	7.607	7.519	-0.088
35	104	7.724	7.605	-0.119
35	105	7.748	7.690	-0.058
50	106	7.607	7.307	-0.300
50	107	7.620	7.364	-0.256
50	108	7.621	7.370	-0.251
50	109	7.584	7.310	-0.274
50	110	7.609	7.322	-0.287
50	111	7.611	7.381	-0.230
55	113	7.598	7.370	-0.228
55	114	7.679	7.384	-0.295
55	115	7.637	7.467	-0.170
55	116	7.761	7.507	-0.254
55	126	7.534	7.368	-0.166
55	128	7.705	7.467	-0.238
100	129	7.641	7.414	-0.227
100	131	7.653	7.479	-0.174
100	132	7.747	7.507	-0.240
100	133	7.707	7.512	-0.195
100	134	7.577	7.335	-0.242
100	135	7.757	7.521	-0.236
100	138	7.616	7.415	-0.201
100	139	7.513	7.287	-0.226
100	140	7.612	7.363	-0.249
100	141	7.678	7.473	-0.205
100	142	7.679	7.446	-0.233
100	143	7.666	7.477	-0.189
100	144	7.534	7.337	-0.197
100	220	7.659	7.465	-0.194
100	221	7.603	7.386	-0.217
100	224	7.530	7.354	-0.176
100	225	7.710	7.424	-0.286
100	226	7.635	7.358	-0.277
100	228	7.730	7.552	-0.178
100	232	7.606	7.377	-0.229
100	233	7.758	7.514	-0.244
100	236	7.647	7.408	-0.239
100	238	7.694	7.456	-0.238
100	243	7.659	7.431	-0.228
105	244	7.694	7.468	-0.226
105	246	7.645	7.370	-0.275
105	248	7.649	7.393	-0.256
105	374	7.662	7.365	-0.297
105	376	7.751	7.492	-0.259
105	Max	7.830	7.805	0.025
105	Average	7.673	7.510	-0.163
105	Min	7.495	7.287	-0.208
105	Std Dev	0.081	0.130	0.091



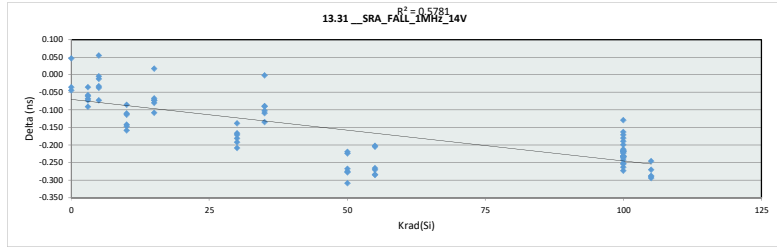
13.23_SRA_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.503	7.534	7.508	7.488	7.430	7.381	7.442	7.307	7.368	7.287	7.365
Average	7.650	7.657	7.658	7.609	7.596	7.549	7.575	7.342	7.427	7.429	7.418
Max	7.755	7.775	7.805	7.792	7.682	7.679	7.690	7.381	7.507	7.552	7.492
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



LDR TID Report
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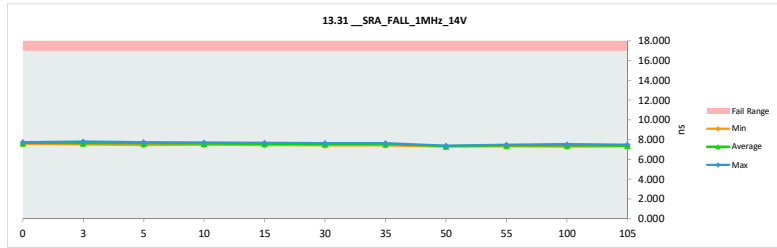
13.31_SRA_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.797	7.763	-0.034
0	118	7.528	7.576	0.048
0	122	7.751	7.708	-0.043
3	61	7.719	7.652	-0.067
3	62	7.874	7.814	-0.060
3	63	7.685	7.595	-0.090
3	64	7.733	7.662	-0.071
3	65	7.575	7.541	-0.034
3	66	7.816	7.759	-0.057
5	67	7.535	7.464	-0.071
5	68	7.662	7.718	0.056
5	69	7.680	7.670	-0.010
5	70	7.624	7.588	-0.036
5	71	7.717	7.686	-0.031
5	72	7.725	7.772	-0.003
10	73	7.719	7.578	-0.141
10	74	7.874	7.729	-0.145
10	75	7.685	7.576	-0.109
10	76	7.733	7.576	-0.157
10	77	7.575	7.491	-0.084
10	78	7.816	7.704	-0.112
15	79	7.535	7.464	-0.071
15	80	7.662	7.681	0.019
15	81	7.680	7.601	-0.079
15	82	7.624	7.517	-0.107
15	83	7.717	7.651	-0.066
15	84	7.775	7.704	-0.071
30	86	7.719	7.528	-0.191
30	89	7.874	7.667	-0.207
30	91	7.685	7.520	-0.165
30	92	7.733	7.553	-0.180
30	94	7.575	7.438	-0.137
30	95	7.816	7.646	-0.170
35	100	7.535	7.434	-0.101
35	101	7.662	7.662	0.000
35	102	7.680	7.592	-0.088
35	103	7.624	7.535	-0.089
35	104	7.717	7.584	-0.133
35	105	7.725	7.667	-0.108
50	106	7.548	7.325	-0.223
50	107	7.630	7.356	-0.274
50	108	7.667	7.360	-0.307
50	109	7.572	7.296	-0.276
50	110	7.628	7.362	-0.266
50	111	7.621	7.403	-0.218
55	113	7.586	7.385	-0.201
55	114	7.694	7.411	-0.283
55	115	7.708	7.439	-0.269
55	116	7.747	7.483	-0.264
55	126	7.626	7.342	-0.284
55	128	7.718	7.515	-0.203
100	129	7.648	7.415	-0.233
100	131	7.653	7.438	-0.215
100	132	7.732	7.553	-0.179
100	133	7.706	7.444	-0.262
100	134	7.583	7.311	-0.272
100	135	7.762	7.520	-0.242
100	138	7.612	7.394	-0.218
100	139	7.496	7.298	-0.198
100	140	7.587	7.369	-0.218
100	141	7.711	7.476	-0.235
100	142	7.657	7.427	-0.230
100	143	7.693	7.462	-0.231
100	144	7.541	7.353	-0.188
100	220	7.695	7.484	-0.211
100	221	7.613	7.383	-0.230
100	224	7.567	7.350	-0.217
100	225	7.670	7.417	-0.253
100	226	7.641	7.390	-0.251
100	228	7.740	7.526	-0.214
100	232	7.601	7.431	-0.170
100	233	7.706	7.474	-0.232
100	236	7.654	7.432	-0.222
100	238	7.631	7.503	-0.128
100	243	7.626	7.464	-0.162
105	244	7.710	7.466	-0.244
105	246	7.638	7.369	-0.269
105	248	7.666	7.377	-0.289
105	374	7.647	7.354	-0.293
105	376	7.787	7.501	-0.286
	Max	7.874	7.814	0.056
	Average	7.675	7.514	-0.161
	Min	7.496	7.296	-0.307
	Std Dev	0.084	0.130	0.093



13.31_SRA_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

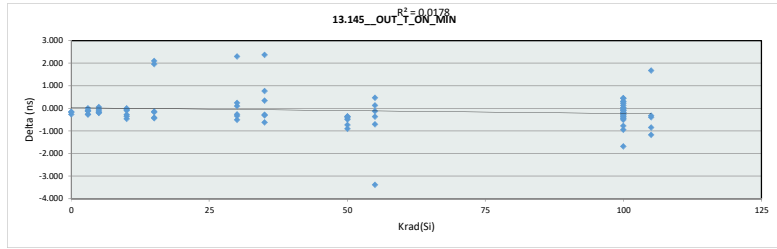
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.576	7.541	7.464	7.491	7.464	7.438	7.434	7.296	7.342	7.298	7.354
Average	7.682	7.671	7.650	7.609	7.603	7.559	7.579	7.350	7.429	7.430	7.413
Max	7.763	7.814	7.772	7.729	7.704	7.667	7.667	7.403	7.515	7.553	7.501
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



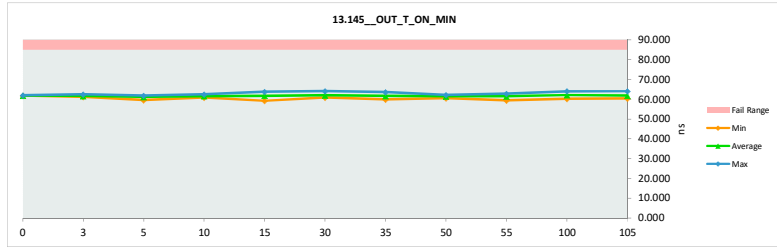
LDR TID Report
TPS7H5001-SP

13.145_OUT_T_ON_MIN	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	85 85
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	62.194	62.046	-0.148
0	118	61.936	61.809	-0.127
0	122	62.069	61.827	-0.242
3	61	62.508	62.529	0.021
3	62	61.780	61.681	-0.099
3	63	61.428	61.182	-0.246
3	64	61.685	61.628	-0.057
3	65	61.842	61.745	-0.097
3	66	61.761	61.518	-0.243
5	67	61.840	61.664	-0.176
5	68	61.378	61.311	-0.067
5	69	62.081	61.906	-0.175
5	70	61.278	61.357	0.079
5	71	61.598	61.470	-0.128
5	72	59.635	59.636	0.001
10	73	62.508	62.471	-0.037
10	74	61.780	61.808	0.028
10	75	61.428	60.976	-0.452
10	76	61.685	61.609	-0.076
10	77	61.842	61.582	-0.260
10	78	61.761	61.435	-0.326
15	79	61.840	63.812	1.972
15	80	61.378	61.261	-0.117
15	81	62.081	61.928	-0.153
15	82	61.278	63.394	2.116
15	83	61.598	61.210	-0.388
15	84	59.635	59.219	-0.416
30	86	62.508	62.266	-0.242
30	89	61.780	64.094	2.314
30	91	61.428	60.934	-0.494
30	92	61.685	61.803	0.118
30	94	61.842	62.100	0.258
30	95	61.761	61.448	-0.313
35	100	61.840	61.554	-0.286
35	101	61.378	60.774	-0.604
35	102	62.081	61.814	-0.267
35	103	61.278	63.660	2.382
35	104	61.598	62.384	0.786
35	105	59.635	60.000	0.365
50	106	61.004	60.652	-0.352
50	107	62.561	62.156	-0.405
50	108	62.694	61.975	-0.719
50	109	62.422	61.540	-0.882
50	110	61.625	61.146	-0.479
50	111	61.945	61.610	-0.335
55	113	60.862	61.016	0.154
55	114	63.003	62.902	-0.101
55	115	60.109	59.429	-0.680
55	116	65.227	61.862	-3.365
55	126	62.586	62.236	-0.350
55	128	61.675	62.166	0.491
100	129	60.544	60.286	-0.258
100	131	62.657	63.113	0.456
100	132	60.800	60.752	-0.048
100	133	61.969	61.653	-0.316
100	134	62.743	62.261	-0.482
100	135	63.668	63.525	-0.143
100	138	61.265	60.930	-0.335
100	139	63.211	63.268	0.057
100	140	61.700	60.938	-0.762
100	141	63.559	63.870	0.311
100	142	61.470	61.061	-0.409
100	143	62.422	62.462	0.040
100	144	62.144	61.917	-0.227
100	220	60.954	60.887	-0.067
100	221	63.467	63.614	0.147
100	224	62.128	62.069	-0.059
100	225	63.476	63.709	0.233
100	226	63.103	61.443	-1.660
100	228	61.497	60.573	-0.924
100	232	62.399	61.964	-0.435
100	233	63.745	64.080	0.335
100	236	63.672	63.499	-0.173
100	238	61.054	61.532	0.478
100	243	62.175	61.998	-0.177
105	244	62.614	62.244	-0.370
105	246	62.715	61.565	-1.150
105	248	60.732	60.429	-0.303
105	374	61.925	61.092	-0.833
105	376	62.411	64.104	1.693
105	375	65.227	64.104	2.382
105	Max	61.932	61.830	-0.102
105	Average	61.932	61.830	-0.102
105	Min	59.635	59.219	-3.365
105	Std Dev	0.941	1.060	0.767



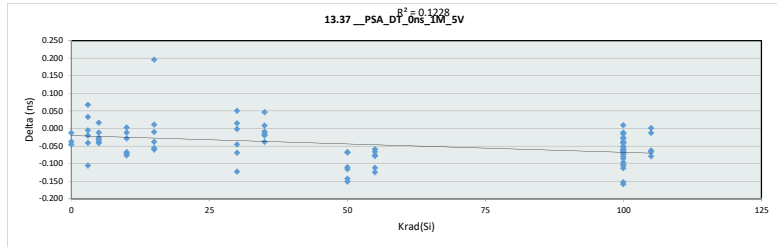
13.145_OUT_T_ON_MIN											
Test Site											
Tester											
Test Number											
Min Limit	85 ns										
Max Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	61.809	61.182	59.636	60.976	59.219	60.934	60.000	60.652	59.429	60.286	60.429
Average	61.894	61.714	61.224	61.647	61.804	62.108	61.698	61.513	61.602	62.142	61.887
Max	62.046	62.529	61.906	62.471	63.812	64.094	63.660	62.156	62.902	64.080	64.104
UL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000



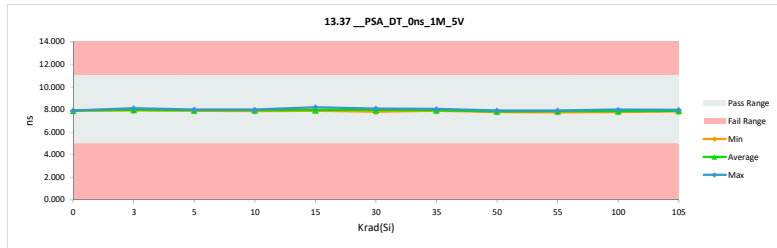
LDR TID Report
TPS7H5001-SP

		13.37 PSA_DT_0ns_1M_5V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	11	11	
Min Limit	5	5	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.941	7.897	-0.044
0	118	7.950	7.914	-0.036
0	122	7.927	7.915	-0.012
3	61	7.986	8.020	0.034
3	62	8.052	7.948	-0.104
3	63	7.991	7.951	-0.040
3	64	7.934	7.915	-0.019
3	65	8.047	8.116	0.069
3	66	7.983	7.979	-0.004
5	67	7.911	7.900	-0.011
5	68	7.959	7.934	-0.025
5	69	7.932	7.901	-0.031
5	70	8.005	7.964	-0.041
5	71	8.044	8.008	-0.036
5	72	7.890	7.908	0.018
10	73	7.986	7.990	0.004
10	74	8.052	7.986	-0.066
10	75	7.991	7.964	-0.027
10	76	7.934	7.864	-0.070
10	77	8.047	7.972	-0.075
10	78	7.983	7.972	-0.011
15	79	7.911	7.902	-0.009
15	80	7.959	7.905	-0.054
15	81	7.932	7.895	-0.037
15	82	8.005	8.202	0.197
15	83	8.044	7.985	-0.059
15	84	7.890	7.902	0.012
30	86	7.986	7.985	-0.001
30	89	8.052	7.984	-0.068
30	91	7.991	8.007	0.016
30	92	7.934	7.812	-0.122
30	94	8.047	8.098	0.051
30	95	7.983	7.939	-0.044
35	100	7.911	7.897	-0.014
35	101	7.959	7.940	-0.019
35	102	7.932	7.941	0.009
35	103	8.005	8.053	0.048
35	104	8.044	8.007	-0.037
35	105	7.890	7.882	-0.008
50	106	7.955	7.805	-0.150
50	107	8.036	7.894	-0.142
50	108	7.947	7.881	-0.066
50	109	7.886	7.777	-0.109
50	110	7.916	7.802	-0.114
50	111	7.996	7.929	-0.067
55	113	7.973	7.862	-0.111
55	114	7.961	7.896	-0.065
55	115	8.006	7.929	-0.077
55	116	7.958	7.900	-0.058
55	126	7.885	7.761	-0.124
55	128	7.966	7.891	-0.075
100	129	7.927	7.913	-0.014
100	131	7.989	7.885	-0.104
100	132	7.872	7.812	-0.060
100	133	7.954	7.859	-0.095
100	134	7.881	7.822	-0.059
100	135	7.935	7.851	-0.084
100	138	7.975	7.914	-0.061
100	139	7.944	7.832	-0.112
100	140	7.859	7.780	-0.079
100	141	8.034	7.877	-0.157
100	142	7.903	7.878	-0.025
100	143	7.984	7.931	-0.053
100	144	7.960	7.890	-0.070
100	220	7.934	7.869	-0.065
100	221	7.804	7.814	0.010
100	224	7.872	7.800	-0.072
100	225	7.893	7.790	-0.103
100	226	8.001	7.965	-0.036
100	228	7.936	7.925	-0.011
100	232	7.937	7.910	-0.027
100	233	7.895	7.854	-0.041
100	236	8.049	7.999	-0.050
100	238	7.958	7.806	-0.152
100	243	7.952	7.913	-0.039
105	244	7.932	7.870	-0.062
105	246	7.884	7.886	0.002
105	248	7.931	7.919	-0.012
105	374	8.046	7.981	-0.065
105	376	7.900	7.822	-0.078
105	Max	8.052	8.202	0.197
105	Average	7.958	7.913	-0.045
105	Min	7.804	7.761	-0.157
105	Std Dev	0.055	0.078	0.055



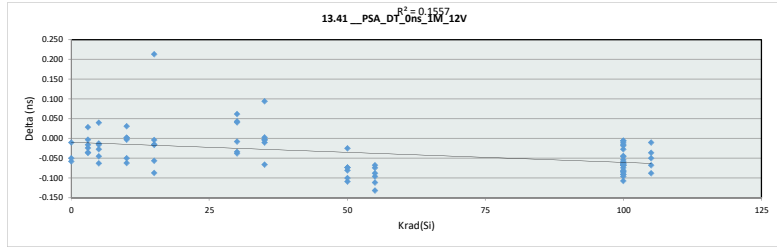
		13.37 PSA_DT_0	
Test Site			
Tester			
Test Number			
Max Limit	11	ns	
Min Limit	5	ns	
Krad(Si)	0	3	5
LL	5.000	5.000	5.000
Min	7.897	7.915	7.900
Average	7.909	7.988	7.936
Max	7.915	8.116	8.008
UL	11.000	11.000	11.000



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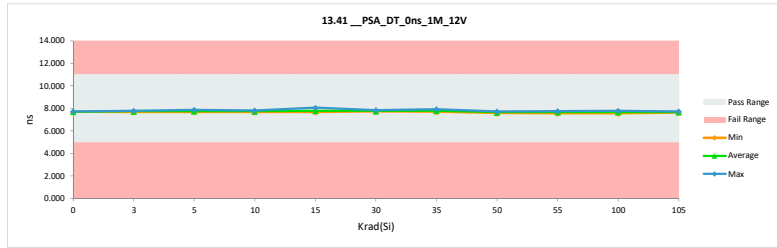
13.41 PSA_DT_Ons_1M_12V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	11	11
Min Limit	5	5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.784	7.735	-0.049
0	118	7.711	7.702	-0.009
0	122	7.774	7.717	-0.057
3	61	7.748	7.778	0.030
3	62	7.810	7.776	-0.034
3	63	7.818	7.803	-0.015
3	64	7.699	7.697	-0.002
3	65	7.770	7.735	-0.035
3	66	7.797	7.774	-0.023
5	67	7.746	7.684	-0.062
5	68	7.777	7.762	-0.015
5	69	7.776	7.764	-0.012
5	70	7.853	7.894	0.041
5	71	7.892	7.848	-0.044
5	72	7.710	7.684	-0.026
10	73	7.748	7.780	0.032
10	74	7.810	7.813	0.003
10	75	7.818	7.821	0.003
10	76	7.699	7.697	-0.002
10	77	7.770	7.709	-0.061
10	78	7.797	7.748	-0.049
15	79	7.746	7.732	-0.014
15	80	7.777	7.774	-0.003
15	81	7.776	7.721	-0.055
15	82	7.853	8.067	0.214
15	83	7.892	7.806	-0.086
15	84	7.710	7.695	-0.015
30	86	7.748	7.811	0.063
30	89	7.810	7.777	-0.033
30	91	7.818	7.860	0.042
30	92	7.699	7.743	0.044
30	94	7.770	7.763	-0.007
30	95	7.797	7.760	-0.037
35	100	7.746	7.737	-0.009
35	101	7.777	7.781	0.004
35	102	7.776	7.773	-0.003
35	103	7.853	7.948	0.095
35	104	7.892	7.827	-0.065
35	105	7.710	7.708	-0.002
50	106	7.677	7.653	-0.024
50	107	7.783	7.703	-0.080
50	108	7.736	7.664	-0.072
50	109	7.757	7.658	-0.099
50	110	7.700	7.592	-0.108
50	111	7.823	7.751	-0.072
55	113	7.760	7.665	-0.095
55	114	7.782	7.651	-0.131
55	115	7.852	7.785	-0.067
55	116	7.777	7.704	-0.073
55	126	7.691	7.581	-0.110
55	128	7.822	7.735	-0.087
100	129	7.726	7.642	-0.084
100	131	7.783	7.730	-0.053
100	132	7.716	7.649	-0.067
100	133	7.679	7.584	-0.095
100	134	7.692	7.634	-0.058
100	135	7.721	7.716	-0.005
100	138	7.789	7.728	-0.061
100	139	7.732	7.643	-0.089
100	140	7.670	7.655	-0.015
100	141	7.780	7.700	-0.080
100	142	7.712	7.702	-0.010
100	143	7.809	7.742	-0.067
100	144	7.766	7.676	-0.090
100	220	7.779	7.696	-0.083
100	221	7.741	7.635	-0.106
100	224	7.638	7.593	-0.045
100	225	7.668	7.642	-0.026
100	226	7.762	7.689	-0.073
100	228	7.717	7.713	-0.004
100	232	7.770	7.703	-0.067
100	233	7.709	7.692	-0.017
100	236	7.847	7.804	-0.043
100	238	7.780	7.716	-0.064
100	243	7.791	7.728	-0.063
105	244	7.721	7.686	-0.035
105	246	7.728	7.641	-0.087
105	248	7.750	7.701	-0.049
105	374	7.809	7.742	-0.067
105	376	7.683	7.674	-0.009
	Max	7.892	8.067	0.214
	Average	7.764	7.728	-0.036
	Min	7.638	7.581	-0.131
	Std Dev	0.055	0.079	0.052



13.41 PSA_DT		
Test Site		
Tester		
Test Number		
Max Limit	11	ns
Min Limit	5	ns

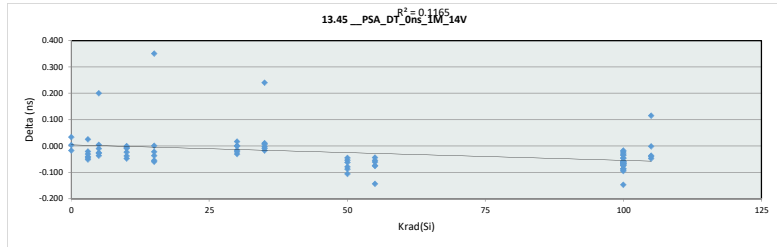
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.702	7.697	7.684	7.697	7.695	7.743	7.708	7.592	7.581	7.584	7.641
Average	7.718	7.761	7.773	7.761	7.799	7.786	7.796	7.670	7.687	7.684	7.689
Max	7.735	7.803	7.894	7.821	8.067	7.860	7.948	7.751	7.785	7.804	7.742
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000



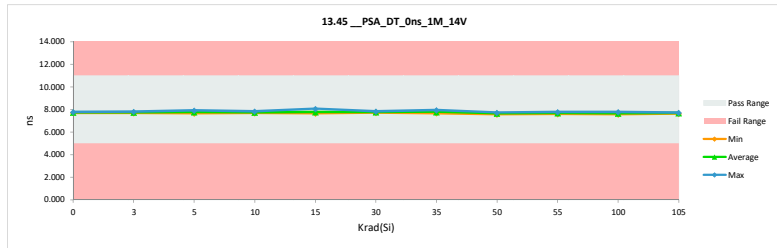
LDR TID Report
TPS7H5001-SP

13.45_PSA_DT_0ns_1M_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	11 11
Min Limit	5 5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.758	7.743	-0.015
0	118	7.714	7.719	0.005
0	122	7.762	7.798	0.036
3	61	7.782	7.763	-0.019
3	62	7.861	7.822	-0.039
3	63	7.823	7.794	-0.029
3	64	7.752	7.702	-0.050
3	65	7.762	7.718	-0.044
3	66	7.772	7.799	0.027
5	67	7.743	7.735	-0.008
5	68	7.759	7.765	0.006
5	69	7.767	7.732	-0.035
5	70	7.729	7.931	0.202
5	71	7.857	7.833	-0.024
5	72	7.706	7.681	-0.025
10	73	7.782	7.746	-0.036
10	74	7.861	7.839	-0.022
10	75	7.823	7.824	0.001
10	76	7.752	7.706	-0.046
10	77	7.762	7.755	-0.007
10	78	7.772	7.768	-0.004
15	79	7.743	7.685	-0.058
15	80	7.759	7.739	-0.020
15	81	7.767	7.732	-0.035
15	82	7.729	8.081	0.352
15	83	7.857	7.804	-0.053
15	84	7.706	7.709	0.003
30	86	7.782	7.785	0.003
30	89	7.861	7.847	-0.014
30	91	7.823	7.824	0.001
30	92	7.752	7.723	-0.029
30	94	7.762	7.742	-0.020
30	95	7.772	7.791	0.019
35	100	7.743	7.736	-0.007
35	101	7.759	7.768	0.009
35	102	7.767	7.766	-0.001
35	103	7.729	7.971	0.242
35	104	7.857	7.869	0.012
35	105	7.706	7.691	-0.015
50	106	7.686	7.634	-0.052
50	107	7.758	7.681	-0.077
50	108	7.758	7.654	-0.104
50	109	7.734	7.691	-0.043
50	110	7.670	7.584	-0.086
50	111	7.804	7.743	-0.061
55	113	7.743	7.669	-0.074
55	114	7.832	7.690	-0.142
55	115	7.844	7.785	-0.059
55	116	7.777	7.705	-0.072
55	126	7.665	7.612	-0.053
55	128	7.785	7.743	-0.042
100	129	7.754	7.669	-0.085
100	131	7.784	7.699	-0.085
100	132	7.721	7.659	-0.062
100	133	7.662	7.589	-0.073
100	134	7.711	7.638	-0.073
100	135	7.738	7.693	-0.045
100	138	7.801	7.708	-0.093
100	139	7.704	7.671	-0.033
100	140	7.650	7.583	-0.067
100	141	7.786	7.696	-0.090
100	142	7.677	7.657	-0.020
100	143	7.802	7.745	-0.057
100	144	7.702	7.669	-0.033
100	220	7.772	7.707	-0.065
100	221	7.716	7.659	-0.057
100	224	7.685	7.621	-0.064
100	225	7.665	7.600	-0.065
100	226	7.819	7.673	-0.146
100	228	7.746	7.721	-0.025
100	232	7.788	7.719	-0.069
100	233	7.690	7.630	-0.060
100	236	7.852	7.796	-0.056
100	238	7.743	7.727	-0.016
100	243	7.789	7.746	-0.043
105	244	7.728	7.690	-0.038
105	246	7.596	7.713	0.117
105	248	7.711	7.711	0.000
105	374	7.791	7.745	-0.046
105	376	7.685	7.650	-0.035
105	378	7.861	8.081	0.352
105	Average	7.756	7.730	-0.026
105	Min	7.596	7.583	-0.146
105	Std Dev	0.055	0.083	0.071



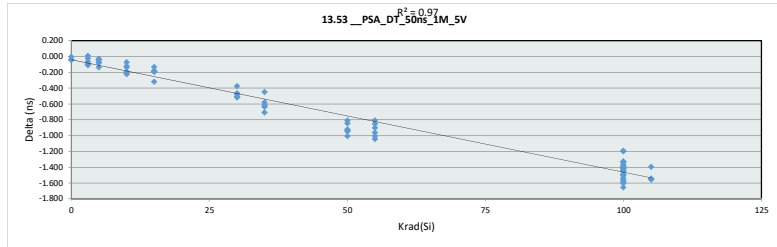
13.45_PSA_DT_0											
Test Site											
Tester											
Test Number											
Max Limit	11 ns										
Min Limit	5 ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.719	7.702	7.681	7.706	7.685	7.723	7.691	7.584	7.612	7.583	7.650
Average	7.753	7.766	7.780	7.773	7.792	7.785	7.800	7.665	7.701	7.678	7.702
Max	7.798	7.822	7.931	7.839	8.081	7.847	7.971	7.743	7.785	7.796	7.745
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000



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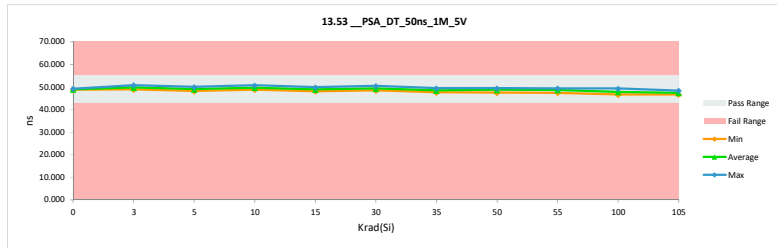
13.53 PSA_DT_50ns_1M_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	55 55
Min Limit	43 43

Krad(Si)	Serial #	PRE	POST	Delta
0	117	48.763	48.724	-0.039
0	118	49.284	49.284	0.000
0	122	48.872	48.836	-0.036
3	61	49.443	49.424	-0.019
3	62	50.945	50.842	-0.103
3	63	48.950	48.891	-0.059
3	64	50.024	49.929	-0.095
3	65	49.871	49.778	-0.093
3	66	49.743	49.757	0.014
5	67	49.259	49.186	-0.073
5	68	48.543	48.500	-0.043
5	69	48.434	48.300	-0.134
5	70	48.232	48.204	-0.028
5	71	50.067	49.994	-0.073
5	72	49.989	49.961	-0.028
10	73	49.443	49.374	-0.069
10	74	50.945	50.726	-0.219
10	75	48.950	48.817	-0.133
10	76	50.024	49.810	-0.214
10	77	49.871	49.681	-0.190
10	78	49.743	49.627	-0.116
15	79	49.259	49.078	-0.181
15	80	48.543	48.358	-0.185
15	81	48.434	48.120	-0.314
15	82	48.232	48.103	-0.129
15	83	50.067	49.888	-0.179
15	84	49.989	49.799	-0.190
30	86	49.443	49.070	-0.373
30	89	50.945	50.431	-0.514
30	91	48.950	48.489	-0.461
30	92	50.024	49.553	-0.471
30	94	49.871	49.366	-0.505
30	95	49.743	49.241	-0.502
35	100	49.259	48.634	-0.625
35	101	48.543	48.097	-0.446
35	102	48.434	47.731	-0.703
35	103	48.232	47.659	-0.573
35	104	50.067	49.437	-0.630
35	105	49.989	49.384	-0.605
50	106	50.389	49.473	-0.916
50	107	49.696	48.891	-0.805
50	108	48.355	47.515	-0.840
50	109	49.291	48.347	-0.944
50	110	49.935	49.011	-0.924
50	111	50.165	49.161	-1.004
55	113	50.175	49.218	-0.957
55	114	48.263	47.412	-0.851
55	115	50.220	49.218	-1.002
55	116	50.225	49.421	-0.804
55	126	49.167	48.268	-0.899
55	128	49.720	48.679	-1.041
100	129	49.798	48.433	-1.365
100	131	48.555	46.986	-1.569
100	132	49.133	47.644	-1.489
100	133	48.490	46.988	-1.502
100	134	50.073	48.483	-1.590
100	135	49.518	48.070	-1.448
100	138	49.690	48.286	-1.404
100	139	48.114	46.710	-1.404
100	140	48.309	46.899	-1.410
100	141	50.030	48.378	-1.652
100	142	48.311	46.927	-1.384
100	143	48.209	46.726	-1.483
100	144	49.693	48.361	-1.332
100	220	49.301	47.769	-1.532
100	221	49.776	48.588	-1.188
100	224	48.483	47.153	-1.330
100	225	49.930	48.522	-1.408
100	226	50.892	49.334	-1.558
100	228	50.023	48.427	-1.596
100	232	47.972	46.651	-1.321
100	233	49.639	48.447	-1.192
100	236	48.348	46.961	-1.387
100	238	49.646	48.207	-1.439
100	243	50.112	48.681	-1.431
105	244	48.519	46.978	-1.541
105	246	48.229	46.680	-1.549
105	248	49.386	47.844	-1.542
105	374	48.517	47.127	-1.390
105	376	49.925	48.377	-1.548
105	Max	50.945	50.842	0.014
105	Average	49.371	48.592	-0.779
105	Min	47.972	46.651	-1.652
105	Std Dev	0.777	1.011	0.582



13.53 PSA_DT_50ns_1M_5V	
Test Site	
Tester	
Test Number	
Max Limit	55 ns
Min Limit	43 ns

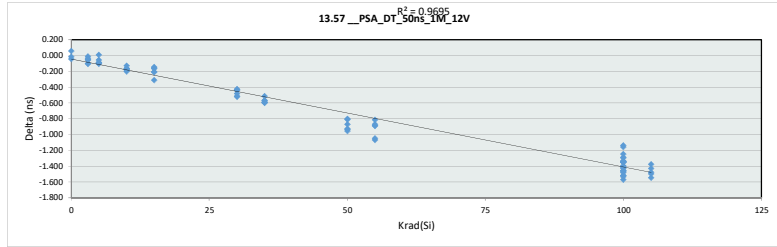
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.724	48.891	48.204	48.817	48.103	48.489	47.659	47.515	47.412	46.651	46.680
Average	48.948	49.770	49.024	49.673	48.891	49.358	48.490	48.733	48.703	47.818	47.401
Max	49.284	50.842	49.994	50.726	49.888	50.431	49.437	49.473	49.421	49.334	48.377
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000



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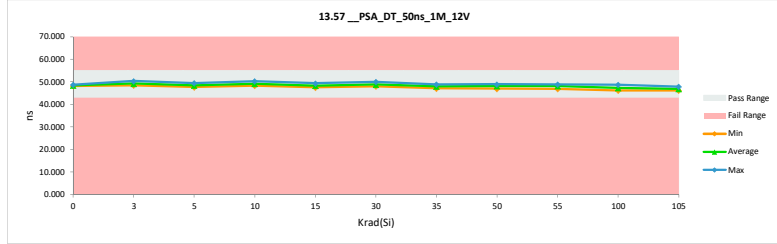
13.57 PSA_DT 50ns_1M_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	55 55
Min Limit	43 43

Krad(Si)	Serial #	PRE	POST	Delta
0	117	48.273	48.261	-0.012
0	118	48.721	48.784	0.063
0	122	48.415	48.377	-0.038
3	61	48.966	48.935	-0.031
3	62	50.477	50.425	-0.052
3	63	48.527	48.480	-0.047
3	64	49.510	49.412	-0.098
3	65	49.314	49.212	-0.102
3	66	49.286	49.279	-0.007
5	67	48.809	48.726	-0.083
5	68	48.146	48.093	-0.053
5	69	47.914	47.828	-0.086
5	70	47.744	47.755	0.011
5	71	49.572	49.479	-0.093
5	72	49.506	49.405	-0.101
10	73	48.966	48.809	-0.157
10	74	50.477	50.314	-0.163
10	75	48.527	48.350	-0.177
10	76	49.510	49.314	-0.196
10	77	49.314	49.139	-0.175
10	78	49.286	49.161	-0.125
15	79	48.809	48.605	-0.204
15	80	48.146	47.945	-0.201
15	81	47.914	47.609	-0.305
15	82	47.744	47.593	-0.151
15	83	49.572	49.431	-0.141
15	84	49.506	49.346	-0.160
30	86	48.966	48.547	-0.419
30	89	50.477	50.041	-0.436
30	91	48.527	48.098	-0.429
30	92	49.510	48.992	-0.518
30	94	49.314	48.806	-0.508
30	95	49.286	48.811	-0.475
35	100	48.809	48.249	-0.560
35	101	48.146	47.635	-0.511
35	102	47.914	47.343	-0.571
35	103	47.744	47.157	-0.587
35	104	49.572	48.981	-0.591
35	105	49.506	48.913	-0.593
50	106	49.884	49.018	-0.866
50	107	49.194	48.391	-0.803
50	108	47.820	47.022	-0.798
50	109	48.911	47.960	-0.951
50	110	49.352	48.423	-0.929
50	111	49.667	48.745	-0.922
55	113	49.577	48.691	-0.886
55	114	47.864	46.997	-0.867
55	115	49.865	48.803	-1.062
55	116	49.793	48.984	-0.809
55	126	48.695	47.815	-0.880
55	128	49.323	48.281	-1.042
100	129	49.261	47.924	-1.337
100	131	48.160	46.595	-1.565
100	132	48.702	47.188	-1.514
100	133	47.939	46.488	-1.451
100	134	49.528	47.998	-1.530
100	135	49.087	47.666	-1.421
100	138	49.230	47.889	-1.341
100	139	47.602	46.261	-1.341
100	140	47.844	46.447	-1.397
100	141	49.451	47.987	-1.464
100	142	47.823	46.494	-1.329
100	143	47.753	46.305	-1.448
100	144	49.214	47.923	-1.291
100	220	48.804	47.389	-1.415
100	221	49.342	48.188	-1.154
100	224	48.013	46.683	-1.330
100	225	49.348	48.008	-1.340
100	226	50.336	48.824	-1.512
100	228	49.438	47.963	-1.475
100	232	47.494	46.213	-1.281
100	233	49.072	47.938	-1.134
100	236	47.775	46.532	-1.243
100	238	49.134	47.741	-1.393
100	243	49.549	48.204	-1.345
105	244	47.960	46.535	-1.425
105	246	47.796	46.255	-1.541
105	248	48.905	47.433	-1.472
105	374	48.016	46.645	-1.371
105	376	49.418	47.924	-1.494
	Max	50.477	50.425	0.063
	Average	48.883	48.130	-0.753
	Min	47.494	46.213	-1.565
	Std Dev	0.767	0.998	0.559



13.57 PSA_DT	
Test Site	
Tester	
Test Number	
Max Limit	55 ns
Min Limit	43 ns

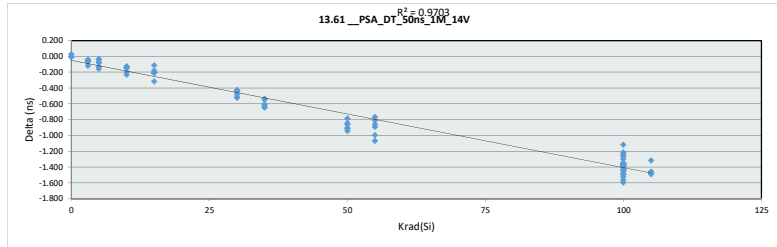
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.261	48.480	47.755	48.350	47.593	48.098	47.157	47.022	46.997	46.213	46.255
Average	48.474	49.291	48.548	49.181	48.422	48.883	48.046	48.260	48.262	47.369	46.958
Max	48.784	50.425	49.479	50.314	49.431	50.041	48.981	49.018	48.984	48.824	47.924
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000



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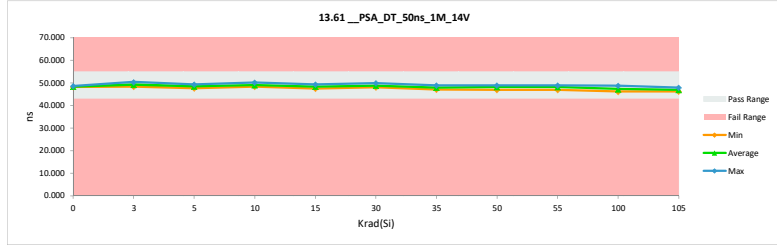
13.61_PSA_DT_50ns_1M_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	55 55
Min Limit	43 43

Krad(Si)	Serial #	PRE	POST	Delta
0	117	48.245	48.240	-0.005
0	118	48.741	48.747	0.006
0	122	48.361	48.395	0.034
3	61	48.972	48.933	-0.039
3	62	50.482	50.446	-0.036
3	63	48.514	48.454	-0.060
3	64	49.532	49.421	-0.111
3	65	49.271	49.217	-0.054
3	66	49.307	49.268	-0.039
5	67	48.792	48.725	-0.067
5	68	48.115	48.081	-0.034
5	69	47.934	47.810	-0.124
5	70	47.753	47.724	-0.029
5	71	49.596	49.445	-0.151
5	72	49.517	49.442	-0.075
10	73	48.972	48.852	-0.120
10	74	50.482	50.290	-0.192
10	75	48.514	48.388	-0.126
10	76	49.532	49.307	-0.225
10	77	49.271	49.134	-0.137
10	78	49.307	49.164	-0.143
15	79	48.792	48.620	-0.172
15	80	48.115	47.922	-0.193
15	81	47.934	47.625	-0.309
15	82	47.753	47.551	-0.202
15	83	49.596	49.390	-0.206
15	84	49.517	49.407	-0.110
30	86	48.972	48.545	-0.427
30	89	50.482	50.043	-0.439
30	91	48.514	48.092	-0.422
30	92	49.532	49.054	-0.478
30	94	49.271	48.763	-0.508
30	95	49.307	48.791	-0.516
35	100	48.792	48.192	-0.600
35	101	48.115	47.587	-0.528
35	102	47.934	47.295	-0.639
35	103	47.753	47.212	-0.541
35	104	49.596	48.977	-0.619
35	105	49.517	48.880	-0.637
50	106	49.865	49.028	-0.837
50	107	49.211	48.354	-0.857
50	108	47.838	47.056	-0.782
50	109	48.894	47.957	-0.937
50	110	49.370	48.460	-0.910
50	111	49.640	48.744	-0.896
55	113	49.584	48.698	-0.886
55	114	47.784	46.978	-0.806
55	115	49.868	48.804	-1.064
55	116	49.744	48.983	-0.761
55	126	48.680	47.826	-0.854
55	128	49.329	48.338	-0.991
100	129	49.245	47.904	-1.341
100	131	48.179	46.591	-1.588
100	132	48.677	47.243	-1.434
100	133	47.942	46.506	-1.436
100	134	49.587	48.028	-1.559
100	135	49.076	47.644	-1.432
100	138	49.270	47.907	-1.363
100	139	47.626	46.250	-1.376
100	140	47.810	46.439	-1.371
100	141	49.477	47.985	-1.492
100	142	47.851	46.496	-1.355
100	143	47.692	46.301	-1.391
100	144	49.192	47.850	-1.342
100	220	48.790	47.421	-1.369
100	221	49.308	48.194	-1.114
100	224	47.959	46.727	-1.232
100	225	49.329	47.979	-1.350
100	226	50.329	48.814	-1.515
100	228	49.455	47.984	-1.471
100	232	47.476	46.219	-1.257
100	233	49.150	47.940	-1.210
100	236	47.812	46.519	-1.293
100	238	49.198	47.770	-1.428
100	243	49.559	48.157	-1.402
105	244	48.002	46.551	-1.451
105	246	47.802	46.345	-1.457
105	248	48.894	47.410	-1.484
105	374	47.975	46.666	-1.309
105	376	49.467	47.993	-1.474
105	105	50.482	50.446	0.034
	Average	48.883	48.131	-0.752
	Min	47.476	46.219	-1.588
	Std Dev	0.773	0.995	0.556



13.61_PSA_DT_50ns_1M_14V	
Test Site	
Tester	
Test Number	
Max Limit	55 ns
Min Limit	43 ns

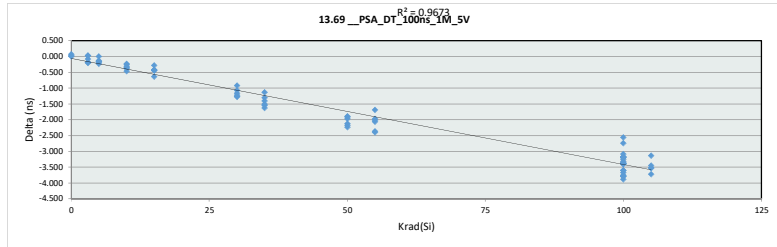
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.240	48.454	47.724	48.388	47.551	48.092	47.212	47.056	46.978	46.219	46.345
Average	48.461	49.290	48.538	49.189	48.419	48.881	48.024	48.267	48.271	47.370	46.993
Max	48.747	50.446	49.445	50.290	49.407	50.043	48.977	49.028	48.983	48.814	47.993
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000



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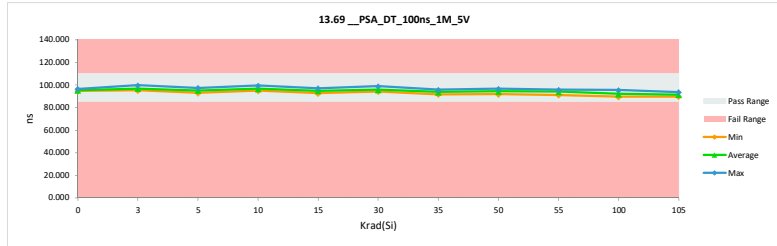
13.69_PSA_DT_100ns_1M_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	110 110
Min Limit	85 85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	94.675	94.691	0.016
0	118	96.440	96.478	0.038
0	122	94.897	94.981	0.084
3	61	95.431	95.475	0.044
3	62	99.889	99.824	-0.065
3	63	95.486	95.312	-0.174
3	64	97.108	96.930	-0.178
3	65	96.827	96.638	-0.189
3	66	96.803	96.838	0.035
5	67	95.613	95.497	-0.116
5	68	94.254	94.103	-0.151
5	69	93.862	93.707	-0.155
5	70	92.964	92.980	0.016
5	71	97.333	97.115	-0.218
5	72	97.490	97.332	-0.158
10	73	95.431	95.181	-0.250
10	74	99.889	99.516	-0.373
10	75	95.486	95.034	-0.452
10	76	97.108	96.787	-0.321
10	77	96.827	96.501	-0.326
10	78	96.803	96.581	-0.222
15	79	95.613	95.197	-0.416
15	80	94.254	93.820	-0.434
15	81	93.862	93.235	-0.627
15	82	92.964	92.697	-0.267
15	83	97.333	96.908	-0.425
15	84	97.490	97.075	-0.415
30	86	95.431	94.522	-0.909
30	89	99.889	98.833	-1.056
30	91	95.486	94.221	-1.265
30	92	97.108	95.961	-1.147
30	94	96.827	95.579	-1.248
30	95	96.803	95.592	-1.211
35	100	95.613	94.215	-1.398
35	101	94.254	93.136	-1.118
35	102	93.862	92.323	-1.539
35	103	92.964	91.672	-1.292
35	104	97.333	95.838	-1.495
35	105	97.490	95.875	-1.615
50	106	98.563	96.599	-1.964
50	107	95.935	94.033	-1.902
50	108	93.747	91.867	-1.880
50	109	95.555	93.441	-2.114
50	110	97.445	95.281	-2.164
50	111	97.593	95.364	-2.229
55	113	97.404	95.355	-2.049
55	114	93.056	91.098	-1.958
55	115	97.653	95.294	-2.359
55	116	97.544	95.870	-1.674
55	126	95.591	93.592	-1.999
55	128	96.250	93.866	-2.384
100	129	96.687	93.537	-3.150
100	131	94.159	90.379	-3.780
100	132	95.526	91.763	-3.763
100	133	94.345	90.750	-3.595
100	134	97.561	93.687	-3.874
100	135	96.111	92.744	-3.367
100	138	96.131	92.969	-3.162
100	139	93.285	90.051	-3.234
100	140	93.428	90.078	-3.350
100	141	97.040	93.383	-3.657
100	142	93.719	90.412	-3.307
100	143	93.255	89.670	-3.585
100	144	96.747	93.573	-3.174
100	220	95.186	91.820	-3.366
100	221	96.989	94.261	-2.728
100	224	93.877	90.691	-3.186
100	225	97.042	93.643	-3.399
100	226	99.320	95.582	-3.738
100	228	97.229	93.447	-3.782
100	232	92.657	89.580	-3.077
100	233	96.405	93.854	-2.551
100	236	93.372	90.207	-3.165
100	238	95.907	92.533	-3.374
100	243	97.518	94.142	-3.376
105	244	93.927	90.413	-3.514
105	246	93.466	89.758	-3.708
105	248	95.644	92.122	-3.522
105	374	94.169	91.047	-3.122
105	376	97.084	93.642	-3.442
	Max	99.889	99.824	0.084
	Average	95.866	94.070	-1.796
	Min	92.657	89.580	-3.874
	Std Dev	1.762	2.360	1.375



13.69_PSA_DT	
Test Site	
Tester	
Test Number	
Max Limit	110 ns
Min Limit	85 ns

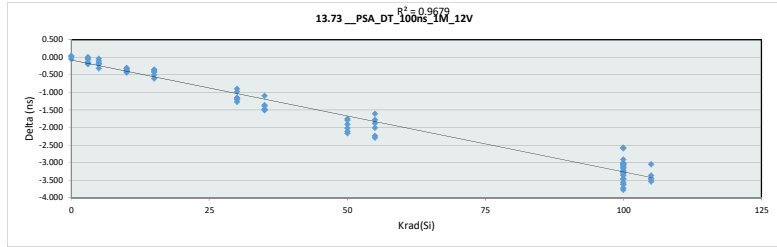
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.691	95.312	92.980	95.034	92.697	94.221	91.672	91.867	91.098	89.580	89.758
Average	95.383	96.836	95.122	96.600	94.822	95.785	93.843	94.431	94.179	92.198	91.396
Max	96.478	99.824	97.332	99.516	97.075	98.833	95.875	96.599	95.870	95.582	93.642
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



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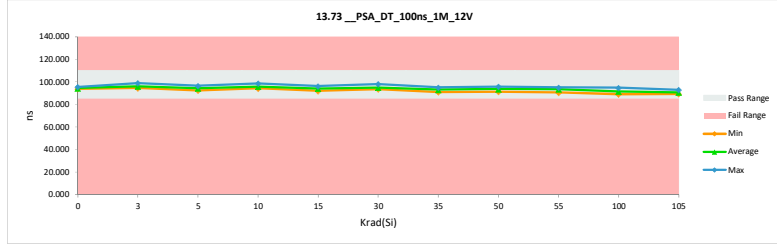
13.73_PSA_DT_100ns_1M_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	110 110
Min Limit	85 85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	93.956	93.978	0.022
0	118	95.586	95.551	-0.035
0	122	94.171	94.221	0.050
3	61	94.782	94.797	0.015
3	62	99.041	99.030	-0.011
3	63	94.714	94.586	-0.128
3	64	96.399	96.211	-0.188
3	65	95.940	95.773	-0.167
3	66	96.152	96.093	-0.059
5	67	94.878	94.785	-0.093
5	68	93.547	93.383	-0.164
5	69	93.207	93.019	-0.188
5	70	92.442	92.413	-0.029
5	71	96.595	96.296	-0.299
5	72	96.708	96.555	-0.153
10	73	94.782	94.492	-0.290
10	74	99.041	98.640	-0.401
10	75	94.714	94.289	-0.425
10	76	96.399	96.029	-0.370
10	77	95.940	95.633	-0.307
10	78	96.152	95.831	-0.321
15	79	94.878	94.469	-0.409
15	80	93.547	93.156	-0.391
15	81	93.207	92.610	-0.597
15	82	92.442	92.102	-0.340
15	83	96.595	96.104	-0.491
15	84	96.708	96.360	-0.348
30	86	94.782	93.897	-0.885
30	89	99.041	98.077	-0.964
30	91	94.714	93.574	-1.140
30	92	96.399	95.253	-1.146
30	94	95.940	94.744	-1.196
30	95	96.152	94.897	-1.255
35	100	94.878	93.526	-1.352
35	101	93.547	92.463	-1.084
35	102	93.207	91.742	-1.465
35	103	92.442	91.061	-1.381
35	104	96.595	95.121	-1.474
35	105	96.708	95.211	-1.497
50	106	97.735	95.839	-1.896
50	107	95.230	93.455	-1.775
50	108	93.007	91.268	-1.739
50	109	94.888	92.882	-2.006
50	110	96.603	94.451	-2.152
50	111	96.784	94.683	-2.101
55	113	96.579	94.581	-1.998
55	114	92.434	90.663	-1.771
55	115	96.899	94.673	-2.226
55	116	96.792	95.192	-1.600
55	126	94.816	92.946	-1.870
55	128	95.684	93.407	-2.277
100	129	95.887	92.831	-3.056
100	131	93.569	89.856	-3.713
100	132	94.802	91.198	-3.604
100	133	93.432	90.080	-3.352
100	134	96.817	93.050	-3.767
100	135	95.488	92.219	-3.269
100	138	95.435	92.434	-3.001
100	139	92.544	89.466	-3.078
100	140	92.707	89.587	-3.120
100	141	96.267	92.811	-3.456
100	142	92.950	89.784	-3.166
100	143	92.585	89.139	-3.446
100	144	95.893	92.874	-3.019
100	220	94.587	91.274	-3.313
100	221	96.211	93.632	-2.579
100	224	93.134	90.131	-3.003
100	225	96.147	93.026	-3.121
100	226	98.453	94.840	-3.613
100	228	96.444	92.895	-3.549
100	232	92.069	89.166	-2.903
100	233	95.653	93.089	-2.564
100	236	92.667	89.635	-3.032
100	238	95.256	92.040	-3.216
100	243	96.684	93.440	-3.244
105	244	93.241	89.781	-3.460
105	246	92.890	89.363	-3.527
105	248	94.875	91.439	-3.436
105	374	93.401	90.363	-3.038
105	376	96.362	93.008	-3.354
105	Max	99.041	99.030	0.050
105	Average	95.135	93.406	-1.730
105	Min	92.069	89.139	-3.767
105	Std Dev	1.709	2.277	1.307



13.73_PSA_DT	
Test Site	
Tester	
Test Number	
Max Limit	110 ns
Min Limit	85 ns

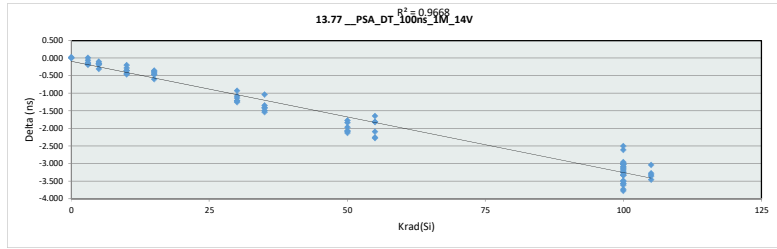
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.978	94.586	92.413	94.289	92.102	93.574	91.061	91.268	90.663	89.139	89.363
Average	94.583	96.082	94.409	95.819	94.134	95.074	93.187	93.763	93.577	91.604	90.791
Max	95.551	99.030	96.555	98.640	96.360	98.077	95.211	95.839	95.192	94.840	93.008
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



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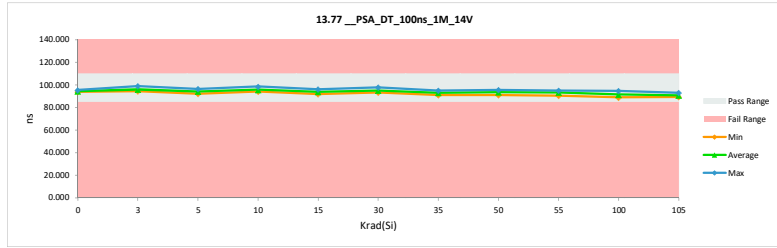
13.77 PSA_DT_100ns_1M_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	110 110
Min Limit	85 85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	93.955	93.979	0.024
0	118	95.575	95.590	0.015
0	122	94.179	94.225	0.046
3	61	94.745	94.766	0.021
3	62	99.120	99.006	-0.114
3	63	94.728	94.594	-0.134
3	64	96.375	96.217	-0.158
3	65	95.940	95.760	-0.180
3	66	96.129	96.082	-0.047
5	67	94.902	94.774	-0.128
5	68	93.536	93.360	-0.176
5	69	93.195	93.043	-0.152
5	70	92.466	92.382	-0.084
5	71	96.536	96.245	-0.291
5	72	96.722	96.577	-0.145
10	73	94.745	94.564	-0.181
10	74	99.120	98.673	-0.447
10	75	94.728	94.310	-0.418
10	76	96.375	96.034	-0.341
10	77	95.940	95.596	-0.344
10	78	96.129	95.861	-0.268
15	79	94.902	94.442	-0.460
15	80	93.536	93.134	-0.402
15	81	93.195	92.611	-0.584
15	82	92.466	92.128	-0.338
15	83	96.536	96.099	-0.437
15	84	96.722	96.353	-0.369
30	86	94.745	93.830	-0.915
30	89	99.120	98.048	-1.072
30	91	94.728	93.528	-1.200
30	92	96.375	95.252	-1.123
30	94	95.940	94.735	-1.205
30	95	96.129	94.888	-1.241
35	100	94.902	93.499	-1.403
35	101	93.536	92.514	-1.022
35	102	93.195	91.715	-1.480
35	103	92.466	91.135	-1.331
35	104	96.536	95.138	-1.398
35	105	96.722	95.202	-1.520
50	106	97.764	95.814	-1.950
50	107	95.234	93.410	-1.824
50	108	93.002	91.242	-1.760
50	109	94.909	92.839	-2.070
50	110	96.512	94.464	-2.048
50	111	96.757	94.640	-2.117
55	113	96.626	94.551	-2.075
55	114	92.446	90.646	-1.800
55	115	96.882	94.643	-2.239
55	116	96.775	95.144	-1.631
55	126	94.796	92.992	-1.804
55	128	95.643	93.381	-2.262
100	129	95.882	92.814	-3.068
100	131	93.571	89.804	-3.767
100	132	94.767	91.211	-3.556
100	133	93.436	90.114	-3.322
100	134	96.747	93.029	-3.718
100	135	95.479	92.169	-3.310
100	138	95.413	92.440	-2.973
100	139	92.521	89.433	-3.088
100	140	92.700	89.575	-3.125
100	141	96.247	92.776	-3.471
100	142	93.007	89.842	-3.165
100	143	92.587	89.084	-3.503
100	144	95.889	92.879	-3.010
100	220	94.576	91.256	-3.320
100	221	96.228	93.628	-2.600
100	224	93.160	90.161	-2.999
100	225	96.225	92.986	-3.239
100	226	98.438	94.841	-3.597
100	228	96.456	92.889	-3.567
100	232	92.028	89.087	-2.941
100	233	95.596	93.107	-2.489
100	236	92.633	89.619	-3.014
100	238	95.267	92.025	-3.242
100	243	96.682	93.479	-3.203
105	244	93.115	89.804	-3.311
105	246	92.842	89.394	-3.448
105	248	94.923	91.582	-3.341
105	374	93.382	90.357	-3.025
105	375	96.341	93.077	-3.264
Max		99.120	99.006	0.046
Average		95.130	93.401	-1.728
Min		92.028	89.084	-3.767
Std Dev		1.715	2.275	1.302



13.77 PSA_DT	
Test Site	
Tester	
Test Number	
Max Limit	110 ns
Min Limit	85 ns

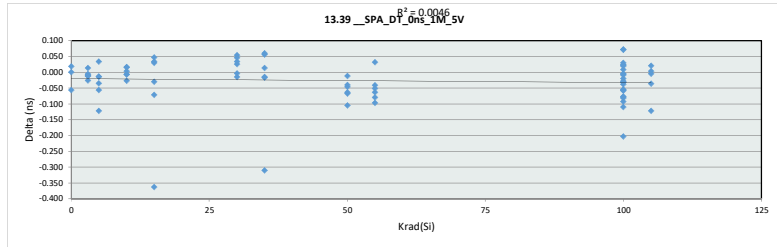
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.979	94.594	92.382	94.310	92.128	93.528	91.135	91.242	90.646	89.084	89.394
Average	94.598	96.071	94.397	95.840	94.128	95.047	93.201	93.735	93.560	91.594	90.843
Max	95.590	99.006	96.577	98.673	96.353	98.048	95.202	95.814	95.144	94.841	93.077
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



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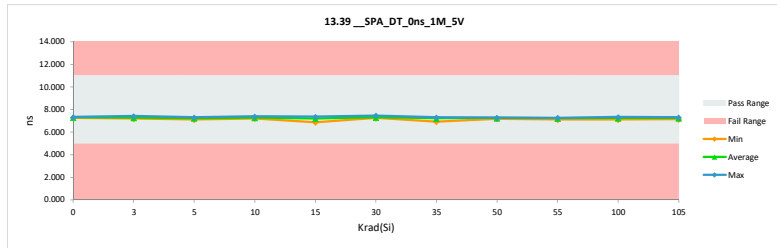
13.39 SPA_DT_0ns_1M_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	11 11
Min Limit	5 5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.326	7.328	0.002
0	118	7.390	7.335	-0.055
0	122	7.256	7.276	0.020
3	61	7.256	7.252	-0.004
3	62	7.281	7.256	-0.025
3	63	7.253	7.268	0.015
3	64	7.214	7.204	-0.010
3	65	7.430	7.423	-0.007
3	66	7.253	7.243	-0.010
5	67	7.243	7.278	0.035
5	68	7.221	7.208	-0.013
5	69	7.286	7.252	-0.034
5	70	7.240	7.119	-0.121
5	71	7.328	7.315	-0.013
5	72	7.306	7.251	-0.055
10	73	7.256	7.273	0.017
10	74	7.281	7.276	-0.005
10	75	7.253	7.271	0.018
10	76	7.214	7.209	-0.005
10	77	7.430	7.404	-0.026
10	78	7.253	7.258	0.005
15	79	7.243	7.291	0.048
15	80	7.221	7.256	0.035
15	81	7.286	7.216	-0.070
15	82	7.240	6.878	-0.362
15	83	7.328	7.359	0.031
15	84	7.306	7.277	-0.029
30	86	7.256	7.283	0.027
30	89	7.281	7.267	-0.014
30	91	7.253	7.251	-0.002
30	92	7.214	7.269	0.055
30	94	7.430	7.477	0.047
30	95	7.253	7.288	0.035
35	100	7.243	7.300	0.057
35	101	7.221	7.282	0.061
35	102	7.286	7.272	-0.014
35	103	7.240	6.931	-0.309
35	104	7.328	7.312	-0.016
35	105	7.306	7.321	0.015
50	106	7.316	7.250	-0.066
50	107	7.288	7.184	-0.104
50	108	7.308	7.263	-0.045
50	109	7.235	7.225	-0.010
50	110	7.317	7.278	-0.039
50	111	7.340	7.278	-0.062
55	113	7.308	7.258	-0.050
55	114	7.296	7.256	-0.040
55	115	7.241	7.274	0.033
55	116	7.238	7.160	-0.078
55	126	7.183	7.121	-0.062
55	128	7.360	7.264	-0.096
100	129	7.357	7.282	-0.075
100	131	7.248	7.321	0.073
100	132	7.249	7.172	-0.077
100	133	7.199	7.271	0.072
100	134	7.310	7.306	-0.004
100	135	7.283	7.253	-0.030
100	138	7.323	7.266	-0.057
100	139	7.298	7.279	-0.019
100	140	7.250	7.275	0.025
100	141	7.393	7.302	-0.091
100	142	7.291	7.264	-0.027
100	143	7.272	7.235	-0.037
100	144	7.240	7.233	-0.007
100	220	7.363	7.254	-0.109
100	221	7.377	7.175	-0.202
100	224	7.265	7.234	-0.031
100	225	7.292	7.212	-0.080
100	226	7.319	7.350	0.031
100	228	7.273	7.270	-0.003
100	232	7.157	7.104	-0.053
100	233	7.208	7.232	0.024
100	236	7.370	7.315	-0.055
100	238	7.259	7.269	0.010
100	243	7.312	7.333	0.021
105	244	7.245	7.267	0.022
105	246	7.273	7.270	-0.003
105	248	7.245	7.250	0.005
105	374	7.353	7.318	-0.035
105	376	7.257	7.136	-0.121
105	Max	7.430	7.477	0.073
105	Average	7.283	7.256	-0.027
105	Min	7.157	6.878	-0.362
105	Std Dev	0.056	0.083	0.070



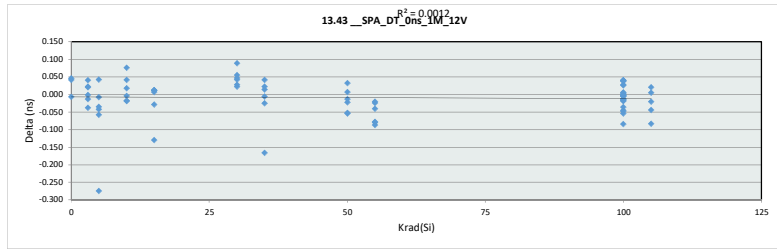
13.39 SPA_DT_0	
Test Site	
Tester	
Test Number	
Max Limit	11 ns
Min Limit	5 ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5,000	5,000	5,000	5,000	5,000	5,000	5,000	5,000	5,000	5,000	5,000
Min	7.276	7.204	7.119	7.209	6.878	7.251	6.931	7.184	7.121	7.104	7.136
Average	7.313	7.274	7.237	7.282	7.213	7.306	7.236	7.246	7.222	7.259	7.248
Max	7.335	7.423	7.315	7.404	7.359	7.477	7.321	7.278	7.274	7.350	7.318
UL	11,000	11,000	11,000	11,000	11,000	11,000	11,000	11,000	11,000	11,000	11,000

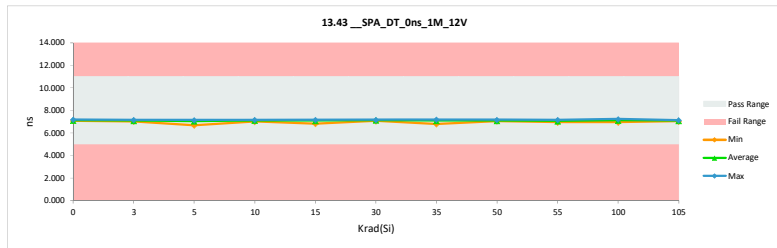


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13.43 SPA_DT_Ons_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.151	7.198	0.047
0	118	7.100	7.094	-0.006
0	122	7.069	7.111	0.042
3	61	7.063	7.051	-0.012
3	62	7.093	7.114	0.021
3	63	7.124	7.147	0.023
3	64	7.038	7.038	0.000
3	65	7.135	7.176	0.041
3	66	7.134	7.097	-0.037
5	67	7.145	7.088	-0.057
5	68	7.179	7.145	-0.034
5	69	7.103	7.146	0.043
5	70	6.968	6.694	-0.274
5	71	7.182	7.141	-0.041
5	72	7.136	7.129	-0.007
10	73	7.063	7.105	0.042
10	74	7.093	7.170	0.077
10	75	7.124	7.143	0.019
10	76	7.038	7.021	-0.017
10	77	7.135	7.132	-0.003
10	78	7.134	7.116	-0.018
15	79	7.145	7.117	-0.028
15	80	7.179	7.192	0.013
15	81	7.103	7.116	0.013
15	82	6.968	6.839	-0.129
15	83	7.182	7.194	0.012
15	84	7.136	7.144	0.008
30	86	7.063	7.092	0.029
30	89	7.093	7.183	0.090
30	91	7.124	7.180	0.056
30	92	7.038	7.081	0.043
30	94	7.135	7.183	0.048
30	95	7.134	7.157	0.023
35	100	7.145	7.140	-0.005
35	101	7.179	7.202	0.023
35	102	7.103	7.118	0.015
35	103	6.968	6.803	-0.165
35	104	7.182	7.158	-0.024
35	105	7.136	7.178	0.042
50	106	7.156	7.105	-0.051
50	107	7.051	7.059	0.008
50	108	7.096	7.129	0.033
50	109	7.111	7.099	-0.012
50	110	7.134	7.081	-0.053
50	111	7.194	7.173	-0.021
55	113	7.095	7.075	-0.020
55	114	7.107	7.067	-0.040
55	115	7.181	7.158	-0.023
55	116	7.124	7.047	-0.077
55	126	7.044	6.958	-0.086
55	128	7.255	7.176	-0.079
100	129	7.156	7.112	-0.044
100	131	7.161	7.157	-0.004
100	132	7.123	7.076	-0.047
100	133	7.105	7.094	-0.011
100	134	7.188	7.171	-0.017
100	135	7.186	7.188	0.002
100	138	7.199	7.181	-0.018
100	139	7.132	7.132	0.000
100	140	7.131	7.136	0.005
100	141	7.205	7.246	0.041
100	142	7.088	7.087	-0.001
100	143	7.081	7.108	0.027
100	144	7.075	7.104	0.029
100	220	7.127	7.123	-0.004
100	221	7.103	7.110	0.007
100	224	7.181	7.098	-0.083
100	225	7.134	7.120	-0.014
100	226	7.135	7.100	-0.035
100	228	7.140	7.128	-0.012
100	232	7.010	6.957	-0.053
100	233	7.064	7.063	-0.001
100	236	7.146	7.187	0.041
100	238	7.140	7.179	0.039
100	243	7.172	7.212	0.040
105	244	7.130	7.048	-0.082
105	246	7.142	7.148	0.006
105	248	7.119	7.076	-0.043
105	374	7.146	7.126	-0.020
105	376	7.095	7.116	0.021
	Max	7.255	7.246	0.090
	Average	7.120	7.111	-0.009
	Min	6.968	6.694	-0.274
	Std Dev	0.054	0.085	0.053



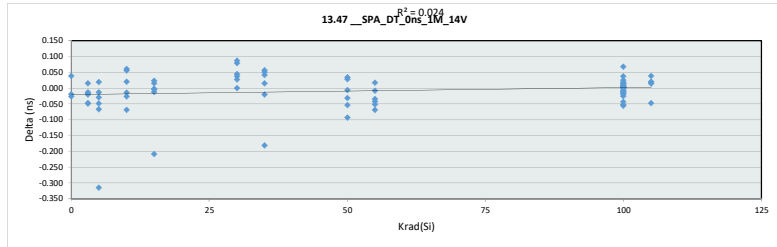
13.43 SPA_DT											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.094	7.038	6.694	7.021	6.839	7.081	6.803	7.059	6.958	6.957	7.048
Average	7.134	7.104	7.057	7.115	7.100	7.146	7.100	7.108	7.080	7.128	7.103
Max	7.198	7.176	7.146	7.170	7.194	7.183	7.202	7.173	7.176	7.246	7.148
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000



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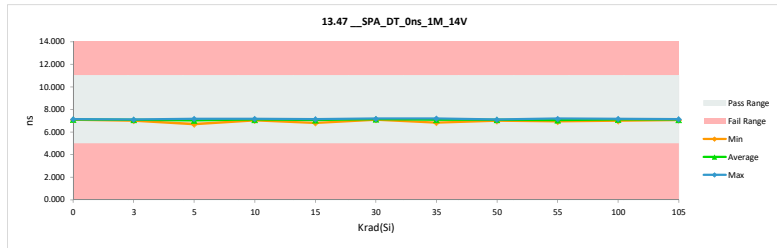
13.47 SPA_DT_Ons_1M_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	11 11
Min Limit	5 5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.125	7.165	0.040
0	118	7.145	7.127	-0.018
0	122	7.128	7.103	-0.025
3	61	7.083	7.099	0.016
3	62	7.124	7.112	-0.012
3	63	7.156	7.141	-0.015
3	64	7.070	7.023	-0.047
3	65	7.186	7.138	-0.048
3	66	7.084	7.065	-0.019
5	67	7.096	7.048	-0.048
5	68	7.131	7.119	-0.012
5	69	7.140	7.074	-0.066
5	70	7.028	6.714	-0.314
5	71	7.170	7.190	0.020
5	72	7.139	7.111	-0.028
10	73	7.083	7.104	0.021
10	74	7.124	7.186	0.062
10	75	7.156	7.143	-0.013
10	76	7.070	7.045	-0.025
10	77	7.186	7.118	-0.068
10	78	7.084	7.141	0.057
15	79	7.096	7.093	-0.003
15	80	7.131	7.147	0.016
15	81	7.140	7.129	-0.011
15	82	7.028	6.821	-0.207
15	83	7.170	7.169	-0.001
15	84	7.139	7.163	0.024
30	86	7.083	7.121	0.038
30	89	7.124	7.204	0.080
30	91	7.156	7.202	0.046
30	92	7.070	7.098	0.028
30	94	7.186	7.187	0.001
30	95	7.084	7.172	0.088
35	100	7.096	7.154	0.058
35	101	7.131	7.174	0.043
35	102	7.140	7.121	-0.019
35	103	7.028	6.848	-0.180
35	104	7.170	7.222	0.052
35	105	7.139	7.155	0.016
50	106	7.160	7.107	-0.053
50	107	7.100	7.008	-0.092
50	108	7.113	7.108	-0.005
50	109	7.083	7.118	0.035
50	110	7.091	7.120	0.029
50	111	7.158	7.128	-0.030
55	113	7.043	7.061	0.018
55	114	7.129	7.061	-0.068
55	115	7.183	7.150	-0.033
55	116	7.124	7.083	-0.041
55	126	7.004	6.953	-0.051
55	128	7.210	7.203	-0.007
100	129	7.113	7.114	0.001
100	131	7.169	7.156	-0.013
100	132	7.077	7.085	0.008
100	133	7.138	7.086	-0.052
100	134	7.180	7.166	-0.014
100	135	7.144	7.151	0.007
100	138	7.170	7.146	-0.024
100	139	7.172	7.130	-0.042
100	140	7.116	7.185	0.069
100	141	7.243	7.188	-0.055
100	142	7.096	7.096	0.000
100	143	7.125	7.119	-0.006
100	144	7.098	7.089	-0.009
100	220	7.116	7.127	0.011
100	221	7.118	7.138	0.020
100	224	7.117	7.120	0.003
100	225	7.116	7.124	0.008
100	226	7.083	7.086	0.003
100	228	7.134	7.116	-0.018
100	232	7.001	7.005	0.004
100	233	7.080	7.096	0.016
100	236	7.147	7.173	0.026
100	238	7.140	7.178	0.038
100	243	7.167	7.183	0.016
105	244	7.131	7.084	-0.047
105	246	7.151	7.172	0.021
105	248	7.086	7.101	0.015
105	374	7.105	7.144	0.039
105	376	7.076	7.096	0.020
105	Max	7.243	7.222	0.088
105	Average	7.120	7.111	-0.009
105	Min	7.001	6.714	-0.314
105	Std Dev	0.045	0.080	0.059



13.47 SPA_DT	
Test Site	
Tester	
Test Number	
Max Limit	11 ns
Min Limit	5 ns

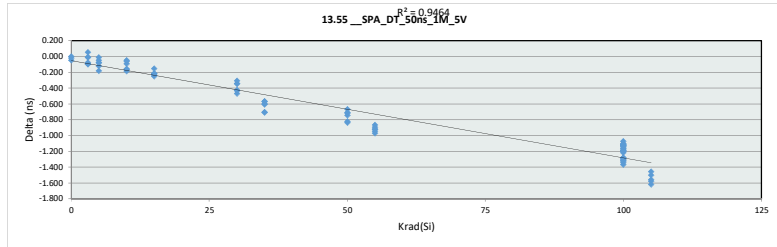
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.103	7.023	6.714	7.045	6.821	7.098	6.848	7.008	6.953	7.005	7.084
Average	7.132	7.096	7.043	7.123	7.087	7.164	7.112	7.098	7.085	7.127	7.119
Max	7.165	7.141	7.190	7.186	7.169	7.204	7.222	7.128	7.203	7.188	7.172
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000



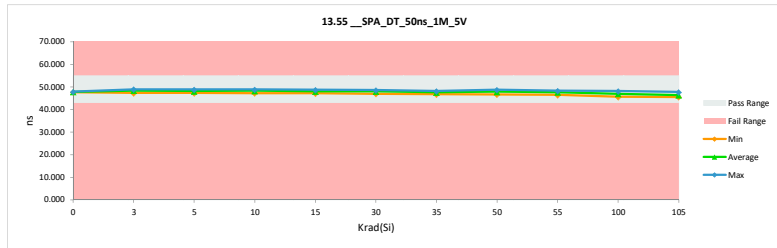
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		13.55 SPA_DT_50ns_1M_5V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	55	55	
Min Limit	43	43	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	47.893	47.899	0.006
0	118	47.754	47.710	-0.044
0	122	47.819	47.805	-0.014
3	61	48.655	48.646	-0.009
3	62	48.469	48.467	-0.002
3	63	47.420	47.348	-0.072
3	64	49.042	48.950	-0.092
3	65	48.257	48.164	-0.093
3	66	48.862	48.919	0.057
5	67	48.211	48.141	-0.070
5	68	47.341	47.334	-0.007
5	69	48.016	47.838	-0.178
5	70	48.195	48.117	-0.078
5	71	48.726	48.610	-0.116
5	72	48.926	48.883	-0.043
10	73	48.655	48.613	-0.042
10	74	48.469	48.412	-0.057
10	75	47.420	47.239	-0.181
10	76	49.042	48.890	-0.152
10	77	48.257	48.100	-0.157
10	78	48.862	48.774	-0.088
15	79	48.211	47.998	-0.213
15	80	47.341	47.126	-0.215
15	81	48.016	47.768	-0.248
15	82	48.195	48.046	-0.149
15	83	48.726	48.496	-0.230
15	84	48.926	48.718	-0.208
30	86	48.655	48.353	-0.302
30	89	48.469	48.129	-0.340
30	91	47.420	47.957	-0.463
30	92	49.042	48.615	-0.427
30	94	48.257	47.836	-0.421
30	95	48.862	48.523	-0.339
35	100	48.211	47.649	-0.562
35	101	47.341	46.777	-0.564
35	102	48.016	47.315	-0.701
35	103	48.195	47.601	-0.594
35	104	48.726	48.124	-0.602
35	105	48.926	48.224	-0.702
50	106	49.088	48.386	-0.702
50	107	49.551	48.734	-0.817
50	108	47.382	46.671	-0.711
50	109	48.480	47.819	-0.661
50	110	48.283	47.450	-0.833
50	111	49.009	48.271	-0.738
55	113	49.241	48.299	-0.942
55	114	47.412	46.493	-0.919
55	115	48.808	47.919	-0.889
55	116	49.146	48.283	-0.863
55	126	47.934	47.021	-0.913
55	128	49.053	48.085	-0.968
100	129	48.927	47.617	-1.310
100	131	47.592	46.412	-1.180
100	132	48.045	46.762	-1.283
100	133	47.457	46.245	-1.212
100	134	49.182	47.992	-1.190
100	135	48.625	47.465	-1.160
100	138	48.989	47.625	-1.364
100	139	46.830	45.631	-1.199
100	140	47.406	46.107	-1.299
100	141	48.906	47.587	-1.319
100	142	47.363	46.267	-1.096
100	143	47.241	45.904	-1.337
100	144	47.928	46.764	-1.164
100	220	48.252	47.134	-1.118
100	221	48.067	46.936	-1.131
100	224	47.191	46.068	-1.123
100	225	48.845	47.567	-1.278
100	226	48.761	47.651	-1.110
100	228	49.082	47.963	-1.119
100	232	47.867	46.725	-1.142
100	233	48.410	47.344	-1.066
100	236	47.250	46.146	-1.104
100	238	49.379	48.189	-1.190
100	243	48.155	46.882	-1.273
105	244	47.318	45.746	-1.572
105	246	47.407	45.955	-1.452
105	248	48.510	47.013	-1.497
105	374	47.211	45.596	-1.615
105	376	49.334	47.779	-1.555
	Max	49.551	48.950	0.057
	Average	48.284	47.595	-0.689
	Min	46.830	45.596	-1.615
	Std Dev	0.668	0.878	0.510



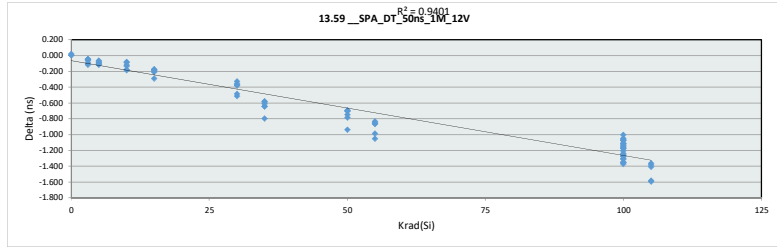
		13.55 SPA_DT_5								
Test Site										
Tester										
Test Number										
Max Limit	55	ns								
Min Limit	43	ns								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	47.710	47.348	47.334	47.239	47.126	46.957	46.777	46.671	46.493	45.596
Average	47.805	48.416	48.154	48.338	48.025	48.069	47.615	47.889	47.683	46.958
Max	47.899	48.950	48.883	48.890	48.718	48.615	48.224	48.734	48.299	47.779
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000



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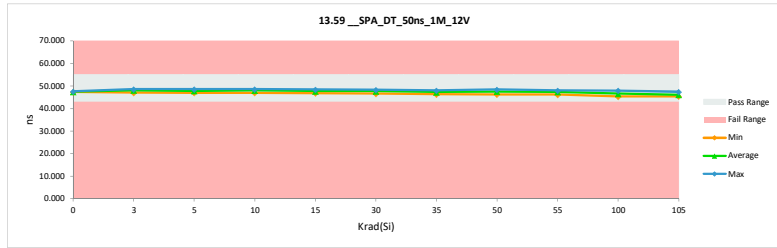
13.59 SPA_DT_50ns_1M_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	55 55
Min Limit	43 43

Krad(Si)	Serial #	PRE	POST	Delta
0	117	47.521	47.545	0.024
0	118	47.312	47.320	0.008
0	122	47.465	47.474	0.009
3	61	48.331	48.286	-0.045
3	62	48.201	48.163	-0.038
3	63	47.102	47.014	-0.088
3	64	48.661	48.603	-0.058
3	65	47.886	47.773	-0.113
3	66	48.586	48.541	-0.045
5	67	47.888	47.776	-0.112
5	68	46.973	46.915	-0.058
5	69	47.668	47.562	-0.106
5	70	47.847	47.761	-0.086
5	71	48.389	48.298	-0.091
5	72	48.641	48.567	-0.074
10	73	48.331	48.248	-0.083
10	74	48.201	48.083	-0.118
10	75	47.102	46.919	-0.183
10	76	48.661	48.583	-0.078
10	77	47.886	47.711	-0.175
10	78	48.586	48.459	-0.127
15	79	47.888	47.713	-0.175
15	80	46.973	46.766	-0.207
15	81	47.668	47.381	-0.287
15	82	47.847	47.677	-0.170
15	83	48.389	48.203	-0.186
15	84	48.641	48.455	-0.186
30	86	48.331	47.964	-0.367
30	89	48.201	47.826	-0.375
30	91	47.102	46.593	-0.509
30	92	48.661	48.311	-0.350
30	94	47.886	47.405	-0.481
30	95	48.586	48.262	-0.324
35	100	47.888	47.280	-0.608
35	101	46.973	46.333	-0.640
35	102	47.668	46.877	-0.791
35	103	47.847	47.264	-0.583
35	104	48.389	47.816	-0.573
35	105	48.641	48.002	-0.639
50	106	48.764	48.020	-0.744
50	107	49.168	48.386	-0.782
50	108	46.944	46.246	-0.698
50	109	48.194	47.500	-0.694
50	110	47.927	46.994	-0.933
50	111	48.657	47.970	-0.687
55	113	48.806	47.947	-0.859
55	114	47.019	46.176	-0.843
55	115	48.536	47.676	-0.860
55	116	48.806	47.976	-0.830
55	126	47.651	46.604	-1.047
55	128	48.795	47.812	-0.983
100	129	48.557	47.251	-1.306
100	131	47.280	46.029	-1.251
100	132	47.746	46.379	-1.367
100	133	47.015	45.844	-1.171
100	134	48.870	47.670	-1.200
100	135	48.329	47.221	-1.108
100	138	48.642	47.298	-1.344
100	139	46.408	45.336	-1.072
100	140	47.075	45.778	-1.297
100	141	48.610	47.261	-1.349
100	142	46.924	45.924	-1.000
100	143	46.864	45.631	-1.233
100	144	47.623	46.347	-1.276
100	220	47.938	46.789	-1.149
100	221	47.720	46.614	-1.106
100	224	46.795	45.740	-1.055
100	225	48.452	47.185	-1.267
100	226	48.402	47.284	-1.118
100	228	48.756	47.597	-1.159
100	232	47.453	46.387	-1.066
100	233	48.181	47.012	-1.169
100	236	46.843	45.800	-1.043
100	238	49.082	47.948	-1.134
100	243	47.801	46.451	-1.350
105	244	46.877	45.498	-1.379
105	246	47.059	45.701	-1.358
105	248	48.177	46.601	-1.576
105	374	46.757	45.352	-1.405
105	376	49.003	47.412	-1.591
	Max	49.168	48.603	0.024
	Average	47.942	47.255	-0.687
	Min	46.408	45.336	-1.591
	Std Dev	0.691	0.883	0.499



13.59 SPA_DT_50ns_1M_12V	
Test Site	
Tester	
Test Number	
Max Limit	55 ns
Min Limit	43 ns

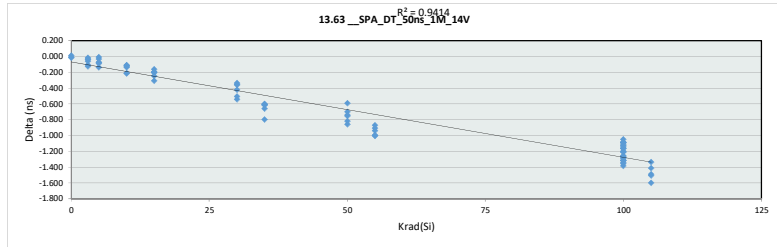
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	47.320	47.014	46.915	46.919	46.766	46.593	46.333	46.246	46.176	45.336	45.352
Average	47.446	48.063	47.813	48.001	47.699	47.727	47.262	47.519	47.365	46.616	46.113
Max	47.545	48.603	48.567	48.583	48.455	48.311	48.002	48.386	47.976	47.948	47.412
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000



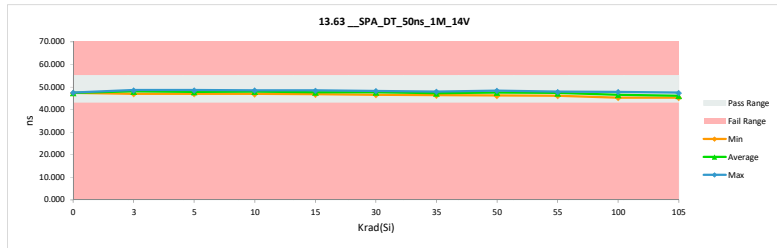
LDR TID Report
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13.63 SPA_DT 50ns_1M_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	55 55
Min Limit	43 43

Krad(Si)	Serial #	PRE	POST	Delta
0	117	47.549	47.539	-0.010
0	118	47.316	47.335	0.019
0	122	47.483	47.481	-0.002
3	61	48.331	48.303	-0.028
3	62	48.227	48.107	-0.120
3	63	47.117	47.017	-0.100
3	64	48.661	48.651	-0.010
3	65	47.891	47.834	-0.057
3	66	48.570	48.534	-0.036
5	67	47.889	47.821	-0.068
5	68	46.979	46.975	-0.004
5	69	47.698	47.565	-0.133
5	70	47.819	47.748	-0.071
5	71	48.419	48.335	-0.084
5	72	48.628	48.602	-0.026
10	73	48.331	48.211	-0.120
10	74	48.227	48.095	-0.132
10	75	47.117	46.911	-0.206
10	76	48.661	48.551	-0.110
10	77	47.891	47.683	-0.208
10	78	48.570	48.465	-0.105
15	79	47.889	47.701	-0.188
15	80	46.979	46.786	-0.193
15	81	47.698	47.397	-0.301
15	82	47.819	47.619	-0.200
15	83	48.419	48.177	-0.242
15	84	48.628	48.471	-0.157
30	86	48.331	47.983	-0.348
30	89	48.227	47.809	-0.418
30	91	47.117	46.580	-0.537
30	92	48.661	48.306	-0.355
30	94	47.891	47.391	-0.500
30	95	48.570	48.237	-0.333
35	100	47.889	47.282	-0.607
35	101	46.979	46.383	-0.596
35	102	47.698	46.907	-0.791
35	103	47.819	47.217	-0.602
35	104	48.419	47.809	-0.610
35	105	48.628	47.973	-0.655
50	106	48.742	48.047	-0.695
50	107	49.181	48.369	-0.812
50	108	46.952	46.203	-0.749
50	109	48.150	47.563	-0.587
50	110	47.906	47.053	-0.853
50	111	48.713	47.979	-0.734
55	113	48.787	47.923	-0.864
55	114	47.051	46.149	-0.902
55	115	48.598	47.609	-0.989
55	116	48.839	47.904	-0.935
55	126	47.620	46.626	-0.994
55	128	48.793	47.796	-0.997
100	129	48.574	47.290	-1.284
100	131	47.248	46.041	-1.207
100	132	47.708	46.396	-1.312
100	133	47.014	45.860	-1.154
100	134	48.828	47.635	-1.193
100	135	48.333	47.230	-1.103
100	138	48.685	47.332	-1.353
100	139	46.382	45.301	-1.081
100	140	47.048	45.736	-1.312
100	141	48.631	47.252	-1.379
100	142	46.944	45.901	-1.043
100	143	46.900	45.592	-1.308
100	144	47.597	46.339	-1.258
100	220	47.948	46.789	-1.159
100	221	47.776	46.621	-1.155
100	224	46.812	45.734	-1.078
100	225	48.453	47.203	-1.250
100	226	48.412	47.286	-1.126
100	228	48.732	47.605	-1.127
100	232	47.449	46.370	-1.079
100	233	48.197	46.995	-1.202
100	236	46.857	45.769	-1.088
100	238	49.160	47.886	-1.274
100	243	47.750	46.417	-1.333
105	244	46.833	45.502	-1.331
105	246	47.069	45.660	-1.409
105	248	48.193	46.598	-1.595
105	374	46.787	45.303	-1.484
105	376	48.973	47.473	-1.500
105	375	49.181	48.651	0.019
Average		47.946	47.252	-0.694
Min		46.382	45.301	-1.595
Std Dev		0.691	0.888	0.502



13.63 SPA_DT 5		5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	47.335	47.017	46.975	46.911	46.786	46.580	46.383	46.203	46.149	45.301
Average	47.452	48.074	47.841	47.986	47.692	47.718	47.262	47.536	47.335	46.608
Max	47.539	48.651	48.602	48.551	48.471	48.306	47.973	48.369	47.923	47.886
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000

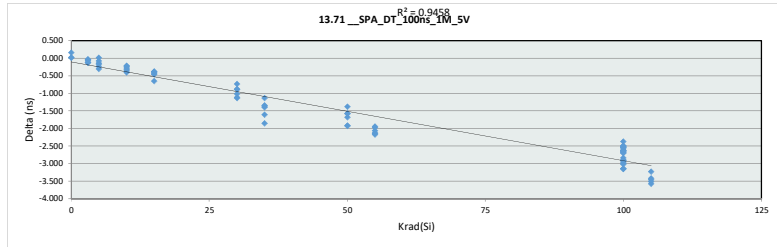


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13.71 SPA_DT_100ns_1M_5V

Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	110 110
Min Limit	85 85

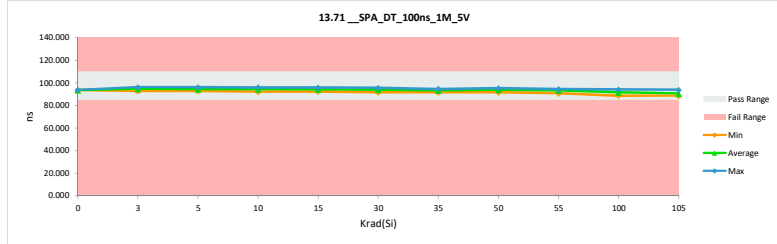
Krad(Si)	Serial #	PRE	POST	Delta
0	117	93.821	93.849	0.028
0	118	93.899	93.936	0.037
0	122	93.624	93.795	0.171
3	61	95.004	94.981	-0.023
3	62	95.541	95.427	-0.114
3	63	92.877	92.822	-0.055
3	64	96.085	95.970	-0.115
3	65	94.002	93.887	-0.115
3	66	96.410	96.384	-0.026
5	67	94.306	94.180	-0.126
5	68	92.868	92.892	0.024
5	69	95.000	94.709	-0.291
5	70	94.775	94.557	-0.218
5	71	95.523	95.363	-0.160
5	72	96.353	96.285	-0.068
10	73	95.004	94.800	-0.204
10	74	95.541	95.271	-0.270
10	75	92.877	92.483	-0.394
10	76	96.085	95.786	-0.299
10	77	94.002	93.654	-0.348
10	78	96.410	96.188	-0.222
15	79	94.306	93.931	-0.375
15	80	92.868	92.504	-0.364
15	81	95.000	94.359	-0.641
15	82	94.775	94.386	-0.389
15	83	95.523	95.075	-0.448
15	84	96.353	95.924	-0.429
30	86	95.004	94.125	-0.879
30	89	95.541	94.671	-0.870
30	91	92.877	91.757	-1.120
30	92	96.085	95.080	-1.005
30	94	94.002	92.898	-1.104
30	95	96.410	95.685	-0.725
35	100	94.306	92.955	-1.351
35	101	92.868	91.747	-1.121
35	102	95.000	93.158	-1.842
35	103	94.775	93.392	-1.383
35	104	95.523	94.188	-1.335
35	105	96.353	94.758	-1.595
50	106	97.046	95.376	-1.670
50	107	97.369	95.465	-1.904
50	108	93.413	91.854	-1.559
50	109	95.160	93.795	-1.365
50	110	94.735	92.818	-1.917
50	111	95.968	94.399	-1.569
55	113	96.849	94.719	-2.130
55	114	93.032	90.917	-2.115
55	115	95.730	93.797	-1.933
55	116	96.662	94.702	-1.960
55	126	94.234	92.074	-2.160
55	128	96.384	94.332	-2.052
100	129	96.181	93.170	-3.011
100	131	93.552	90.724	-2.828
100	132	94.957	91.821	-3.136
100	133	92.815	90.162	-2.653
100	134	96.750	94.065	-2.685
100	135	95.942	93.339	-2.603
100	138	96.255	93.117	-3.138
100	139	91.461	88.811	-2.650
100	140	92.725	89.830	-2.895
100	141	95.824	92.843	-2.981
100	142	93.103	90.743	-2.360
100	143	92.497	89.362	-3.135
100	144	93.440	90.762	-2.678
100	220	94.698	92.177	-2.521
100	221	93.904	91.415	-2.489
100	224	92.612	90.001	-2.611
100	225	96.002	93.121	-2.881
100	226	95.599	93.051	-2.548
100	228	96.402	93.787	-2.615
100	232	94.564	92.068	-2.496
100	233	94.742	92.211	-2.531
100	236	92.293	89.811	-2.482
100	238	97.335	94.394	-2.941
100	243	93.757	90.861	-2.896
105	244	92.629	89.219	-3.410
105	246	93.256	90.041	-3.215
105	248	95.228	91.668	-3.560
105	374	92.163	88.716	-3.447
105	376	97.643	94.143	-3.500
Max		97.643	96.384	0.171
Average		94.781	93.219	-1.562
Min		91.461	88.716	-3.560
Std Dev		1.461	1.923	1.167



13.71 SPA_DT

Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	110 110
Min Limit	85 85

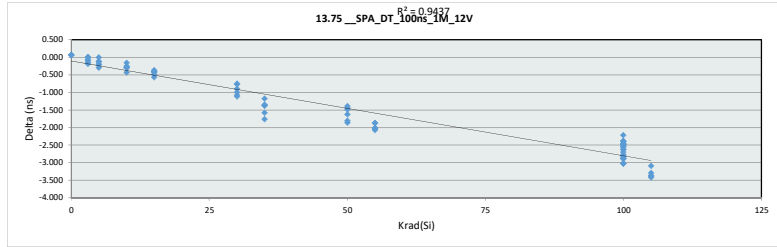
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.795	92.822	92.892	92.483	92.504	91.757	91.747	91.854	90.917	88.811	88.716
Average	93.860	94.912	94.664	94.697	94.363	94.036	93.366	93.951	93.424	91.735	90.757
Max	93.936	96.384	96.285	96.188	95.924	95.685	94.758	95.465	94.719	94.394	94.143
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



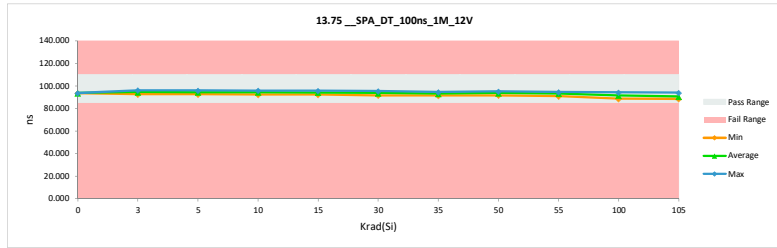
LDR TID Report TPS7H5001-SP

13.75 SPA_DT_100ns_1M_12V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	110	110
Min Limit	85	85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	93.612	93.689	0.077
0	118	93.515	93.589	0.074
0	122	93.496	93.571	0.075
3	61	94.767	94.785	0.018
3	62	95.166	95.180	0.014
3	63	92.679	92.507	-0.172
3	64	95.786	95.653	-0.133
3	65	93.676	93.591	-0.085
3	66	96.147	96.095	-0.052
5	67	94.074	93.890	-0.184
5	68	92.640	92.648	0.008
5	69	94.672	94.383	-0.289
5	70	94.475	94.272	-0.203
5	71	95.251	95.130	-0.121
5	72	96.089	95.987	-0.102
10	73	94.767	94.532	-0.235
10	74	95.166	95.014	-0.152
10	75	92.679	92.258	-0.421
10	76	95.786	95.489	-0.297
10	77	93.676	93.406	-0.270
10	78	96.147	95.865	-0.282
15	79	94.074	93.684	-0.390
15	80	92.640	92.284	-0.356
15	81	94.672	94.118	-0.554
15	82	94.475	94.063	-0.412
15	83	95.251	94.777	-0.474
15	84	96.089	95.687	-0.402
30	86	94.767	93.880	-0.887
30	89	95.166	94.405	-0.761
30	91	92.679	91.572	-1.107
30	92	95.786	94.804	-0.982
30	94	93.676	92.604	-1.072
30	95	96.147	95.405	-0.742
35	100	94.074	92.704	-1.370
35	101	92.640	91.471	-1.169
35	102	94.672	92.917	-1.755
35	103	94.475	93.121	-1.354
35	104	95.251	93.913	-1.338
35	105	96.089	94.521	-1.568
50	106	96.711	95.100	-1.611
50	107	97.019	95.222	-1.797
50	108	93.093	91.617	-1.476
50	109	94.922	93.546	-1.376
50	110	94.417	92.566	-1.851
50	111	95.622	94.186	-1.436
55	113	96.600	94.572	-2.028
55	114	92.775	90.777	-1.998
55	115	95.460	93.595	-1.865
55	116	96.391	94.528	-1.863
55	126	93.921	91.857	-2.064
55	128	96.170	94.165	-2.005
100	129	95.811	92.949	-2.862
100	131	93.322	90.631	-2.691
100	132	94.666	91.654	-3.012
100	133	92.528	89.986	-2.542
100	134	96.511	93.903	-2.608
100	135	95.652	93.129	-2.523
100	138	95.950	92.943	-3.007
100	139	91.262	88.650	-2.612
100	140	92.506	89.719	-2.787
100	141	95.561	92.692	-2.869
100	142	92.794	90.590	-2.204
100	143	92.216	89.195	-3.021
100	144	93.150	90.670	-2.480
100	220	94.492	92.013	-2.479
100	221	93.704	91.259	-2.445
100	224	92.269	89.806	-2.463
100	225	95.668	92.906	-2.762
100	226	95.236	92.856	-2.380
100	228	96.049	93.578	-2.471
100	232	94.277	91.909	-2.368
100	233	94.488	92.032	-2.456
100	236	92.036	89.657	-2.379
100	238	97.100	94.216	-2.884
100	243	93.501	90.673	-2.828
105	244	92.308	89.026	-3.282
105	246	93.029	89.946	-3.083
105	248	94.836	91.428	-3.408
105	374	91.947	88.592	-3.355
105	376	97.345	93.977	-3.368
Max		97.345	96.095	0.077
Average		94.503	92.997	-1.505
Min		91.262	88.592	-3.408
Std Dev		1.445	1.884	1.118



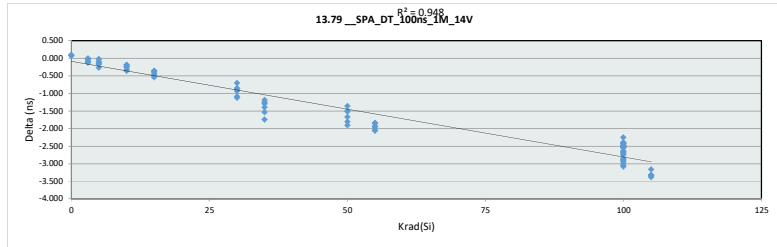
13.75 SPA_DT												
Test Site												
Tester												
Test Number												
Max Limit	110	ns										
Min Limit	85	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	
Min	93.571	92.507	92.648	92.258	92.284	91.572	91.471	91.617	90.777	88.650	88.592	
Average	93.616	94.635	94.385	94.427	94.102	93.778	93.108	93.706	93.249	91.567	90.594	
Max	93.689	96.095	95.987	95.865	95.687	95.405	94.521	95.222	94.572	94.216	93.977	
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	



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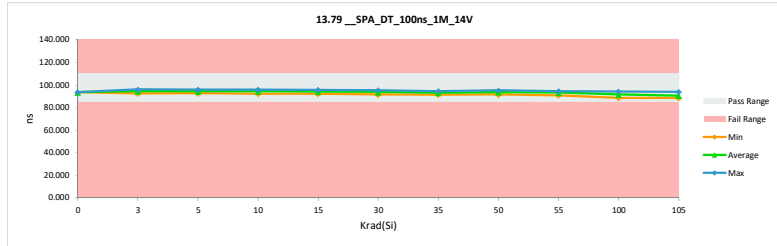
13.79 SPA_DT_100ns_1M_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	110 110
Min Limit	85 85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	93.565	93.659	0.094
0	118	93.535	93.616	0.081
0	122	93.462	93.566	0.104
3	61	94.745	94.742	-0.003
3	62	95.193	95.189	-0.004
3	63	92.639	92.536	-0.103
3	64	95.754	95.685	-0.069
3	65	93.700	93.584	-0.116
3	66	96.130	96.129	-0.001
5	67	94.002	93.881	-0.121
5	68	92.659	92.648	-0.011
5	69	94.659	94.405	-0.254
5	70	94.474	94.321	-0.153
5	71	95.192	95.049	-0.143
5	72	96.098	96.023	-0.075
10	73	94.745	94.572	-0.173
10	74	95.193	95.004	-0.189
10	75	92.639	92.291	-0.348
10	76	95.754	95.522	-0.232
10	77	93.700	93.398	-0.302
10	78	96.130	95.889	-0.241
15	79	94.002	93.653	-0.349
15	80	92.659	92.313	-0.346
15	81	94.659	94.130	-0.529
15	82	94.474	94.093	-0.381
15	83	95.192	94.755	-0.437
15	84	96.098	95.624	-0.474
30	86	94.745	93.907	-0.838
30	89	95.193	94.316	-0.877
30	91	92.639	91.560	-1.079
30	92	95.754	94.831	-0.923
30	94	93.700	92.584	-1.116
30	95	96.130	95.434	-0.696
35	100	94.002	92.712	-1.290
35	101	92.659	91.480	-1.179
35	102	94.659	92.927	-1.732
35	103	94.474	93.091	-1.383
35	104	95.192	93.949	-1.243
35	105	96.098	94.576	-1.522
50	106	96.748	95.090	-1.658
50	107	97.032	95.237	-1.795
50	108	93.058	91.573	-1.485
50	109	94.905	93.557	-1.348
50	110	94.406	92.513	-1.893
50	111	95.637	94.136	-1.501
55	113	96.532	94.476	-2.056
55	114	92.761	90.810	-1.951
55	115	95.446	93.616	-1.830
55	116	96.360	94.523	-1.837
55	126	93.924	91.894	-2.030
55	128	96.087	94.169	-1.918
100	129	95.886	92.969	-2.917
100	131	93.330	90.648	-2.682
100	132	94.685	91.609	-3.076
100	133	92.529	90.016	-2.513
100	134	96.405	93.868	-2.537
100	135	95.669	93.179	-2.490
100	138	95.944	92.961	-2.983
100	139	91.222	88.586	-2.636
100	140	92.521	89.688	-2.833
100	141	95.525	92.656	-2.869
100	142	92.839	90.595	-2.244
100	143	92.224	89.193	-3.031
100	144	93.258	90.622	-2.636
100	220	94.430	92.008	-2.422
100	221	93.661	91.256	-2.405
100	224	92.219	89.849	-2.470
100	225	95.636	92.932	-2.704
100	226	95.220	92.839	-2.381
100	228	96.092	93.562	-2.530
100	232	94.321	91.892	-2.429
100	233	94.512	92.021	-2.491
100	236	92.129	89.648	-2.481
100	238	97.106	94.187	-2.919
100	243	93.421	90.677	-2.744
105	244	92.320	89.010	-3.310
105	246	93.024	89.872	-3.152
105	248	94.854	91.522	-3.332
105	374	91.901	88.577	-3.324
105	375	97.334	93.962	-3.372
105	Max	97.334	96.129	0.104
105	Average	94.494	92.996	-1.498
105	Min	91.222	88.577	-3.372
105	Std Dev	1.439	1.888	1.129



13.79 SPA_DT	
Test Site	
Tester	
Test Number	
Max Limit	110 ns
Min Limit	85 ns

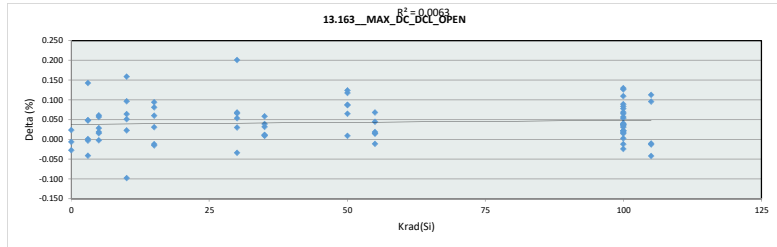
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.566	92.536	92.648	92.291	92.313	91.560	91.480	91.573	90.810	88.586	88.577
Average	93.614	94.644	94.388	94.446	94.095	93.772	93.123	93.684	93.248	91.561	90.589
Max	93.659	96.129	96.023	95.889	95.624	95.434	94.576	95.237	94.523	94.187	93.962
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



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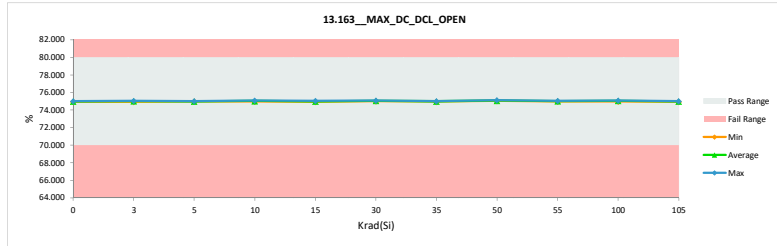
13.163_MAX_DC_DCL_OPEN			
Test Site			
Tester			
Test Number			
Unit	%	%	
Max Limit	80	80	
Min Limit	70	70	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	74.979	74.974	-0.005
0	118	75.036	75.010	-0.026
0	122	75.004	75.029	0.025
3	61	74.905	75.049	0.144
3	62	75.085	75.045	-0.040
3	63	74.995	75.044	0.049
3	64	75.051	75.049	-0.002
3	65	75.008	75.058	0.050
3	66	74.971	74.973	0.002
5	67	74.984	75.004	0.020
5	68	74.965	75.023	0.058
5	69	74.971	74.988	0.017
5	70	75.022	75.021	-0.001
5	71	74.941	75.003	0.062
5	72	74.970	75.000	0.030
10	73	74.905	75.065	0.160
10	74	75.085	74.988	-0.097
10	75	74.995	75.060	0.065
10	76	75.051	75.075	0.024
10	77	75.008	75.106	0.098
10	78	74.971	75.023	0.052
15	79	74.984	75.066	0.082
15	80	74.965	75.026	0.061
15	81	74.971	75.003	0.032
15	82	75.022	75.011	-0.011
15	83	74.941	75.036	0.095
15	84	74.970	74.956	-0.014
30	86	74.905	75.107	0.202
30	89	75.085	75.052	-0.033
30	91	74.995	75.050	0.055
30	92	75.051	75.082	0.031
30	94	75.008	75.077	0.069
30	95	74.971	75.038	0.067
35	100	74.984	75.017	0.033
35	101	74.965	75.025	0.060
35	102	74.971	74.983	0.012
35	103	75.022	75.035	0.013
35	104	74.941	74.981	0.040
35	105	74.970	74.980	0.010
50	106	74.975	75.064	0.089
50	107	75.014	75.133	0.119
50	108	74.988	75.076	0.088
50	109	75.075	75.085	0.010
50	110	74.971	75.096	0.125
50	111	74.995	75.061	0.066
55	113	75.010	75.000	-0.010
55	114	74.996	75.065	0.069
55	115	74.984	75.030	0.046
55	116	75.015	75.032	0.017
55	126	75.034	75.050	0.016
55	128	74.963	74.983	0.020
100	129	75.045	75.083	0.038
100	131	74.999	75.022	0.023
100	132	74.970	75.002	0.032
100	133	75.066	75.089	0.023
100	134	74.972	74.990	0.018
100	135	75.027	75.004	-0.023
100	138	74.988	75.046	0.058
100	139	75.100	75.116	0.016
100	140	75.022	75.064	0.042
100	141	74.932	75.011	0.079
100	142	75.004	75.043	0.039
100	143	75.008	75.030	0.022
100	144	74.999	75.069	0.070
100	220	75.002	75.092	0.090
100	221	74.980	75.111	0.131
100	224	75.036	75.071	0.035
100	225	75.026	75.015	-0.011
100	226	74.956	75.084	0.128
100	228	74.985	74.989	0.004
100	232	75.009	75.048	0.039
100	233	75.002	75.087	0.085
100	236	75.046	75.101	0.055
100	238	74.956	75.022	0.066
100	243	74.997	75.108	0.111
105	244	75.018	75.006	-0.012
105	246	75.006	74.965	-0.041
105	248	75.038	75.029	-0.009
105	374	74.910	75.007	0.097
105	376	74.913	75.027	0.114
105	Max	75.100	75.133	0.202
105	Average	74.996	75.039	0.043
105	Min	74.905	74.956	-0.097
105	Std Dev	0.042	0.040	0.050



13.163_MAX_DC			
Test Site			
Tester			
Test Number			
Max Limit	80	%	
Min Limit	70	%	

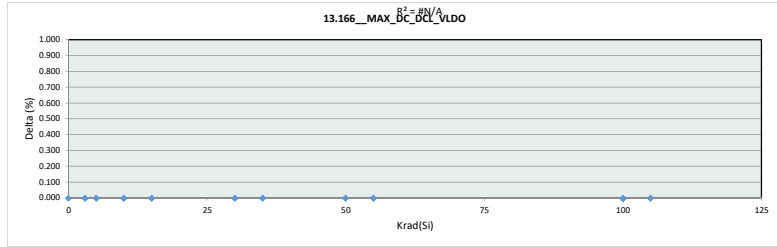
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000
Min	74.974	74.973	74.988	74.988	74.956	75.038	74.980	75.061	74.983	74.989	74.965
Average	75.004	75.036	75.007	75.053	75.016	75.068	75.004	75.086	75.027	75.054	75.007
Max	75.029	75.058	75.023	75.106	75.066	75.107	75.035	75.133	75.065	75.116	75.029
UL	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000



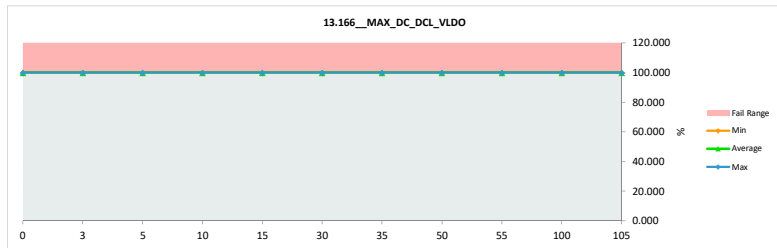
LDR TID Report
TPS7H5001-SP

13.166_MAX_DC_DCL_VLDO	
Test Site	
Tester	
Test Number	
Unit	%
Max Limit	100
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	100.000	100.000	0.000
0	118	100.000	100.000	0.000
0	122	100.000	100.000	0.000
3	61	100.000	100.000	0.000
3	62	100.000	100.000	0.000
3	63	100.000	100.000	0.000
3	64	100.000	100.000	0.000
3	65	100.000	100.000	0.000
3	66	100.000	100.000	0.000
5	67	100.000	100.000	0.000
5	68	100.000	100.000	0.000
5	69	100.000	100.000	0.000
5	70	100.000	100.000	0.000
5	71	100.000	100.000	0.000
5	72	100.000	100.000	0.000
10	73	100.000	100.000	0.000
10	74	100.000	100.000	0.000
10	75	100.000	100.000	0.000
10	76	100.000	100.000	0.000
10	77	100.000	100.000	0.000
10	78	100.000	100.000	0.000
15	79	100.000	100.000	0.000
15	80	100.000	100.000	0.000
15	81	100.000	100.000	0.000
15	82	100.000	100.000	0.000
15	83	100.000	100.000	0.000
15	84	100.000	100.000	0.000
30	86	100.000	100.000	0.000
30	89	100.000	100.000	0.000
30	91	100.000	100.000	0.000
30	92	100.000	100.000	0.000
30	94	100.000	100.000	0.000
30	95	100.000	100.000	0.000
35	100	100.000	100.000	0.000
35	101	100.000	100.000	0.000
35	102	100.000	100.000	0.000
35	103	100.000	100.000	0.000
35	104	100.000	100.000	0.000
35	105	100.000	100.000	0.000
50	106	100.000	100.000	0.000
50	107	100.000	100.000	0.000
50	108	100.000	100.000	0.000
50	109	100.000	100.000	0.000
50	110	100.000	100.000	0.000
50	111	100.000	100.000	0.000
55	113	100.000	100.000	0.000
55	114	100.000	100.000	0.000
55	115	100.000	100.000	0.000
55	116	100.000	100.000	0.000
55	126	100.000	100.000	0.000
55	128	100.000	100.000	0.000
100	129	100.000	100.000	0.000
100	131	100.000	100.000	0.000
100	132	100.000	100.000	0.000
100	133	100.000	100.000	0.000
100	134	100.000	100.000	0.000
100	135	100.000	100.000	0.000
100	138	100.000	100.000	0.000
100	139	100.000	100.000	0.000
100	140	100.000	100.000	0.000
100	141	100.000	100.000	0.000
100	142	100.000	100.000	0.000
100	143	100.000	100.000	0.000
100	144	100.000	100.000	0.000
100	220	100.000	100.000	0.000
100	221	100.000	100.000	0.000
100	224	100.000	100.000	0.000
100	225	100.000	100.000	0.000
100	226	100.000	100.000	0.000
100	228	100.000	100.000	0.000
100	232	100.000	100.000	0.000
100	233	100.000	100.000	0.000
100	236	100.000	100.000	0.000
100	238	100.000	100.000	0.000
100	243	100.000	100.000	0.000
105	244	100.000	100.000	0.000
105	246	100.000	100.000	0.000
105	248	100.000	100.000	0.000
105	374	100.000	100.000	0.000
105	376	100.000	100.000	0.000
105	Max	100.000	100.000	0.000
105	Average	100.000	100.000	0.000
105	Min	100.000	100.000	0.000
105	Std Dev	0.000	0.000	0.000



13.166_MAX_DC											
Test Site											
Tester											
Test Number											
Max Limit	100 %										
Min Limit	%										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Average	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Max	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
UL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000



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