



**ABSTRACT**

This report discusses the results of the total ionizing dose (TID) testing for TPS7H5003-SP, Texas Instruments 4-V to 14-V radiation-hardness-assured, current mode, single output PWM controller optimized for DC-DC converters in space applications. The study was done to determine TID effects under high dose rate (HDR) up to 100 krad(Si). The results show that all samples passed within the specified limits up to 100 krad(Si).

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## 1 Device Information

### 1.1 Product Description

The TPS7H5003-SP is a single output radiation-hardness-assured PWM controller that contains an integrated synchronous rectification output, optimized for DC-DC converters in space applications. The dead time and leading edge blank time are fixed at 50 ns for this device. The controller can be configured for maximum duty cycle of 75% or 100%. As such, the DCL pin can be left floating or connected to VLDO. Connection of the DCL pin to AVSS is not permissible for this device. The 0.613 V +0.7%/-1% accurate internal reference allows design of high-current buck converters for FPGA core voltages.

The TPS7H5003-SP can be driven using an external clock through the SYNC pin or run using its internal oscillator at a frequency programmed by the user. Other programmable features include the UVLO threshold, soft start, and slope compensation. The TPS7H5003-SP is packaged in a very small 22-pin ceramic dual flat package.

### 1.2 Device Details

[Table 1-1](#) lists the device information used in the initial TID HDR characterization.

[Table 1-2](#) lists the device information used in the initial TID LDR characterization.

TPS7H5003-SP is part of TPS7H500x-SP family of PWM controllers. Although each device in the TPS7H500x-SP family of PWM controllers has distinct functionality, all of the controllers share an identical device architecture, layout, and process. The differences are the features enabled in each device. As such, the post-TID electrical characterization results for both HDR and LDR are expected to be similar for the full-featured TPS7H5001-SP and the other devices in the family. HDR results were considered worse in comparison to LDR for the TPS7H5001-SP. As a result, HDR testing was performed on each individual device in the TPS7H500x-SP family and the TPS7H5001-SP LDR results are deemed applicable for all of the devices. Each specification that is TID tested for TPS7H5002-SP, TPS7H5003-SP, and TPS7H5004-SP all have a corresponding specification in the TPS7H5001-SP, with the exception of a few parameters for TPS7H5003-SP. The leading edge blank time and dead time are programmable for the TPS7H5001-SP, yet these are fixed for TPS7H5003-SP. The TPS7H5003-SP is internally configured to produce 50 ns dead times (typical) and 50 ns leading edge blank time (typical). The fixed 50 ns leading edge blank time leads to a longer minimum on-time for this device in comparison to the other controllers in the TPS7H500x-SP family. Examination of the datasheet shows that the specification limits for dead time and minimum on-time differ between the TPS7H5001-SP and TPS7H5003-SP. The consequence here is that there is no direct correlation through which LDR results for TPS7H5001-SP can be extended as applicable for the TPS7H5003-SP for these parameters. For this reason, dead time and minimum on-time are considered only HDR tested for the TPS7H5003-SP. However, given that the TPS7H5001-SP shows the higher parametric drift for these parameters after HDR exposure and that the same is expected to be true of the TPS7H5003-SP, the user can have confidence that HDR parametric drifts presented in the TPS7H5003-SP report represent worst-case shifts due to total ionizing dose effects.

**Table 1-1. Device and Exposure Details for HDR**

TID Exposure Details	
TI Device	TPS7H5003-SP
TI Part Name	5962R1822203VXC
Package	22-pin CFP (HFT)
Technology	LBC7
Quantity Tested	HDR - 70
Lot Accept/Reject	All levels tested and passed up to 100 krad(Si) for HDR
HDR Radiation Facility	DCLAB, Texas Instruments, Dallas, TX
HDR Dose Level	3krad(Si), 10 krad(Si), 30 krad(Si), 50 krad(Si), 100 krad(Si)
HDR Dose Rate	233.83 rads(Si)/s
HDR Radiation Source	Gammacell 220 Excel (GC-220E) Co-60
Irradiation Temperature	Ambient room temperature

**Table 1-2. Device and Exposure Details for LDR**

TID Exposure Details	
TI Device	TPS7H5001-SP
TI Part Name	5962R1822201VXC
Package	22-pin CFP (HFT)
Technology	LBC7
Quantity Tested	LDR - 77
Lot Accept/Reject	All levels tested and passed up to 100 krad(Si) for LDR
HDR Radiation Facility	Radiation Test Solutions (RTS) - Colorado Springs, CO
HDR Dose Level	3krad(Si), 10 krad(Si), 30 krad(Si), 50 krad(Si), 100 krad(Si)
HDR Dose Rate	0.01 rads(Si)/s
HDR Radiation Source	Gammacell (JLS-81-22) Co-60
Irradiation Temperature	Ambient room temperature



**Figure 1-1. TPS7H5003-SP Device Picture**

## 2 Total Dose Test Setup

### 2.1 Test Overview

The TPS7H5003-SP was tested according to MIL-STD-883, Test Method 1019.9. For this testing, Conditions A and D were used. The product was irradiated up to the target radiation level, and then put through full electrical parametric testing on the production automated test equipment (ATE). All devices remained functional passing all parametric test limits.

### 2.2 Test Description and Facilities

The TPS7H5003-SP HDR exposure was performed on biased and unbiased devices in a Co-60 gamma cell at TI facility in Santa Clara, California. The un-attenuated dose rate of this cell is 233.83 rads(Si)/s. After exposure, the devices were packed in dry ice (per MIL-STD-883 Method 1019.9 section 3.10) and bring to Junkins lab in TI Dallas for full post-radiation electrical evaluation using Texas Instruments ATE. ATE test limits are set per SMD electrical limits based on qualification and characterization data. Post-radiation measurements were taken within 30 minutes of removing the devices from the dry ice container. The devices were allowed to reach room temperature prior to electrical post-radiation measurements.

The TPS7H5001-SP LDR exposure was performed on biased and unbiased devices in a Co-60 gammacell under a 10-mrad(Si)/s exposure rate. The dose rate of the irradiator used in the exposure ranges from < 10 mrad(Si)/s to a maximum of approximately 84 rad(Si)/s, determined by the distance from the source. For the LDR (10 mrad(Si)/s) exposure, the test box was positioned approximately 2 m from the source. The exposure boards are housed in a lead-aluminum box (as specified in MIL-STD-883 TM 1019.9) to harden the gamma spectrum and minimize dose enhancement effects. The irradiator calibration is maintained by Logmire Laboratories using Thermoluminescence Dosimeters (TLDs) traceable to the National Institute of Standards and Technology (NIST) and the dosimetry was verified using TLDs prior to the radiation exposures. After exposure, devices were electrically tested at Texas Instruments. ATE test limits are set per SMD electrical limits based on qualification and characterization data.

### 2.3 Test Setup Details

The devices under HDR and LDR exposure were tested in two conditions, biased and unbiased, as described in the following. LDR data taken from TPS7H5001-SP.

#### 2.3.1 Unbiased

For the unbiased HDR and LDR conditions, the exposure was performed with all pins grounded. LDR data taken from TPS7H5001-SP.

#### 2.3.2 Biased

[Figure 2-1](#) shows the diagram for HDR and LDR exposure with biased condition. Bias diagram is same as TPS7H5001-SP as below, except pin# 2, 3, 4, 11 and 12 is NC (no connect) for TPS7H5003-SP.

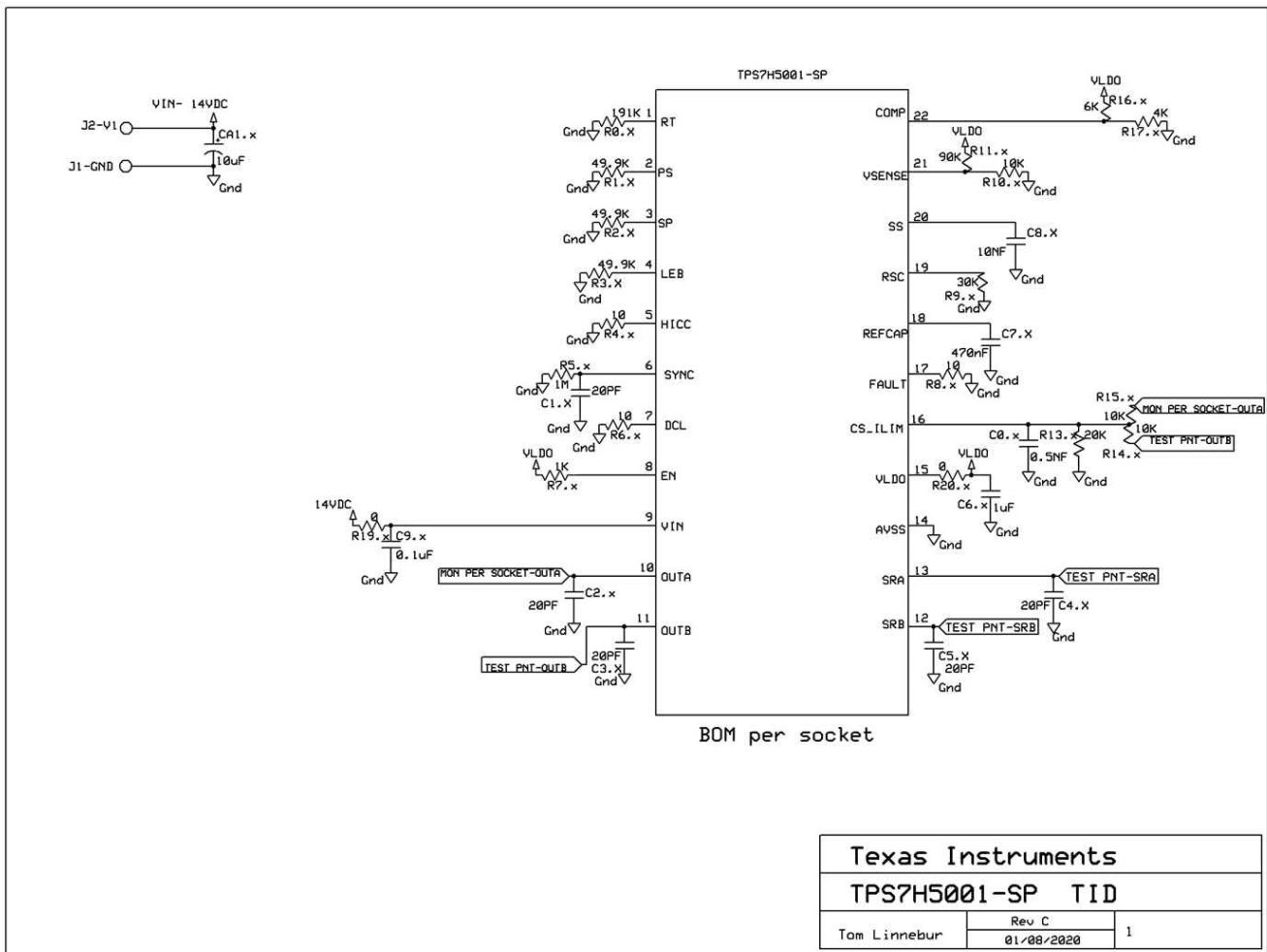


Figure 2-1. Bias Diagram Used in TID Exposure

## 2.4 Test Configuration and Condition

HDR devices were stressed at 3 krad(Si), 10 krad(Si), 30 krad(Si), 50 krad(Si), and 100 krad(Si) for biased and unbiased conditions.

LDR devices were stressed at 3 krad(Si), 10 krad(Si), 30 krad(Si), 50 krad(Si), and 100 krad(Si) for biased and unbiased conditions. LDR data taken from TPS7H5001-SP.

**Table 2-1. HDR: 3-krad(Si) to 100-krad(Si)/s biased device information**

Total Samples: 44				
Exposure Levels				
3-krad(Si)	10-krad(Si)	30-krad(Si)	50-krad(Si)	100-krad(Si)
197, 198, 199, 202, 203	210, 211, 212, 213, 214	222, 223, 224, 225, 226	170, 172, 173, 175, 176	154, 155, 156, 157, 158, 159, 160, 161, 162, 163, 164, 165, 166, 168, 169, 182, 183, 184, 185, 186, 187, 188, 189, 190

**Table 2-2. HDR: 3-krad(Si) to 100-krad(Si)/s unbiased device information**

Total Samples: 26				
Exposure Levels				
3-krad(Si)	10-krad(Si)	30-krad(Si)	50-krad(Si)	100-krad(Si)
204, 205, 206, 208, 209	215, 217, 219, 220, 221	227, 228, 229, 230, 231	177, 178, 179, 180, 181	191, 192, 193, 194, 195, 196

**Table 2-3. LDR: 3-krad(Si) to 100-krad(Si)/s biased device information**

Total Samples: 48				
Exposure Levels				
3-krad(Si)	10-krad(Si)	30-krad(Si)	50-krad(Si)	100-krad(Si)
61, 62, 63, 64, 65, 66	73, 74, 75, 76, 77, 78	86, 89, 91, 92, 94, 95	106, 107, 108, 109, 110, 111	129, 131, 132, 133, 134, 135, 138, 139, 140, 141, 142, 143, 144, 220, 221, 224, 225, 226, 228, 232, 233, 236, 238, 243

**Table 2-4. LDR: 3-krad(Si) to 100-krad(Si)/s unbiased device information**

Total Samples: 29				
Exposure Levels				
3-krad(Si)	10-krad(Si)	30-krad(Si)	50-krad(Si)	100-krad(Si)
67, 68, 69, 70, 71, 72	79, 80, 81, 82, 83, 84	100, 101, 102, 103, 104, 105	113, 114, 115, 116, 126, 128	244, 246, 248, 374, 376

### 3 TI Characterization Test Results

#### 3.1 TID Characterization Summary Results

The parametric data for the TPS7H5003-SP passes up to 100-krad(Si) HDR TID irradiation. The drifts of SMD electrical parameters through HDR were within the SMD limits.

The parametric data for the TPS7H5001-SP passes up to 100-krad(Si) LDR TID irradiation. The drifts of SMD electrical parameters through LDR were within the SMD limits. TPS7H5003-SP is spin of TPS7H5001-SP, sharing an identical device architecture, layout, and process. The differences are the features enabled in each device. As such, the radiation performance due to dose effects is expected to be similar for the full-featured device, TPS7H5001-SP, and TPS7H5003-SP under LDR TID irradiation.

Overall, the TPS7H5003-SP showed a strong degree of hardness to HDR TID irradiation up to 100 krad(Si) for both biased and unbiased exposure conditions. The measurements taken post-irradiation for each sample set showed a marginal shift for most parameters at each dose level for both biased and unbiased. The parameters that did show a greater degree of change between pre- and post-irradiation were still within the Electrical Performance Characteristics specified in Standard Microcircuit Drawing (Data Sheet Electrical Parameters). See [Table 3-1](#) for the Data Sheet Electrical Parameters and Associated Tests.

See [Appendix A](#) for Specifications Requiring Clarification.

See [Appendix B](#) for HDR report up to 100 krad(Si) of TPS7H5003-SP.

See [Appendix C](#) for LDR report up to 100 krad(Si) of TPS7H5003-SP.

#### 3.2 Specification Compliance Matrix

**Table 3-1. TPS7H5003-SP Electrical Parameters Table**

PARAMETER	TEST CONDITION	TPS7H5003-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
<b>SUPPLY VOLTAGES AND CURRENTS</b>						
IDD Operating supply current	f <sub>SW</sub> = 500 kHz, No load for OUTA and SRA		6.25	8	mA	5.24 __IDD_ACT_500K_NOLOAD_4V, 5.25 __IDD_ACT_500K_NOLOAD_5V, 5.26 __IDD_ACT_500K_NOLOAD_12V, 5.27 __IDD_ACT_500K_NOLOAD_14V
	f <sub>SW</sub> = 1 MHz, No load for OUTA and SRA		6.75	9.5	mA	5.28 __IDD_ACT_1M_NOLOAD_4V, 5.29 __IDD_ACT_1M_NOLOAD_5V, 5.30 __IDD_ACT_1M_NOLOAD_12V, 5.31 __IDD_ACT_1M_NOLOAD_14V
	f <sub>SW</sub> = 2 MHz, No load for OUTA and SRA		8.5	13.5	mA	5.36 __IDD_ACT_2M_NOLOAD_4V, 5.37 __IDD_ACT_2M_NOLOAD_5V, 5.38 __IDD_ACT_2M_NOLOAD_12V, 5.39 __IDD_ACT_2M_NOLOAD_14V
	f <sub>SW</sub> = 500 kHz, C <sub>LOAD</sub> = 100pF for OUTA and SRA		7.5	9.5	mA	5.48 __IDD_ACT_500K_4V, 5.49 __IDD_ACT_500K_5V, 5.50 __IDD_ACT_500K_12V, 5.51 __IDD_ACT_500K_14V
	f <sub>SW</sub> = 1 MHz, C <sub>LOAD</sub> = 100pF for OUTA and SRA		9	12	mA	5.52 __IDD_ACT_1M_4V, 5.53 __IDD_ACT_1M_5V, 5.54 __IDD_ACT_1M_12V, 5.55 __IDD_ACT_1M_14V
	f <sub>SW</sub> = 2 MHz, C <sub>LOAD</sub> = 100pF for OUTA and SRA		14	19.5	mA	5.60 __IDD_ACT_2M_4V, 5.61 __IDD_ACT_2M_5V, 5.62 __IDD_ACT_2M_12V, 5.63 __IDD_ACT_2M_14V
I <sub>DD(dis)</sub> Standby current	EN = 0 V			3	mA	5.1 __IDD_DIS_4V, 5.2 __IDD_DIS_5V, 5.3 __IDD_DIS_12V 5.4 __IDD_DIS_14V
VLDO Internal linear regulator output voltage	5 V ≤ VIN ≤ 14 V, f <sub>sw</sub> ≤ 1 MHz	4.75	5	5.2	V	5.65 __V_LDO_100K_5V, 5.66 __V_LDO_100K_12V, 5.67 __V_LDO_100K_14V, 5.69 __V_LDO_200K_5V, 5.70 __V_LDO_200K_12V, 5.71 __V_LDO_200K_14V, 5.73 __V_LDO_500K_5V, 5.74 __V_LDO_500K_12V, 5.75 __V_LDO_500K_14V, 5.77 __V_LDO_1M_5V, 5.78 __V_LDO_1M_12V, 5.79 __V_LDO_1M_14V
	5 V ≤ VIN ≤ 14 V, f <sub>sw</sub> = 2 MHz	4.65	5	5.2	V	5.85 __V_LDO_2M_5V, 5.86 __V_LDO_2M_12V, 5.87 __V_LDO_2M_14V
<b>ENABLE AND UNDERVOLTAGE LOCKOUT</b>						
V <sub>ENR</sub> EN threshold rising		0.57	0.6	0.65	V	6.5 __V_EN_RISE_4V, 6.8 __V_EN_RISE_5V, 6.11 __V_EN_RISE_12V, 6.14 __V_EN_RISE_14V
V <sub>ENF</sub> EN threshold falling		0.47	0.5	0.55	V	6.6 __V_EN_FALL_4V, 6.9 __V_EN_FALL_5V, 6.12 __V_EN_FALL_12V, 6.15 __V_EN_FALL_14V

**Table 3-1. TPS7H5003-SP Electrical Parameters Table (continued)**

PARAMETER	TEST CONDITION	TPS7H5003-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
$V_{ENH}$ EN hysteresis voltage		85	95	105	mV	6.7 __V_EN_HYS_4V, 6.10 __V_EN_HYS_5V, 6.13 __V_EN_HYS_12V, 6.16 __V_EN_HYS_14V
$I_{EN}$ EN pin input leakage current	$V_{IN} = 14\text{ V}$ , $EN = 5\text{ V}$		5	50	nA	6.1 __I_EN_LEAK_4V, 6.2 __I_EN_LEAK_5V, 6.3 __I_EN_LEAK_12V, 6.4 __I_EN_LEAK_14V
VLDO <sub>UVLOR</sub> VLDO UVLO rising		3.44	3.55	3.66	V	6.34 __UVLO_VLDO_RISE_1MHz, 6.37 __UVLO_VLDO_RISE_100kHz, 6.40 __UVLO_VLDO_RISE_200kHz, 6.43 __UVLO_VLDO_RISE_500kHz, 6.46 __UVLO_VLDO_RISE_2MHz
VLDO <sub>UVLOF</sub> VLDO UVLO falling		3.29	3.4	3.51	V	6.35 __UVLO_VLDO_FALL_1MHz, 6.38 __UVLO_VLDO_FALL_100kHz, 6.41 __UVLO_VLDO_FALL_200kHz, 6.44 __UVLO_VLDO_FALL_500kHz, 6.47 __UVLO_VLDO_FALL_2MHz
VLDO <sub>UVLOH</sub> VLDO UVLO hysteresis		115	135	160	mV	6.36 __UVLO_VLDO_HYS_1MHz, 6.39 __UVLO_VLDO_HYS_100kHz, 6.42 __UVLO_VLDO_HYS_200kHz, 6.45 __UVLO_VLDO_HYS_500kHz, 6.48 __UVLO_VLDO_HYS_2MHz
<b>SOFT START</b>						
$I_{SS}$ Soft-start current	$SS = 0.3\text{ V}$	1.98	2.7	3.32	$\mu\text{A}$	7.1 __I_SS_4V, 7.3 __I_SS_5V, 7.5 __I_SS_12V, 7.7 __I_SS_14V
<b>ERROR AMPLIFIER</b>						
$E_{A_{gm}}$ Transconductance	$-2\ \mu\text{A} < I_{COMP} < 2\ \mu\text{A}$ , $V_{(COMP)} = 1\text{ V}$	1150	1800	2500	$\mu\text{A/V}$	8.9 __EA_GM_4V, 8.10 __EA_GM_5V, 8.11 __EA_GM_12V, 8.12 __EA_GM_14V
$E_{A_{ISRC}}$ Error amplifier source current	$V_{(COMP)} = 1\text{ V}$ , 100-mV input overdrive	100		190	$\mu\text{A}$	8.13 __EA_I_SOURCE_4V, 8.14 __EA_I_SOURCE_5V, 8.15 __EA_I_SOURCE_12V, 8.16 __EA_I_SOURCE_14V
$E_{A_{ISNK}}$ Error amplifier sink current	$V_{(COMP)} = 1\text{ V}$ , 100-mV input overdrive	100		190	$\mu\text{A}$	8.17 __EA_I_SINK_4V, 8.18 __EA_I_SINK_5V, 8.19 __EA_I_SINK_12V, 8.20 __EA_I_SINK_14V
$E_{A_{OS}}$ Error amplifier offset voltage		-2		2	mV	8.5 __EA_OS_4V, 8.6 __EA_OS_5V, 8.7 __EA_OS_12V, 8.8 __EA_OS_14V
<b>OSCILLATOR</b>						
SYNC <sub>RT</sub> SYNC out low-to-high rise time (10%/90%)	$C_{LOAD} = 25\text{ pF}$		6	15	ns	9.1 __T_SYNC_RISE_100kHz_4V, 9.5 __T_SYNC_RISE_200kHz_4V, 9.9 __T_SYNC_RISE_500kHz_4V, 9.13 __T_SYNC_RISE_1MHz_4V, 9.17 __T_SYNC_RISE_1p5MHz_4V, 9.21 __T_SYNC_RISE_2MHz_4V, 9.25 __T_SYNC_RISE_100kHz_5V, 9.29 __T_SYNC_RISE_200kHz_5V, 9.33 __T_SYNC_RISE_500kHz_5V, 9.37 __T_SYNC_RISE_1MHz_5V, 9.41 __T_SYNC_RISE_1p5MHz_5V, 9.45 __T_SYNC_RISE_2MHz_5V, 9.49 __T_SYNC_RISE_100kHz_12V, 9.53 __T_SYNC_RISE_200kHz_12V, 9.57 __T_SYNC_RISE_500kHz_12V, 9.61 __T_SYNC_RISE_1MHz_12V, 9.65 __T_SYNC_RISE_1p5MHz_12V, 9.69 __T_SYNC_RISE_2MHz_12V, 9.73 __T_SYNC_RISE_100kHz_14V, 9.77 __T_SYNC_RISE_200kHz_14V, 9.81 __T_SYNC_RISE_500kHz_14V, 9.85 __T_SYNC_RISE_1MHz_14V, 9.89 __T_SYNC_RISE_1p5MHz_14V, 9.93 __T_SYNC_RISE_2MHz_14V
SYNC <sub>FT</sub> SYNC out high-to-low fall time (10%/90%)	$C_{LOAD} = 25\text{ pF}$		6	17	ns	9.2 __T_SYNC_FALL_100kHz_4V, 9.6 __T_SYNC_FALL_200kHz_4V, 9.10 __T_SYNC_FALL_500kHz_4V, 9.14 __T_SYNC_FALL_1MHz_4V, 9.18 __T_SYNC_FALL_1p5MHz_4V, 9.22 __T_SYNC_FALL_2MHz_4V, 9.26 __T_SYNC_FALL_100kHz_5V, 9.30 __T_SYNC_FALL_200kHz_5V, 9.34 __T_SYNC_FALL_500kHz_5V, 9.38 __T_SYNC_FALL_1MHz_5V, 9.42 __T_SYNC_FALL_1p5MHz_5V, 9.46 __T_SYNC_FALL_2MHz_5V, 9.50 __T_SYNC_FALL_100kHz_12V, 9.54 __T_SYNC_FALL_200kHz_12V, 9.58 __T_SYNC_FALL_500kHz_12V, 9.62 __T_SYNC_FALL_1MHz_12V, 9.66 __T_SYNC_FALL_1p5MHz_12V, 9.70 __T_SYNC_FALL_2MHz_12V, 9.74 __T_SYNC_FALL_100kHz_14V, 9.78 __T_SYNC_FALL_200kHz_14V, 9.82 __T_SYNC_FALL_500kHz_14V, 9.86 __T_SYNC_FALL_1MHz_14V, 9.90 __T_SYNC_FALL_1p5MHz_14V, 9.94 __T_SYNC_FALL_2MHz_14V



**Table 3-1. TPS7H5003-SP Electrical Parameters Table (continued)**

PARAMETER	TEST CONDITION	TPS7H5003-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
SYNC <sub>OL</sub> SYNC out low level	I <sub>OL</sub> = 10 mA			500	mV	9.186 __SYNC_VOL_4V, 9.187 __SYNC_VOL_5V, 9.188 __SYNC_VOL_12V, 9.189 __SYNC_VOL_14V
EXT <sub>DT</sub> Externally set frequency detection time	RT = Open, f = 200 kHz			20	μs	9.185 __T_SYNC_DETECT
FSW <sub>EXT</sub> Externally set frequency	RT = 1.07 MΩ	95	105	115	kHz	9.4 __FSW_EXT_RT_100kHz_4V, 9.28 __FSW_EXT_RT_100kHz_5V, 9.52 __FSW_EXT_RT_100kHz_12V, 9.76 __FSW_EXT_RT_100kHz_14V
	RT = 511 kΩ	190	210	230	kHz	9.8 __FSW_EXT_RT_200kHz_4V, 9.32 __FSW_EXT_RT_200kHz_5V, 9.56 __FSW_EXT_RT_200kHz_12V, 9.80 __FSW_EXT_RT_200kHz_14V
	RT = 90.9 kΩ	900	1000	1100	kHz	9.16 __FSW_EXT_RT_1MHz_4V, 9.40 __FSW_EXT_RT_1MHz_5V, 9.64 __FSW_EXT_RT_1MHz_12V, 9.88 __FSW_EXT_RT_1MHz_14V
	RT = 34.8 kΩ	1700	2000	2300	kHz	9.24 __FSW_EXT_RT_2MHz_4V, 9.48 __FSW_EXT_RT_2MHz_5V, 9.72 __FSW_EXT_RT_2MHz_12V, 9.96 __FSW_EXT_RT_2MHz_14V
<b>VOLTAGE REFERENCE</b>						
VREF Internal voltage reference initial tolerance	Measured at COMP, 25°C	0.609	0.613	0.615	V	8.1 __VREF_4V, 8.2 __VREF_5V, 8.3 __VREF_12V, 8.4 __VREF_14V
REFCAP voltage	REFCAP = 470 nF	1.213	1.225	1.237	V	5.88 __V_REFCAP_100K_4V, 5.89 __V_REFCAP_100K_5V, 5.90 __V_REFCAP_100K_12V, 5.91 __V_REFCAP_100K_14V, 5.92 __V_REFCAP_200K_4V, 5.93 __V_REFCAP_200K_5V, 5.94 __V_REFCAP_200K_12V, 5.95 __V_REFCAP_200K_14V, 5.96 __V_REFCAP_500K_4V, 5.97 __V_REFCAP_500K_5V, 5.98 __V_REFCAP_500K_12V, 5.99 __V_REFCAP_500K_14V, 5.100 __V_REFCAP_1M_4V, 5.101 __V_REFCAP_1M_5V, 5.102 __V_REFCAP_1M_12V, 5.103 __V_REFCAP_1M_14V, 5.104 __V_REFCAP_1P5M_4V, 5.105 __V_REFCAP_1P5M_5V, 5.106 __V_REFCAP_1P5M_12V, 5.107 __V_REFCAP_1P5M_14V, 5.108 __V_REFCAP_2M_4V, 5.109 __V_REFCAP_2M_5V, 5.110 __V_REFCAP_2M_12V, 5.111 __V_REFCAP_2M_14V
<b>CURRENT SENSE, CURRENT LIMIT, AND HICCUP</b>						
CCSR	COMP to CS_ILIM ratio	2.00	2.06	2.12		10.49 __CCSR_Ratio
V <sub>CS_ILIM</sub> Current limit (over-current) threshold			1.05	1.09	V	10.1 __V_CS_ILIM_OC_Rise_4V, 10.2 __V_CS_ILIM_OC_Fall_4V, 10.3 __V_CS_ILIM_OC_Rise_5V, 10.4 __V_CS_ILIM_OC_Fall_5V, 10.5 __V_CS_ILIM_OC_Rise_12V, 10.6 __V_CS_ILIM_OC_Fall_12V, 10.7 __V_CS_ILIM_OC_Rise_14V, 10.8 __V_CS_ILIM_OC_Fall_14V
<b>FAULT</b>						
V <sub>FLTR</sub> FLT threshold rising		0.57	0.6	0.65	V	12.1 __V_FAULT_RISE_4V, 12.4 __V_FAULT_RISE_5V, 12.7 __V_FAULT_RISE_12V, 12.10 __V_FAULT_RISE_14V
V <sub>FLTF</sub> FLT threshold falling		0.47	0.5	0.55	V	12.2 __V_FAULT_FALL_4V, 12.5 __V_FAULT_FALL_5V, 12.8 __V_FAULT_FALL_12V, 12.11 __V_FAULT_FALL_14V
V <sub>FLTH</sub> FLT hysteresis voltage		90	100	110	mV	12.3 __V_FAULT_HYS_4V, 12.6 __V_FAULT_HYS_5V, 12.9 __V_FAULT_HYS_12V, 12.12 __V_FAULT_HYS_14V
T <sub>FLT</sub> FLT minimum pulse width	V <sub>FLT</sub> = 1 V	0.4		1.4	μs	12.14 __T_FAULT_MIN
t <sub>DFLT</sub> FLT delay duration	f <sub>sw</sub> = 100 kHz	140	152	169	μs	12.15 __T_FAULT_DELAY_100kHz
	f <sub>sw</sub> = 200 kHz	66	78	86	μs	12.17 __T_FAULT_DELAY_200kHz
	f <sub>sw</sub> = 1 MHz	14	17	21	μs	12.21 __T_FAULT_DELAY_1MHz
	f <sub>sw</sub> = 2 MHz	7	11	14	μs	12.23 __T_FAULT_DELAY_2MHz
<b>PRIMARY AND SYNCHRONOUS RECTIFIER OUTPUTS</b>						

**Table 3-1. TPS7H5003-SP Electrical Parameters Table (continued)**

PARAMETER	TEST CONDITION	TPS7H5003-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
Rise/fall time	$R_{LOAD} = 50\text{ k}\Omega$ , $C_{LOAD} = 100\text{ pF}$ , 10% to 90%		10	17	ns	13.1 __OUTA_RISE_1MHz_4V, 13.9 __OUTA_RISE_1MHz_5V, 13.17 __OUTA_RISE_1MHz_12V, 13.25 __OUTA_RISE_1MHz_14V, 13.3 __OUTA_FALL_1MHz_4V, 13.11 __OUTA_FALL_1MHz_5V, 13.19 __OUTA_FALL_1MHz_12V, 13.27 __OUTA_FALL_1MHz_14V, 13.13 __SRA_RISE_1MHz_5V, 13.21 __SRA_RISE_1MHz_12V, 13.29 __SRA_RISE_1MHz_14V, 13.5 __SRA_RISE_1MHz_4V, 13.15 __SRA_FALL_1MHz_5V, 13.23 __SRA_FALL_1MHz_12V, 13.31 __SRA_FALL_1MHz_14V, 13.7 __SRA_FALL_1MHz_4V
$t_{MIN}$ Minimum on-time	$5\text{ V} \leq V_{IN} \leq 14\text{ V}$			115	ns	13.145 __OUT_T_ON_MIN
$T_{DPS}$ Primary off to secondary on dead time	$5\text{ V} \leq V_{IN} \leq 14\text{ V}$ , 90% of OUTA falling edge to 10% of SRA rising edge with OUTA and SRA floating	40	50	60	ns	13.53 __PSA_DT_50ns_1M_5V, 13.57 __PSA_DT_50ns_1M_12V, 13.61 __PSA_DT_50ns_1M_14V
$T_{DSP}$ Secondary off to primary on dead time	$5\text{ V} \leq V_{IN} \leq 14\text{ V}$ , 90% of SRA falling edge to 10% of OUTA rising edge with OUTA and SRA floating	40	50	60	ns	13.55 __SPA_DT_50ns_1M_5V, 13.59 __SPA_DT_50ns_1M_12V, 13.63 __SPA_DT_50ns_1M_14V
<b>DUTY CYCLE</b>						
$D_{MAX}$ Maximum duty cycle	DCL = floating	70	75	80	%	13.163 __MAX_DC_DCL_OPEN
	DCL =VLDO			100	%	13.166 __MAX_DC_DCL_VLDO

## 4 Applicable and Reference Documents

### 4.1 Applicable Documents

- Texas Instruments, [TPS7H500x-SP Radiation-Hardness-Assured 2-MHz Current Mode PWM Controllers datasheet \(Rev. B\)](#)
- Texas Instruments, [TPS7H500xEVM-CVAL User's Guide](#)
- Texas Instruments, [TPS7H5001-SP Total Ionizing Dose \(TID\) \(Rev. A\)](#)
- Texas Instruments, [TPS7H500x-SP Single-Event Effects \(SEE\) \(Rev. A\)](#)

### 4.2 Reference Documents

Texas Instruments total ionizing dose radiation (total dose) test procedure follows the standards put forth in MIL-STD-883 TM 1019. The document can be found at the DLA website.

## 5 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

<b>Changes from Revision * (March 2022) to Revision A (August 2022)</b>	<b>Page</b>
• Added LDR and HDR data and plots.....	<a href="#">1</a>
• HDR TID Plots include 3/10/30/50/100 Krad(Si) irradiation in both biased and unbiased condition.....	<a href="#">14</a>
• LDR TID plots of TPS7H5001-SP are added. This include 3/10/30/50/100 Krad(Si) irradiation in both biased and unbiased condition.....	<a href="#">15</a>

## A Appendix: Specifications Requiring Clarification

**Table A-1. Specifications Requiring Clarification - Set #1**

Data sheet specifications for which there is no parametric data and, therefore, not in the TID report. Functionality is assured by ATE testing.

PARAMETER	TEST CONDITION	TPS7H5003-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
V <sub>IN</sub> Operating voltage		4		14	V
F <sub>SYNC</sub> SYNC in frequency range		200		4000	kHz
D <sub>SYNC</sub> SYNC in duty cycle	Duty cycle of external clock	40		60	%

**Table A-2. Specifications Requiring Clarification - Set #2**

Data sheet specifications for which no data is available in TID report as specification was verified through bench testing. TID testing is by ATE.

PARAMETER	TEST CONDITION	TPS7H5003-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
E <sub>A<sub>DC</sub></sub> DC gain	V <sub>SENSE</sub> = 0.6 V		10000		V/V
E <sub>A<sub>r<sub>o</sub></sub></sub> Error amplifier output resistance			7		MΩ
E <sub>A<sub>I<sub>B</sub></sub></sub> Error amplifier bias current				35	nA
E <sub>A<sub>B<sub>W</sub></sub></sub> Bandwidth			10		MHz
S <sub>Y<sub>N</sub>C<sub>I<sub>L</sub></sub></sub> SYNC in low-level	V <sub>IN</sub> < 5 V			0.8	V
	V <sub>IN</sub> ≥ 5 V			0.8	V
S <sub>Y<sub>N</sub>C<sub>I<sub>H</sub></sub></sub> SYNC in high-level	V <sub>IN</sub> < 5 V		3.5		V
	V <sub>IN</sub> ≥ 5 V		3.5		V
(V <sub>LDO</sub> - S <sub>Y<sub>N</sub>C<sub>O<sub>H</sub></sub></sub> ) SYNC out high level	I <sub>OH</sub> = 10 mA			0.5	V

**Table A-3. Specifications Requiring Clarification - Set #3**

Data sheet specifications for which there is no 'MIN'/'MAX' value, only 'TYP', therefore, not in the TID report. Functionality is assured by ATE testing.

PARAMETER	TEST CONDITION	TPS7H5003-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
I <sub>HICC_DEL</sub> Hiccup delay current	C <sub>S_LIM</sub> = 1.3 V, C <sub>OMP</sub> = 3 V, V <sub>SENSE</sub> = REF <sub>CAP</sub> /2 V, C <sub>HICC</sub> = 3 nF, L <sub>EB</sub> = 49.9 kΩ, f <sub>sw</sub> = 100 kHz		80		μA
I <sub>HICC_RST</sub> Hiccup restart current			1		μA
V <sub>HICC_PU</sub> Hiccup pull-up threshold			1.0		V
V <sub>HICC_SD</sub> Hiccup shut-down threshold			0.6		V
V <sub>HICC_RST</sub> Hiccup restart threshold			0.3		V
Slope compensation	f <sub>sw</sub> = 100 kHz, R <sub>SC</sub> = 1.18 MΩ		0.033		V/μs
	f <sub>sw</sub> = 200 kHz, R <sub>SC</sub> = 562 kΩ		0.066		V/μs
	f <sub>sw</sub> = 1000 kHz, R <sub>SC</sub> = 100 kΩ		0.333		V/μs
	f <sub>sw</sub> = 2000 kHz, R <sub>SC</sub> = 49.9 kΩ		0.666		V/μs
PRIMARY AND SYNCHRONOUS RECTIFIER OUTPUTS Low-level threshold	I <sub>SINK</sub> = 10 mA		0.5		V

**Table A-3. Specifications Requiring Clarification - Set #3 (continued)**

Data sheet specifications for which there is no 'MIN'/'MAX' value, only 'TYP', therefore, not in the TID report. Functionality is assured by ATE testing.

PARAMETER	TEST CONDITION	TPS7H5003-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
PRIMARY AND SYNCHRONOUS RECTIFIER OUTPUTS High-level threshold	$I_{SOURCE} = 10 \text{ mA}$		4.5		V
$R_{SRC\_P}$ Output source resistance	$I_{OUT} = 20 \text{ mA}$		15		$\Omega$
$R_{SINK\_P}$ Output sink resistance	$I_{OUT} = 20 \text{ mA}$		15		$\Omega$

**Table A-4. Specifications Requiring Clarification - Set #4**

Data sheet specifications that are not covered in TID report because specification condition is at temperature other than 25°C. TID testing is at 25°C only.

PARAMETER	TEST CONDITION	TPS7H5003-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
VREF Internal voltage reference	Measured at COMP, -55°C	0.607	0.609	0.612	V
	Measured at COMP, +125°C	0.611	0.614	0.617	V

## B Appendix: HDR TID Report Data

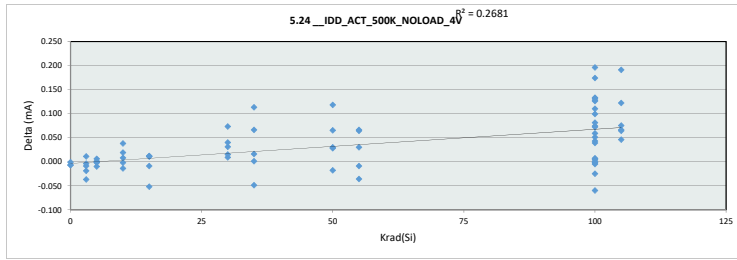
### Description of identifiers for HDR TID report

IDENTIFIER - Krad(Si)	DESCRIPTION
0	Control Unit
3	3 krad(Si) biased
5	3 krad(Si) unbiased
10	10 krad(Si) biased
15	10 krad(Si) unbiased
30	30 krad(Si) biased
35	30 krad(Si) unbiased
50	50 krad(Si) biased
55	50 krad(Si) unbiased
100	100 krad(Si) biased
105	100 krad(Si) unbiased

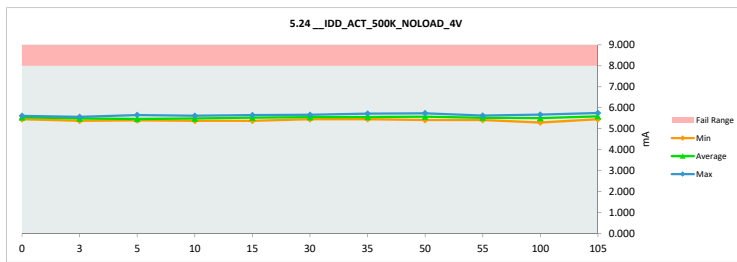
HDR TID Report  
TPS7H5003-SP

HDR TID Report  
TPS7H5003-SP

5.24 IDD_ACT_500K_NOLOAD_4V				
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	Min Limit	8	8	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	5.553	5.552	-0.001
0	138	5.619	5.612	-0.007
0	139	5.457	5.450	-0.007
3	197	5.495	5.506	0.011
3	198	5.516	5.507	-0.009
3	199	5.391	5.372	-0.019
3	202	5.473	5.469	-0.004
3	203	5.600	5.563	-0.037
5	204	5.397	5.396	-0.001
5	205	5.659	5.649	-0.010
5	206	5.404	5.410	0.006
5	208	5.434	5.435	0.001
5	209	5.415	5.413	-0.002
10	210	5.354	5.373	0.019
10	211	5.547	5.545	-0.002
10	212	5.511	5.519	0.008
10	213	5.402	5.388	-0.014
10	214	5.579	5.617	0.038
15	215	5.473	5.464	-0.009
15	217	5.636	5.584	-0.052
15	219	5.532	5.543	0.011
15	220	5.361	5.372	0.011
15	221	5.633	5.645	0.012
30	222	5.563	5.603	0.040
30	223	5.438	5.447	0.009
30	224	5.442	5.457	0.015
30	225	5.506	5.579	0.073
30	226	5.629	5.660	0.031
35	227	5.383	5.496	0.113
35	228	5.502	5.453	-0.049
35	229	5.647	5.713	0.066
35	230	5.546	5.547	0.001
35	231	5.542	5.558	0.016
50	170	5.428	5.410	-0.018
50	172	5.550	5.615	0.065
50	173	5.570	5.688	0.118
50	175	5.705	5.733	0.028
50	176	5.382	5.412	0.030
55	177	5.416	5.407	-0.009
55	178	5.450	5.414	-0.036
55	179	5.494	5.558	0.064
55	180	5.591	5.621	0.030
55	181	5.534	5.600	0.066
100	154	5.288	5.362	0.074
100	155	5.461	5.594	0.133
100	156	5.553	5.548	-0.005
100	157	5.409	5.583	0.174
100	158	5.381	5.388	0.007
100	159	5.572	5.671	0.099
100	160	5.527	5.653	0.126
100	161	5.535	5.645	0.110
100	162	5.546	5.605	0.059
100	163	5.465	5.508	0.043
100	164	5.427	5.499	0.072
100	165	5.366	5.417	0.051
100	166	5.468	5.466	-0.002
100	168	5.526	5.466	-0.060
100	169	5.496	5.494	-0.002
100	182	5.318	5.293	-0.025
100	183	5.422	5.503	0.081
100	184	5.478	5.479	0.001
100	185	5.501	5.633	0.132
100	186	5.461	5.467	0.006
100	187	5.388	5.427	0.039
100	188	5.282	5.478	0.196
100	189	5.437	5.442	0.005
100	190	5.230	5.358	0.128
105	191	5.399	5.445	0.046
105	192	5.482	5.557	0.075
105	193	5.325	5.516	0.191
105	194	5.558	5.624	0.066
105	195	5.576	5.640	0.064
105	196	5.626	5.748	0.122
Max		5.705	5.748	0.196
Average		5.483	5.519	0.036
Min		5.230	5.293	-0.060
Std Dev		0.097	0.103	0.057



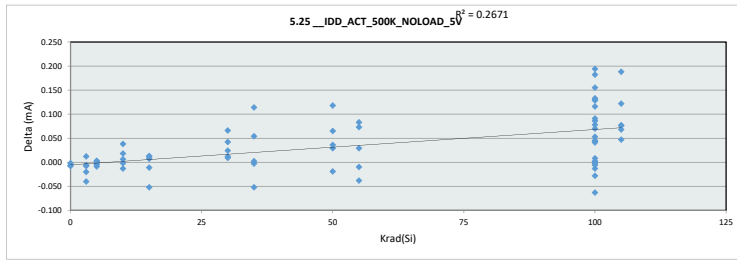
5.24 IDD_ACT_500K_NOLOAD_4V											
Test Site	Tester										
Test Number	Unit	mA	mA								
Max Limit	Min Limit	8	8								
LL	0	3	5	10	15	30	35	50	55	100	105
Min	5.450	5.372	5.396	5.373	5.372	5.447	5.453	5.410	5.407	5.293	5.445
Average	5.538	5.483	5.461	5.488	5.522	5.549	5.553	5.572	5.520	5.499	5.588
Max	5.612	5.563	5.649	5.617	5.645	5.660	5.713	5.733	5.621	5.671	5.748
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000



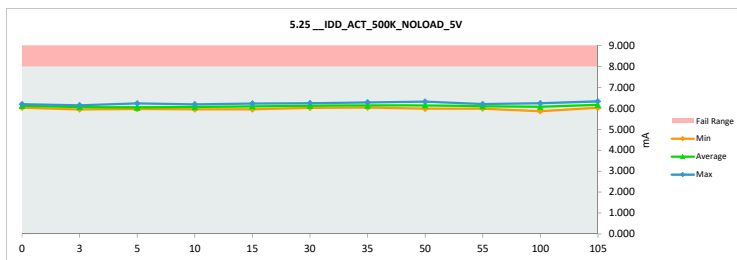


# HDR TID Report TPS7H5003-SP

5.25_IDD_ACT_500K_NOLOAD_5V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	8
Min Limit			mA	8
Krad(Si)	Serial #	PRE	POST	Delta
0	137	6.134	6.132	-0.002
0	138	6.203	6.195	-0.008
0	139	6.036	6.029	-0.007
3	197	6.073	6.085	0.012
3	198	6.096	6.088	-0.008
3	199	5.965	5.945	-0.020
3	202	6.047	6.041	-0.006
3	203	6.184	6.144	-0.040
5	204	5.973	5.972	-0.001
5	205	6.243	6.234	-0.009
5	206	5.982	5.985	0.003
5	208	6.011	6.012	0.001
5	209	5.995	5.990	-0.005
10	210	5.933	5.951	0.018
10	211	6.120	6.118	-0.002
10	212	6.097	6.103	0.006
10	213	5.976	5.963	-0.013
10	214	6.154	6.192	0.038
15	215	6.049	6.038	-0.011
15	217	6.220	6.168	-0.052
15	219	6.111	6.118	0.007
15	220	5.937	5.950	0.013
15	221	6.219	6.230	0.011
30	222	6.145	6.187	0.042
30	223	6.012	6.021	0.009
30	224	6.014	6.026	0.012
30	225	6.082	6.148	0.066
30	226	6.221	6.245	0.024
35	227	5.956	6.070	0.114
35	228	6.086	6.034	-0.052
35	229	6.227	6.281	0.054
35	230	6.127	6.124	-0.003
35	231	6.118	6.120	0.002
50	170	6.001	5.982	-0.019
50	172	6.129	6.194	0.065
50	173	6.145	6.263	0.118
50	175	6.281	6.317	0.036
50	176	5.954	5.983	0.029
55	177	5.994	5.984	-0.010
55	178	6.022	5.984	-0.038
55	179	6.067	6.150	0.083
55	180	6.173	6.202	0.029
55	181	6.144	6.187	0.043
100	154	5.860	5.938	0.078
100	155	6.039	6.168	0.129
100	156	6.127	6.114	-0.013
100	157	5.980	6.162	0.182
100	158	5.957	5.957	0.000
100	159	6.151	6.242	0.091
100	160	6.101	6.234	0.133
100	161	6.114	6.230	0.116
100	162	6.108	6.161	0.053
100	163	6.044	6.088	0.044
100	164	5.996	6.066	0.070
100	165	5.937	5.982	0.045
100	166	6.047	6.041	-0.006
100	168	6.099	6.036	-0.063
100	169	6.070	6.068	-0.002
100	182	5.889	5.861	-0.028
100	183	6.000	6.086	0.086
100	184	6.055	6.055	0.000
100	185	6.079	6.234	0.155
100	186	6.036	6.044	0.008
100	187	5.961	6.002	0.041
100	188	5.858	6.052	0.194
100	189	6.017	6.019	0.002
100	190	5.796	5.925	0.129
105	191	5.979	6.026	0.047
105	192	6.053	6.129	0.076
105	193	5.901	6.089	0.188
105	194	6.133	6.210	0.077
105	195	6.152	6.220	0.068
105	196	6.207	6.329	0.122
Max		6.281	6.329	0.194
Average		6.060	6.095	0.035
Min		5.796	5.861	-0.063
Std Dev		0.100	0.105	0.058



5.25_IDD_ACT_5V																	
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit	LL	0	3	5	10	15	30	35	50	55	100	105
			mA	8	8		6.029	5.945	5.972	5.951	5.950	6.021	6.034	5.982	5.984	5.861	6.026
			mA				6.119	6.061	6.039	6.065	6.101	6.125	6.126	6.148	6.101	6.074	6.167
							6.195	6.144	6.234	6.192	6.230	6.245	6.281	6.317	6.202	6.242	6.329
							8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

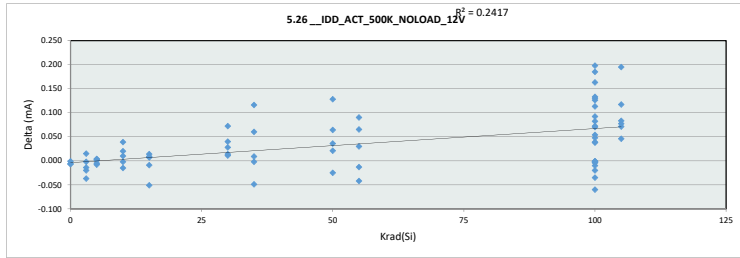


# HDR TID Report TPS7H5003-SP

5.26\_IDD\_ACT\_500K\_NOLOAD\_12V

Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	8
Min Limit	8

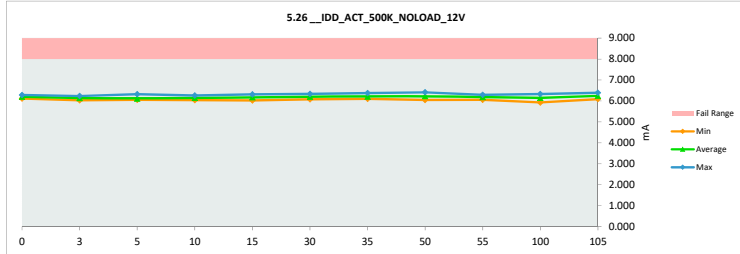
Krad(Si)	Serial #	PRE	POST	Delta
0	137	6.194	6.193	-0.001
0	138	6.291	6.285	-0.006
0	139	6.121	6.114	-0.007
3	197	6.139	6.154	0.015
3	198	6.170	6.156	-0.014
3	199	6.055	6.035	-0.020
3	202	6.106	6.104	-0.002
3	203	6.273	6.236	-0.037
5	204	6.052	6.053	0.001
5	205	6.333	6.325	-0.008
5	206	6.051	6.055	0.004
5	208	6.078	6.081	0.003
5	209	6.074	6.069	-0.005
10	210	6.017	6.037	0.020
10	211	6.201	6.199	-0.002
10	212	6.186	6.196	0.010
10	213	6.059	6.044	-0.015
10	214	6.222	6.261	0.039
15	215	6.106	6.097	-0.009
15	217	6.293	6.242	-0.051
15	219	6.197	6.206	0.009
15	220	6.021	6.028	0.007
15	221	6.299	6.313	0.014
30	222	6.221	6.261	0.040
30	223	6.070	6.081	0.011
30	224	6.096	6.110	0.014
30	225	6.154	6.226	0.072
30	226	6.313	6.341	0.028
35	227	6.034	6.150	0.116
35	228	6.147	6.098	-0.049
35	229	6.316	6.376	0.060
35	230	6.204	6.202	-0.002
35	231	6.192	6.201	0.009
50	170	6.075	6.050	-0.025
50	172	6.223	6.287	0.064
50	173	6.214	6.342	0.128
50	175	6.376	6.412	0.036
50	176	6.058	6.079	0.021
55	177	6.067	6.054	-0.013
55	178	6.120	6.078	-0.042
55	179	6.151	6.241	0.090
55	180	6.262	6.292	0.030
55	181	6.187	6.252	0.065
100	154	5.937	6.007	0.070
100	155	6.118	6.251	0.133
100	156	6.198	6.188	-0.010
100	157	6.044	6.229	0.185
100	158	6.031	6.030	-0.001
100	159	6.237	6.329	0.092
100	160	6.175	6.305	0.130
100	161	6.178	6.291	0.113
100	162	6.197	6.251	0.054
100	163	6.116	6.155	0.039
100	164	6.059	6.131	0.072
100	165	5.997	6.045	0.048
100	166	6.131	6.111	-0.020
100	168	6.189	6.129	-0.060
100	169	6.127	6.123	-0.004
100	182	5.961	5.926	-0.035
100	183	6.053	6.135	0.082
100	184	6.132	6.130	-0.002
100	185	6.156	6.319	0.163
100	186	6.096	6.095	-0.001
100	187	6.037	6.075	0.038
100	188	5.937	6.135	0.198
100	189	6.076	6.073	-0.003
100	190	5.862	5.988	0.126
105	191	6.044	6.090	0.046
105	192	6.134	6.211	0.077
105	193	5.991	6.186	0.195
105	194	6.209	6.292	0.083
105	195	6.213	6.284	0.071
105	196	6.276	6.393	0.117
Max		6.376	6.412	0.198
Average		6.136	6.172	0.036
Min		5.862	5.926	-0.060
Std Dev		0.102	0.109	0.059



5.26\_IDD\_ACT\_500K\_NOLOAD\_12V

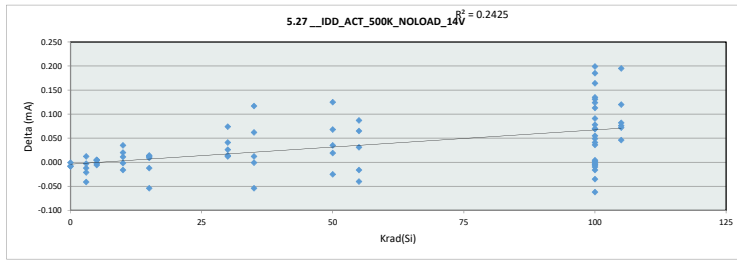
Test Site	
Tester	
Test Number	
Max Limit	8
Min Limit	8

LL	0	3	5	10	15	30	35	50	55	100	105
Min	6.114	6.035	6.053	6.037	6.028	6.081	6.098	6.050	6.054	5.926	6.090
Average	6.197	6.137	6.117	6.147	6.177	6.204	6.205	6.234	6.183	6.144	6.243
Max	6.285	6.236	6.325	6.261	6.313	6.341	6.376	6.412	6.292	6.329	6.393
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

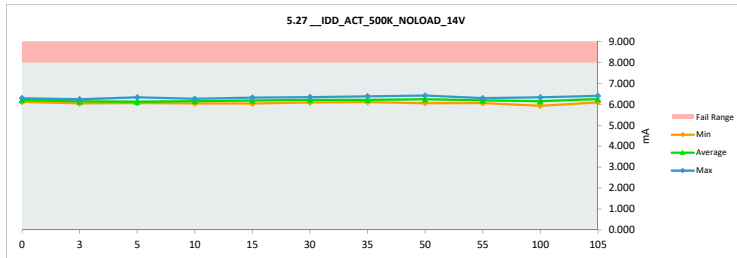


HDR TID Report  
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5.27_IDD_ACT_500K_NOLOAD_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	8	8		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	6.200	6.199	-0.001
0	138	6.298	6.289	-0.009
0	139	6.127	6.119	-0.008
3	197	6.143	6.155	0.012
3	198	6.172	6.160	-0.012
3	199	6.061	6.040	-0.021
3	202	6.112	6.109	-0.003
3	203	6.279	6.238	-0.041
5	204	6.052	6.056	0.004
5	205	6.338	6.332	-0.006
5	206	6.056	6.060	0.004
5	208	6.084	6.089	0.005
5	209	6.077	6.074	-0.003
10	210	6.023	6.043	0.020
10	211	6.205	6.203	-0.002
10	212	6.191	6.202	0.011
10	213	6.066	6.050	-0.016
10	214	6.229	6.264	0.035
15	215	6.110	6.098	-0.012
15	217	6.299	6.245	-0.054
15	219	6.204	6.213	0.009
15	220	6.023	6.035	0.012
15	221	6.303	6.317	0.014
30	222	6.224	6.265	0.041
30	223	6.075	6.087	0.012
30	224	6.100	6.114	0.014
30	225	6.158	6.232	0.074
30	226	6.319	6.345	0.026
35	227	6.037	6.154	0.117
35	228	6.155	6.101	-0.054
35	229	6.321	6.383	0.062
35	230	6.206	6.205	-0.001
35	231	6.197	6.209	0.012
50	170	6.078	6.053	-0.025
50	172	6.227	6.295	0.068
50	173	6.222	6.347	0.125
50	175	6.382	6.417	0.035
50	176	6.065	6.084	0.019
55	177	6.074	6.058	-0.016
55	178	6.124	6.084	-0.040
55	179	6.156	6.243	0.087
55	180	6.264	6.295	0.031
55	181	6.190	6.255	0.065
100	154	5.943	6.013	0.070
100	155	6.122	6.257	0.135
100	156	6.202	6.192	-0.010
100	157	6.048	6.233	0.185
100	158	6.034	6.038	0.004
100	159	6.241	6.332	0.091
100	160	6.181	6.312	0.131
100	161	6.183	6.296	0.113
100	162	6.199	6.254	0.055
100	163	6.122	6.158	0.036
100	164	6.065	6.134	0.069
100	165	6.003	6.052	0.049
100	166	6.134	6.118	-0.016
100	168	6.195	6.133	-0.062
100	169	6.133	6.127	-0.006
100	182	5.964	5.929	-0.035
100	183	6.060	6.138	0.078
100	184	6.138	6.133	-0.005
100	185	6.159	6.323	0.164
100	186	6.099	6.100	0.001
100	187	6.041	6.082	0.041
100	188	5.943	6.142	0.199
100	189	6.078	6.076	-0.002
100	190	5.867	5.991	0.124
105	191	6.047	6.093	0.046
105	192	6.139	6.215	0.076
105	193	5.997	6.192	0.195
105	194	6.215	6.297	0.082
105	195	6.217	6.289	0.072
105	196	6.280	6.400	0.120
Max		6.382	6.417	0.199
Average		6.141	6.176	0.035
Min		5.867	5.929	-0.062
Std Dev		0.103	0.109	0.060

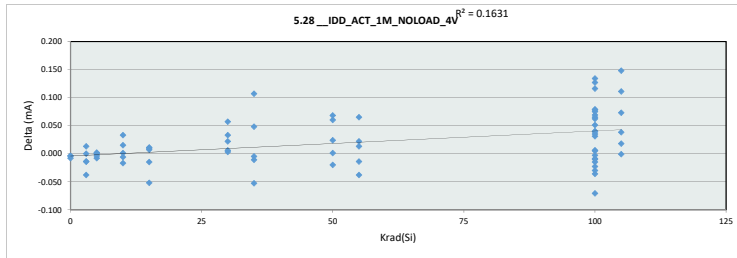


5.27_IDD_ACT_500K_NOLOAD_14V											
Test Site											
Tester											
Test Number											
Max Limit	8	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	6.119	6.040	6.056	6.043	6.035	6.087	6.101	6.053	6.058	5.929	6.093
Average	6.202	6.140	6.122	6.152	6.182	6.209	6.210	6.239	6.187	6.148	6.248
Max	6.289	6.238	6.332	6.264	6.317	6.345	6.383	6.417	6.295	6.332	6.400
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

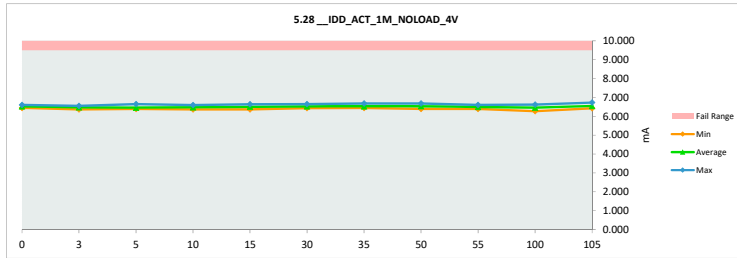


HDR TID Report  
TPS7H5003-SP

		5.28 IDD_ACT_1M_NOLOAD_4V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	Min Limit	9.5	9.5	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	6.542	6.538	-0.004
0	138	6.609	6.601	-0.008
0	139	6.449	6.445	-0.004
3	197	6.489	6.502	0.013
3	198	6.508	6.494	-0.014
3	199	6.379	6.365	-0.014
3	202	6.451	6.451	0.000
3	203	6.589	6.551	-0.038
5	204	6.393	6.393	0.000
5	205	6.652	6.644	-0.008
5	206	6.395	6.395	0.000
5	208	6.426	6.428	0.002
5	209	6.403	6.399	-0.004
10	210	6.347	6.362	0.015
10	211	6.532	6.526	-0.006
10	212	6.508	6.509	0.001
10	213	6.392	6.375	-0.017
10	214	6.565	6.598	0.033
15	215	6.458	6.443	-0.015
15	217	6.622	6.570	-0.052
15	219	6.521	6.528	0.007
15	220	6.351	6.362	0.011
15	221	6.625	6.635	0.010
30	222	6.552	6.585	0.033
30	223	6.431	6.434	0.003
30	224	6.425	6.431	0.006
30	225	6.492	6.549	0.057
30	226	6.626	6.648	0.022
35	227	6.371	6.478	0.107
35	228	6.497	6.444	-0.053
35	229	6.633	6.681	0.048
35	230	6.536	6.531	-0.005
35	231	6.527	6.516	-0.011
50	170	6.408	6.388	-0.020
50	172	6.536	6.596	0.060
50	173	6.560	6.628	0.068
50	175	6.676	6.677	0.001
50	176	6.368	6.392	0.024
55	177	6.409	6.395	-0.014
55	178	6.431	6.393	-0.038
55	179	6.464	6.477	0.013
55	180	6.581	6.603	0.022
55	181	6.520	6.585	0.065
100	154	6.272	6.347	0.075
100	155	6.441	6.506	0.065
100	156	6.537	6.507	-0.030
100	157	6.390	6.517	0.127
100	158	6.367	6.344	-0.023
100	159	6.550	6.619	0.069
100	160	6.510	6.588	0.078
100	161	6.520	6.571	0.051
100	162	6.516	6.522	0.006
100	163	6.458	6.497	0.039
100	164	6.406	6.441	0.035
100	165	6.340	6.380	0.040
100	166	6.458	6.449	-0.009
100	168	6.509	6.438	-0.071
100	169	6.483	6.468	-0.015
100	182	6.306	6.270	-0.036
100	183	6.415	6.494	0.079
100	184	6.471	6.468	-0.003
100	185	6.484	6.546	0.062
100	186	6.443	6.448	0.005
100	187	6.370	6.401	0.031
100	188	6.275	6.409	0.134
100	189	6.424	6.414	-0.010
100	190	6.215	6.331	0.116
105	191	6.388	6.426	0.038
105	192	6.460	6.533	0.073
105	193	6.317	6.465	0.148
105	194	6.546	6.545	-0.001
105	195	6.557	6.575	0.018
105	196	6.617	6.728	0.111
Max		6.676	6.728	0.148
Average		6.470	6.490	0.021
Min		6.215	6.270	-0.071
Std Dev		0.097	0.096	0.046

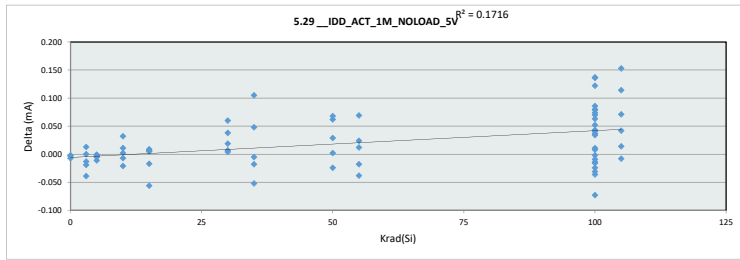


		5.28 IDD_ACT										
Test Site	Tester											
Test Number	Unit	9.5	mA									
Max Limit	Min Limit	9.5	mA									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		6.445	6.365	6.393	6.362	6.362	6.431	6.444	6.388	6.393	6.270	6.426
Average		6.528	6.473	6.452	6.474	6.508	6.529	6.530	6.536	6.491	6.457	6.545
Max		6.601	6.551	6.644	6.598	6.635	6.648	6.681	6.677	6.603	6.619	6.728
UL		9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

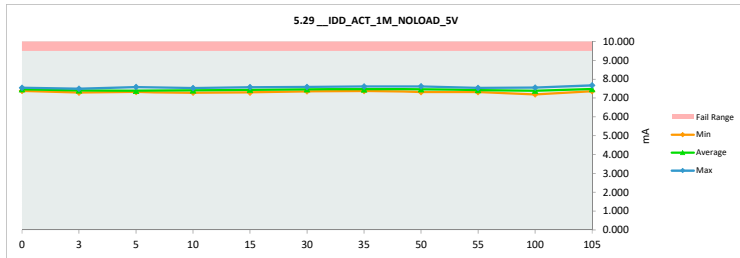


HDR TID Report  
TPS7H5003-SP

5.29 IDD_ACT_1M_NOLOAD_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.477	7.475	-0.002
0	138	7.545	7.542	-0.003
0	139	7.385	7.378	-0.007
3	197	7.428	7.441	0.013
3	198	7.443	7.430	-0.013
3	199	7.311	7.292	-0.019
3	202	7.376	7.376	0.000
3	203	7.529	7.490	-0.039
5	204	7.327	7.326	-0.001
5	205	7.595	7.584	-0.011
5	206	7.330	7.325	-0.005
5	208	7.359	7.358	-0.001
5	209	7.333	7.329	-0.004
10	210	7.278	7.289	0.011
10	211	7.462	7.455	-0.007
10	212	7.449	7.451	0.002
10	213	7.323	7.302	-0.021
10	214	7.494	7.526	0.032
15	215	7.384	7.367	-0.017
15	217	7.561	7.505	-0.056
15	219	7.454	7.461	0.007
15	220	7.287	7.292	0.005
15	221	7.566	7.575	0.009
30	222	7.489	7.527	0.038
30	223	7.361	7.365	0.004
30	224	7.347	7.354	0.007
30	225	7.421	7.481	0.060
30	226	7.569	7.588	0.019
35	227	7.300	7.405	0.105
35	228	7.427	7.375	-0.052
35	229	7.566	7.614	0.048
35	230	7.468	7.463	-0.005
35	231	7.459	7.441	-0.018
50	170	7.337	7.313	-0.024
50	172	7.470	7.532	0.062
50	173	7.489	7.557	0.068
50	175	7.613	7.615	0.002
50	176	7.291	7.320	0.029
55	177	7.344	7.326	-0.018
55	178	7.353	7.315	-0.038
55	179	7.394	7.406	0.012
55	180	7.514	7.538	0.024
55	181	7.453	7.522	0.069
100	154	7.193	7.279	0.086
100	155	7.369	7.439	0.070
100	156	7.467	7.436	-0.031
100	157	7.314	7.451	0.137
100	158	7.296	7.272	-0.024
100	159	7.481	7.555	0.074
100	160	7.437	7.516	0.079
100	161	7.452	7.504	0.052
100	162	7.432	7.443	0.011
100	163	7.395	7.432	0.037
100	164	7.328	7.369	0.041
100	165	7.255	7.298	0.043
100	166	7.390	7.381	-0.009
100	168	7.440	7.367	-0.073
100	169	7.414	7.398	-0.016
100	182	7.228	7.192	-0.036
100	183	7.353	7.433	0.080
100	184	7.407	7.405	-0.002
100	185	7.416	7.479	0.063
100	186	7.372	7.380	0.008
100	187	7.297	7.331	0.034
100	188	7.201	7.337	0.136
100	189	7.358	7.344	-0.014
100	190	7.135	7.257	0.122
105	191	7.320	7.362	0.042
105	192	7.384	7.455	0.071
105	193	7.244	7.397	0.153
105	194	7.480	7.472	-0.008
105	195	7.488	7.502	0.014
105	196	7.556	7.670	0.114
Max		7.613	7.670	0.153
Average		7.401	7.422	0.021
Min		7.135	7.192	-0.073
Std Dev		0.101	0.099	0.048

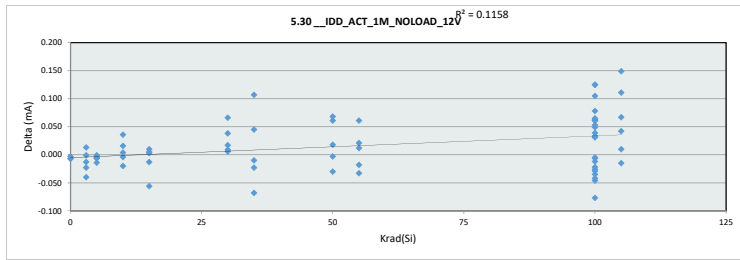


5.29 IDD_ACT											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.378	7.292	7.325	7.289	7.292	7.354	7.375	7.313	7.315	7.192	7.362
Average	7.465	7.406	7.384	7.405	7.440	7.463	7.460	7.467	7.421	7.387	7.476
Max	7.542	7.490	7.584	7.526	7.575	7.588	7.614	7.615	7.538	7.555	7.670
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

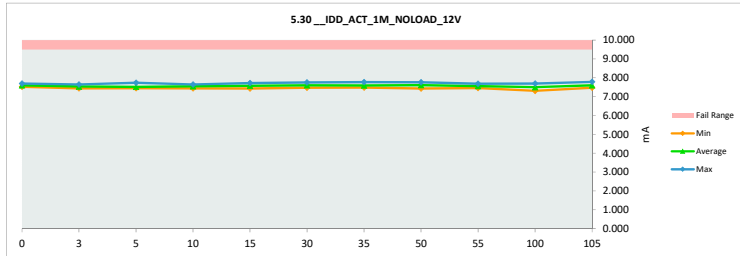


HDR TID Report  
TPS7H5003-SP

		5.30_IDD_ACT_1M_NOLOAD_12V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	Min Limit	9.5	9.5	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.584	7.581	-0.003
0	138	7.702	7.695	-0.007
0	139	7.532	7.525	-0.007
3	197	7.546	7.559	0.013
3	198	7.567	7.554	-0.013
3	199	7.464	7.441	-0.023
3	202	7.487	7.486	-0.001
3	203	7.686	7.646	-0.040
5	204	7.467	7.461	-0.006
5	205	7.753	7.739	-0.014
5	206	7.452	7.447	-0.005
5	208	7.483	7.482	-0.001
5	209	7.473	7.467	-0.006
10	210	7.427	7.443	0.016
10	211	7.598	7.594	-0.004
10	212	7.608	7.612	0.004
10	213	7.464	7.444	-0.020
10	214	7.610	7.646	0.036
15	215	7.483	7.470	-0.013
15	217	7.686	7.630	-0.056
15	219	7.609	7.612	0.003
15	220	7.429	7.434	0.005
15	221	7.709	7.719	0.010
30	222	7.616	7.654	0.038
30	223	7.466	7.472	0.006
30	224	7.488	7.497	0.009
30	225	7.550	7.616	0.066
30	226	7.736	7.753	0.017
35	227	7.436	7.543	0.107
35	228	7.548	7.480	-0.068
35	229	7.725	7.770	0.045
35	230	7.603	7.593	-0.010
35	231	7.593	7.570	-0.023
50	170	7.464	7.434	-0.030
50	172	7.632	7.693	0.061
50	173	7.610	7.678	0.068
50	175	7.768	7.765	-0.003
50	176	7.469	7.487	0.018
55	177	7.476	7.458	-0.018
55	178	7.516	7.483	-0.033
55	179	7.531	7.543	0.012
55	180	7.667	7.688	0.021
55	181	7.577	7.638	0.061
100	154	7.328	7.406	0.078
100	155	7.503	7.556	0.053
100	156	7.590	7.548	-0.042
100	157	7.422	7.547	0.125
100	158	7.422	7.387	-0.035
100	159	7.629	7.692	0.063
100	160	7.570	7.631	0.061
100	161	7.564	7.603	0.039
100	162	7.584	7.579	-0.005
100	163	7.518	7.549	0.031
100	164	7.435	7.468	0.033
100	165	7.361	7.410	0.049
100	166	7.536	7.507	-0.029
100	168	7.599	7.522	-0.077
100	169	7.513	7.487	-0.026
100	182	7.352	7.306	-0.046
100	183	7.452	7.517	0.065
100	184	7.544	7.532	-0.012
100	185	7.548	7.609	0.061
100	186	7.475	7.469	-0.006
100	187	7.429	7.462	0.033
100	188	7.345	7.470	0.125
100	189	7.460	7.438	-0.022
100	190	7.256	7.361	0.105
105	191	7.429	7.471	0.042
105	192	7.525	7.592	0.067
105	193	7.403	7.552	0.149
105	194	7.611	7.596	-0.015
105	195	7.593	7.603	0.010
105	196	7.675	7.786	0.111
	Max	7.768	7.786	0.149
	Average	7.534	7.550	0.016
	Min	7.256	7.306	-0.077
	Std Dev	0.106	0.104	0.047

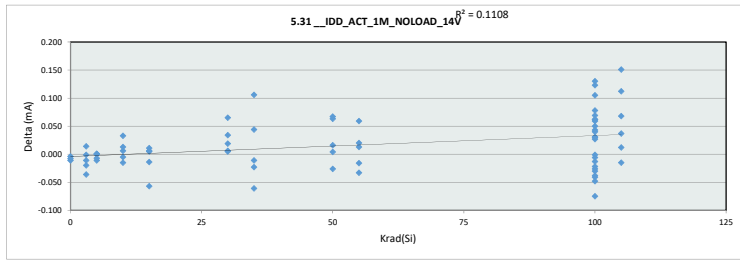


		5.30_IDD_ACT										
Test Site	Tester											
Test Number	Unit	9.5	mA									
Max Limit	Min Limit	9.5	mA									
LL	Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
Min		7.525	7.441	7.447	7.443	7.434	7.472	7.480	7.434	7.458	7.306	7.471
Average		7.600	7.537	7.519	7.548	7.573	7.598	7.591	7.611	7.562	7.502	7.600
Max		7.695	7.646	7.739	7.646	7.719	7.753	7.770	7.765	7.688	7.692	7.786
UL		9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

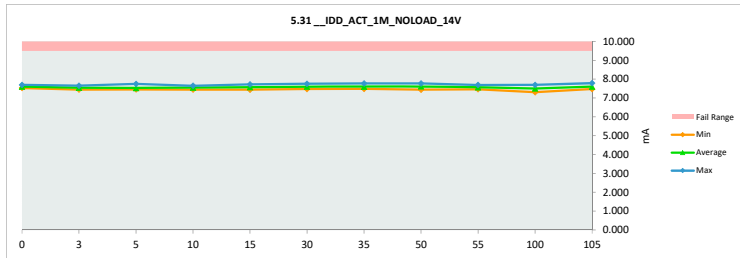


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5.31_IDD_ACT_1M_NOLOAD_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.590	7.586	-0.004
0	138	7.707	7.696	-0.011
0	139	7.538	7.530	-0.008
3	197	7.548	7.562	0.014
3	198	7.571	7.560	-0.011
3	199	7.467	7.447	-0.020
3	202	7.491	7.490	-0.001
3	203	7.689	7.653	-0.036
5	204	7.468	7.468	0.000
5	205	7.756	7.745	-0.011
5	206	7.454	7.454	0.000
5	208	7.487	7.488	0.001
5	209	7.476	7.469	-0.007
10	210	7.433	7.446	0.013
10	211	7.604	7.599	-0.005
10	212	7.608	7.614	0.006
10	213	7.466	7.451	-0.015
10	214	7.616	7.649	0.033
15	215	7.490	7.476	-0.014
15	217	7.692	7.635	-0.057
15	219	7.614	7.619	0.005
15	220	7.435	7.441	0.006
15	221	7.713	7.724	0.011
30	222	7.621	7.655	0.034
30	223	7.470	7.475	0.005
30	224	7.495	7.501	0.006
30	225	7.555	7.620	0.065
30	226	7.739	7.758	0.019
35	227	7.442	7.548	0.106
35	228	7.548	7.487	-0.061
35	229	7.731	7.775	0.044
35	230	7.607	7.596	-0.011
35	231	7.597	7.574	-0.023
50	170	7.467	7.441	-0.026
50	172	7.634	7.697	0.063
50	173	7.616	7.683	0.067
50	175	7.770	7.774	0.004
50	176	7.475	7.491	0.016
55	177	7.479	7.463	-0.016
55	178	7.523	7.490	-0.033
55	179	7.535	7.548	0.013
55	180	7.673	7.693	0.020
55	181	7.584	7.643	0.059
100	154	7.334	7.412	0.078
100	155	7.509	7.559	0.050
100	156	7.593	7.552	-0.041
100	157	7.428	7.551	0.123
100	158	7.428	7.390	-0.038
100	159	7.634	7.696	0.062
100	160	7.572	7.634	0.062
100	161	7.567	7.607	0.040
100	162	7.586	7.585	-0.001
100	163	7.524	7.551	0.027
100	164	7.441	7.472	0.031
100	165	7.369	7.412	0.043
100	166	7.540	7.510	-0.030
100	168	7.602	7.527	-0.075
100	169	7.516	7.494	-0.022
100	182	7.357	7.309	-0.048
100	183	7.454	7.523	0.069
100	184	7.549	7.536	-0.013
100	185	7.556	7.615	0.059
100	186	7.480	7.474	-0.006
100	187	7.434	7.464	0.030
100	188	7.349	7.479	0.130
100	189	7.467	7.441	-0.026
100	190	7.259	7.364	0.105
105	191	7.437	7.474	0.037
105	192	7.530	7.598	0.068
105	193	7.407	7.558	0.151
105	194	7.614	7.599	-0.015
105	195	7.597	7.609	0.012
105	196	7.677	7.789	0.112
Max		7.770	7.789	0.151
Average		7.538	7.555	0.017
Min		7.259	7.309	-0.075
Std Dev		0.105	0.104	0.046

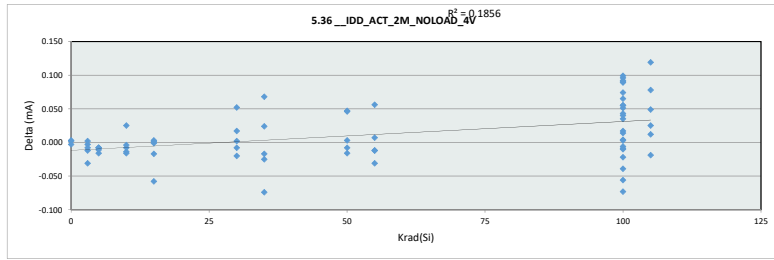


5.31_IDD_ACT											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.530	7.447	7.454	7.446	7.441	7.475	7.487	7.441	7.463	7.309	7.474
Average	7.604	7.542	7.525	7.552	7.579	7.602	7.596	7.617	7.567	7.507	7.605
Max	7.696	7.653	7.745	7.649	7.724	7.758	7.775	7.774	7.693	7.696	7.789
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

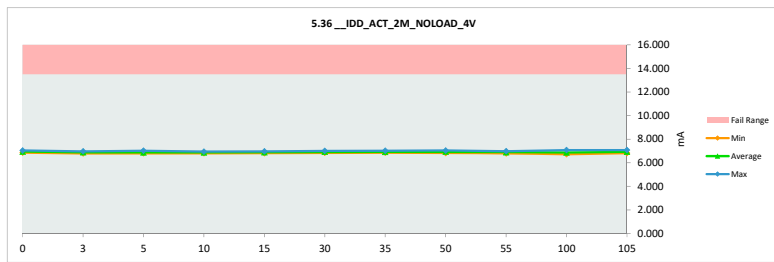


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5.36 IDD_ACT_2M_NOLOAD_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	13.5	13.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	6.935	6.932	-0.003
0	138	7.040	7.043	0.003
0	139	6.861	6.863	0.002
3	197	6.927	6.929	0.002
3	198	6.896	6.888	-0.008
3	199	6.806	6.794	-0.012
3	202	6.836	6.833	-0.003
3	203	6.993	6.962	-0.031
5	204	6.823	6.815	-0.008
5	205	7.040	7.024	-0.016
5	206	6.814	6.806	-0.008
5	208	6.878	6.867	-0.011
5	209	6.894	6.886	-0.008
10	210	6.820	6.816	-0.004
10	211	6.894	6.878	-0.016
10	212	6.894	6.896	-0.008
10	213	6.821	6.807	-0.014
10	214	6.923	6.948	0.025
15	215	6.841	6.824	-0.017
15	217	7.007	6.949	-0.058
15	219	6.924	6.923	-0.001
15	220	6.798	6.799	0.001
15	221	6.959	6.962	0.003
30	222	6.942	6.959	0.017
30	223	6.874	6.854	-0.020
30	224	6.855	6.847	-0.008
30	225	6.887	6.939	0.052
30	226	7.006	7.008	0.002
35	227	6.805	6.873	0.068
35	228	6.940	6.866	-0.074
35	229	6.999	7.023	0.024
35	230	6.923	6.906	-0.017
35	231	6.910	6.885	-0.025
50	170	6.833	6.817	-0.016
50	172	6.933	6.979	0.046
50	173	6.960	7.007	0.047
50	175	7.048	7.040	-0.008
50	176	6.831	6.834	0.003
55	177	6.828	6.797	-0.031
55	178	6.830	6.837	0.007
55	179	6.835	6.823	-0.012
55	180	6.990	6.978	-0.012
55	181	6.911	6.967	0.056
100	154	6.692	6.782	0.090
100	155	6.843	6.898	0.055
100	156	6.911	6.902	-0.009
100	157	6.815	6.911	0.096
100	158	6.814	6.775	-0.039
100	159	6.982	7.071	0.089
100	160	6.866	6.940	0.074
100	161	6.894	6.937	0.043
100	162	6.870	6.874	0.004
100	163	6.885	6.902	0.017
100	164	6.763	6.803	0.040
100	165	6.810	6.845	0.035
100	166	6.900	6.894	-0.006
100	168	6.858	6.802	-0.056
100	169	6.902	6.905	0.003
100	182	6.794	6.721	-0.073
100	183	6.852	6.917	0.065
100	184	6.869	6.859	-0.010
100	185	6.900	6.955	0.055
100	186	6.860	6.874	0.014
100	187	6.772	6.823	0.051
100	188	6.754	6.853	0.099
100	189	6.906	6.884	-0.022
100	190	6.655	6.747	0.092
105	191	6.792	6.817	0.025
105	192	6.914	6.963	0.049
105	193	6.763	6.882	0.119
105	194	6.934	6.915	-0.019
105	195	6.909	6.921	0.012
105	196	6.993	7.071	0.078
Max		7.048	7.071	0.119
Average		6.880	6.892	0.012
Min		6.655	6.721	-0.074
Std Dev		0.078	0.077	0.041



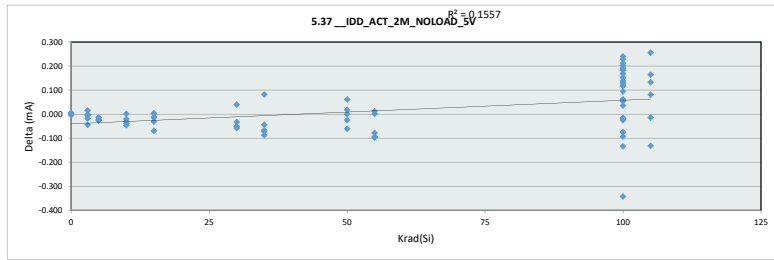
5.36 IDD_ACT_2M_NOLOAD_4V											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.863	6.794	6.806	6.807	6.799	6.847	6.866	6.817	6.797	6.721	6.817
Average	6.946	6.881	6.880	6.867	6.891	6.921	6.911	6.935	6.880	6.870	6.928
Max	7.043	6.962	7.024	6.948	6.962	7.008	7.023	7.040	6.978	7.071	7.071
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



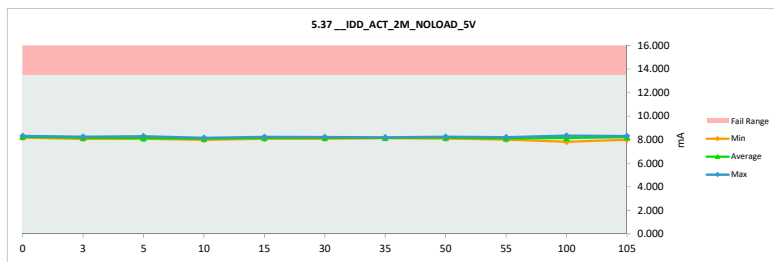


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5.37_IDD_ACT_2M_NOLOAD_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	13.5	13.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.233	8.229	-0.004
0	138	8.318	8.320	0.002
0	139	8.173	8.179	0.006
3	197	8.200	8.215	0.015
3	198	8.167	8.167	0.000
3	199	8.078	8.060	-0.018
3	202	8.108	8.102	-0.006
3	203	8.295	8.250	-0.045
5	204	8.103	8.089	-0.014
5	205	8.324	8.300	-0.024
5	206	8.107	8.081	-0.026
5	208	8.131	8.117	-0.014
5	209	8.100	8.076	-0.024
10	210	8.007	7.987	-0.020
10	211	8.168	8.122	-0.046
10	212	8.191	8.142	-0.039
10	213	8.086	8.055	-0.031
10	214	8.165	8.166	0.001
15	215	8.117	8.086	-0.031
15	217	8.308	8.239	-0.069
15	219	8.230	8.218	-0.012
15	220	8.074	8.078	0.004
15	221	8.129	8.114	-0.015
30	222	8.255	8.204	-0.051
30	223	8.148	8.096	-0.052
30	224	8.106	8.073	-0.033
30	225	8.176	8.216	0.040
30	226	8.285	8.227	-0.058
35	227	8.067	8.149	0.082
35	228	8.206	8.118	-0.088
35	229	8.247	8.203	-0.044
35	230	8.222	8.155	-0.067
35	231	8.213	8.142	-0.071
50	170	8.070	8.088	0.018
50	172	8.224	8.199	-0.025
50	173	8.258	8.258	0.000
50	175	8.313	8.252	-0.061
50	176	8.023	8.084	0.061
55	177	8.125	8.047	-0.078
55	178	8.092	8.093	0.001
55	179	8.100	8.003	-0.097
55	180	8.300	8.206	-0.094
55	181	8.187	8.199	0.012
100	154	7.926	8.128	0.202
100	155	8.095	8.264	0.169
100	156	8.182	8.299	0.117
100	157	8.063	8.292	0.229
100	158	8.057	8.093	0.036
100	159	8.206	8.344	0.138
100	160	8.093	8.068	-0.025
100	161	8.167	8.092	-0.075
100	162	8.119	7.985	-0.134
100	163	8.170	8.300	0.130
100	164	7.984	7.968	-0.016
100	165	7.967	7.892	-0.075
100	166	8.160	8.222	0.062
100	168	8.048	7.955	-0.093
100	169	8.181	8.235	0.054
100	182	7.962	8.202	0.240
100	183	8.141	8.324	0.183
100	184	8.162	7.820	-0.342
100	185	8.183	8.335	0.152
100	186	8.127	8.221	0.094
100	187	8.035	8.156	0.121
100	188	7.926	8.118	0.192
100	189	8.102	8.086	-0.016
100	190	7.874	8.085	0.211
105	191	8.072	8.205	0.133
105	192	8.144	8.308	0.164
105	193	7.993	8.248	0.255
105	194	8.233	8.314	0.081
105	195	8.130	7.998	-0.132
105	196	8.250	8.236	-0.014
Max		8.324	8.344	0.255
Average		8.139	8.153	0.014
Min		7.874	7.820	-0.342
Std Dev		0.098	0.109	0.103

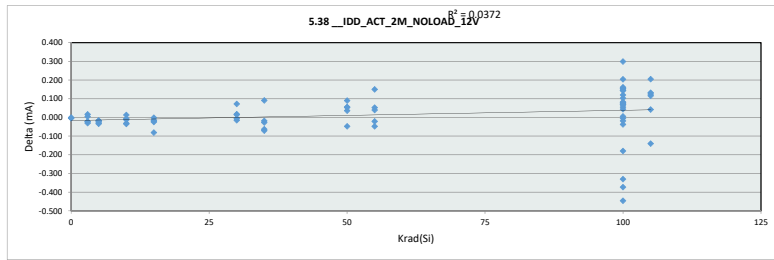


5.37_IDD_ACT_2M_NOLOAD_5V											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.179	8.060	8.076	7.987	8.078	8.073	8.118	8.084	8.003	7.820	7.998
Average	8.243	8.159	8.133	8.094	8.147	8.163	8.153	8.176	8.110	8.145	8.218
Max	8.320	8.250	8.300	8.166	8.239	8.227	8.203	8.258	8.206	8.344	8.314
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

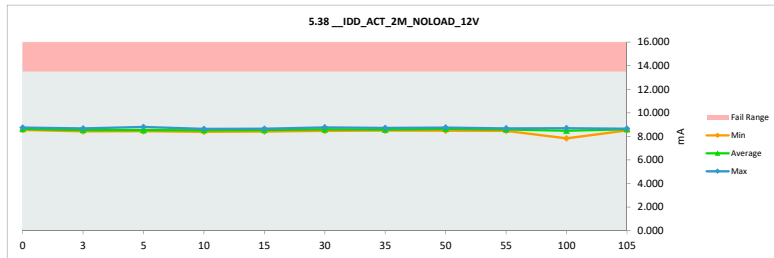


# HDR TID Report TPS7H5003-SP

5.38 IDD_ACT_2M_NOLOAD_12V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	13.5
			mA	13.5
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.548	8.544	-0.004
0	138	8.748	8.745	-0.003
0	139	8.575	8.574	-0.001
3	197	8.545	8.561	0.016
3	198	8.545	8.524	-0.021
3	199	8.508	8.484	-0.024
3	202	8.416	8.420	0.004
3	203	8.706	8.675	-0.031
5	204	8.489	8.471	-0.018
5	205	8.827	8.793	-0.034
5	206	8.456	8.433	-0.023
5	208	8.489	8.472	-0.017
5	209	8.488	8.458	-0.030
10	210	8.415	8.383	-0.032
10	211	8.573	8.560	-0.013
10	212	8.641	8.634	-0.007
10	213	8.482	8.447	-0.035
10	214	8.579	8.592	0.013
15	215	8.425	8.405	-0.020
15	217	8.678	8.597	-0.081
15	219	8.651	8.649	-0.002
15	220	8.480	8.455	-0.025
15	221	8.571	8.556	-0.015
30	222	8.609	8.624	0.015
30	223	8.463	8.457	-0.006
30	224	8.505	8.522	0.017
30	225	8.538	8.610	0.072
30	226	8.778	8.763	-0.015
35	227	8.457	8.548	0.091
35	228	8.555	8.484	-0.071
35	229	8.735	8.716	-0.019
35	230	8.612	8.585	-0.027
35	231	8.576	8.513	-0.063
50	170	8.436	8.472	0.036
50	172	8.654	8.710	0.056
50	173	8.600	8.653	0.053
50	175	8.803	8.755	-0.048
50	176	8.487	8.577	0.090
55	177	8.492	8.531	0.039
55	178	8.532	8.584	0.052
55	179	8.511	8.463	-0.048
55	180	8.699	8.678	-0.021
55	181	8.536	8.686	0.150
100	154	8.302	8.463	0.161
100	155	8.470	8.590	0.120
100	156	8.565	8.609	0.044
100	157	8.383	8.587	0.204
100	158	8.428	8.390	-0.038
100	159	8.605	8.687	0.082
100	160	8.535	8.679	0.144
100	161	8.548	8.218	-0.330
100	162	8.561	8.629	0.068
100	163	8.525	8.628	0.103
100	164	8.376	8.196	-0.180
100	165	8.277	7.831	-0.446
100	166	8.566	8.561	-0.005
100	168	8.554	8.627	0.073
100	169	8.489	8.470	-0.019
100	182	8.314	8.613	0.299
100	183	8.439	8.595	0.156
100	184	8.575	8.654	0.079
100	185	8.550	8.694	0.144
100	186	8.432	8.487	0.055
100	187	8.414	8.482	0.068
100	188	8.312	8.317	0.005
100	189	8.404	8.031	-0.373
100	190	8.222	8.374	0.152
105	191	8.394	8.510	0.116
105	192	8.528	8.660	0.132
105	193	8.428	8.633	0.205
105	194	8.602	8.644	0.042
105	195	8.515	8.640	0.125
105	196	8.662	8.522	-0.140
Max		8.827	8.793	0.299
Average		8.526	8.540	0.014
Min		8.222	7.831	-0.446
Std Dev		0.119	0.157	0.116

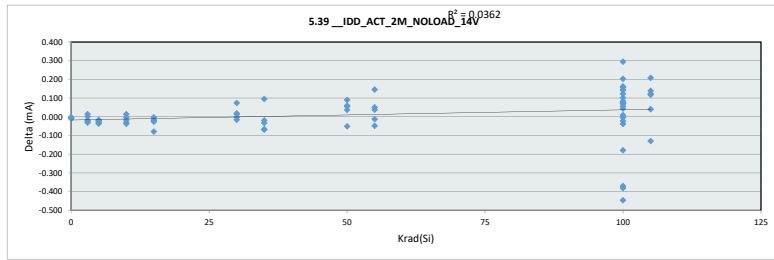


5.38 IDD_ACT															
Test Site	Tester	Test Number	Max Limit	Min Limit	Krad(Si)										
			13.5	mA	0	3	5	10	15	30	35	50	55	100	105
LL															
Min			8.544	8.420	8.433	8.383	8.405	8.457	8.484	8.472	8.463	7.831	8.510		
Average			8.621	8.533	8.525	8.523	8.532	8.595	8.569	8.633	8.588	8.476	8.602		
Max			8.745	8.675	8.793	8.634	8.649	8.763	8.716	8.755	8.686	8.694	8.660		
UL			13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500		

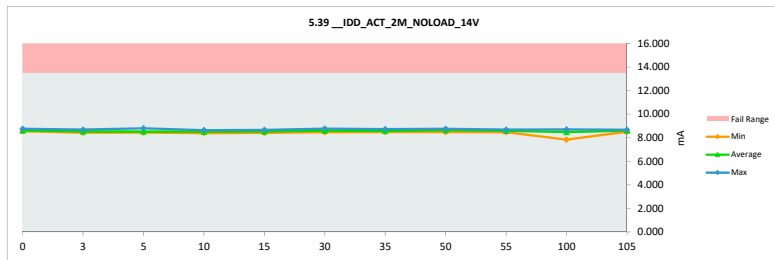


HDR TID Report  
TPS7H5003-SP

5.39 IDD_ACT_2M_NOLOAD_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	13.5	13.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.554	8.544	-0.010
0	138	8.751	8.749	-0.002
0	139	8.581	8.576	-0.005
3	197	8.551	8.565	0.014
3	198	8.550	8.531	-0.019
3	199	8.513	8.490	-0.023
3	202	8.423	8.423	0.000
3	203	8.711	8.680	-0.031
5	204	8.492	8.475	-0.017
5	205	8.832	8.800	-0.032
5	206	8.463	8.439	-0.024
5	208	8.495	8.475	-0.020
5	209	8.496	8.460	-0.036
10	210	8.419	8.389	-0.030
10	211	8.580	8.563	-0.017
10	212	8.643	8.641	-0.002
10	213	8.487	8.450	-0.037
10	214	8.585	8.599	0.014
15	215	8.436	8.410	-0.026
15	217	8.683	8.604	-0.079
15	219	8.659	8.656	-0.003
15	220	8.483	8.462	-0.021
15	221	8.576	8.559	-0.017
30	222	8.614	8.628	0.014
30	223	8.469	8.467	-0.002
30	224	8.508	8.526	0.018
30	225	8.543	8.617	0.074
30	226	8.781	8.765	-0.016
35	227	8.459	8.554	0.095
35	228	8.558	8.489	-0.069
35	229	8.740	8.721	-0.019
35	230	8.620	8.587	-0.033
35	231	8.580	8.514	-0.066
50	170	8.441	8.478	0.037
50	172	8.658	8.713	0.055
50	173	8.602	8.661	0.059
50	175	8.810	8.759	-0.051
50	176	8.493	8.583	0.090
55	177	8.495	8.533	0.038
55	178	8.538	8.589	0.051
55	179	8.514	8.466	-0.048
55	180	8.701	8.689	-0.012
55	181	8.541	8.687	0.146
100	154	8.306	8.468	0.162
100	155	8.471	8.594	0.123
100	156	8.570	8.614	0.044
100	157	8.387	8.590	0.203
100	158	8.432	8.394	-0.038
100	159	8.609	8.692	0.083
100	160	8.541	8.684	0.143
100	161	8.551	8.181	-0.370
100	162	8.563	8.633	0.070
100	163	8.530	8.632	0.102
100	164	8.380	8.201	-0.179
100	165	8.279	7.833	-0.446
100	166	8.567	8.563	-0.004
100	168	8.561	8.633	0.072
100	169	8.494	8.471	-0.023
100	182	8.319	8.614	0.295
100	183	8.440	8.599	0.159
100	184	8.580	8.658	0.078
100	185	8.555	8.698	0.143
100	186	8.438	8.494	0.056
100	187	8.417	8.489	0.072
100	188	8.317	8.326	0.009
100	189	8.412	8.031	-0.381
100	190	8.223	8.380	0.157
105	191	8.394	8.512	0.118
105	192	8.528	8.667	0.139
105	193	8.431	8.639	0.208
105	194	8.610	8.651	0.041
105	195	8.518	8.642	0.124
105	196	8.661	8.531	-0.130
Max		8.832	8.800	0.295
Average		8.530	8.544	0.013
Min		8.223	7.833	-0.446
Std Dev		0.119	0.158	0.118

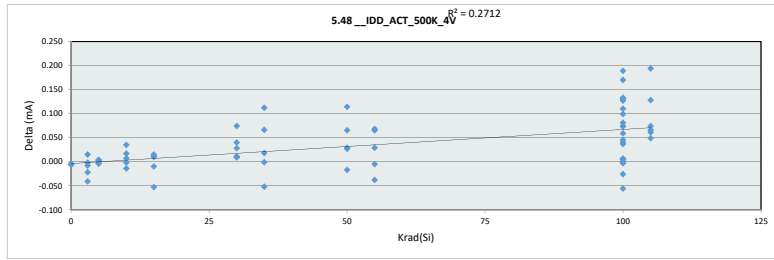


5.39 IDD_ACT											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.544	8.423	8.429	8.389	8.410	8.467	8.489	8.478	8.466	7.833	8.512
Average	8.623	8.538	8.530	8.528	8.538	8.601	8.573	8.639	8.593	8.478	8.607
Max	8.749	8.680	8.800	8.641	8.656	8.765	8.721	8.759	8.689	8.698	8.667
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

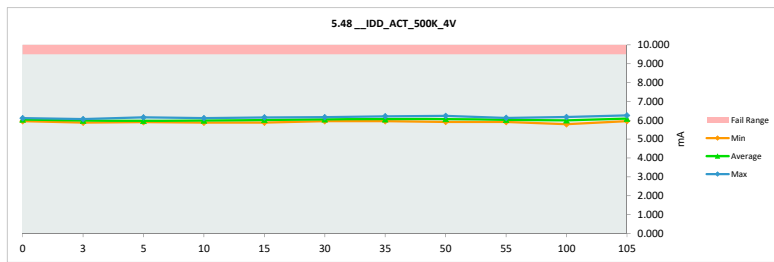


HDR TID Report  
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5.48 IDD_ACT_500K_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	6.055	6.051	-0.004
0	138	6.121	6.115	-0.006
0	139	5.958	5.952	-0.006
3	197	5.997	6.012	0.015
3	198	6.017	6.009	-0.008
3	199	5.896	5.874	-0.022
3	202	5.969	5.967	-0.002
3	203	6.104	6.063	-0.041
5	204	5.900	5.901	0.001
5	205	6.158	6.154	-0.004
5	206	5.906	5.909	0.003
5	208	5.938	5.942	0.004
5	209	5.921	5.918	-0.003
10	210	5.860	5.877	0.017
10	211	6.049	6.047	-0.002
10	212	6.015	6.022	0.007
10	213	5.902	5.888	-0.014
10	214	6.077	6.112	0.035
15	215	5.974	5.964	-0.010
15	217	6.140	6.087	-0.053
15	219	6.033	6.043	0.010
15	220	5.865	5.877	0.012
15	221	6.135	6.150	0.015
30	222	6.065	6.105	0.040
30	223	5.941	5.950	0.009
30	224	5.940	5.951	0.011
30	225	6.002	6.076	0.074
30	226	6.135	6.163	0.028
35	227	5.882	5.994	0.112
35	228	6.006	5.954	-0.052
35	229	6.145	6.211	0.066
35	230	6.046	6.045	-0.001
35	231	6.043	6.061	0.018
50	170	5.929	5.912	-0.017
50	172	6.052	6.117	0.065
50	173	6.074	6.188	0.114
50	175	6.204	6.231	0.027
50	176	5.884	5.913	0.029
55	177	5.915	5.910	-0.005
55	178	5.949	5.911	-0.038
55	179	5.994	6.062	0.068
55	180	6.098	6.127	0.029
55	181	6.038	6.103	0.065
100	154	5.789	5.863	0.074
100	155	5.962	6.089	0.127
100	156	6.049	6.047	-0.002
100	157	5.909	6.079	0.170
100	158	5.883	5.889	0.006
100	159	6.072	6.171	0.099
100	160	6.025	6.154	0.129
100	161	6.035	6.145	0.110
100	162	6.036	6.095	0.059
100	163	5.970	6.010	0.040
100	164	5.925	5.998	0.073
100	165	5.861	5.906	0.045
100	166	5.975	5.974	-0.001
100	168	6.023	5.967	-0.056
100	169	5.996	5.993	-0.003
100	182	5.818	5.792	-0.026
100	183	5.924	6.005	0.081
100	184	5.980	5.984	0.004
100	185	6.002	6.135	0.133
100	186	5.961	5.967	0.006
100	187	5.885	5.922	0.037
100	188	5.789	5.978	0.189
100	189	5.937	5.943	0.006
100	190	5.728	5.859	0.131
105	191	5.906	5.955	0.049
105	192	5.976	6.050	0.074
105	193	5.829	6.023	0.194
105	194	6.054	6.120	0.066
105	195	6.071	6.132	0.061
105	196	6.130	6.258	0.128
	Max	6.204	6.258	0.194
	Average	5.984	6.019	0.035
	Min	5.728	5.792	-0.056
	Std Dev	0.097	0.103	0.056

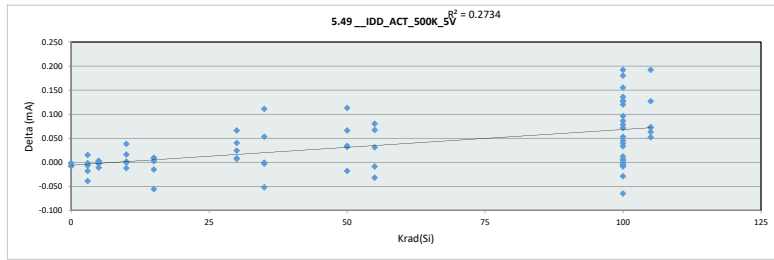


5.48 IDD_ACT_500K_4V											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	5.952	5.874	5.901	5.877	5.877	5.950	5.954	5.912	5.910	5.792	5.955
Average	6.039	5.985	5.965	5.989	6.024	6.049	6.053	6.072	6.023	5.999	6.090
Max	6.115	6.063	6.154	6.112	6.150	6.163	6.211	6.231	6.127	6.171	6.258
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

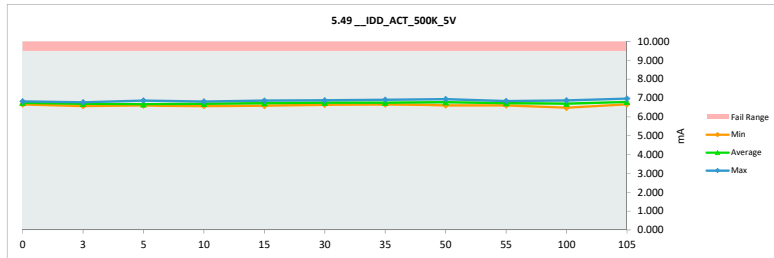


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5.49_IDD_ACT_500K_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	6.759	6.757	-0.002
0	138	6.832	6.824	-0.008
0	139	6.665	6.657	-0.008
3	197	6.703	6.718	0.015
3	198	6.723	6.716	-0.007
3	199	6.595	6.577	-0.018
3	202	6.668	6.666	-0.002
3	203	6.812	6.773	-0.039
5	204	6.607	6.604	-0.003
5	205	6.874	6.863	-0.011
5	206	6.615	6.616	0.001
5	208	6.642	6.645	0.003
5	209	6.626	6.624	-0.002
10	210	6.566	6.582	0.016
10	211	6.750	6.749	-0.001
10	212	6.731	6.732	0.001
10	213	6.601	6.589	-0.012
10	214	6.776	6.814	0.038
15	215	6.677	6.662	-0.015
15	217	6.855	6.799	-0.056
15	219	6.742	6.744	0.002
15	220	6.571	6.580	0.009
15	221	6.850	6.859	0.009
30	222	6.775	6.815	0.040
30	223	6.643	6.650	0.007
30	224	6.635	6.643	0.008
30	225	6.706	6.772	0.066
30	226	6.852	6.876	0.024
35	227	6.582	6.693	0.111
35	228	6.714	6.662	-0.052
35	229	6.853	6.906	0.053
35	230	6.751	6.748	-0.003
35	231	6.746	6.745	-0.001
50	170	6.629	6.611	-0.018
50	172	6.757	6.823	0.066
50	173	6.773	6.886	0.113
50	175	6.908	6.942	0.034
50	176	6.581	6.613	0.032
55	177	6.621	6.612	-0.009
55	178	6.642	6.610	-0.032
55	179	6.696	6.776	0.080
55	180	6.806	6.837	0.031
55	181	6.742	6.809	0.067
100	154	6.485	6.563	0.078
100	155	6.661	6.788	0.127
100	156	6.752	6.745	-0.007
100	157	6.608	6.788	0.180
100	158	6.585	6.584	-0.001
100	159	6.777	6.873	0.096
100	160	6.723	6.859	0.136
100	161	6.737	6.857	0.120
100	162	6.723	6.776	0.053
100	163	6.677	6.716	0.039
100	164	6.618	6.689	0.071
100	165	6.558	6.603	0.045
100	166	6.682	6.673	-0.009
100	168	6.723	6.658	-0.065
100	169	6.697	6.693	-0.004
100	182	6.514	6.485	-0.029
100	183	6.630	6.716	0.086
100	184	6.684	6.688	0.004
100	185	6.706	6.861	0.155
100	186	6.660	6.672	0.012
100	187	6.587	6.620	0.033
100	188	6.487	6.679	0.192
100	189	6.643	6.649	0.006
100	190	6.420	6.548	0.128
105	191	6.614	6.666	0.052
105	192	6.674	6.747	0.073
105	193	6.528	6.720	0.192
105	194	6.756	6.828	0.072
105	195	6.771	6.834	0.063
105	196	6.840	6.967	0.127
Max		6.908	6.967	0.192
Average		6.687	6.722	0.035
Min		6.420	6.485	-0.065
Std Dev		0.100	0.105	0.058

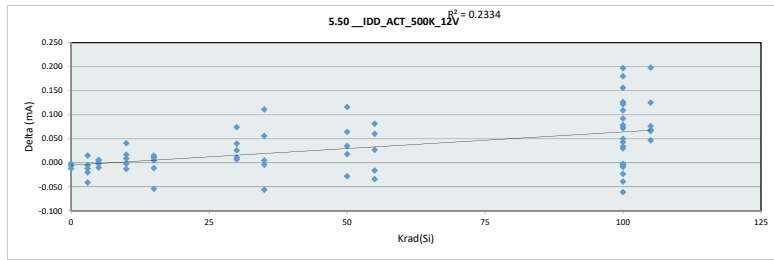


5.49_IDD_ACT_5V											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.657	6.577	6.604	6.582	6.580	6.643	6.662	6.611	6.610	6.485	6.666
Average	6.746	6.690	6.670	6.693	6.729	6.751	6.751	6.775	6.729	6.699	6.794
Max	6.824	6.773	6.863	6.814	6.859	6.876	6.906	6.942	6.837	6.873	6.967
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

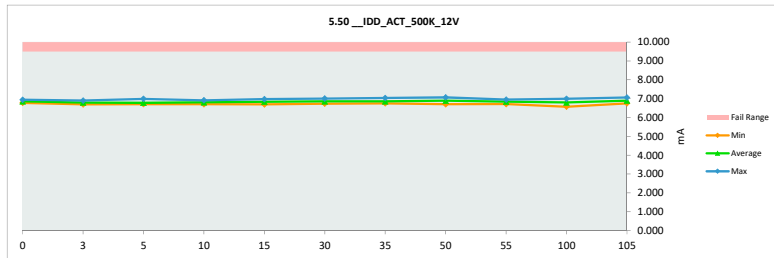


# HDR TID Report TPS7H5003-SP

5.50_IDD_ACT_500K_12V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	9.5	9.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	6.846	6.844	-0.002
0	138	6.955	6.943	-0.012
0	139	6.782	6.776	-0.006
3	197	6.795	6.810	0.015
3	198	6.824	6.812	-0.012
3	199	6.716	6.696	-0.020
3	202	6.757	6.752	-0.005
3	203	6.940	6.899	-0.041
5	204	6.710	6.708	-0.002
5	205	6.997	6.987	-0.010
5	206	6.712	6.717	0.005
5	208	6.738	6.744	0.006
5	209	6.734	6.734	0.000
10	210	6.684	6.701	0.017
10	211	6.858	6.856	-0.002
10	212	6.851	6.860	0.009
10	213	6.715	6.702	-0.013
10	214	6.870	6.911	0.041
15	215	6.755	6.744	-0.011
15	217	6.955	6.901	-0.054
15	219	6.860	6.865	0.005
15	220	6.680	6.695	0.015
15	221	6.962	6.973	0.011
30	222	6.877	6.917	0.040
30	223	6.727	6.734	0.007
30	224	6.748	6.759	0.011
30	225	6.807	6.881	0.074
30	226	6.978	7.004	0.026
35	227	6.692	6.803	0.111
35	228	6.805	6.749	-0.056
35	229	6.976	7.032	0.056
35	230	6.858	6.854	-0.004
35	231	6.852	6.857	0.005
50	170	6.733	6.705	-0.028
50	172	6.885	6.949	0.064
50	173	6.870	6.986	0.116
50	175	7.033	7.068	0.035
50	176	6.720	6.738	0.018
55	177	6.728	6.712	-0.016
55	178	6.771	6.737	-0.034
55	179	6.812	6.893	0.081
55	180	6.924	6.951	0.027
55	181	6.844	6.904	0.060
100	154	6.590	6.663	0.073
100	155	6.770	6.896	0.126
100	156	6.850	6.842	-0.008
100	157	6.696	6.876	0.180
100	158	6.688	6.683	-0.005
100	159	6.898	6.990	0.092
100	160	6.827	6.953	0.126
100	161	6.830	6.939	0.109
100	162	6.840	6.890	0.050
100	163	6.777	6.811	0.034
100	164	6.704	6.776	0.072
100	165	6.646	6.689	0.043
100	166	6.796	6.773	-0.023
100	168	6.847	6.786	-0.061
100	169	6.779	6.773	-0.006
100	182	6.611	6.572	-0.039
100	183	6.706	6.784	0.078
100	184	6.793	6.785	-0.008
100	185	6.812	6.968	0.156
100	186	6.743	6.741	-0.002
100	187	6.691	6.721	0.030
100	188	6.598	6.795	0.197
100	189	6.730	6.727	-0.003
100	190	6.517	6.639	0.122
105	191	6.704	6.751	0.047
105	192	6.784	6.860	0.076
105	193	6.653	6.851	0.198
105	194	6.862	6.930	0.068
105	195	6.857	6.923	0.066
105	196	6.933	7.058	0.125
	Max	7.033	7.068	0.198
	Average	6.793	6.826	0.033
	Min	6.517	6.572	-0.061
	Std Dev	0.104	0.109	0.059

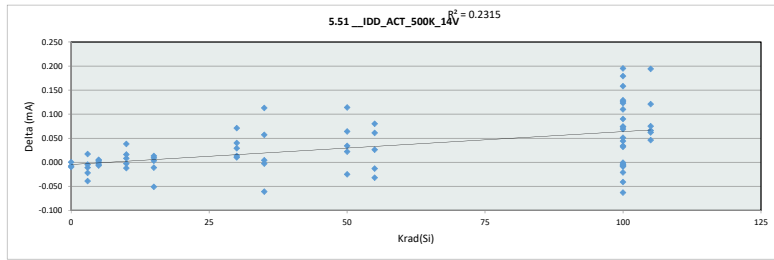


5.50_IDD_ACT_500K_12V											
Test Site											
Tester											
Test Number											
Max Limit	9.5	mA									
Min Limit		mA									
LL											
Min	6.776	6.696	6.708	6.701	6.695	6.734	6.749	6.705	6.712	6.572	6.751
Average	6.854	6.794	6.778	6.806	6.836	6.859	6.859	6.889	6.839	6.795	6.896
Max	6.943	6.899	6.987	6.911	6.973	7.004	7.032	7.068	6.951	6.990	7.058
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

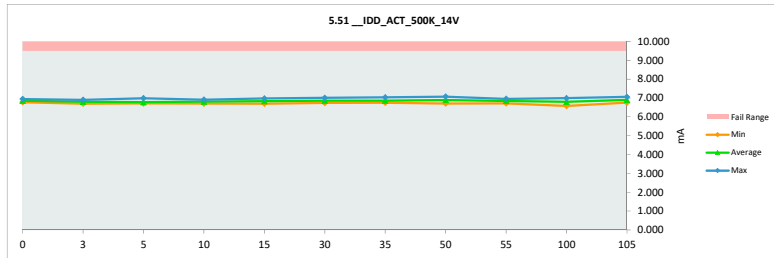


HDR TID Report  
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5.51_IDD_ACT_500K_14V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	9.5
			mA	9.5
Krad(Si)	Serial #	PRE	POST	Delta
0	137	6.850	6.850	0.000
0	138	6.958	6.948	-0.010
0	139	6.785	6.777	-0.008
3	197	6.800	6.817	0.017
3	198	6.828	6.817	-0.011
3	199	6.722	6.700	-0.022
3	202	6.759	6.754	-0.005
3	203	6.943	6.904	-0.039
5	204	6.713	6.713	0.000
5	205	6.998	6.991	-0.007
5	206	6.714	6.719	0.005
5	208	6.745	6.748	0.003
5	209	6.741	6.737	-0.004
10	210	6.689	6.705	0.016
10	211	6.863	6.860	-0.003
10	212	6.857	6.865	0.008
10	213	6.717	6.705	-0.012
10	214	6.878	6.916	0.038
15	215	6.759	6.748	-0.011
15	217	6.957	6.906	-0.051
15	219	6.866	6.869	0.003
15	220	6.685	6.698	0.013
15	221	6.968	6.977	0.009
30	222	6.881	6.921	0.040
30	223	6.729	6.739	0.010
30	224	6.752	6.765	0.013
30	225	6.812	6.883	0.071
30	226	6.982	7.011	0.029
35	227	6.694	6.807	0.113
35	228	6.812	6.751	-0.061
35	229	6.980	7.037	0.057
35	230	6.861	6.858	-0.003
35	231	6.855	6.859	0.004
50	170	6.733	6.708	-0.025
50	172	6.888	6.952	0.064
50	173	6.874	6.988	0.114
50	175	7.039	7.073	0.034
50	176	6.725	6.747	0.022
55	177	6.731	6.718	-0.013
55	178	6.776	6.744	-0.032
55	179	6.816	6.896	0.080
55	180	6.930	6.956	0.026
55	181	6.846	6.907	0.061
100	154	6.597	6.671	0.074
100	155	6.775	6.901	0.126
100	156	6.854	6.846	-0.008
100	157	6.699	6.878	0.179
100	158	6.692	6.683	-0.009
100	159	6.903	6.993	0.090
100	160	6.832	6.961	0.129
100	161	6.832	6.942	0.110
100	162	6.847	6.898	0.051
100	163	6.781	6.815	0.034
100	164	6.711	6.780	0.069
100	165	6.650	6.694	0.044
100	166	6.798	6.777	-0.021
100	168	6.853	6.790	-0.063
100	169	6.780	6.776	-0.004
100	182	6.617	6.576	-0.041
100	183	6.715	6.789	0.074
100	184	6.796	6.790	-0.006
100	185	6.818	6.976	0.158
100	186	6.750	6.749	-0.001
100	187	6.695	6.727	0.032
100	188	6.602	6.797	0.195
100	189	6.734	6.728	-0.006
100	190	6.520	6.643	0.123
105	191	6.710	6.756	0.046
105	192	6.791	6.866	0.075
105	193	6.659	6.853	0.194
105	194	6.867	6.933	0.066
105	195	6.863	6.925	0.062
105	196	6.941	7.062	0.121
Max		7.039	7.073	0.195
Average		6.797	6.830	0.033
Min		6.520	6.576	-0.063
Std Dev		0.104	0.109	0.058

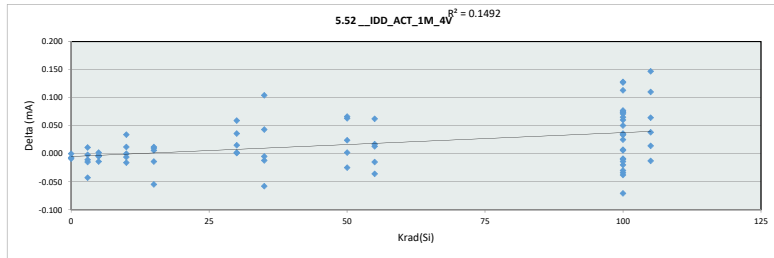


5.51_IDD_ACT_500K_14V																	
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit	LL	0	3	5	10	15	30	35	50	55	100	105
			mA	9.5													
			mA														
Min							6.777	6.700	6.713	6.705	6.698	6.739	6.751	6.708	6.718	6.576	6.756
Average							6.858	6.798	6.782	6.810	6.840	6.864	6.862	6.894	6.844	6.799	6.899
Max							6.948	6.904	6.991	6.916	6.977	7.011	7.037	7.073	6.956	6.993	7.062
UL							9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

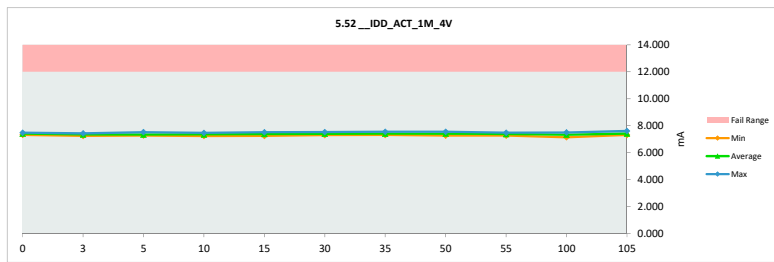


HDR TID Report  
TPS7H5003-SP

5.52_IDD_ACT_1M_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	12	12		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.414	7.414	0.000
0	138	7.484	7.475	-0.009
0	139	7.329	7.322	-0.007
3	197	7.369	7.380	0.011
3	198	7.381	7.370	-0.011
3	199	7.255	7.240	-0.015
3	202	7.320	7.318	-0.002
3	203	7.469	7.426	-0.043
5	204	7.274	7.269	-0.005
5	205	7.532	7.518	-0.014
5	206	7.274	7.271	-0.003
5	208	7.304	7.306	0.002
5	209	7.281	7.277	-0.004
10	210	7.222	7.234	0.012
10	211	7.405	7.399	-0.006
10	212	7.385	7.385	0.000
10	213	7.264	7.248	-0.016
10	214	7.430	7.464	0.034
15	215	7.329	7.315	-0.014
15	217	7.501	7.446	-0.055
15	219	7.396	7.402	0.006
15	220	7.228	7.240	0.012
15	221	7.503	7.513	0.010
30	222	7.426	7.462	0.036
30	223	7.309	7.310	0.001
30	224	7.293	7.295	0.002
30	225	7.359	7.418	0.059
30	226	7.505	7.520	0.015
35	227	7.240	7.344	0.104
35	228	7.370	7.312	-0.058
35	229	7.506	7.549	0.043
35	230	7.406	7.401	-0.005
35	231	7.402	7.390	-0.012
50	170	7.285	7.260	-0.025
50	172	7.412	7.475	0.063
50	173	7.433	7.499	0.066
50	175	7.549	7.551	0.002
50	176	7.240	7.264	0.024
55	177	7.286	7.271	-0.015
55	178	7.298	7.262	-0.036
55	179	7.338	7.351	0.013
55	180	7.462	7.479	0.017
55	181	7.395	7.457	0.062
100	154	7.139	7.216	0.077
100	155	7.312	7.377	0.065
100	156	7.408	7.374	-0.034
100	157	7.263	7.390	0.127
100	158	7.239	7.209	-0.030
100	159	7.422	7.493	0.071
100	160	7.381	7.455	0.074
100	161	7.389	7.439	0.050
100	162	7.378	7.384	0.006
100	163	7.337	7.371	0.034
100	164	7.276	7.312	0.036
100	165	7.205	7.239	0.034
100	166	7.339	7.329	-0.010
100	168	7.380	7.309	-0.071
100	169	7.355	7.335	-0.020
100	182	7.175	7.137	-0.038
100	183	7.297	7.371	0.074
100	184	7.352	7.343	-0.009
100	185	7.360	7.420	0.060
100	186	7.316	7.323	0.007
100	187	7.240	7.265	0.025
100	188	7.154	7.282	0.128
100	189	7.297	7.283	-0.014
100	190	7.088	7.201	0.113
105	191	7.269	7.307	0.038
105	192	7.329	7.393	0.064
105	193	7.195	7.342	0.147
105	194	7.418	7.405	-0.013
105	195	7.422	7.436	0.014
105	196	7.495	7.605	0.110
	Max	7.549	7.605	0.147
	Average	7.344	7.362	0.019
	Min	7.088	7.137	-0.071
	Std Dev	0.097	0.096	0.046



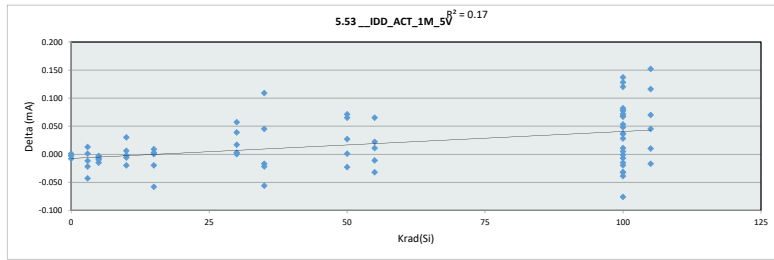
5.52_IDD_ACT											
Test Site											
Tester											
Test Number	12										
Max Limit	12	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.322	7.240	7.269	7.234	7.240	7.295	7.312	7.260	7.262	7.137	7.307
Average	7.404	7.347	7.328	7.346	7.383	7.401	7.399	7.410	7.364	7.327	7.415
Max	7.475	7.426	7.518	7.464	7.513	7.520	7.549	7.551	7.479	7.493	7.605
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000



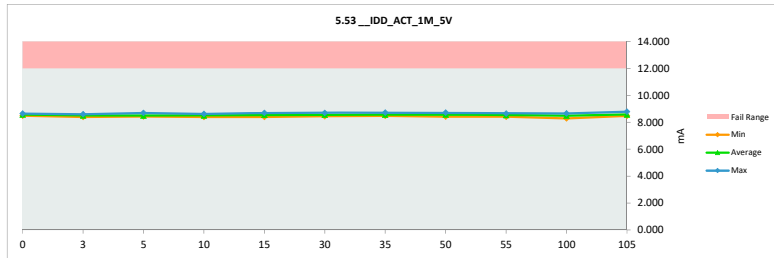


HDR TID Report  
TPS7H5003-SP

5.53_IDD_ACT_1M_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	12	12		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.579	8.580	0.001
0	138	8.652	8.644	-0.008
0	139	8.493	8.490	-0.003
3	197	8.534	8.547	0.013
3	198	8.549	8.537	-0.012
3	199	8.416	8.394	-0.022
3	202	8.470	8.471	0.001
3	203	8.637	8.594	-0.043
5	204	8.440	8.430	-0.010
5	205	8.705	8.690	-0.015
5	206	8.437	8.433	-0.004
5	208	8.469	8.466	-0.003
5	209	8.439	8.431	-0.008
10	210	8.385	8.391	0.006
10	211	8.565	8.559	-0.006
10	212	8.558	8.556	-0.002
10	213	8.422	8.402	-0.020
10	214	8.590	8.620	0.030
15	215	8.486	8.466	-0.020
15	217	8.674	8.616	-0.058
15	219	8.562	8.565	0.003
15	220	8.397	8.397	0.000
15	221	8.679	8.688	0.009
30	222	8.591	8.630	0.039
30	223	8.469	8.469	0.000
30	224	8.443	8.446	0.003
30	225	8.520	8.577	0.057
30	226	8.680	8.697	0.017
35	227	8.396	8.505	0.109
35	228	8.533	8.477	-0.056
35	229	8.669	8.714	0.045
35	230	8.572	8.555	-0.017
35	231	8.565	8.543	-0.022
50	170	8.438	8.415	-0.023
50	172	8.576	8.641	0.065
50	173	8.592	8.663	0.071
50	175	8.717	8.718	0.001
50	176	8.390	8.417	0.027
55	177	8.447	8.436	-0.011
55	178	8.445	8.413	-0.032
55	179	8.498	8.509	0.011
55	180	8.627	8.649	0.022
55	181	8.558	8.623	0.065
100	154	8.291	8.373	0.082
100	155	8.465	8.532	0.067
100	156	8.566	8.534	-0.032
100	157	8.414	8.551	0.137
100	158	8.398	8.366	-0.032
100	159	8.581	8.653	0.072
100	160	8.538	8.616	0.078
100	161	8.551	8.604	0.053
100	162	8.520	8.531	0.011
100	163	8.503	8.540	0.037
100	164	8.423	8.471	0.048
100	165	8.348	8.384	0.036
100	166	8.500	8.493	-0.007
100	168	8.540	8.464	-0.076
100	169	8.512	8.497	-0.015
100	182	8.323	8.284	-0.039
100	183	8.461	8.539	0.078
100	184	8.514	8.513	-0.001
100	185	8.521	8.589	0.068
100	186	8.472	8.477	0.005
100	187	8.392	8.420	0.028
100	188	8.306	8.434	0.128
100	189	8.460	8.440	-0.020
100	190	8.235	8.355	0.120
105	191	8.429	8.474	0.045
105	192	8.477	8.547	0.070
105	193	8.350	8.502	0.152
105	194	8.579	8.562	-0.017
105	195	8.579	8.589	0.010
105	196	8.670	8.786	0.116
Max		8.717	8.786	0.152
Average		8.504	8.523	0.019
Min		8.235	8.284	-0.076
Std Dev		0.102	0.101	0.048

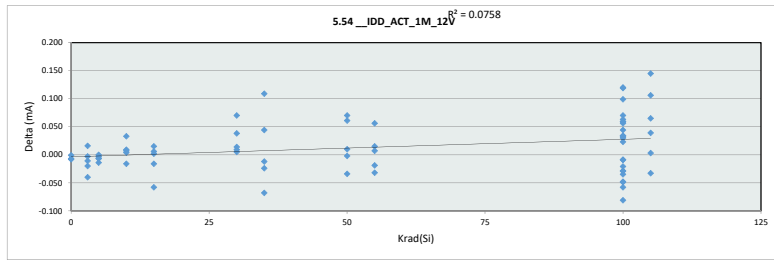


5.53_IDD_ACT											
Test Site											
Tester											
Test Number	12										
Max Limit	12	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.490	8.394	8.420	8.391	8.397	8.446	8.477	8.415	8.413	8.284	8.474
Average	8.571	8.509	8.490	8.506	8.546	8.564	8.559	8.571	8.526	8.486	8.577
Max	8.644	8.594	8.690	8.620	8.688	8.697	8.714	8.718	8.649	8.653	8.786
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

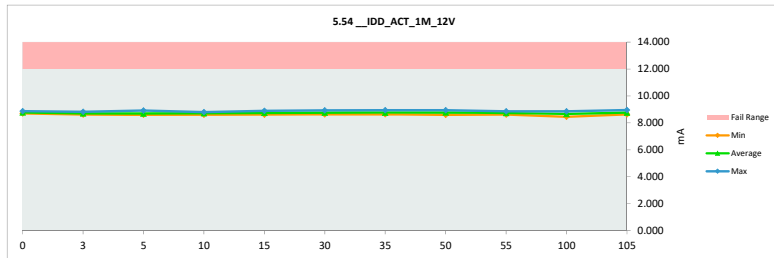


# HDR TID Report TPS7H5003-SP

5.54 IDD_ACT_1M_12V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	12			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.746	8.745	-0.001
0	138	8.877	8.870	-0.007
0	139	8.712	8.704	-0.008
3	197	8.711	8.727	0.016
3	198	8.736	8.725	-0.011
3	199	8.640	8.620	-0.020
3	202	8.641	8.638	-0.003
3	203	8.864	8.824	-0.040
5	204	8.639	8.632	-0.007
5	205	8.932	8.918	-0.014
5	206	8.619	8.616	-0.003
5	208	8.651	8.651	0.000
5	209	8.640	8.634	-0.006
10	210	8.603	8.612	0.009
10	211	8.763	8.767	0.004
10	212	8.785	8.792	0.007
10	213	8.627	8.611	-0.016
10	214	8.769	8.802	0.033
15	215	8.641	8.625	-0.016
15	217	8.862	8.804	-0.058
15	219	8.785	8.787	0.002
15	220	8.601	8.607	0.006
15	221	8.883	8.898	0.015
30	222	8.782	8.820	0.038
30	223	8.628	8.633	0.005
30	224	8.647	8.656	0.009
30	225	8.708	8.778	0.070
30	226	8.916	8.930	0.014
35	227	8.600	8.709	0.109
35	228	8.709	8.641	-0.068
35	229	8.896	8.940	0.044
35	230	8.765	8.753	-0.012
35	231	8.760	8.736	-0.024
50	170	8.629	8.595	-0.034
50	172	8.809	8.870	0.061
50	173	8.776	8.846	0.070
50	175	8.943	8.941	-0.002
50	176	8.643	8.653	0.010
55	177	8.646	8.627	-0.019
55	178	8.685	8.653	-0.032
55	179	8.703	8.710	0.007
55	180	8.849	8.864	0.015
55	181	8.746	8.802	0.056
100	154	8.491	8.561	0.070
100	155	8.668	8.712	0.044
100	156	8.750	8.702	-0.048
100	157	8.585	8.704	0.119
100	158	8.592	8.543	-0.049
100	159	8.801	8.860	0.059
100	160	8.732	8.791	0.059
100	161	8.725	8.757	0.032
100	162	8.738	8.729	-0.009
100	163	8.690	8.720	0.030
100	164	8.589	8.623	0.034
100	165	8.514	8.548	0.034
100	166	8.714	8.679	-0.035
100	168	8.770	8.689	-0.081
100	169	8.670	8.641	-0.029
100	182	8.511	8.453	-0.058
100	183	8.615	8.678	0.063
100	184	8.721	8.700	-0.021
100	185	8.723	8.779	0.056
100	186	8.633	8.624	-0.009
100	187	8.590	8.613	0.023
100	188	8.516	8.636	0.120
100	189	8.616	8.587	-0.029
100	190	8.418	8.517	0.099
105	191	8.601	8.640	0.039
105	192	8.683	8.748	0.065
105	193	8.579	8.724	0.145
105	194	8.779	8.746	-0.033
105	195	8.746	8.749	0.003
105	196	8.847	8.953	0.106
Max		8.943	8.953	0.145
Average		8.701	8.715	0.014
Min		8.418	8.453	-0.081
Std Dev		0.109	0.109	0.047

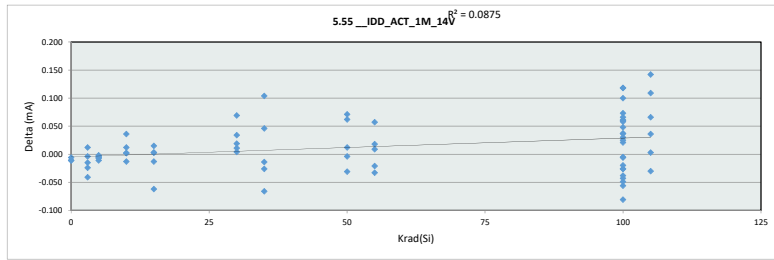


5.54 IDD_ACT											
Test Site											
Tester											
Test Number											
Max Limit	12	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.704	8.620	8.616	8.611	8.607	8.633	8.641	8.595	8.627	8.453	8.640
Average	8.773	8.707	8.690	8.717	8.744	8.763	8.756	8.781	8.731	8.660	8.760
Max	8.870	8.824	8.918	8.802	8.898	8.930	8.940	8.941	8.864	8.860	8.953
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

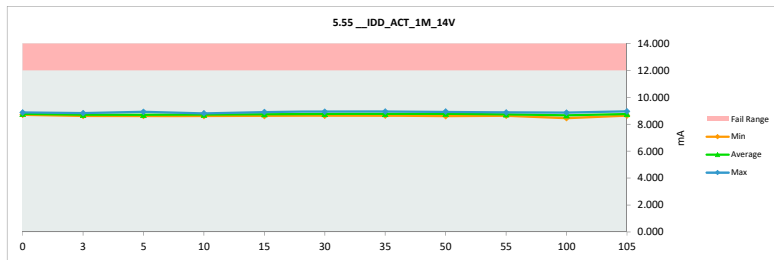


# HDR TID Report TPS7H5003-SP

5.55_IDD_ACT_1M_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	12	12		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.755	8.749	-0.006
0	138	8.883	8.872	-0.011
0	139	8.717	8.706	-0.011
3	197	8.718	8.730	0.012
3	198	8.744	8.729	-0.015
3	199	8.646	8.622	-0.024
3	202	8.645	8.641	-0.004
3	203	8.870	8.829	-0.041
5	204	8.643	8.636	-0.007
5	205	8.935	8.924	-0.011
5	206	8.626	8.622	-0.004
5	208	8.657	8.655	-0.002
5	209	8.648	8.642	-0.006
10	210	8.603	8.615	0.012
10	211	8.767	8.769	0.002
10	212	8.792	8.794	0.002
10	213	8.631	8.618	-0.013
10	214	8.774	8.810	0.036
15	215	8.642	8.629	-0.013
15	217	8.870	8.808	-0.062
15	219	8.790	8.793	0.003
15	220	8.609	8.612	0.003
15	221	8.887	8.902	0.015
30	222	8.789	8.823	0.034
30	223	8.634	8.638	0.004
30	224	8.651	8.662	0.011
30	225	8.714	8.783	0.069
30	226	8.918	8.937	0.019
35	227	8.607	8.711	0.104
35	228	8.712	8.646	-0.066
35	229	8.899	8.945	0.046
35	230	8.772	8.758	-0.014
35	231	8.766	8.740	-0.026
50	170	8.633	8.602	-0.031
50	172	8.813	8.875	0.062
50	173	8.780	8.851	0.071
50	175	8.946	8.942	-0.004
50	176	8.647	8.659	0.012
55	177	8.651	8.630	-0.021
55	178	8.687	8.654	-0.033
55	179	8.707	8.716	0.009
55	180	8.853	8.871	0.018
55	181	8.750	8.807	0.057
100	154	8.498	8.571	0.073
100	155	8.672	8.720	0.048
100	156	8.756	8.707	-0.049
100	157	8.588	8.706	0.118
100	158	8.594	8.551	-0.043
100	159	8.803	8.863	0.060
100	160	8.736	8.797	0.061
100	161	8.730	8.760	0.030
100	162	8.741	8.736	-0.005
100	163	8.697	8.722	0.025
100	164	8.594	8.631	0.037
100	165	8.520	8.557	0.037
100	166	8.720	8.682	-0.038
100	168	8.773	8.692	-0.081
100	169	8.673	8.647	-0.026
100	182	8.513	8.457	-0.056
100	183	8.619	8.685	0.066
100	184	8.724	8.704	-0.020
100	185	8.725	8.783	0.058
100	186	8.636	8.630	-0.006
100	187	8.597	8.618	0.021
100	188	8.521	8.639	0.118
100	189	8.619	8.593	-0.026
100	190	8.422	8.522	0.100
105	191	8.609	8.645	0.036
105	192	8.688	8.754	0.066
105	193	8.585	8.727	0.142
105	194	8.783	8.753	-0.030
105	195	8.749	8.752	0.003
105	196	8.850	8.959	0.109
Max		8.946	8.959	0.142
Average		8.706	8.719	0.014
Min		8.422	8.457	-0.081
Std Dev		0.109	0.108	0.047

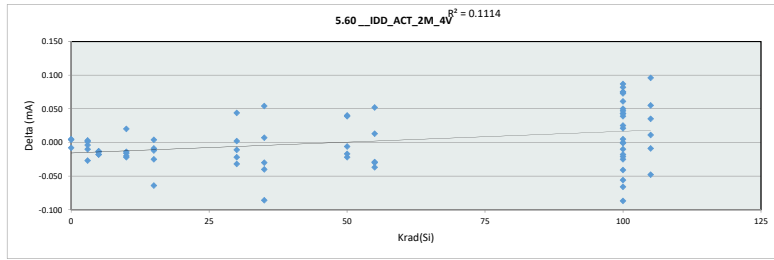


5.55_IDD_ACT											
Test Site											
Tester											
Test Number	12										
Max Limit	12	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.706	8.622	8.622	8.615	8.612	8.638	8.646	8.602	8.630	8.457	8.645
Average	8.776	8.710	8.696	8.721	8.749	8.769	8.760	8.786	8.736	8.566	8.765
Max	8.872	8.829	8.924	8.810	8.902	8.937	8.945	8.942	8.871	8.863	8.959
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

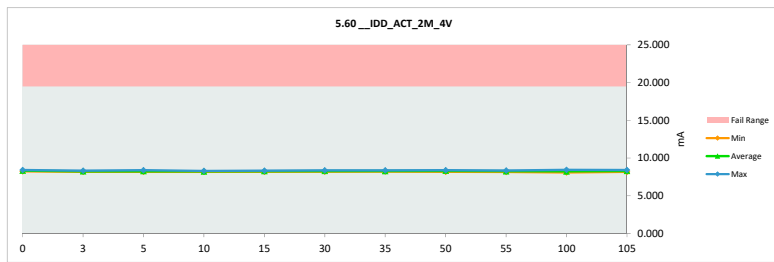


HDR TID Report  
TPS7H5003-SP

5.60_IDD_ACT_2M_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	19.5	19.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.298	8.290	-0.008
0	138	8.411	8.415	0.004
0	139	8.230	8.235	0.005
3	197	8.299	8.300	0.001
3	198	8.253	8.249	-0.004
3	199	8.176	8.166	-0.010
3	202	8.183	8.186	0.003
3	203	8.357	8.330	-0.027
5	204	8.200	8.182	-0.018
5	205	8.406	8.393	-0.013
5	206	8.194	8.176	-0.018
5	208	8.261	8.247	-0.014
5	209	8.287	8.273	-0.014
10	210	8.199	8.185	-0.014
10	211	8.254	8.232	-0.022
10	212	8.256	8.240	-0.016
10	213	8.188	8.168	-0.020
10	214	8.274	8.294	0.020
15	215	8.199	8.174	-0.025
15	217	8.381	8.317	-0.064
15	219	8.293	8.281	-0.012
15	220	8.176	8.167	-0.009
15	221	8.314	8.318	0.004
30	222	8.306	8.308	0.002
30	223	8.253	8.221	-0.032
30	224	8.217	8.195	-0.022
30	225	8.245	8.289	0.044
30	226	8.367	8.356	-0.011
35	227	8.167	8.221	0.054
35	228	8.313	8.227	-0.086
35	229	8.355	8.362	0.007
35	230	8.281	8.251	-0.030
35	231	8.276	8.236	-0.040
50	170	8.204	8.182	-0.022
50	172	8.300	8.340	0.040
50	173	8.322	8.361	0.039
50	175	8.406	8.389	-0.017
50	176	8.200	8.194	-0.006
55	177	8.192	8.155	-0.037
55	178	8.184	8.197	0.013
55	179	8.203	8.174	-0.029
55	180	8.368	8.338	-0.030
55	181	8.264	8.316	0.052
100	154	8.051	8.138	0.087
100	155	8.198	8.245	0.047
100	156	8.264	8.246	-0.018
100	157	8.186	8.268	0.082
100	158	8.183	8.127	-0.056
100	159	8.354	8.429	0.075
100	160	8.212	8.273	0.061
100	161	8.251	8.276	0.025
100	162	8.213	8.203	-0.010
100	163	8.257	8.257	0.000
100	164	8.109	8.148	0.039
100	165	8.186	8.207	0.021
100	166	8.287	8.266	-0.021
100	168	8.206	8.140	-0.066
100	169	8.261	8.260	-0.001
100	182	8.170	8.083	-0.087
100	183	8.225	8.275	0.050
100	184	8.235	8.210	-0.025
100	185	8.274	8.317	0.043
100	186	8.229	8.234	0.005
100	187	8.132	8.175	0.043
100	188	8.134	8.207	0.073
100	189	8.284	8.243	-0.041
100	190	8.024	8.099	0.075
105	191	8.166	8.177	0.011
105	192	8.284	8.319	0.035
105	193	8.139	8.235	0.096
105	194	8.295	8.247	-0.048
105	195	8.254	8.245	-0.009
105	196	8.357	8.412	0.055
Max		8.411	8.429	0.096
Average		8.246	8.248	0.002
Min		8.024	8.083	-0.087
Std Dev		0.078	0.076	0.040

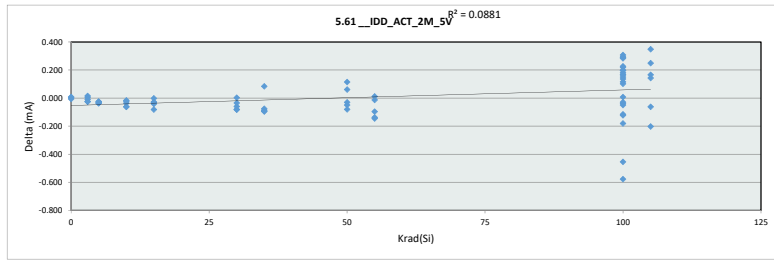


5.60_IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.235	8.166	8.176	8.168	8.167	8.195	8.221	8.182	8.155	8.083	8.177
Average	8.313	8.246	8.254	8.224	8.251	8.274	8.259	8.293	8.236	8.222	8.273
Max	8.415	8.330	8.393	8.294	8.318	8.356	8.362	8.389	8.338	8.429	8.412
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

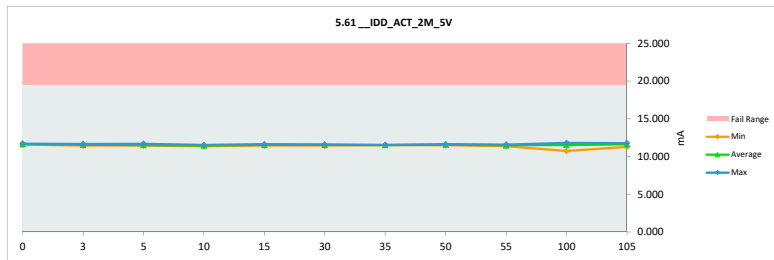


# HDR TID Report TPS7H5003-SP

5.61_IDD_ACT_2M_5V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	19.5	19.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	11.621	11.615	-0.006
0	138	11.714	11.718	0.004
0	139	11.578	11.587	0.009
3	197	11.603	11.618	0.015
3	198	11.550	11.556	0.006
3	199	11.476	11.452	-0.024
3	202	11.488	11.479	-0.009
3	203	11.700	11.674	-0.026
5	204	11.522	11.497	-0.025
5	205	11.730	11.697	-0.033
5	206	11.542	11.505	-0.037
5	208	11.534	11.507	-0.027
5	209	11.488	11.453	-0.035
10	210	11.380	11.358	-0.022
10	211	11.569	11.507	-0.062
10	212	11.583	11.521	-0.062
10	213	11.490	11.454	-0.036
10	214	11.547	11.531	-0.016
15	215	11.506	11.465	-0.041
15	217	11.729	11.647	-0.082
15	219	11.661	11.633	-0.028
15	220	11.477	11.475	-0.002
15	221	11.501	11.471	-0.030
30	222	11.662	11.581	-0.081
30	223	11.551	11.491	-0.060
30	224	11.485	11.450	-0.035
30	225	11.600	11.603	0.003
30	226	11.680	11.599	-0.081
35	227	11.458	11.542	0.084
35	228	11.596	11.510	-0.086
35	229	11.624	11.548	-0.076
35	230	11.618	11.525	-0.093
35	231	11.616	11.521	-0.095
50	170	11.454	11.515	0.061
50	172	11.626	11.576	-0.050
50	173	11.673	11.643	-0.030
50	175	11.693	11.613	-0.080
50	176	11.382	11.497	0.115
55	177	11.539	11.443	-0.096
55	178	11.489	11.501	0.012
55	179	11.504	11.367	-0.137
55	180	11.738	11.593	-0.145
55	181	11.591	11.578	-0.013
100	154	11.290	11.583	0.293
100	155	11.495	11.715	0.220
100	156	11.561	11.742	0.181
100	157	11.464	11.756	0.292
100	158	11.438	11.554	0.116
100	159	11.600	11.798	0.198
100	160	11.456	11.407	-0.049
100	161	11.561	11.445	-0.116
100	162	11.489	11.308	-0.181
100	163	11.590	11.758	0.168
100	164	11.351	11.313	-0.038
100	165	11.328	11.299	-0.029
100	166	11.552	11.688	0.136
100	168	11.416	11.294	-0.122
100	169	11.590	11.692	0.102
100	182	11.311	10.734	-0.577
100	183	11.558	11.785	0.227
100	184	11.558	11.104	-0.454
100	185	11.609	11.807	0.198
100	186	11.540	11.690	0.150
100	187	11.434	11.599	0.165
100	188	11.280	11.563	0.283
100	189	11.459	11.467	0.008
100	190	11.244	11.551	0.307
105	191	11.510	11.676	0.166
105	192	11.522	11.771	0.249
105	193	11.365	11.714	0.349
105	194	11.623	11.766	0.143
105	195	11.490	11.288	-0.202
105	196	11.647	11.585	-0.062
Max		11.738	11.807	0.349
Average		11.533	11.542	0.009
Min		11.244	10.734	-0.577
Std Dev		0.110	0.168	0.154

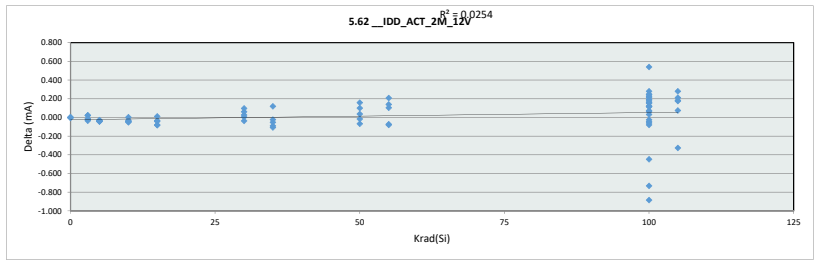


5.61_IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5	mA									
Min Limit		mA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	11.587	11.452	11.453	11.358	11.465	11.450	11.510	11.497	11.367	10.734	11.288
Average	11.640	11.556	11.532	11.474	11.538	11.545	11.529	11.569	11.496	11.527	11.633
Max	11.718	11.674	11.697	11.531	11.647	11.603	11.548	11.643	11.593	11.807	11.771
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

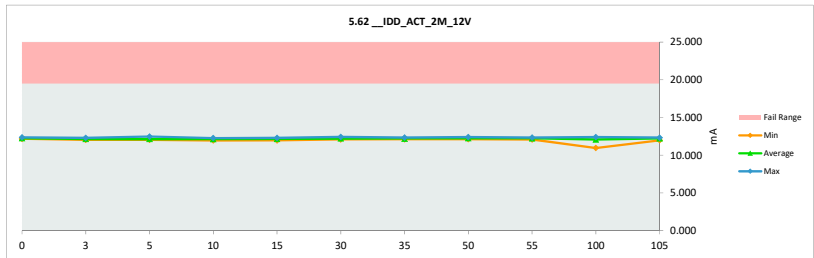


# HDR TID Report TPS7H5003-SP

		5.62_IDD_ACT_2M_12V		
Test Site	Tester			
Test Number	Unit	mA	mA	
Max Limit	Min Limit	19.5	19.5	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	12.187	12.181	-0.006
0	138	12.352	12.353	0.001
0	139	12.206	12.207	0.001
3	197	12.124	12.145	0.021
3	198	12.185	12.172	-0.013
3	199	12.119	12.094	-0.025
3	202	12.013	12.035	0.022
3	203	12.337	12.301	-0.036
5	204	12.096	12.067	-0.029
5	205	12.520	12.475	-0.045
5	206	12.081	12.038	-0.043
5	208	12.099	12.066	-0.033
5	209	12.093	12.053	-0.040
10	210	12.005	11.960	-0.045
10	211	12.228	12.197	-0.031
10	212	12.315	12.272	-0.043
10	213	12.086	12.033	-0.053
10	214	12.211	12.212	0.001
15	215	12.051	11.967	-0.084
15	217	12.350	12.270	-0.080
15	219	12.279	12.289	0.010
15	220	12.089	12.055	-0.034
15	221	12.199	12.156	-0.043
30	222	12.241	12.244	0.003
30	223	12.068	12.094	0.026
30	224	12.088	12.148	0.060
30	225	12.150	12.246	0.096
30	226	12.455	12.417	-0.038
35	227	12.061	12.179	0.118
35	228	12.139	12.115	-0.024
35	229	12.390	12.337	-0.053
35	230	12.260	12.152	-0.108
35	231	12.210	12.120	-0.090
50	170	12.027	12.126	0.099
50	172	12.308	12.291	-0.017
50	173	12.210	12.246	0.036
50	175	12.463	12.395	-0.068
50	176	12.080	12.237	0.157
55	177	12.106	12.244	0.138
55	178	12.138	12.240	0.102
55	179	12.168	12.088	-0.080
55	180	12.363	12.290	-0.073
55	181	12.139	12.346	0.207
100	154	11.871	12.119	0.248
100	155	12.060	12.245	0.185
100	156	12.179	12.251	0.072
100	157	11.984	12.263	0.279
100	158	12.019	11.959	-0.060
100	159	12.226	12.179	-0.047
100	160	12.154	12.366	0.212
100	161	12.191	12.166	-0.025
100	162	12.177	12.335	0.158
100	163	12.136	12.300	0.164
100	164	11.997	11.548	-0.449
100	165	11.837	10.952	-0.885
100	166	12.174	12.235	0.061
100	168	12.198	12.349	0.151
100	169	12.068	12.099	0.031
100	182	11.860	12.400	0.540
100	183	12.031	12.255	0.224
100	184	12.235	12.357	0.122
100	185	12.179	12.371	0.192
100	186	12.033	12.147	0.114
100	187	12.001	12.116	0.115
100	188	11.881	11.800	-0.081
100	189	11.952	11.219	-0.733
100	190	11.789	12.030	0.241
105	191	12.014	12.189	0.175
105	192	12.120	12.330	0.210
105	193	12.019	12.299	0.280
105	194	12.232	12.306	0.074
105	195	12.124	12.306	0.182
105	196	12.309	11.982	-0.327
Max		12.520	12.475	0.540
Average		12.142	12.159	0.018
Min		11.789	10.952	-0.885
Std Dev		0.144	0.236	0.197

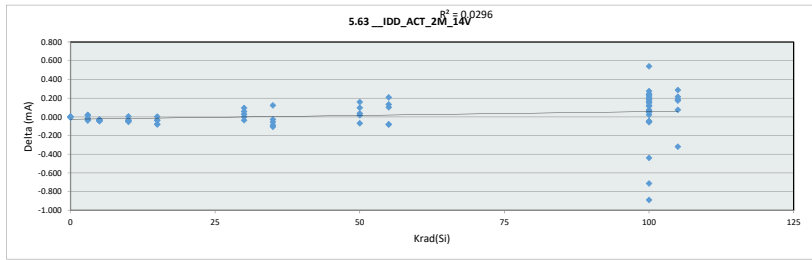


		5.62_IDD_ACT										
Test Site	Tester											
Test Number	Unit	19.5	mA									
Max Limit	Min Limit	19.5	mA									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		12.181	12.035	12.038	11.960	11.967	12.094	12.115	12.126	12.088	10.952	11.982
Average		12.247	12.149	12.140	12.135	12.147	12.230	12.181	12.259	12.242	12.086	12.235
Max		12.353	12.301	12.475	12.272	12.289	12.417	12.337	12.395	12.346	12.400	12.330
UL		19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

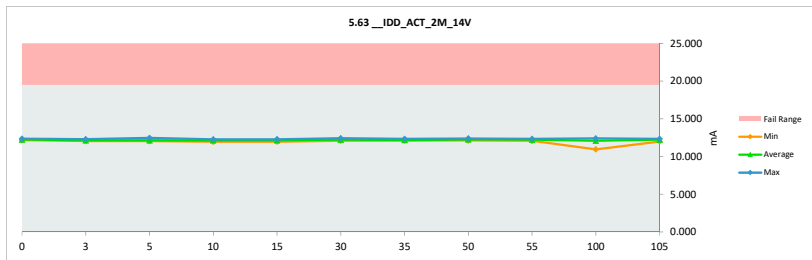


HDR TID Report  
TPS7H5003-SP

5.63_IDD_ACT_2M_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	19.5	19.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	12.191	12.182	-0.009
0	138	12.357	12.357	0.000
0	139	12.209	12.214	0.005
3	197	12.131	12.148	0.017
3	198	12.191	12.177	-0.014
3	199	12.121	12.098	-0.023
3	202	12.017	12.039	0.022
3	203	12.344	12.304	-0.040
5	204	12.102	12.073	-0.029
5	205	12.524	12.478	-0.046
5	206	12.086	12.047	-0.039
5	208	12.104	12.070	-0.034
5	209	12.101	12.056	-0.045
10	210	12.008	11.963	-0.045
10	211	12.234	12.201	-0.033
10	212	12.320	12.280	-0.040
10	213	12.092	12.038	-0.054
10	214	12.213	12.217	0.004
15	215	12.056	11.974	-0.082
15	217	12.352	12.274	-0.078
15	219	12.288	12.290	0.002
15	220	12.097	12.061	-0.036
15	221	12.198	12.162	-0.036
30	222	12.248	12.248	0.000
30	223	12.070	12.099	0.029
30	224	12.093	12.150	0.057
30	225	12.155	12.250	0.095
30	226	12.458	12.421	-0.037
35	227	12.062	12.185	0.123
35	228	12.148	12.119	-0.029
35	229	12.396	12.340	-0.056
35	230	12.265	12.158	-0.107
35	231	12.213	12.125	-0.088
50	170	12.033	12.130	0.097
50	172	12.315	12.331	0.016
50	173	12.216	12.253	0.037
50	175	12.468	12.398	-0.070
50	176	12.083	12.242	0.159
55	177	12.113	12.246	0.133
55	178	12.140	12.242	0.102
55	179	12.173	12.092	-0.081
55	180	12.372	12.290	-0.082
55	181	12.142	12.349	0.207
100	154	11.876	12.120	0.244
100	155	12.065	12.249	0.184
100	156	12.183	12.258	0.075
100	157	11.987	12.262	0.275
100	158	12.021	11.971	-0.050
100	159	12.227	12.182	-0.045
100	160	12.162	12.371	0.209
100	161	12.198	12.245	0.047
100	162	12.182	12.337	0.155
100	163	12.144	12.309	0.165
100	164	12.000	11.561	-0.439
100	165	11.842	10.954	-0.888
100	166	12.176	12.238	0.062
100	168	12.201	12.353	0.152
100	169	12.074	12.096	0.022
100	182	11.867	12.406	0.539
100	183	12.036	12.262	0.226
100	184	12.242	12.361	0.119
100	185	12.184	12.374	0.190
100	186	12.038	12.158	0.120
100	187	12.005	12.118	0.113
100	188	11.884	11.827	-0.057
100	189	11.957	11.245	-0.712
100	190	11.792	12.035	0.243
105	191	12.019	12.192	0.173
105	192	12.125	12.339	0.214
105	193	12.022	12.308	0.286
105	194	12.235	12.309	0.074
105	195	12.126	12.312	0.186
105	196	12.311	11.992	-0.319
Max		12.524	12.478	0.539
Average		12.146	12.166	0.020
Min		11.792	10.954	-0.888
Std Dev		0.144	0.234	0.196

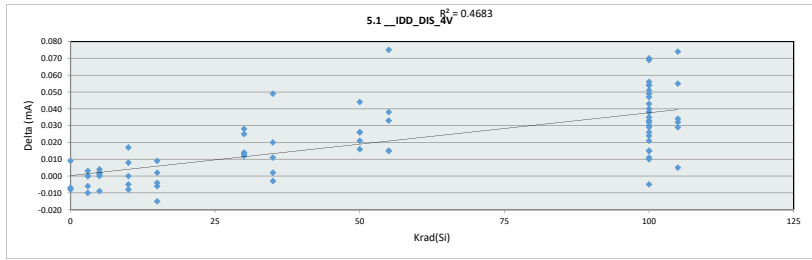


5.63_IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	12.182	12.039	12.047	11.963	11.974	12.099	12.119	12.130	12.092	10.954	11.992
Average	12.251	12.153	12.145	12.140	12.152	12.234	12.185	12.271	12.244	12.096	12.242
Max	12.357	12.304	12.478	12.280	12.290	12.421	12.340	12.398	12.349	12.406	12.339
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

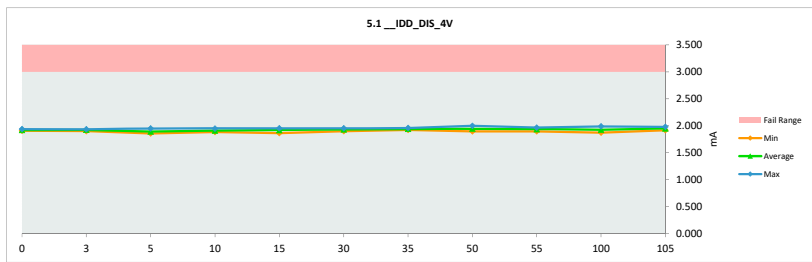


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5.1 IDD_DIS_4V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	3
Min Limit			mA	3
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1.904	1.913	0.009
0	138	1.944	1.936	-0.008
0	139	1.913	1.906	-0.007
3	197	1.929	1.932	0.003
3	198	1.933	1.933	0.000
3	199	1.905	1.899	-0.006
3	202	1.914	1.914	0.000
3	203	1.936	1.926	-0.010
5	204	1.869	1.871	0.002
5	205	1.959	1.950	-0.009
5	206	1.856	1.856	0.000
5	208	1.898	1.899	0.001
5	209	1.878	1.882	0.004
10	210	1.876	1.884	0.008
10	211	1.931	1.931	0.000
10	212	1.894	1.899	-0.005
10	213	1.911	1.903	-0.008
10	214	1.930	1.947	0.017
15	215	1.883	1.885	0.002
15	217	1.949	1.934	-0.015
15	219	1.931	1.940	0.009
15	220	1.869	1.863	-0.006
15	221	1.957	1.953	-0.004
30	222	1.914	1.942	0.028
30	223	1.905	1.918	0.013
30	224	1.886	1.898	0.012
30	225	1.927	1.952	0.025
30	226	1.938	1.952	0.014
35	227	1.872	1.921	0.049
35	228	1.946	1.943	-0.003
35	229	1.938	1.958	0.020
35	230	1.922	1.933	0.011
35	231	1.943	1.945	0.002
50	170	1.880	1.896	0.016
50	172	1.904	1.930	0.026
50	173	1.932	1.953	0.021
50	175	1.971	1.997	0.026
50	176	1.907	1.951	0.044
55	177	1.879	1.894	0.015
55	178	1.864	1.939	0.075
55	179	1.906	1.921	0.015
55	180	1.932	1.965	0.033
55	181	1.916	1.954	0.038
100	154	1.824	1.871	0.047
100	155	1.888	1.928	0.040
100	156	1.943	1.953	0.010
100	157	1.894	1.964	0.070
100	158	1.894	1.909	0.015
100	159	1.927	1.983	0.056
100	160	1.938	1.987	0.049
100	161	1.904	1.928	0.024
100	162	1.921	1.975	0.054
100	163	1.904	1.947	0.043
100	164	1.888	1.914	0.026
100	165	1.844	1.874	0.030
100	166	1.914	1.949	0.035
100	168	1.919	1.914	-0.005
100	169	1.904	1.942	0.038
100	182	1.863	1.874	0.011
100	183	1.867	1.918	0.051
100	184	1.912	1.927	0.015
100	185	1.883	1.912	0.029
100	186	1.893	1.914	0.021
100	187	1.896	1.928	0.032
100	188	1.855	1.909	0.054
100	189	1.852	1.885	0.033
100	190	1.827	1.896	0.069
105	191	1.883	1.915	0.032
105	192	1.901	1.930	0.029
105	193	1.893	1.967	0.074
105	194	1.928	1.933	0.005
105	195	1.940	1.974	0.034
105	196	1.923	1.978	0.055
Max		1.971	1.997	0.075
Average		1.905	1.926	0.021
Min		1.824	1.856	-0.015
Std Dev		0.031	0.031	0.022



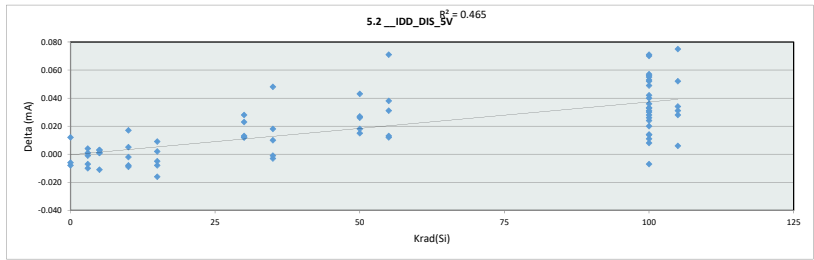
5.1 IDD DIS 4V																	
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit	LL	0	3	5	10	15	30	35	50	55	100	105
			mA	3			1.906	1.899	1.856	1.884	1.863	1.898	1.921	1.896	1.894	1.871	1.915
			mA				1.918	1.921	1.892	1.911	1.915	1.932	1.940	1.945	1.935	1.925	1.950
							1.936	1.933	1.950	1.947	1.953	1.952	1.958	1.997	1.965	1.987	1.978
							3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000



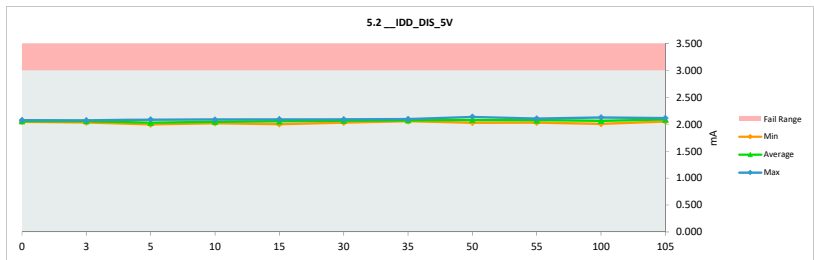


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5.2 IDD_DIS_5V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	3
Min Limit			mA	3
Krad(Si)	Serial #	PRE	POST	Delta
0	137	2.042	2.054	0.012
0	138	2.084	2.076	-0.008
0	139	2.052	2.046	-0.006
3	197	2.065	2.069	0.004
3	198	2.072	2.073	0.001
3	199	2.041	2.034	-0.007
3	202	2.050	2.049	-0.001
3	203	2.076	2.066	-0.010
5	204	2.004	2.007	0.003
5	205	2.099	2.088	-0.011
5	206	1.995	1.996	0.001
5	208	2.034	2.035	0.001
5	209	2.013	2.016	0.003
10	210	2.013	2.018	0.005
10	211	2.068	2.066	-0.002
10	212	2.034	2.026	-0.008
10	213	2.050	2.041	-0.009
10	214	2.067	2.084	0.017
15	215	2.019	2.021	0.002
15	217	2.090	2.074	-0.016
15	219	2.069	2.078	0.009
15	220	2.008	2.000	-0.008
15	221	2.098	2.093	-0.005
30	222	2.056	2.084	0.028
30	223	2.041	2.054	0.013
30	224	2.020	2.032	0.012
30	225	2.067	2.090	0.023
30	226	2.079	2.091	0.012
35	227	2.007	2.055	0.048
35	228	2.086	2.083	-0.003
35	229	2.078	2.096	0.018
35	230	2.061	2.071	0.010
35	231	2.080	2.079	-0.001
50	170	2.014	2.029	0.015
50	172	2.043	2.069	0.026
50	173	2.068	2.086	0.018
50	175	2.110	2.137	0.027
50	176	2.043	2.086	0.043
55	177	2.019	2.032	0.013
55	178	2.002	2.073	0.071
55	179	2.046	2.058	0.012
55	180	2.073	2.104	0.031
55	181	2.055	2.093	0.038
100	154	1.958	2.007	0.049
100	155	2.027	2.067	0.040
100	156	2.083	2.091	0.008
100	157	2.033	2.103	0.070
100	158	2.033	2.047	0.014
100	159	2.068	2.123	0.055
100	160	2.075	2.127	0.052
100	161	2.044	2.068	0.024
100	162	2.055	2.112	0.057
100	163	2.044	2.086	0.042
100	164	2.027	2.053	0.026
100	165	1.982	2.010	0.028
100	166	2.050	2.083	0.033
100	168	2.060	2.053	-0.007
100	169	2.043	2.079	0.036
100	182	1.997	2.008	0.011
100	183	2.004	2.057	0.053
100	184	2.051	2.065	0.014
100	185	2.023	2.053	0.030
100	186	2.033	2.053	0.020
100	187	2.035	2.066	0.031
100	188	1.989	2.045	0.056
100	189	1.991	2.024	0.033
100	190	1.960	2.031	0.071
105	191	2.022	2.053	0.031
105	192	2.041	2.069	0.028
105	193	2.028	2.103	0.075
105	194	2.066	2.072	0.006
105	195	2.081	2.115	0.034
105	196	2.063	2.115	0.052
105	197	2.110	2.137	0.027
105	198	2.043	2.064	0.021
105	199	1.958	1.996	-0.038
105	200	0.032	0.032	0.000
Max		2.110	2.137	0.027
Average		2.043	2.064	0.021
Min		1.958	1.996	-0.038
Std Dev		0.032	0.032	0.000

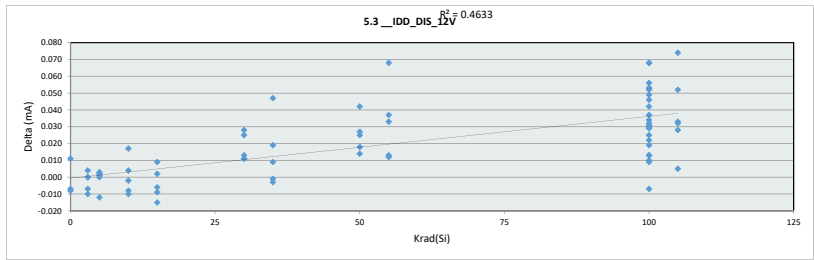


5.2 IDD DIS 5V																	
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit	LL	0	3	5	10	15	30	35	50	55	100	105
			mA	3	3		2.046	2.034	1.996	2.018	2.000	2.032	2.055	2.029	2.032	2.007	2.053
			mA				2.059	2.058	2.028	2.047	2.053	2.070	2.077	2.081	2.072	2.063	2.088
							2.076	2.073	2.088	2.084	2.093	2.091	2.096	2.137	2.104	2.127	2.115
							3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

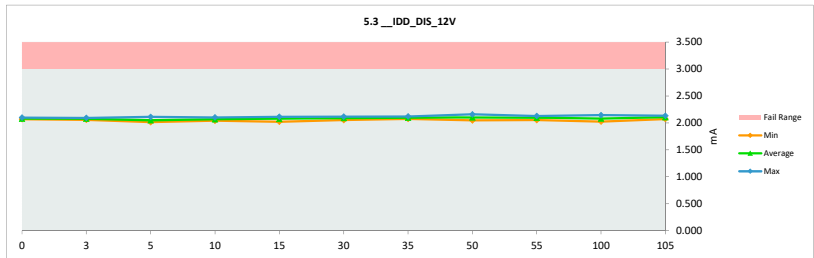


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5.3 IDD_DIS_12V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	3
Min Limit			mA	3
Krad(Si)	Serial #	PRE	POST	Delta
0	137	2.059	2.070	0.011
0	138	2.107	2.099	-0.008
0	139	2.075	2.068	-0.007
3	197	2.082	2.086	0.004
3	198	2.092	2.092	0.000
3	199	2.064	2.057	-0.007
3	202	2.066	2.066	0.000
3	203	2.100	2.090	-0.010
5	204	2.025	2.027	0.002
5	205	2.124	2.112	-0.012
5	206	2.014	2.014	0.000
5	208	2.052	2.053	0.001
5	209	2.034	2.037	0.003
10	210	2.036	2.040	0.004
10	211	2.088	2.086	-0.002
10	212	2.058	2.050	-0.008
10	213	2.072	2.062	-0.010
10	214	2.084	2.101	0.017
15	215	2.035	2.037	0.002
15	217	2.109	2.094	-0.015
15	219	2.092	2.101	0.009
15	220	2.030	2.021	-0.009
15	221	2.120	2.114	-0.006
30	222	2.075	2.103	0.028
30	223	2.057	2.070	0.013
30	224	2.042	2.053	0.011
30	225	2.086	2.111	0.025
30	226	2.105	2.116	0.011
35	227	2.028	2.075	0.047
35	228	2.103	2.100	-0.003
35	229	2.101	2.120	0.019
35	230	2.082	2.091	0.009
35	231	2.099	2.098	-0.001
50	170	2.033	2.047	0.014
50	172	2.068	2.093	0.025
50	173	2.085	2.103	0.018
50	175	2.133	2.160	0.027
50	176	2.069	2.111	0.042
55	177	2.039	2.052	0.013
55	178	2.028	2.096	0.068
55	179	2.068	2.080	0.012
55	180	2.095	2.128	0.033
55	181	2.073	2.110	0.037
100	154	1.978	2.024	0.046
100	155	2.048	2.085	0.037
100	156	2.102	2.111	0.009
100	157	2.051	2.119	0.068
100	158	2.052	2.065	0.013
100	159	2.090	2.143	0.053
100	160	2.095	2.144	0.049
100	161	2.061	2.083	0.022
100	162	2.076	2.132	0.056
100	163	2.062	2.104	0.042
100	164	2.044	2.069	0.025
100	165	1.999	2.028	0.029
100	166	2.071	2.102	0.031
100	168	2.084	2.077	-0.007
100	169	2.059	2.093	0.034
100	182	2.015	2.025	0.010
100	183	2.018	2.070	0.052
100	184	2.072	2.085	0.013
100	185	2.044	2.074	0.030
100	186	2.049	2.068	0.019
100	187	2.055	2.085	0.030
100	188	2.010	2.063	0.053
100	189	2.007	2.039	0.032
100	190	1.979	2.047	0.068
105	191	2.039	2.071	0.032
105	192	2.063	2.091	0.028
105	193	2.051	2.125	0.074
105	194	2.085	2.090	0.005
105	195	2.097	2.130	0.033
105	196	2.080	2.132	0.052
Max		2.133	2.160	0.074
Average		2.063	2.083	0.020
Min		1.978	2.014	-0.015
Std Dev		0.033	0.033	0.022

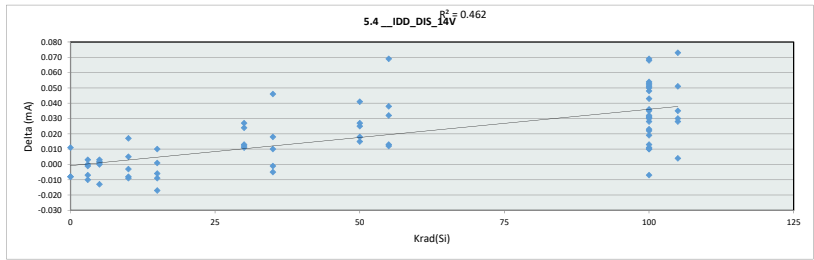


5.3 IDD DIS 12V																	
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit	LL	0	3	5	10	15	30	35	50	55	100	105
			mA	3	3												
			mA														
Min							2.068	2.057	2.014	2.040	2.021	2.053	2.075	2.047	2.052	2.024	2.071
Average							2.079	2.078	2.049	2.068	2.073	2.091	2.097	2.103	2.093	2.081	2.107
Max							2.099	2.092	2.112	2.101	2.114	2.116	2.120	2.160	2.128	2.144	2.132
UL							3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

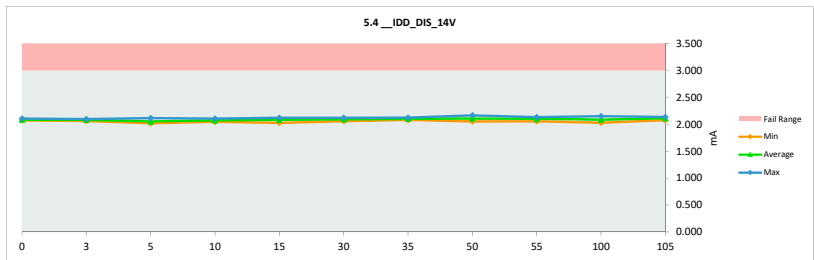


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5.4 IDD_DIS_14V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	3
Min Limit			mA	3
Krad(Si)	Serial #	PRE	POST	Delta
0	137	2.063	2.074	0.011
0	138	2.112	2.104	-0.008
0	139	2.080	2.072	-0.008
3	197	2.087	2.090	0.003
3	198	2.096	2.096	0.000
3	199	2.068	2.061	-0.007
3	202	2.071	2.070	-0.001
3	203	2.104	2.094	-0.010
5	204	2.028	2.031	0.003
5	205	2.128	2.115	-0.013
5	206	2.019	2.019	0.000
5	208	2.056	2.057	0.001
5	209	2.038	2.040	0.002
10	210	2.040	2.045	0.005
10	211	2.092	2.089	-0.003
10	212	2.052	2.054	-0.008
10	213	2.076	2.067	-0.009
10	214	2.089	2.106	0.017
15	215	2.039	2.040	0.001
15	217	2.114	2.097	-0.017
15	219	2.095	2.105	0.010
15	220	2.034	2.025	-0.009
15	221	2.125	2.119	-0.006
30	222	2.080	2.107	0.027
30	223	2.061	2.074	0.013
30	224	2.045	2.057	0.012
30	225	2.091	2.115	0.024
30	226	2.108	2.119	0.011
35	227	2.033	2.079	0.046
35	228	2.108	2.103	-0.005
35	229	2.106	2.124	0.018
35	230	2.086	2.096	0.010
35	231	2.103	2.102	-0.001
50	170	2.037	2.052	0.015
50	172	2.072	2.097	0.025
50	173	2.090	2.108	0.018
50	175	2.137	2.164	0.027
50	176	2.074	2.115	0.041
55	177	2.043	2.056	0.013
55	178	2.032	2.101	0.069
55	179	2.072	2.084	0.012
55	180	2.100	2.132	0.032
55	181	2.077	2.115	0.038
100	154	1.982	2.030	0.048
100	155	2.053	2.089	0.036
100	156	2.105	2.115	0.010
100	157	2.055	2.123	0.068
100	158	2.057	2.068	0.011
100	159	2.096	2.146	0.050
100	160	2.098	2.149	0.051
100	161	2.065	2.087	0.022
100	162	2.011	2.135	0.054
100	163	2.068	2.111	0.043
100	164	2.049	2.072	0.023
100	165	2.004	2.032	0.028
100	166	2.075	2.106	0.031
100	168	2.088	2.081	-0.007
100	169	2.063	2.098	0.035
100	182	2.019	2.029	0.010
100	183	2.022	2.074	0.052
100	184	2.076	2.089	0.013
100	185	2.047	2.077	0.030
100	186	2.052	2.071	0.019
100	187	2.059	2.090	0.031
100	188	2.014	2.067	0.053
100	189	2.011	2.043	0.032
100	190	1.982	2.051	0.069
105	191	2.044	2.074	0.030
105	192	2.066	2.094	0.028
105	193	2.056	2.129	0.073
105	194	2.090	2.094	0.004
105	195	2.101	2.136	0.035
105	196	2.085	2.136	0.051
Max		2.137	2.164	0.073
Average		2.068	2.087	0.020
Min		1.982	2.019	-0.017
Std Dev		0.033	0.033	0.022

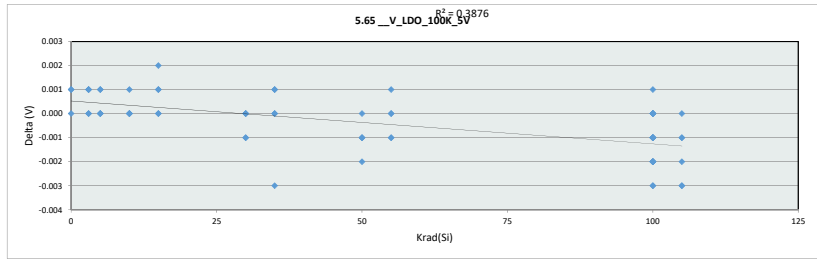


5.4 IDD DIS 14																	
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit	LL	0	3	5	10	15	30	35	50	55	100	105
			mA	3	3		2.072	2.061	2.019	2.045	2.025	2.057	2.079	2.052	2.056	2.029	2.074
			mA	3	3		2.083	2.082	2.052	2.072	2.077	2.094	2.101	2.107	2.098	2.085	2.111
			mA	3	3		2.104	2.096	2.115	2.106	2.119	2.119	2.124	2.164	2.132	2.149	2.136
			mA	3	3		3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

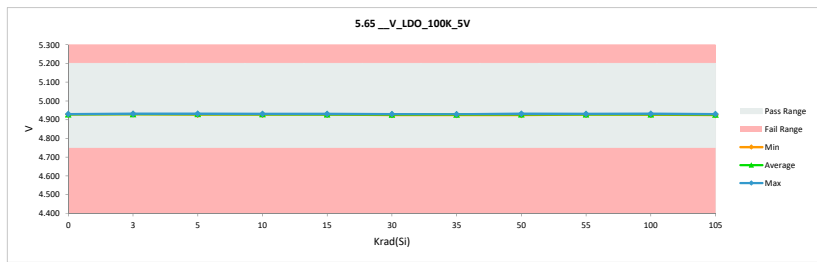


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5.65_V_LDO_100K_5V				
Test Site	Tester	Test Number	Unit	Max Limit
			V	5.2
			V	4.75
Krad(Si)	Serial #	PRE	POST	Delta
0	137	4.928	4.928	0.000
0	138	4.927	4.928	0.001
0	139	4.929	4.930	0.001
3	197	4.929	4.929	0.000
3	198	4.928	4.929	0.001
3	199	4.931	4.932	0.001
3	202	4.929	4.929	0.000
3	203	4.928	4.929	0.001
5	204	4.931	4.931	0.000
5	205	4.926	4.927	0.001
5	206	4.931	4.931	0.000
5	208	4.931	4.931	0.000
5	209	4.931	4.932	0.001
10	210	4.931	4.931	0.000
10	211	4.928	4.929	0.001
10	212	4.928	4.928	0.000
10	213	4.930	4.930	0.000
10	214	4.927	4.927	0.000
15	215	4.929	4.929	0.000
15	217	4.926	4.928	0.002
15	219	4.928	4.928	0.000
15	220	4.930	4.931	0.001
15	221	4.926	4.927	0.001
30	222	4.927	4.926	-0.001
30	223	4.930	4.930	0.000
30	224	4.929	4.929	0.000
30	225	4.928	4.927	-0.001
30	226	4.925	4.925	0.000
35	227	4.931	4.928	-0.003
35	228	4.928	4.929	0.001
35	229	4.925	4.925	0.000
35	230	4.927	4.927	0.000
35	231	4.928	4.929	0.001
50	170	4.932	4.932	0.000
50	172	4.929	4.928	-0.001
50	173	4.929	4.928	-0.001
50	175	4.926	4.925	-0.001
50	176	4.932	4.930	-0.002
55	177	4.930	4.929	-0.001
55	178	4.930	4.931	0.001
55	179	4.929	4.929	0.000
55	180	4.928	4.928	0.000
55	181	4.928	4.927	-0.001
100	154	4.932	4.932	0.000
100	155	4.929	4.928	-0.001
100	156	4.928	4.928	0.000
100	157	4.931	4.928	-0.003
100	158	4.931	4.930	-0.001
100	159	4.928	4.926	-0.002
100	160	4.928	4.926	-0.002
100	161	4.928	4.926	-0.002
100	162	4.929	4.928	-0.001
100	163	4.929	4.928	-0.001
100	164	4.930	4.929	-0.001
100	165	4.931	4.931	0.000
100	166	4.931	4.931	0.000
100	168	4.928	4.929	0.001
100	169	4.929	4.928	-0.001
100	182	4.932	4.932	0.000
100	183	4.930	4.928	-0.002
100	184	4.930	4.928	-0.002
100	185	4.929	4.928	-0.001
100	186	4.930	4.929	-0.001
100	187	4.931	4.929	-0.002
100	188	4.933	4.930	-0.003
100	189	4.929	4.929	0.000
100	190	4.933	4.931	-0.002
105	191	4.931	4.930	-0.001
105	192	4.930	4.928	-0.002
105	193	4.932	4.929	-0.003
105	194	4.928	4.928	0.000
105	195	4.928	4.927	-0.001
105	196	4.928	4.925	-0.003
	Max	4.933	4.932	0.002
	Average	4.929	4.929	0.000
	Min	4.925	4.925	-0.003
	Std Dev	0.002	0.002	0.001

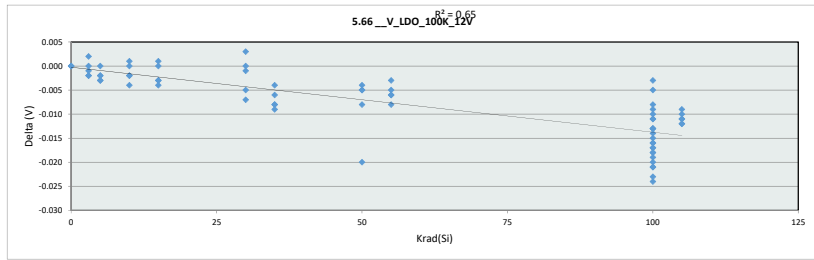


5.65_V_LDO_100K_5V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.928	4.929	4.927	4.927	4.927	4.925	4.925	4.925	4.927	4.926	4.925
Average	4.929	4.930	4.930	4.929	4.929	4.927	4.928	4.929	4.929	4.929	4.928
Max	4.930	4.932	4.932	4.931	4.931	4.930	4.929	4.932	4.931	4.932	4.930
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

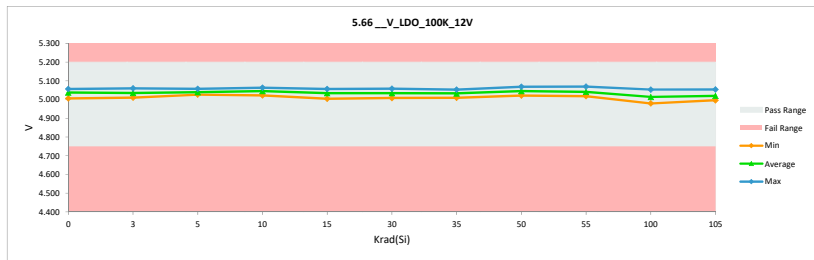


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5.66_V_LDO_100K_12V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			5.2	5.2
Min Limit			4.75	4.75
Krad(Si)	Serial #	PRE	POST	Delta
0	137	5.006	5.006	0.000
0	138	5.056	5.056	0.000
0	139	5.051	5.051	0.000
3	197	5.019	5.019	0.000
3	198	5.026	5.025	-0.001
3	199	5.062	5.060	-0.002
3	202	5.012	5.010	-0.002
3	203	5.058	5.060	0.002
5	204	5.043	5.043	0.000
5	205	5.059	5.057	-0.002
5	206	5.028	5.026	-0.002
5	208	5.030	5.027	-0.003
5	209	5.047	5.044	-0.003
10	210	5.059	5.055	-0.004
10	211	5.043	5.041	-0.002
10	212	5.062	5.063	0.001
10	213	5.047	5.045	-0.002
10	214	5.022	5.022	0.000
15	215	5.004	5.004	0.000
15	217	5.029	5.025	-0.004
15	219	5.059	5.056	-0.003
15	220	5.046	5.043	-0.003
15	221	5.041	5.042	0.001
30	222	5.030	5.023	-0.007
30	223	5.008	5.008	0.000
30	224	5.048	5.047	-0.001
30	225	5.031	5.034	0.003
30	226	5.063	5.058	-0.005
35	227	5.048	5.040	-0.008
35	228	5.018	5.009	-0.009
35	229	5.059	5.053	-0.006
35	230	5.035	5.031	-0.004
35	231	5.039	5.031	-0.008
50	170	5.034	5.026	-0.008
50	172	5.067	5.062	-0.005
50	173	5.025	5.021	-0.004
50	175	5.054	5.049	-0.005
50	176	5.088	5.068	-0.020
55	177	5.036	5.028	-0.008
55	178	5.074	5.069	-0.005
55	179	5.044	5.041	-0.003
55	180	5.053	5.047	-0.006
55	181	5.024	5.018	-0.006
100	154	5.041	5.031	-0.010
100	155	5.039	5.020	-0.019
100	156	5.022	5.011	-0.011
100	157	5.015	4.997	-0.018
100	158	5.032	5.016	-0.016
100	159	5.053	5.036	-0.017
100	160	5.032	5.008	-0.024
100	161	5.011	4.988	-0.023
100	162	5.057	5.037	-0.020
100	163	5.027	5.013	-0.014
100	164	5.012	4.997	-0.015
100	165	5.013	5.010	-0.003
100	166	5.050	5.029	-0.021
100	168	5.061	5.053	-0.008
100	169	4.999	4.988	-0.011
100	182	5.032	5.016	-0.016
100	183	4.997	4.979	-0.018
100	184	5.038	5.025	-0.013
100	185	5.037	5.032	-0.005
100	186	5.005	4.988	-0.017
100	187	5.037	5.028	-0.009
100	188	5.048	5.035	-0.013
100	189	5.002	4.989	-0.013
100	190	5.027	5.006	-0.021
105	191	5.015	5.006	-0.009
105	192	5.046	5.035	-0.011
105	193	5.066	5.054	-0.012
105	194	5.034	5.022	-0.012
105	195	5.007	4.996	-0.011
105	196	5.015	5.005	-0.010
Max		5.088	5.069	0.003
Average		5.036	5.029	-0.008
Min		4.997	4.979	-0.024
Std Dev		0.020	0.022	0.007

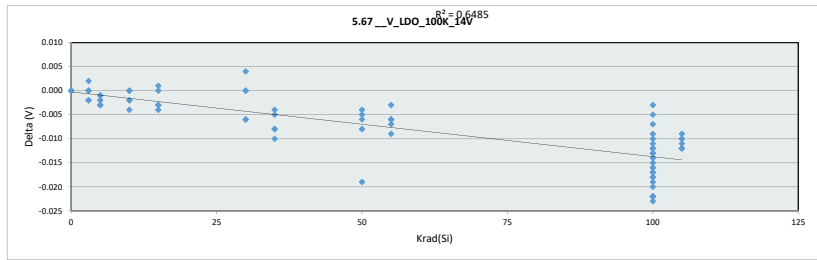


5.66_V_LDO_100K_12V												
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit							
			V	5.2	4.75							V
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	
Min	5.006	5.010	5.026	5.022	5.004	5.008	5.009	5.021	5.018	4.979	4.996	
Average	5.038	5.035	5.039	5.045	5.034	5.034	5.033	5.045	5.041	5.014	5.020	
Max	5.056	5.060	5.057	5.063	5.056	5.058	5.053	5.068	5.069	5.053	5.054	
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	

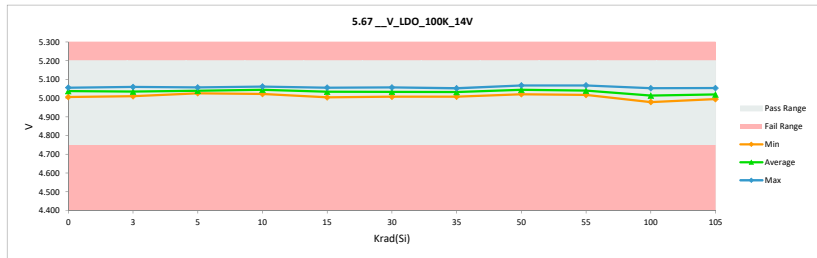


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5.67_V_LDO_100K_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	5.006	5.006	0.000
0	138	5.056	5.056	0.000
0	139	5.051	5.051	0.000
3	197	5.019	5.019	0.000
3	198	5.025	5.025	0.000
3	199	5.062	5.060	-0.002
3	202	5.012	5.010	-0.002
3	203	5.058	5.060	0.002
5	204	5.043	5.042	-0.001
5	205	5.059	5.057	-0.002
5	206	5.027	5.026	-0.001
5	208	5.030	5.027	-0.003
5	209	5.047	5.044	-0.003
10	210	5.059	5.055	-0.004
10	211	5.043	5.041	-0.002
10	212	5.062	5.062	0.000
10	213	5.047	5.045	-0.002
10	214	5.022	5.022	0.000
15	215	5.004	5.004	0.000
15	217	5.029	5.025	-0.004
15	219	5.059	5.056	-0.003
15	220	5.046	5.043	-0.003
15	221	5.041	5.042	0.001
30	222	5.029	5.023	-0.006
30	223	5.008	5.008	0.000
30	224	5.047	5.047	0.000
30	225	5.030	5.034	0.004
30	226	5.063	5.057	-0.006
35	227	5.048	5.040	-0.008
35	228	5.018	5.008	-0.010
35	229	5.058	5.053	-0.005
35	230	5.035	5.031	-0.004
35	231	5.039	5.031	-0.008
50	170	5.034	5.026	-0.008
50	172	5.067	5.062	-0.005
50	173	5.025	5.021	-0.004
50	175	5.054	5.048	-0.006
50	176	5.087	5.068	-0.019
55	177	5.036	5.027	-0.009
55	178	5.074	5.068	-0.006
55	179	5.044	5.041	-0.003
55	180	5.053	5.047	-0.006
55	181	5.024	5.017	-0.007
100	154	5.041	5.031	-0.010
100	155	5.039	5.020	-0.019
100	156	5.022	5.010	-0.012
100	157	5.015	4.997	-0.018
100	158	5.032	5.016	-0.016
100	159	5.053	5.036	-0.017
100	160	5.031	5.008	-0.023
100	161	5.011	4.989	-0.022
100	162	5.057	5.037	-0.020
100	163	5.027	5.013	-0.014
100	164	5.012	4.997	-0.015
100	165	5.013	5.010	-0.003
100	166	5.050	5.028	-0.022
100	168	5.060	5.053	-0.007
100	169	4.999	4.988	-0.011
100	182	5.032	5.016	-0.016
100	183	4.997	4.979	-0.018
100	184	5.038	5.025	-0.013
100	185	5.037	5.032	-0.005
100	186	5.004	4.987	-0.017
100	187	5.037	5.028	-0.009
100	188	5.047	5.035	-0.012
100	189	5.002	4.989	-0.013
100	190	5.027	5.005	-0.022
105	191	5.015	5.006	-0.009
105	192	5.045	5.035	-0.010
105	193	5.066	5.054	-0.012
105	194	5.034	5.022	-0.012
105	195	5.006	4.995	-0.011
105	196	5.015	5.005	-0.010
Max		5.087	5.068	0.004
Average		5.036	5.029	-0.008
Min		4.997	4.979	-0.023
Std Dev		0.020	0.022	0.007

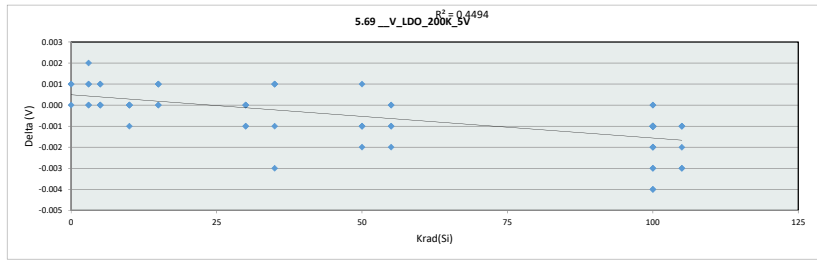


5.67_V_LDO_100K_14V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.006	5.010	5.026	5.022	5.004	5.008	5.008	5.021	5.017	4.979	4.995
Average	5.038	5.035	5.039	5.045	5.034	5.034	5.033	5.045	5.040	5.014	5.020
Max	5.056	5.060	5.057	5.062	5.056	5.057	5.053	5.068	5.068	5.053	5.054
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

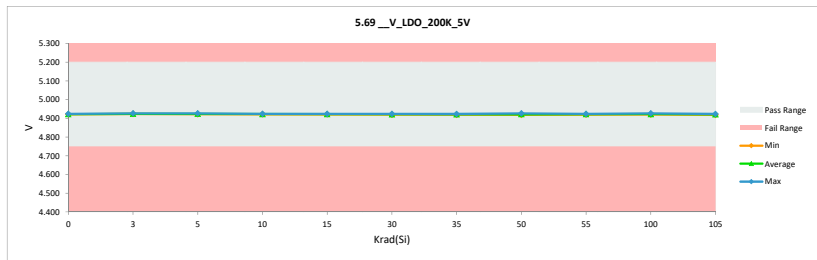


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5.69_V_LDO_200K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	4.921	4.921	0.000
0	138	4.921	4.922	0.001
0	139	4.923	4.924	0.001
3	197	4.923	4.923	0.000
3	198	4.922	4.923	0.001
3	199	4.925	4.926	0.001
3	202	4.923	4.923	0.000
3	203	4.921	4.923	0.002
5	204	4.925	4.925	0.000
5	205	4.920	4.921	0.001
5	206	4.924	4.924	0.000
5	208	4.924	4.924	0.000
5	209	4.925	4.926	0.001
10	210	4.925	4.924	-0.001
10	211	4.922	4.922	0.000
10	212	4.921	4.921	0.000
10	213	4.924	4.924	0.000
10	214	4.921	4.921	0.000
15	215	4.922	4.923	0.001
15	217	4.920	4.921	0.001
15	219	4.922	4.922	0.000
15	220	4.924	4.924	0.000
15	221	4.920	4.921	0.001
30	222	4.921	4.920	-0.001
30	223	4.924	4.924	0.000
30	224	4.923	4.923	0.000
30	225	4.922	4.921	-0.001
30	226	4.919	4.919	0.000
35	227	4.925	4.922	-0.003
35	228	4.922	4.923	0.001
35	229	4.919	4.918	-0.001
35	230	4.920	4.921	0.001
35	231	4.922	4.923	0.001
50	170	4.925	4.926	0.001
50	172	4.923	4.922	-0.001
50	173	4.923	4.921	-0.002
50	175	4.919	4.918	-0.001
50	176	4.925	4.924	-0.001
55	177	4.924	4.923	-0.001
55	178	4.924	4.924	0.000
55	179	4.923	4.923	0.000
55	180	4.922	4.921	-0.001
55	181	4.922	4.920	-0.002
100	154	4.926	4.925	-0.001
100	155	4.923	4.922	-0.001
100	156	4.922	4.922	0.000
100	157	4.925	4.922	-0.003
100	158	4.924	4.923	-0.001
100	159	4.922	4.920	-0.002
100	160	4.922	4.920	-0.002
100	161	4.922	4.920	-0.002
100	162	4.922	4.921	-0.001
100	163	4.923	4.922	-0.001
100	164	4.924	4.923	-0.001
100	165	4.925	4.924	-0.001
100	166	4.925	4.924	-0.001
100	168	4.922	4.922	0.000
100	169	4.923	4.922	-0.001
100	182	4.927	4.926	-0.001
100	183	4.924	4.921	-0.003
100	184	4.923	4.922	-0.001
100	185	4.923	4.922	-0.001
100	186	4.924	4.923	-0.001
100	187	4.924	4.923	-0.001
100	188	4.927	4.923	-0.004
100	189	4.923	4.922	-0.001
100	190	4.928	4.924	-0.004
105	191	4.925	4.923	-0.002
105	192	4.923	4.922	-0.001
105	193	4.925	4.922	-0.003
105	194	4.922	4.921	-0.001
105	195	4.921	4.920	-0.001
105	196	4.921	4.918	-0.003
Max		4.928	4.926	0.002
Average		4.923	4.922	-0.001
Min		4.919	4.918	-0.004
Std Dev		0.002	0.002	0.001

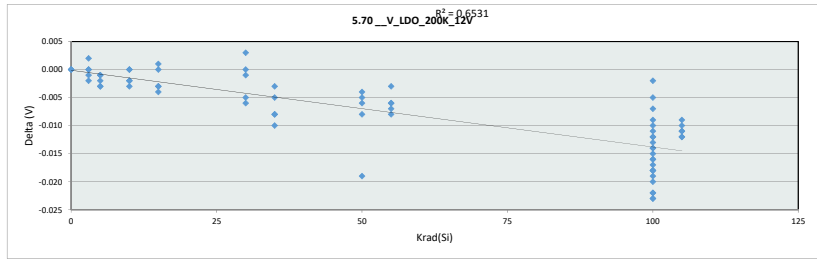


5.69_V_LDO_200K_5V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.921	4.923	4.921	4.921	4.921	4.919	4.918	4.918	4.918	4.920	4.918
Average	4.922	4.924	4.924	4.922	4.922	4.921	4.921	4.922	4.922	4.922	4.921
Max	4.924	4.926	4.926	4.924	4.924	4.924	4.924	4.923	4.926	4.924	4.923
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

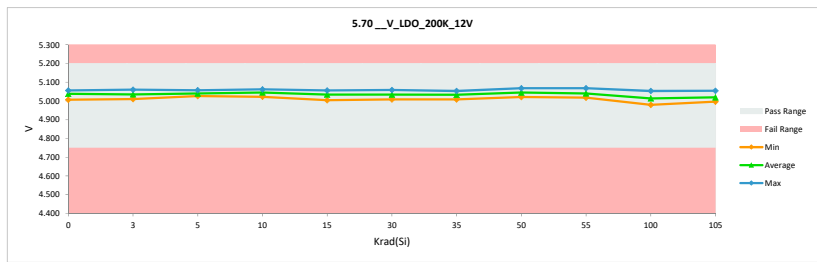


HDR TID Report  
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5.70_V_LDO_200K_12V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			V	5.2	4.75
			V	5.2	4.75
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	5.006	5.006	0.000	
0	138	5.056	5.056	0.000	
0	139	5.051	5.051	0.000	
3	197	5.019	5.019	0.000	
3	198	5.025	5.025	0.000	
3	199	5.061	5.060	-0.001	
3	202	5.012	5.010	-0.002	
3	203	5.058	5.060	0.002	
5	204	5.043	5.042	-0.001	
5	205	5.059	5.057	-0.002	
5	206	5.027	5.026	-0.001	
5	208	5.030	5.027	-0.003	
5	209	5.047	5.044	-0.003	
10	210	5.059	5.056	-0.003	
10	211	5.043	5.041	-0.002	
10	212	5.062	5.062	0.000	
10	213	5.047	5.045	-0.002	
10	214	5.022	5.022	0.000	
15	215	5.004	5.004	0.000	
15	217	5.029	5.025	-0.004	
15	219	5.059	5.056	-0.003	
15	220	5.046	5.043	-0.003	
15	221	5.041	5.042	0.001	
30	222	5.029	5.023	-0.006	
30	223	5.008	5.008	0.000	
30	224	5.047	5.046	-0.001	
30	225	5.031	5.034	0.003	
30	226	5.063	5.058	-0.005	
35	227	5.048	5.040	-0.008	
35	228	5.018	5.008	-0.010	
35	229	5.058	5.053	-0.005	
35	230	5.034	5.031	-0.003	
35	231	5.039	5.031	-0.008	
50	170	5.034	5.026	-0.008	
50	172	5.067	5.062	-0.005	
50	173	5.025	5.021	-0.004	
50	175	5.054	5.048	-0.006	
50	176	5.087	5.068	-0.019	
55	177	5.035	5.027	-0.008	
55	178	5.074	5.068	-0.006	
55	179	5.044	5.041	-0.003	
55	180	5.053	5.047	-0.006	
55	181	5.024	5.017	-0.007	
100	154	5.041	5.031	-0.010	
100	155	5.039	5.020	-0.019	
100	156	5.022	5.010	-0.012	
100	157	5.015	4.997	-0.018	
100	158	5.032	5.016	-0.016	
100	159	5.053	5.036	-0.017	
100	160	5.031	5.008	-0.023	
100	161	5.011	4.988	-0.023	
100	162	5.057	5.037	-0.020	
100	163	5.027	5.013	-0.014	
100	164	5.012	4.997	-0.015	
100	165	5.012	5.010	-0.002	
100	166	5.050	5.028	-0.022	
100	168	5.060	5.053	-0.007	
100	169	4.999	4.988	-0.011	
100	182	5.032	5.016	-0.016	
100	183	4.997	4.979	-0.018	
100	184	5.038	5.024	-0.014	
100	185	5.037	5.032	-0.005	
100	186	5.005	4.987	-0.018	
100	187	5.037	5.028	-0.009	
100	188	5.047	5.035	-0.012	
100	189	5.002	4.989	-0.013	
100	190	5.027	5.005	-0.022	
105	191	5.015	5.006	-0.009	
105	192	5.045	5.034	-0.011	
105	193	5.066	5.054	-0.012	
105	194	5.034	5.022	-0.012	
105	195	5.006	4.996	-0.010	
105	196	5.015	5.004	-0.011	
	Max	5.087	5.068	0.003	
	Average	5.036	5.028	-0.008	
	Min	4.997	4.979	-0.023	
	Std Dev	0.020	0.022	0.007	



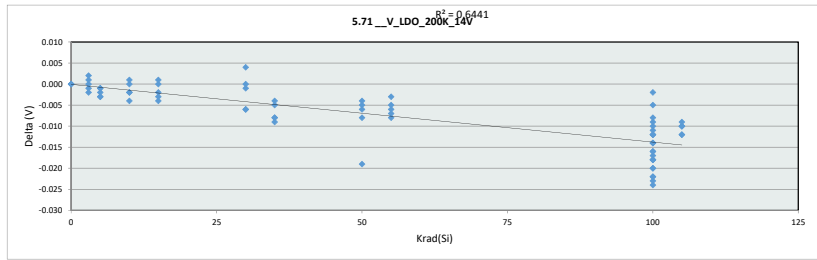
5.70_V_LDO_200K_12V																	
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit												
			V	5.2	4.75												
			V	5.2	4.75												
Krad(Si)	LL	Min	Average	Max	UL	0	3	5	10	15	30	35	50	55	100	105	
	4.750	5.006	5.038	5.056	5.200	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
	5.006	5.010	5.035	5.056	5.200	5.006	5.010	5.026	5.022	5.004	5.008	5.008	5.021	5.017	4.979	4.996	4.996
	5.038	5.035	5.039	5.057	5.200	5.038	5.035	5.039	5.045	5.034	5.034	5.033	5.045	5.040	5.014	5.019	5.019
	5.056	5.060	5.057	5.062	5.200	5.056	5.060	5.057	5.062	5.056	5.058	5.053	5.068	5.068	5.053	5.054	5.054
	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



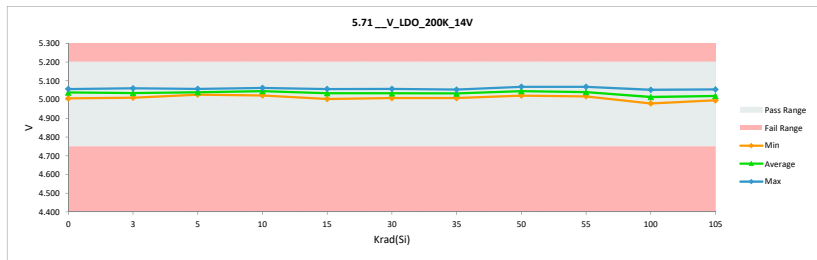


# HDR TID Report TPS7H5003-SP

5.71_V_LDO_200K_14V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			V	5.2	4.75
			V	5.2	4.75
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	5.006	5.006	0.000	
0	138	5.056	5.056	0.000	
0	139	5.051	5.051	0.000	
3	197	5.018	5.019	0.001	
3	198	5.025	5.025	0.000	
3	199	5.061	5.060	-0.001	
3	202	5.012	5.010	-0.002	
3	203	5.058	5.060	0.002	
5	204	5.043	5.042	-0.001	
5	205	5.059	5.057	-0.002	
5	206	5.027	5.026	-0.001	
5	208	5.030	5.027	-0.003	
5	209	5.047	5.044	-0.003	
10	210	5.059	5.055	-0.004	
10	211	5.043	5.041	-0.002	
10	212	5.062	5.062	0.000	
10	213	5.047	5.045	-0.002	
10	214	5.021	5.022	0.001	
15	215	5.003	5.003	0.000	
15	217	5.029	5.025	-0.004	
15	219	5.058	5.056	-0.002	
15	220	5.046	5.043	-0.003	
15	221	5.041	5.042	0.001	
30	222	5.029	5.023	-0.006	
30	223	5.008	5.008	0.000	
30	224	5.047	5.046	-0.001	
30	225	5.030	5.034	0.004	
30	226	5.063	5.057	-0.006	
35	227	5.048	5.040	-0.008	
35	228	5.017	5.008	-0.009	
35	229	5.058	5.053	-0.005	
35	230	5.035	5.031	-0.004	
35	231	5.039	5.031	-0.008	
50	170	5.034	5.026	-0.008	
50	172	5.067	5.062	-0.005	
50	173	5.025	5.021	-0.004	
50	175	5.054	5.048	-0.006	
50	176	5.087	5.068	-0.019	
55	177	5.035	5.027	-0.008	
55	178	5.073	5.068	-0.005	
55	179	5.044	5.041	-0.003	
55	180	5.053	5.047	-0.006	
55	181	5.024	5.017	-0.007	
100	154	5.041	5.031	-0.010	
100	155	5.039	5.019	-0.020	
100	156	5.022	5.010	-0.012	
100	157	5.015	4.997	-0.018	
100	158	5.032	5.016	-0.016	
100	159	5.053	5.036	-0.017	
100	160	5.031	5.007	-0.024	
100	161	5.011	4.988	-0.023	
100	162	5.057	5.037	-0.020	
100	163	5.027	5.013	-0.014	
100	164	5.011	4.997	-0.014	
100	165	5.012	5.010	-0.002	
100	166	5.050	5.028	-0.022	
100	168	5.060	5.052	-0.008	
100	169	4.999	4.988	-0.011	
100	182	5.032	5.016	-0.016	
100	183	4.997	4.979	-0.018	
100	184	5.038	5.024	-0.014	
100	185	5.037	5.032	-0.005	
100	186	5.005	4.987	-0.018	
100	187	5.037	5.028	-0.009	
100	188	5.047	5.035	-0.012	
100	189	5.001	4.989	-0.012	
100	190	5.027	5.005	-0.022	
105	191	5.015	5.006	-0.009	
105	192	5.045	5.035	-0.010	
105	193	5.066	5.054	-0.012	
105	194	5.034	5.022	-0.012	
105	195	5.006	4.996	-0.010	
105	196	5.014	5.004	-0.010	
	Max	5.087	5.068	0.004	
	Average	5.036	5.028	-0.008	
	Min	4.997	4.979	-0.024	
	Std Dev	0.020	0.022	0.007	

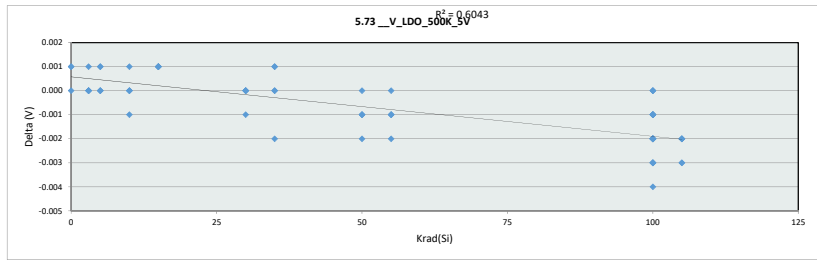


5.71_V_LDO_200K_14V																	
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit												
			V	5.2	4.75												
			V	5.2	4.75												
Krad(Si)	LL	Min	Average	Max	UL	0	3	5	10	15	30	35	50	55	100	105	
	4.750	5.006	5.038	5.056	5.200	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
	5.006	5.010	5.035	5.060	5.200	5.006	5.010	5.026	5.022	5.003	5.008	5.008	5.021	5.017	4.979	4.996	4.996
	5.038	5.035	5.039	5.057	5.200	5.038	5.035	5.039	5.045	5.034	5.034	5.033	5.045	5.040	5.014	5.020	5.020
	5.056	5.060	5.057	5.062	5.200	5.056	5.060	5.057	5.062	5.056	5.057	5.053	5.068	5.068	5.052	5.054	5.054
	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

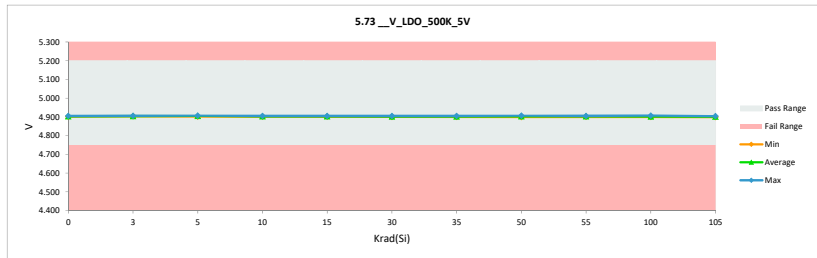


HDR TID Report  
TPS7H5003-SP

5.73_V_LDO_500K_5V				
Test Site	Tester	Test Number	Unit	Max Limit
			V	5.2
			V	4.75
Krad(Si)	Serial #	PRE	POST	Delta
0	137	4.902	4.902	0.000
0	138	4.902	4.903	0.001
0	139	4.904	4.905	0.001
3	197	4.904	4.904	0.000
3	198	4.903	4.903	0.000
3	199	4.906	4.906	0.000
3	202	4.904	4.904	0.000
3	203	4.902	4.903	0.001
5	204	4.906	4.906	0.000
5	205	4.901	4.902	0.001
5	206	4.905	4.905	0.000
5	208	4.906	4.906	0.000
5	209	4.906	4.907	0.001
10	210	4.906	4.905	-0.001
10	211	4.904	4.904	0.000
10	212	4.902	4.902	0.000
10	213	4.904	4.905	0.001
10	214	4.902	4.902	0.000
15	215	4.903	4.904	0.001
15	217	4.902	4.903	0.001
15	219	4.902	4.903	0.001
15	220	4.904	4.905	0.001
15	221	4.901	4.902	0.001
30	222	4.901	4.901	0.000
30	223	4.905	4.905	0.000
30	224	4.904	4.904	0.000
30	225	4.903	4.902	-0.001
30	226	4.900	4.900	0.000
35	227	4.905	4.903	-0.002
35	228	4.902	4.903	0.001
35	229	4.900	4.900	0.000
35	230	4.902	4.902	0.000
35	231	4.904	4.905	0.001
50	170	4.907	4.907	0.000
50	172	4.904	4.903	-0.001
50	173	4.904	4.902	-0.002
50	175	4.900	4.899	-0.001
50	176	4.906	4.905	-0.001
55	177	4.905	4.904	-0.001
55	178	4.905	4.905	0.000
55	179	4.905	4.903	-0.002
55	180	4.903	4.902	-0.001
55	181	4.902	4.901	-0.001
100	154	4.907	4.907	0.000
100	155	4.904	4.902	-0.002
100	156	4.902	4.902	0.000
100	157	4.906	4.903	-0.003
100	158	4.905	4.904	-0.001
100	159	4.903	4.901	-0.002
100	160	4.903	4.900	-0.003
100	161	4.903	4.900	-0.003
100	162	4.904	4.902	-0.002
100	163	4.904	4.902	-0.002
100	164	4.905	4.903	-0.002
100	165	4.906	4.905	-0.001
100	166	4.906	4.905	-0.001
100	168	4.903	4.903	0.000
100	169	4.904	4.902	-0.002
100	182	4.907	4.906	-0.001
100	183	4.904	4.901	-0.003
100	184	4.904	4.902	-0.002
100	185	4.904	4.902	-0.002
100	186	4.905	4.904	-0.001
100	187	4.905	4.903	-0.002
100	188	4.907	4.903	-0.004
100	189	4.904	4.903	-0.001
100	190	4.908	4.905	-0.003
105	191	4.906	4.904	-0.002
105	192	4.905	4.903	-0.002
105	193	4.906	4.903	-0.003
105	194	4.903	4.901	-0.002
105	195	4.902	4.900	-0.002
105	196	4.902	4.899	-0.003
Max		4.908	4.907	0.001
Average		4.904	4.903	-0.001
Min		4.900	4.899	-0.004
Std Dev		0.002	0.002	0.001

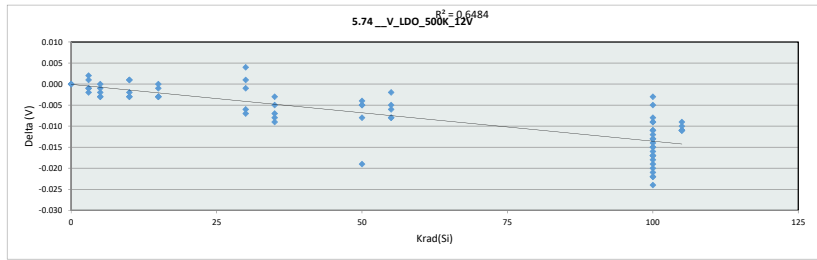


5.73_V_LDO_500K_5V											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	5.2	4.75						
			V	5.2	4.75						
Krad(Si)	LL	Min	Average	Max	UL						
0	4.750	4.902	4.903	4.905	5.200						
3	4.750	4.903	4.904	4.905	5.200						
5	4.750	4.902	4.905	4.906	5.200						
10	4.750	4.902	4.904	4.905	5.200						
15	4.750	4.902	4.903	4.905	5.200						
30	4.750	4.900	4.902	4.905	5.200						
35	4.750	4.899	4.903	4.905	5.200						
50	4.750	4.901	4.903	4.907	5.200						
55	4.750	4.900	4.903	4.905	5.200						
100	4.750	4.900	4.903	4.905	5.200						
105	4.750	4.899	4.903	4.905	5.200						

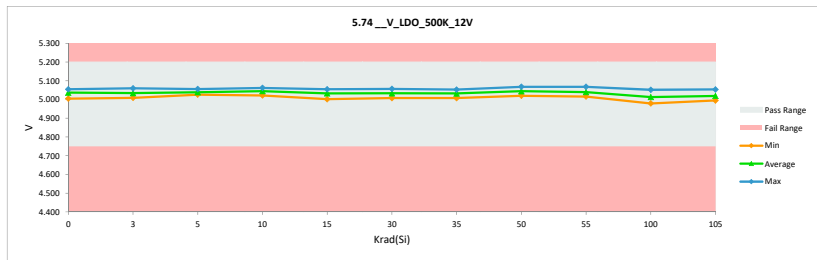


HDR TID Report  
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		5.74_V_LDO_500K_12V		
Test Site				
Tester				
Test Number				
Unit		V	V	
Max Limit		5.2	5.2	
Min Limit		4.75	4.75	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	5.005	5.005	0.000
0	138	5.055	5.055	0.000
0	139	5.050	5.050	0.000
3	197	5.018	5.019	0.001
3	198	5.025	5.024	-0.001
3	199	5.061	5.060	-0.001
3	202	5.011	5.009	-0.002
3	203	5.057	5.059	0.002
5	204	5.042	5.042	0.000
5	205	5.058	5.056	-0.002
5	206	5.027	5.026	-0.001
5	208	5.029	5.026	-0.003
5	209	5.047	5.044	-0.003
10	210	5.058	5.055	-0.003
10	211	5.043	5.041	-0.002
10	212	5.061	5.062	0.001
10	213	5.047	5.044	-0.003
10	214	5.021	5.022	0.001
15	215	5.003	5.002	-0.001
15	217	5.028	5.025	-0.003
15	219	5.058	5.055	-0.003
15	220	5.046	5.043	-0.003
15	221	5.041	5.041	0.000
30	222	5.029	5.022	-0.007
30	223	5.007	5.008	0.001
30	224	5.047	5.046	-0.001
30	225	5.030	5.034	0.004
30	226	5.063	5.057	-0.006
35	227	5.047	5.040	-0.007
35	228	5.017	5.008	-0.009
35	229	5.058	5.053	-0.005
35	230	5.034	5.031	-0.003
35	231	5.038	5.030	-0.008
50	170	5.034	5.026	-0.008
50	172	5.066	5.061	-0.005
50	173	5.024	5.020	-0.004
50	175	5.053	5.048	-0.005
50	176	5.087	5.068	-0.019
55	177	5.035	5.027	-0.008
55	178	5.073	5.068	-0.005
55	179	5.043	5.041	-0.002
55	180	5.053	5.047	-0.006
55	181	5.024	5.016	-0.008
100	154	5.040	5.031	-0.009
100	155	5.039	5.019	-0.020
100	156	5.021	5.010	-0.011
100	157	5.014	4.996	-0.018
100	158	5.031	5.016	-0.015
100	159	5.053	5.036	-0.017
100	160	5.031	5.007	-0.024
100	161	5.010	4.988	-0.022
100	162	5.056	5.037	-0.019
100	163	5.026	5.012	-0.014
100	164	5.011	4.996	-0.015
100	165	5.012	5.009	-0.003
100	166	5.050	5.028	-0.022
100	168	5.060	5.052	-0.008
100	169	4.999	4.988	-0.011
100	182	5.031	5.015	-0.016
100	183	4.996	4.979	-0.017
100	184	5.037	5.024	-0.013
100	185	5.036	5.031	-0.005
100	186	5.004	4.987	-0.017
100	187	5.037	5.028	-0.009
100	188	5.047	5.035	-0.012
100	189	5.001	4.988	-0.013
100	190	5.026	5.005	-0.021
105	191	5.013	5.006	-0.009
105	192	5.045	5.034	-0.011
105	193	5.065	5.054	-0.011
105	194	5.033	5.022	-0.011
105	195	5.006	4.995	-0.011
105	196	5.014	5.004	-0.010
	Max	5.087	5.068	0.004
	Average	5.036	5.028	-0.008
	Min	4.996	4.979	-0.024
	Std Dev	0.020	0.022	0.007

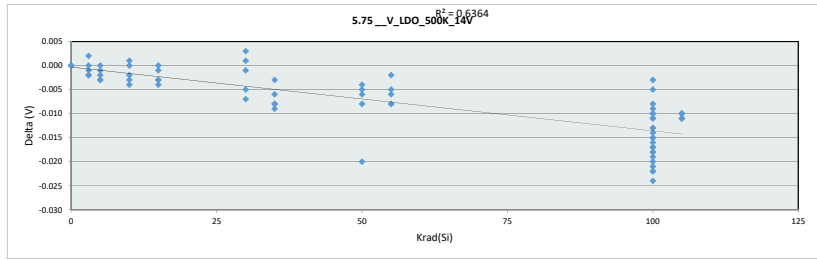


		5.74_V_LDO_500K_12V										
Test Site												
Tester												
Test Number												
Max Limit		5.2	V									
Min Limit		4.75	V									
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min		5.005	5.009	5.026	5.022	5.002	5.008	5.008	5.020	5.016	4.979	4.995
Average		5.037	5.034	5.039	5.045	5.033	5.033	5.032	5.045	5.040	5.013	5.019
Max		5.055	5.060	5.056	5.062	5.055	5.057	5.053	5.068	5.068	5.052	5.054
UL		5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

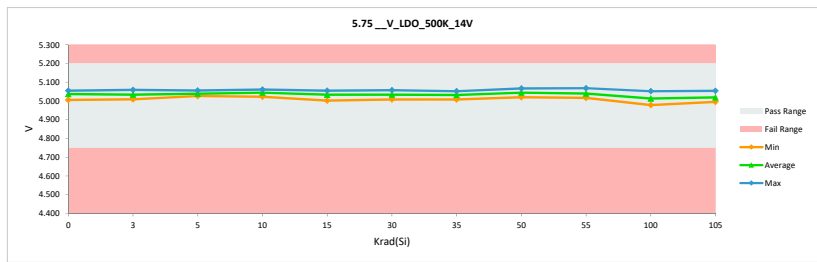


HDR TID Report  
TPS7H5003-SP

5.75_V_LDO_500K_14V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			5.2	5.2
Min Limit			4.75	4.75
Krad(Si)	Serial #	PRE	POST	Delta
0	137	5.005	5.005	0.000
0	138	5.055	5.055	0.000
0	139	5.050	5.050	0.000
3	197	5.018	5.018	0.000
3	198	5.025	5.024	-0.001
3	199	5.061	5.059	-0.002
3	202	5.011	5.009	-0.002
3	203	5.057	5.059	0.002
5	204	5.042	5.041	-0.001
5	205	5.058	5.056	-0.002
5	206	5.026	5.026	0.000
5	208	5.029	5.026	-0.003
5	209	5.046	5.043	-0.003
10	210	5.058	5.054	-0.004
10	211	5.043	5.041	-0.002
10	212	5.061	5.061	0.000
10	213	5.047	5.044	-0.003
10	214	5.021	5.022	0.001
15	215	5.003	5.002	-0.001
15	217	5.028	5.025	-0.003
15	219	5.058	5.055	-0.003
15	220	5.046	5.042	-0.004
15	221	5.041	5.041	0.000
30	222	5.029	5.022	-0.007
30	223	5.007	5.008	0.001
30	224	5.047	5.046	-0.001
30	225	5.030	5.033	0.003
30	226	5.062	5.057	-0.005
35	227	5.047	5.039	-0.008
35	228	5.017	5.008	-0.009
35	229	5.058	5.052	-0.006
35	230	5.034	5.031	-0.003
35	231	5.038	5.030	-0.008
50	170	5.034	5.026	-0.008
50	172	5.066	5.061	-0.005
50	173	5.024	5.020	-0.004
50	175	5.054	5.048	-0.006
50	176	5.087	5.067	-0.020
55	177	5.035	5.027	-0.008
55	178	5.073	5.068	-0.005
55	179	5.043	5.041	-0.002
55	180	5.053	5.047	-0.006
55	181	5.024	5.016	-0.008
100	154	5.040	5.030	-0.010
100	155	5.039	5.019	-0.020
100	156	5.021	5.010	-0.011
100	157	5.014	4.996	-0.018
100	158	5.031	5.016	-0.015
100	159	5.052	5.035	-0.017
100	160	5.031	5.007	-0.024
100	161	5.010	4.988	-0.022
100	162	5.056	5.037	-0.019
100	163	5.026	5.012	-0.014
100	164	5.011	4.996	-0.015
100	165	5.012	5.009	-0.003
100	166	5.050	5.028	-0.022
100	168	5.060	5.052	-0.008
100	169	4.998	4.988	-0.010
100	182	5.031	5.015	-0.016
100	183	4.996	4.978	-0.018
100	184	5.037	5.024	-0.013
100	185	5.036	5.031	-0.005
100	186	5.004	4.987	-0.017
100	187	5.037	5.028	-0.009
100	188	5.047	5.034	-0.013
100	189	5.000	4.989	-0.011
100	190	5.026	5.005	-0.021
105	191	5.015	5.005	-0.010
105	192	5.044	5.034	-0.010
105	193	5.065	5.054	-0.011
105	194	5.033	5.022	-0.011
105	195	5.006	4.995	-0.011
105	196	5.014	5.004	-0.010
Max		5.087	5.068	0.003
Average		5.036	5.028	-0.008
Min		4.996	4.978	-0.024
Std Dev		0.020	0.022	0.007

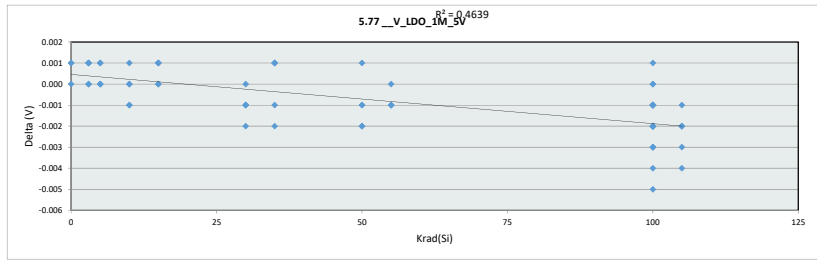


5.75_V_LDO_500K_14V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.005	5.009	5.026	5.022	5.002	5.008	5.008	5.020	5.016	4.978	4.995
Average	5.037	5.034	5.038	5.044	5.033	5.033	5.032	5.044	5.040	5.013	5.019
Max	5.055	5.059	5.056	5.061	5.055	5.057	5.052	5.067	5.068	5.052	5.054
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

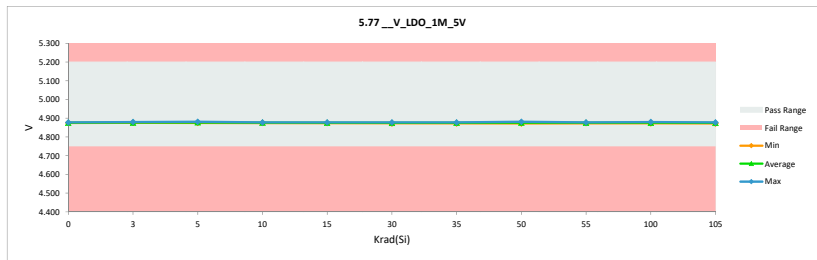


HDR TID Report  
TPS7H5003-SP

		5.77_V_LDO_1M_5V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	Min Limit	5.2	5.2	
		4.75	4.75	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	4.875	4.875	0.000
0	138	4.875	4.876	0.001
0	139	4.877	4.878	0.001
3	197	4.877	4.877	0.000
3	198	4.875	4.876	0.001
3	199	4.879	4.880	0.001
3	202	4.877	4.877	0.000
3	203	4.875	4.876	0.001
5	204	4.879	4.879	0.000
5	205	4.874	4.875	0.001
5	206	4.878	4.878	0.000
5	208	4.879	4.879	0.000
5	209	4.880	4.881	0.001
10	210	4.879	4.878	-0.001
10	211	4.878	4.877	-0.001
10	212	4.875	4.875	0.000
10	213	4.877	4.878	0.001
10	214	4.875	4.875	0.000
15	215	4.877	4.877	0.000
15	217	4.875	4.876	0.001
15	219	4.875	4.876	0.001
15	220	4.877	4.878	0.001
15	221	4.874	4.874	0.000
30	222	4.874	4.873	-0.001
30	223	4.878	4.877	-0.001
30	224	4.878	4.878	0.000
30	225	4.876	4.874	-0.002
30	226	4.873	4.872	-0.001
35	227	4.879	4.877	-0.002
35	228	4.875	4.876	0.001
35	229	4.873	4.872	-0.001
35	230	4.874	4.875	0.001
35	231	4.877	4.878	0.001
50	170	4.880	4.881	0.001
50	172	4.878	4.877	-0.001
50	173	4.878	4.876	-0.002
50	175	4.873	4.872	-0.001
50	176	4.880	4.878	-0.002
55	177	4.877	4.876	-0.001
55	178	4.878	4.878	0.000
55	179	4.878	4.877	-0.001
55	180	4.877	4.876	-0.001
55	181	4.875	4.874	-0.001
100	154	4.880	4.880	0.000
100	155	4.877	4.877	0.000
100	156	4.875	4.876	0.001
100	157	4.879	4.877	-0.002
100	158	4.878	4.877	-0.001
100	159	4.877	4.874	-0.003
100	160	4.876	4.873	-0.003
100	161	4.875	4.873	-0.002
100	162	4.878	4.876	-0.002
100	163	4.877	4.874	-0.003
100	164	4.878	4.877	-0.001
100	165	4.880	4.879	-0.001
100	166	4.879	4.878	-0.001
100	168	4.876	4.875	-0.001
100	169	4.876	4.875	-0.001
100	182	4.881	4.879	-0.002
100	183	4.877	4.874	-0.003
100	184	4.877	4.875	-0.002
100	185	4.878	4.877	-0.001
100	186	4.878	4.877	-0.001
100	187	4.878	4.876	-0.002
100	188	4.881	4.877	-0.004
100	189	4.878	4.876	-0.002
100	190	4.882	4.877	-0.005
105	191	4.880	4.878	-0.002
105	192	4.878	4.876	-0.002
105	193	4.880	4.876	-0.004
105	194	4.876	4.875	-0.001
105	195	4.876	4.874	-0.002
105	196	4.875	4.872	-0.003
	Max	4.882	4.881	0.001
	Average	4.877	4.876	-0.001
	Min	4.873	4.872	-0.005
	Std Dev	0.002	0.002	0.001

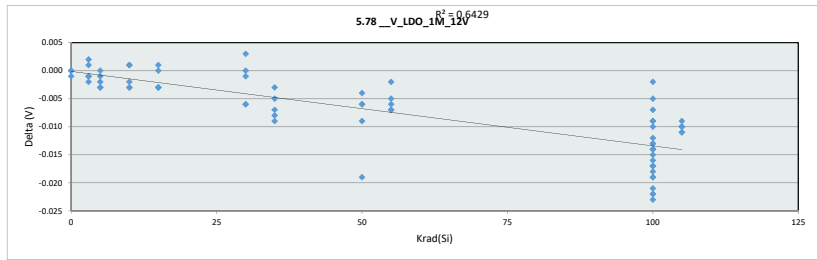


		5.77_V_LDO_1M										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit	Min Limit	5.2	4.75	V	V							
		4.75	4.75									
Krad(Si)	LL	Min	Average	Max	UL							
0	4.750	4.875	4.876	4.878	5.200							
3	4.750	4.875	4.877	4.878	5.200							
5	4.750	4.875	4.877	4.878	5.200							
10	4.750	4.875	4.877	4.878	5.200							
15	4.750	4.875	4.877	4.878	5.200							
30	4.750	4.874	4.876	4.877	5.200							
35	4.750	4.872	4.874	4.875	5.200							
50	4.750	4.872	4.874	4.875	5.200							
55	4.750	4.872	4.874	4.875	5.200							
100	4.750	4.874	4.876	4.877	5.200							
105	4.750	4.873	4.875	4.876	5.200							

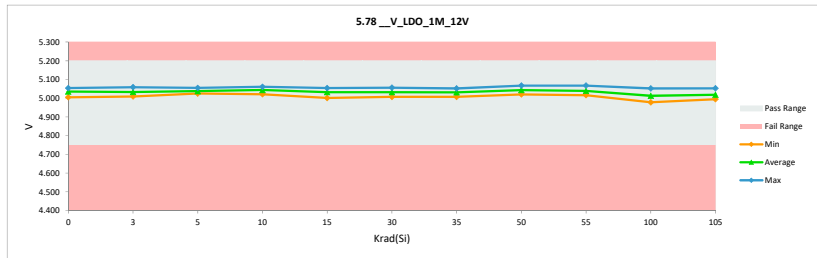


# HDR TID Report TPS7H5003-SP

		5.78_V_LDO_1M_12V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	Min Limit	5.2	4.75	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	5.005	5.005	0.000
0	138	5.055	5.054	-0.001
0	139	5.049	5.049	0.000
3	197	5.017	5.018	0.001
3	198	5.024	5.023	-0.001
3	199	5.061	5.059	-0.002
3	202	5.010	5.009	-0.001
3	203	5.056	5.058	0.002
5	204	5.041	5.041	0.000
5	205	5.057	5.055	-0.002
5	206	5.026	5.025	-0.001
5	208	5.028	5.025	-0.003
5	209	5.046	5.043	-0.003
10	210	5.057	5.054	-0.003
10	211	5.042	5.040	-0.002
10	212	5.060	5.061	0.001
10	213	5.046	5.043	-0.003
10	214	5.020	5.021	0.001
15	215	5.001	5.001	0.000
15	217	5.027	5.024	-0.003
15	219	5.057	5.054	-0.003
15	220	5.045	5.042	-0.003
15	221	5.040	5.041	0.001
30	222	5.027	5.021	-0.006
30	223	5.007	5.007	0.000
30	224	5.046	5.045	-0.001
30	225	5.029	5.032	0.003
30	226	5.062	5.056	-0.006
35	227	5.046	5.038	-0.008
35	228	5.016	5.007	-0.009
35	229	5.057	5.052	-0.005
35	230	5.033	5.030	-0.003
35	231	5.037	5.030	-0.007
50	170	5.034	5.025	-0.009
50	172	5.065	5.059	-0.006
50	173	5.024	5.020	-0.004
50	175	5.053	5.047	-0.006
50	176	5.086	5.067	-0.019
55	177	5.033	5.026	-0.007
55	178	5.072	5.067	-0.005
55	179	5.042	5.040	-0.002
55	180	5.052	5.046	-0.006
55	181	5.023	5.016	-0.007
100	154	5.039	5.030	-0.009
100	155	5.038	5.019	-0.019
100	156	5.021	5.009	-0.012
100	157	5.013	4.996	-0.017
100	158	5.030	5.015	-0.015
100	159	5.052	5.035	-0.017
100	160	5.030	5.007	-0.023
100	161	5.010	4.988	-0.022
100	162	5.055	5.036	-0.019
100	163	5.025	5.011	-0.014
100	164	5.010	4.996	-0.014
100	165	5.011	5.009	-0.002
100	166	5.049	5.027	-0.022
100	168	5.059	5.052	-0.007
100	169	4.998	4.988	-0.010
100	182	5.030	5.014	-0.016
100	183	4.996	4.978	-0.018
100	184	5.037	5.023	-0.014
100	185	5.035	5.030	-0.005
100	186	5.004	4.987	-0.017
100	187	5.036	5.027	-0.009
100	188	5.047	5.034	-0.013
100	189	5.000	4.987	-0.013
100	190	5.026	5.005	-0.021
105	191	5.015	5.005	-0.010
105	192	5.043	5.033	-0.010
105	193	5.064	5.053	-0.011
105	194	5.032	5.021	-0.011
105	195	5.005	4.995	-0.010
105	196	5.013	5.004	-0.009
	Max	5.086	5.067	0.003
	Average	5.035	5.027	-0.007
	Min	4.996	4.978	-0.023
	Std Dev	0.020	0.022	0.007

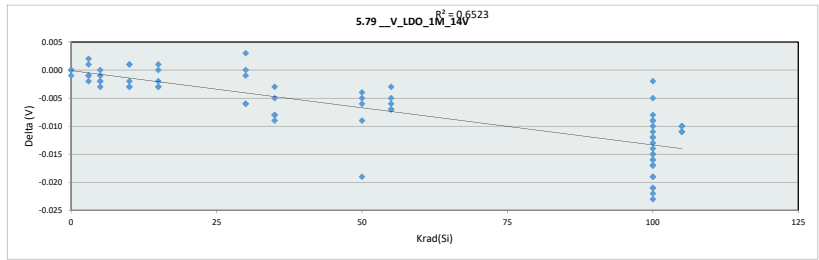


		5.78_V_LDO_1M										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit	Min Limit	5.2	4.75	V	V							
Krad(Si)	LL	3	5	10	15	30	35	50	55	100	105	
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.005	5.009	5.025	5.021	5.001	5.007	5.007	5.020	5.016	4.978	4.995	
Average	5.036	5.033	5.038	5.044	5.032	5.032	5.031	5.044	5.039	5.013	5.019	
Max	5.054	5.059	5.055	5.061	5.054	5.056	5.052	5.067	5.067	5.052	5.053	
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

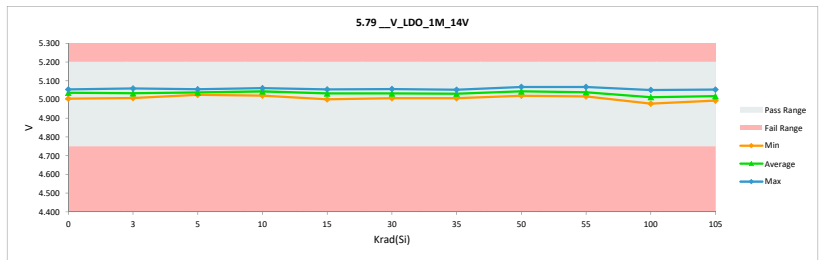


# HDR TID Report TPS7H5003-SP

5.79_V_LDO_1M_14V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			5.2	5.2
Min Limit			4.75	4.75
Krad(Si)	Serial #	PRE	POST	Delta
0	137	5.005	5.004	-0.001
0	138	5.054	5.054	0.000
0	139	5.049	5.049	0.000
3	197	5.017	5.018	0.001
3	198	5.024	5.023	-0.001
3	199	5.060	5.059	-0.001
3	202	5.010	5.008	-0.002
3	203	5.056	5.058	0.002
5	204	5.041	5.040	-0.001
5	205	5.057	5.055	-0.002
5	206	5.025	5.025	0.000
5	208	5.028	5.025	-0.003
5	209	5.045	5.043	-0.002
10	210	5.057	5.054	-0.003
10	211	5.042	5.039	-0.003
10	212	5.060	5.061	0.001
10	213	5.045	5.043	-0.002
10	214	5.020	5.021	0.001
15	215	5.001	5.001	0.000
15	217	5.027	5.024	-0.003
15	219	5.057	5.054	-0.003
15	220	5.044	5.042	-0.002
15	221	5.040	5.041	0.001
30	222	5.027	5.021	-0.006
30	223	5.007	5.007	0.000
30	224	5.046	5.045	-0.001
30	225	5.029	5.032	0.003
30	226	5.062	5.056	-0.006
35	227	5.046	5.038	-0.008
35	228	5.016	5.007	-0.009
35	229	5.057	5.052	-0.005
35	230	5.033	5.030	-0.003
35	231	5.037	5.029	-0.008
50	170	5.034	5.025	-0.009
50	172	5.065	5.059	-0.006
50	173	5.024	5.020	-0.004
50	175	5.052	5.047	-0.005
50	176	5.086	5.067	-0.019
55	177	5.033	5.026	-0.007
55	178	5.072	5.067	-0.005
55	179	5.042	5.039	-0.003
55	180	5.052	5.046	-0.006
55	181	5.023	5.016	-0.007
100	154	5.039	5.030	-0.009
100	155	5.038	5.019	-0.019
100	156	5.020	5.010	-0.010
100	157	5.013	4.996	-0.017
100	158	5.030	5.015	-0.015
100	159	5.052	5.035	-0.017
100	160	5.030	5.007	-0.023
100	161	5.010	4.988	-0.022
100	162	5.055	5.036	-0.019
100	163	5.025	5.010	-0.015
100	164	5.010	4.996	-0.014
100	165	5.011	5.009	-0.002
100	166	5.048	5.027	-0.021
100	168	5.059	5.051	-0.008
100	169	4.998	4.987	-0.011
100	182	5.030	5.014	-0.016
100	183	4.995	4.978	-0.017
100	184	5.036	5.023	-0.013
100	185	5.035	5.030	-0.005
100	186	5.003	4.987	-0.016
100	187	5.036	5.027	-0.009
100	188	5.046	5.034	-0.012
100	189	5.000	4.988	-0.012
100	190	5.025	5.004	-0.021
105	191	5.015	5.005	-0.010
105	192	5.043	5.033	-0.010
105	193	5.064	5.053	-0.011
105	194	5.032	5.021	-0.011
105	195	5.005	4.994	-0.011
105	196	5.013	5.003	-0.010
Max		5.086	5.067	0.003
Average		5.035	5.027	-0.007
Min		4.995	4.978	-0.023
Std Dev		0.020	0.022	0.007

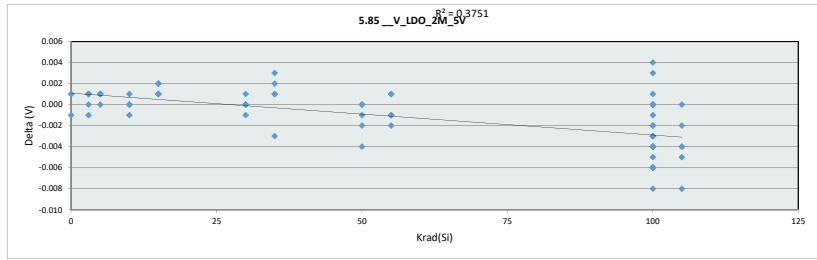


5.79_V_LDO_1M											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit			5.2	5.2							
Min Limit			4.75	4.75							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.004	5.008	5.025	5.021	5.001	5.007	5.007	5.020	5.016	4.978	4.994
Average	5.036	5.033	5.038	5.044	5.032	5.032	5.031	5.044	5.039	5.013	5.018
Max	5.054	5.059	5.055	5.061	5.054	5.056	5.052	5.067	5.067	5.051	5.053
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

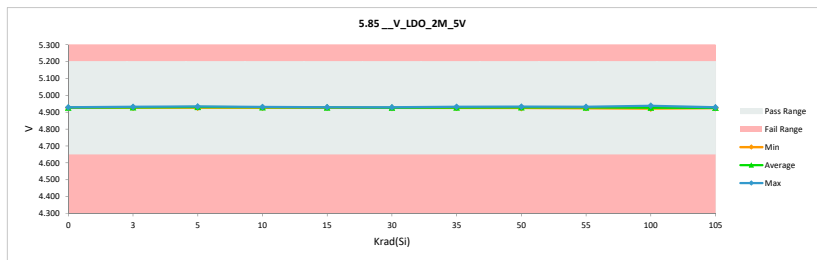


HDR TID Report  
TPS7H5003-SP

		5.85_V_LDO_2M_5V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	Min Limit	5.2	5.2	
		4.65	4.65	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	4.927	4.926	-0.001
0	138	4.926	4.927	0.001
0	139	4.928	4.929	0.001
3	197	4.929	4.928	-0.001
3	198	4.927	4.927	0.000
3	199	4.931	4.932	0.001
3	202	4.928	4.929	0.001
3	203	4.926	4.927	0.001
5	204	4.931	4.931	0.000
5	205	4.926	4.927	0.001
5	206	4.929	4.930	0.001
5	208	4.931	4.932	0.001
5	209	4.933	4.934	0.001
10	210	4.932	4.931	-0.001
10	211	4.931	4.931	0.000
10	212	4.927	4.927	0.000
10	213	4.928	4.928	0.000
10	214	4.928	4.929	0.001
10	215	4.928	4.929	0.001
15	217	4.926	4.928	0.002
15	219	4.926	4.927	0.001
15	220	4.927	4.928	0.001
15	221	4.928	4.930	0.002
30	222	4.925	4.925	0.000
30	223	4.929	4.929	0.000
30	224	4.929	4.929	0.000
30	225	4.926	4.925	-0.001
30	226	4.924	4.925	0.001
35	227	4.930	4.927	-0.003
35	228	4.926	4.927	0.001
35	229	4.925	4.926	0.001
35	230	4.925	4.927	0.002
35	231	4.929	4.932	0.003
50	170	4.934	4.933	-0.001
50	172	4.930	4.930	0.000
50	173	4.929	4.929	0.000
50	175	4.926	4.924	-0.002
50	176	4.932	4.928	-0.004
55	177	4.928	4.927	-0.001
55	178	4.930	4.928	-0.002
55	179	4.931	4.932	0.001
55	180	4.928	4.925	-0.001
55	181	4.926	4.925	-0.001
100	154	4.932	4.929	-0.003
100	155	4.929	4.926	-0.003
100	156	4.927	4.924	-0.003
100	157	4.932	4.926	-0.006
100	158	4.929	4.925	-0.004
100	159	4.929	4.923	-0.006
100	160	4.929	4.927	-0.002
100	161	4.927	4.926	-0.001
100	162	4.931	4.931	0.000
100	163	4.928	4.922	-0.006
100	164	4.931	4.931	0.000
100	165	4.932	4.933	0.001
100	166	4.932	4.928	-0.004
100	168	4.929	4.929	0.000
100	169	4.927	4.923	-0.004
100	182	4.934	4.938	0.004
100	183	4.927	4.921	-0.006
100	184	4.929	4.932	0.003
100	185	4.930	4.926	-0.004
100	186	4.930	4.927	-0.003
100	187	4.929	4.924	-0.005
100	188	4.933	4.927	-0.006
100	189	4.929	4.927	-0.002
100	190	4.934	4.926	-0.008
105	191	4.931	4.927	-0.004
105	192	4.930	4.925	-0.005
105	193	4.932	4.924	-0.008
105	194	4.928	4.924	-0.004
105	195	4.929	4.929	0.000
105	196	4.928	4.926	-0.002
	Max	4.934	4.938	0.004
	Average	4.929	4.928	-0.001
	Min	4.924	4.921	-0.008
	Std Dev	0.002	0.003	0.003



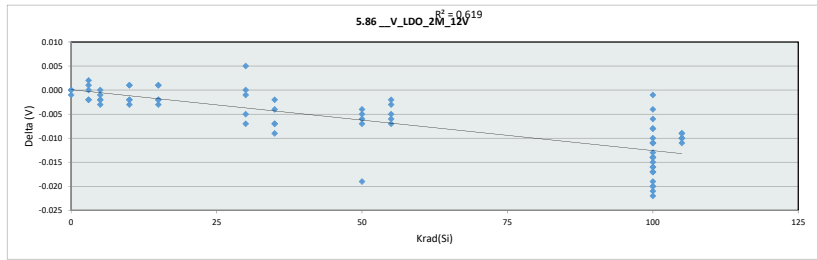
		5.85_V_LDO_2M														
Test Site	Tester															
Test Number	Unit	V	V													
Max Limit	Min Limit	5.2	5.2													
		4.65	4.65													
Krad(Si)	LL	Min	Average	Max	UL	0	3	5	10	15	30	35	50	55	100	105
		4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
		4.926	4.927	4.927	4.927	4.927	4.927	4.927	4.927	4.925	4.925	4.926	4.924	4.925	4.921	4.924
		4.927	4.929	4.931	4.929	4.928	4.927	4.928	4.927	4.928	4.929	4.929	4.929	4.928	4.927	4.926
		4.929	4.932	4.934	4.931	4.930	4.929	4.932	4.932	4.933	4.932	4.933	4.932	4.938	4.929	4.929
		5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



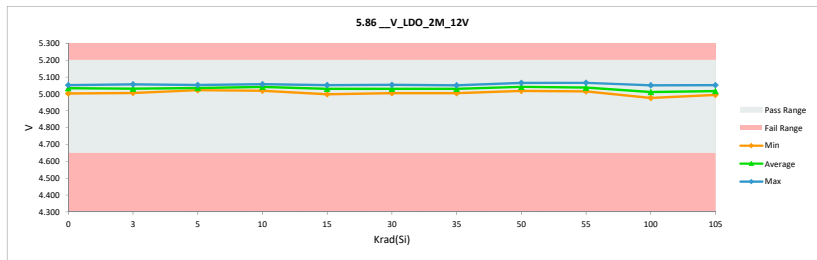


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5.86_V_LDO_2M_12V				
Test Site	Tester	Test Number	Unit	Max Limit
			V	5.2
			V	4.65
Krad(Si)	Serial #	PRE	POST	Delta
0	137	5.003	5.003	0.000
0	138	5.052	5.052	0.000
0	139	5.047	5.046	-0.001
3	197	5.015	5.016	0.001
3	198	5.021	5.021	0.000
3	199	5.059	5.057	-0.002
3	202	5.008	5.006	-0.002
3	203	5.054	5.056	0.002
5	204	5.038	5.038	0.000
5	205	5.055	5.053	-0.002
5	206	5.023	5.022	-0.001
5	208	5.026	5.023	-0.003
5	209	5.043	5.041	-0.002
10	210	5.055	5.052	-0.003
10	211	5.039	5.037	-0.002
10	212	5.057	5.058	0.001
10	213	5.043	5.041	-0.002
10	214	5.018	5.019	0.001
15	215	4.997	4.998	0.001
15	217	5.025	5.022	-0.003
15	219	5.054	5.052	-0.002
15	220	5.042	5.040	-0.002
15	221	5.038	5.039	0.001
30	222	5.025	5.018	-0.007
30	223	5.005	5.005	0.000
30	224	5.043	5.042	-0.001
30	225	5.026	5.031	0.005
30	226	5.059	5.054	-0.005
35	227	5.044	5.037	-0.007
35	228	5.014	5.005	-0.009
35	229	5.055	5.051	-0.004
35	230	5.030	5.028	-0.002
35	231	5.035	5.028	-0.007
50	170	5.032	5.025	-0.007
50	172	5.062	5.057	-0.005
50	173	5.022	5.018	-0.004
50	175	5.050	5.044	-0.006
50	176	5.084	5.065	-0.019
55	177	5.031	5.024	-0.007
55	178	5.069	5.066	-0.003
55	179	5.039	5.037	-0.002
55	180	5.050	5.045	-0.005
55	181	5.021	5.015	-0.006
100	154	5.037	5.029	-0.008
100	155	5.036	5.017	-0.019
100	156	5.018	5.007	-0.011
100	157	5.011	4.994	-0.017
100	158	5.028	5.013	-0.015
100	159	5.050	5.034	-0.016
100	160	5.028	5.006	-0.022
100	161	5.009	4.988	-0.021
100	162	5.052	5.035	-0.017
100	163	5.022	5.008	-0.014
100	164	5.008	4.994	-0.014
100	165	5.009	5.008	-0.001
100	166	5.046	5.026	-0.020
100	168	5.057	5.051	-0.006
100	169	4.997	4.987	-0.010
100	182	5.026	5.012	-0.014
100	183	4.993	4.976	-0.017
100	184	5.034	5.021	-0.013
100	185	5.032	5.028	-0.004
100	186	5.002	4.986	-0.016
100	187	5.034	5.026	-0.008
100	188	5.045	5.034	-0.011
100	189	4.997	4.986	-0.011
100	190	5.023	5.003	-0.020
105	191	5.013	5.004	-0.009
105	192	5.040	5.031	-0.009
105	193	5.062	5.052	-0.010
105	194	5.030	5.019	-0.011
105	195	5.004	4.994	-0.010
105	196	5.011	5.001	-0.010
	Max	5.084	5.066	0.005
	Average	5.032	5.025	-0.007
	Min	4.993	4.976	-0.022
	Std Dev	0.020	0.022	0.007

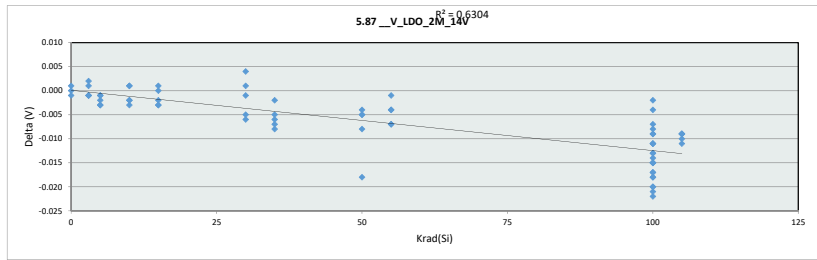


5.86_V_LDO_2M											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	5.2	4.65						
			V	5.2	4.65						
Krad(Si)	LL	3	5	10	15	30	35	50	55	100	105
	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
	5.003	5.006	5.022	5.019	4.998	5.005	5.005	5.018	5.015	4.976	4.994
	5.034	5.031	5.035	5.041	5.030	5.030	5.030	5.042	5.037	5.011	5.017
	5.052	5.057	5.053	5.058	5.052	5.054	5.051	5.065	5.066	5.051	5.052
	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

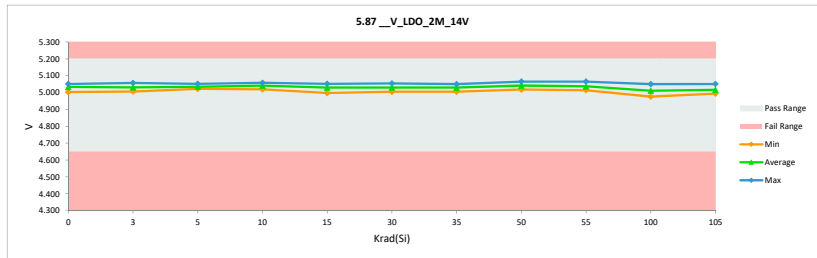


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5.87_V_LDO_2M_14V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			5.2	5.2
Min Limit			4.65	4.65
Krad(Si)	Serial #	PRE	POST	Delta
0	137	5.002	5.003	0.001
0	138	5.052	5.051	-0.001
0	139	5.046	5.046	0.000
3	197	5.015	5.016	0.001
3	198	5.021	5.020	-0.001
3	199	5.058	5.057	-0.001
3	202	5.007	5.006	-0.001
3	203	5.054	5.056	0.002
5	204	5.038	5.037	-0.001
5	205	5.055	5.052	-0.003
5	206	5.023	5.022	-0.001
5	208	5.025	5.023	-0.002
5	209	5.043	5.040	-0.003
10	210	5.054	5.051	-0.003
10	211	5.039	5.037	-0.002
10	212	5.057	5.058	0.001
10	213	5.043	5.041	-0.002
10	214	5.018	5.019	0.001
15	215	4.997	4.997	0.000
15	217	5.024	5.021	-0.003
15	219	5.054	5.052	-0.002
15	220	5.042	5.039	-0.003
15	221	5.038	5.039	0.001
30	222	5.024	5.018	-0.006
30	223	5.004	5.005	0.001
30	224	5.043	5.042	-0.001
30	225	5.026	5.030	0.004
30	226	5.059	5.054	-0.005
35	227	5.043	5.037	-0.006
35	228	5.013	5.005	-0.008
35	229	5.055	5.050	-0.005
35	230	5.030	5.028	-0.002
35	231	5.034	5.027	-0.007
50	170	5.032	5.024	-0.008
50	172	5.061	5.056	-0.005
50	173	5.022	5.018	-0.004
50	175	5.050	5.045	-0.005
50	176	5.083	5.065	-0.018
55	177	5.031	5.024	-0.007
55	178	5.069	5.065	-0.004
55	179	5.038	5.037	-0.001
55	180	5.049	5.045	-0.004
55	181	5.021	5.014	-0.007
100	154	5.037	5.028	-0.009
100	155	5.035	5.017	-0.018
100	156	5.018	5.007	-0.011
100	157	5.010	4.993	-0.017
100	158	5.028	5.013	-0.015
100	159	5.049	5.034	-0.015
100	160	5.027	5.005	-0.022
100	161	5.009	4.988	-0.021
100	162	5.052	5.034	-0.018
100	163	5.021	5.008	-0.013
100	164	5.008	4.993	-0.015
100	165	5.009	5.007	-0.002
100	166	5.046	5.026	-0.020
100	168	5.057	5.050	-0.007
100	169	4.996	4.987	-0.009
100	182	5.026	5.012	-0.014
100	183	4.993	4.976	-0.017
100	184	5.034	5.021	-0.013
100	185	5.032	5.028	-0.004
100	186	5.001	4.986	-0.015
100	187	5.034	5.026	-0.008
100	188	5.045	5.034	-0.011
100	189	4.997	4.986	-0.011
100	190	5.023	5.003	-0.020
105	191	5.013	5.004	-0.009
105	192	5.040	5.031	-0.009
105	193	5.062	5.051	-0.011
105	194	5.029	5.019	-0.010
105	195	5.003	4.994	-0.009
105	196	5.010	5.001	-0.009
Max		5.083	5.065	0.004
Average		5.032	5.025	-0.007
Min		4.993	4.976	-0.022
Std Dev		0.020	0.022	0.007

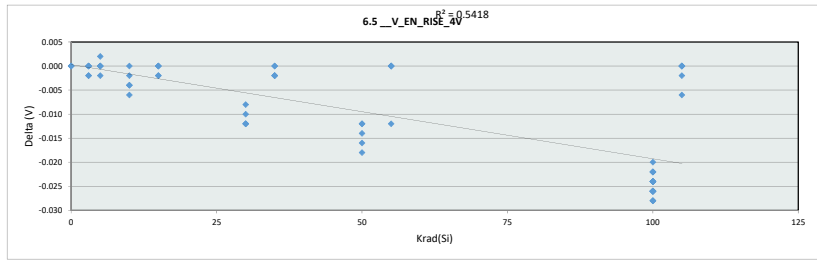


5.87_V_LDO_2M											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit			5.2	5.2							
Min Limit			4.65	4.65							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	5.003	5.006	5.022	5.019	4.997	5.005	5.005	5.018	5.014	4.976	4.994
Average	5.033	5.031	5.035	5.041	5.030	5.030	5.029	5.042	5.037	5.011	5.017
Max	5.051	5.057	5.052	5.058	5.052	5.054	5.050	5.065	5.065	5.050	5.051
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

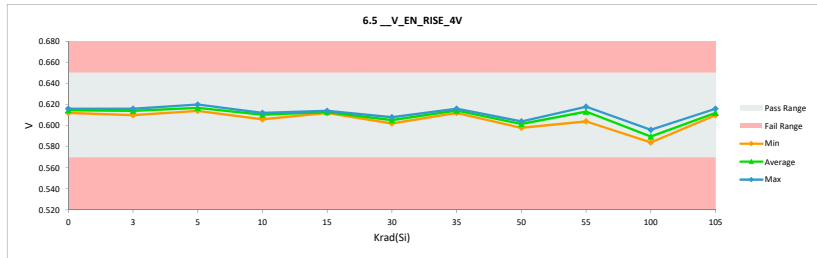


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		6.5_V_EN_RISE_4V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit		0.65	0.65	
Min Limit		0.57	0.57	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.616	0.616	0.000
0	138	0.616	0.616	0.000
0	139	0.612	0.612	0.000
3	197	0.610	0.610	0.000
3	198	0.616	0.616	0.000
3	199	0.618	0.616	-0.002
3	202	0.614	0.614	0.000
3	203	0.616	0.614	-0.002
5	204	0.616	0.614	-0.002
5	205	0.614	0.614	0.000
5	206	0.618	0.620	0.002
5	208	0.618	0.618	0.000
5	209	0.618	0.618	0.000
10	210	0.616	0.610	-0.006
10	211	0.608	0.606	-0.002
10	212	0.616	0.612	-0.004
10	213	0.612	0.612	0.000
10	214	0.616	0.612	-0.004
10	215	0.614	0.612	-0.002
15	217	0.614	0.614	0.000
15	219	0.614	0.612	-0.002
15	220	0.612	0.612	0.000
15	221	0.614	0.614	0.000
30	222	0.616	0.608	-0.008
30	223	0.614	0.604	-0.010
30	224	0.614	0.602	-0.012
30	225	0.618	0.606	-0.012
30	226	0.618	0.606	-0.012
35	227	0.616	0.616	0.000
35	228	0.614	0.612	-0.002
35	229	0.618	0.616	-0.002
35	230	0.614	0.614	0.000
35	231	0.616	0.614	-0.002
50	170	0.618	0.602	-0.016
50	172	0.616	0.598	-0.018
50	173	0.614	0.600	-0.014
50	175	0.616	0.604	-0.012
50	176	0.616	0.604	-0.012
55	177	0.618	0.618	0.000
55	178	0.616	0.604	-0.012
55	179	0.616	0.616	0.000
55	180	0.614	0.614	0.000
55	181	0.614	0.614	0.000
100	154	0.616	0.592	-0.024
100	155	0.612	0.590	-0.022
100	156	0.618	0.592	-0.026
100	157	0.614	0.588	-0.026
100	158	0.616	0.592	-0.024
100	159	0.618	0.592	-0.026
100	160	0.612	0.592	-0.020
100	161	0.612	0.584	-0.028
100	162	0.612	0.588	-0.024
100	163	0.614	0.590	-0.024
100	164	0.612	0.590	-0.022
100	165	0.612	0.590	-0.022
100	166	0.614	0.588	-0.026
100	168	0.620	0.596	-0.024
100	169	0.616	0.590	-0.026
100	182	0.616	0.590	-0.026
100	183	0.616	0.588	-0.028
100	184	0.614	0.588	-0.026
100	185	0.614	0.592	-0.022
100	186	0.610	0.586	-0.024
100	187	0.616	0.590	-0.026
100	188	0.616	0.588	-0.028
100	189	0.612	0.588	-0.024
100	190	0.614	0.588	-0.026
105	191	0.612	0.612	0.000
105	192	0.616	0.610	-0.006
105	193	0.612	0.612	0.000
105	194	0.612	0.612	0.000
105	195	0.612	0.610	-0.002
105	196	0.616	0.616	0.000
Max		0.620	0.620	0.002
Average		0.615	0.604	-0.011
Min		0.608	0.584	-0.028
Std Dev		0.002	0.011	0.011

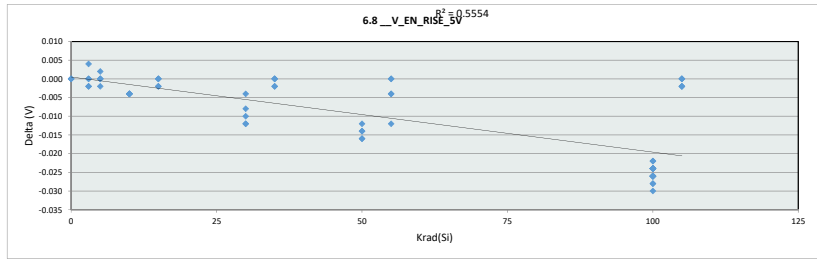


		6.5_V_EN_RISE										
Test Site	Tester											
Test Number	Unit	0.65	V									
Max Limit		0.65	V									
Min Limit		0.57	V									
Krad(Si)	LL	3	5	10	15	30	35	50	55	100	105	UL
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.610	0.614	0.606	0.612	0.602	0.612	0.598	0.604	0.584	0.610	0.610
Average	0.615	0.614	0.617	0.610	0.613	0.605	0.614	0.602	0.613	0.590	0.612	0.612
Max	0.616	0.616	0.620	0.612	0.614	0.608	0.616	0.604	0.618	0.596	0.616	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

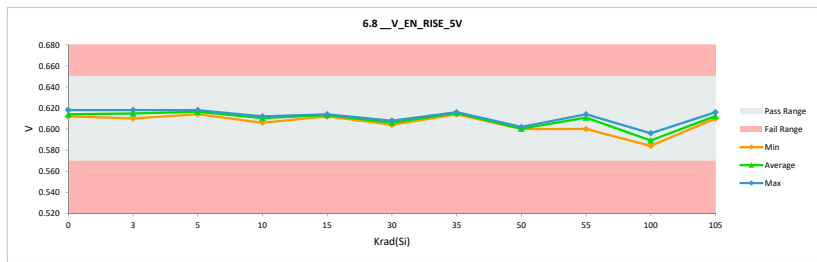


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6.8_V_EN_RISE_5V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			V	0.65	0.57
			V	0.65	0.57
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	0.612	0.612	0.000	
0	138	0.618	0.618	0.000	
0	139	0.612	0.612	0.000	
3	197	0.612	0.610	-0.002	
3	198	0.618	0.618	0.000	
3	199	0.618	0.616	-0.002	
3	202	0.614	0.614	0.000	
3	203	0.612	0.616	0.004	
5	204	0.618	0.616	-0.002	
5	205	0.614	0.614	0.000	
5	206	0.614	0.616	0.002	
5	208	0.618	0.618	0.000	
5	209	0.618	0.618	0.000	
10	210	0.616	0.612	-0.004	
10	211	0.610	0.606	-0.004	
10	212	0.616	0.612	-0.004	
10	213	0.614	0.610	-0.004	
10	214	0.616	0.612	-0.004	
15	215	0.612	0.612	0.000	
15	217	0.614	0.614	0.000	
15	219	0.614	0.612	-0.002	
15	220	0.614	0.614	0.000	
15	221	0.616	0.614	-0.002	
30	222	0.612	0.608	-0.004	
30	223	0.614	0.606	-0.008	
30	224	0.614	0.604	-0.010	
30	225	0.618	0.606	-0.012	
30	226	0.618	0.606	-0.012	
35	227	0.616	0.616	0.000	
35	228	0.614	0.614	0.000	
35	229	0.618	0.616	-0.002	
35	230	0.616	0.614	-0.002	
35	231	0.616	0.616	0.000	
50	170	0.614	0.602	-0.012	
50	172	0.614	0.600	-0.014	
50	173	0.614	0.600	-0.014	
50	175	0.616	0.600	-0.016	
50	176	0.616	0.600	-0.016	
55	177	0.618	0.614	-0.004	
55	178	0.612	0.600	-0.012	
55	179	0.616	0.612	-0.004	
55	180	0.614	0.614	0.000	
55	181	0.614	0.614	0.000	
100	154	0.616	0.592	-0.024	
100	155	0.612	0.586	-0.026	
100	156	0.618	0.592	-0.026	
100	157	0.614	0.588	-0.026	
100	158	0.616	0.586	-0.030	
100	159	0.616	0.590	-0.026	
100	160	0.612	0.590	-0.022	
100	161	0.608	0.584	-0.024	
100	162	0.612	0.588	-0.024	
100	163	0.614	0.590	-0.024	
100	164	0.612	0.590	-0.022	
100	165	0.612	0.588	-0.024	
100	166	0.614	0.588	-0.026	
100	168	0.620	0.596	-0.024	
100	169	0.618	0.590	-0.028	
100	182	0.616	0.590	-0.026	
100	183	0.612	0.588	-0.024	
100	184	0.614	0.590	-0.024	
100	185	0.616	0.592	-0.024	
100	186	0.612	0.586	-0.026	
100	187	0.616	0.590	-0.026	
100	188	0.616	0.588	-0.028	
100	189	0.614	0.588	-0.026	
100	190	0.614	0.590	-0.024	
105	191	0.612	0.612	0.000	
105	192	0.612	0.610	-0.002	
105	193	0.614	0.612	-0.002	
105	194	0.612	0.612	0.000	
105	195	0.614	0.612	-0.002	
105	196	0.616	0.616	0.000	
	Max	0.620	0.618	0.004	
	Average	0.615	0.604	-0.011	
	Min	0.608	0.584	-0.030	
	Std Dev	0.002	0.011	0.011	

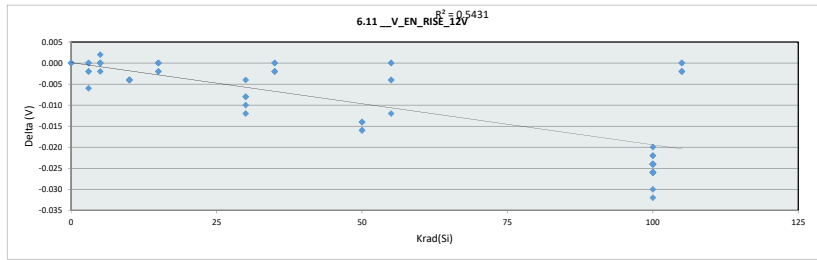


6.8_V_EN_RISE											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	0.65	0.57						
			V	0.65	0.57						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.610	0.614	0.606	0.612	0.604	0.614	0.600	0.600	0.584	0.610
Average	0.614	0.615	0.616	0.610	0.613	0.606	0.615	0.600	0.611	0.589	0.612
Max	0.618	0.618	0.618	0.612	0.614	0.608	0.616	0.602	0.614	0.596	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

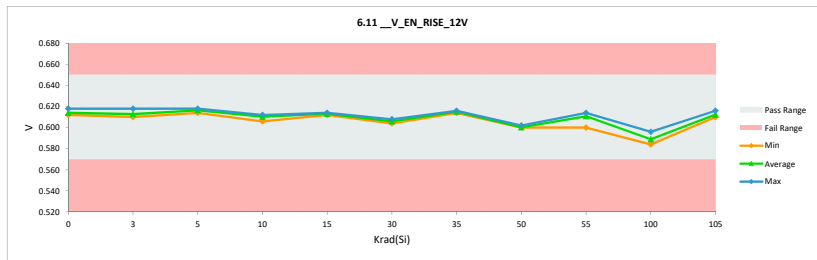


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6.11_V_EN_RISE_12V				
Test Site	Tester	Test Number	Unit	Max Limit
			V	0.65
			V	0.57
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.612	0.612	0.000
0	138	0.618	0.618	0.000
0	139	0.612	0.612	0.000
3	197	0.612	0.610	-0.002
3	198	0.618	0.618	0.000
3	199	0.618	0.612	-0.006
3	202	0.614	0.614	0.000
3	203	0.612	0.610	-0.002
5	204	0.618	0.616	-0.002
5	205	0.614	0.614	0.000
5	206	0.614	0.616	0.002
5	208	0.618	0.618	0.000
5	209	0.618	0.618	0.000
10	210	0.616	0.612	-0.004
10	211	0.610	0.606	-0.004
10	212	0.610	0.612	-0.004
10	213	0.614	0.610	-0.004
10	214	0.616	0.612	-0.004
10	215	0.612	0.612	0.000
15	217	0.614	0.614	0.000
15	219	0.614	0.612	-0.002
15	220	0.614	0.614	0.000
15	221	0.616	0.614	-0.002
30	222	0.612	0.608	-0.004
30	223	0.614	0.606	-0.008
30	224	0.614	0.604	-0.010
30	225	0.618	0.606	-0.012
30	226	0.614	0.606	-0.008
35	227	0.616	0.616	0.000
35	228	0.616	0.614	-0.002
35	229	0.618	0.616	-0.002
35	230	0.616	0.614	-0.002
35	231	0.616	0.616	0.000
50	170	0.616	0.602	-0.014
50	172	0.614	0.600	-0.014
50	173	0.614	0.600	-0.014
50	175	0.616	0.600	-0.016
50	176	0.616	0.600	-0.016
55	177	0.618	0.614	-0.004
55	178	0.612	0.600	-0.012
55	179	0.616	0.612	-0.004
55	180	0.614	0.614	0.000
55	181	0.614	0.614	0.000
100	154	0.616	0.592	-0.024
100	155	0.612	0.586	-0.026
100	156	0.614	0.592	-0.022
100	157	0.614	0.588	-0.026
100	158	0.616	0.586	-0.030
100	159	0.616	0.590	-0.026
100	160	0.612	0.590	-0.022
100	161	0.608	0.584	-0.024
100	162	0.612	0.588	-0.024
100	163	0.614	0.590	-0.024
100	164	0.612	0.592	-0.020
100	165	0.612	0.588	-0.024
100	166	0.614	0.588	-0.026
100	168	0.620	0.596	-0.024
100	169	0.618	0.586	-0.032
100	182	0.616	0.590	-0.026
100	183	0.612	0.588	-0.024
100	184	0.614	0.590	-0.024
100	185	0.616	0.592	-0.024
100	186	0.612	0.586	-0.026
100	187	0.616	0.590	-0.026
100	188	0.612	0.588	-0.024
100	189	0.614	0.588	-0.026
100	190	0.614	0.588	-0.026
105	191	0.612	0.612	0.000
105	192	0.612	0.610	-0.002
105	193	0.614	0.612	-0.002
105	194	0.612	0.612	0.000
105	195	0.614	0.612	-0.002
105	196	0.616	0.616	0.000
Max		0.620	0.618	0.002
Average		0.615	0.604	-0.011
Min		0.608	0.584	-0.032
Std Dev		0.002	0.011	0.011

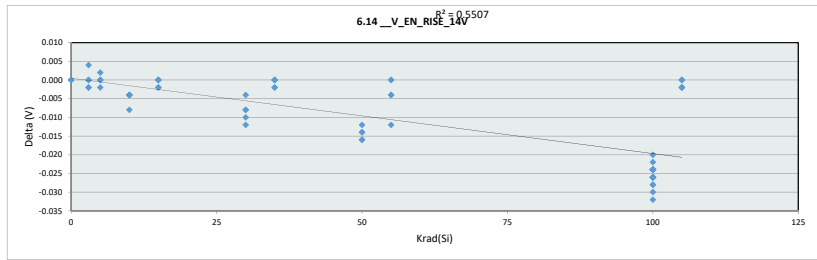


6.11_V_EN_RISE											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	0.65	0.57						
			V	0.65	0.57						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.610	0.614	0.606	0.612	0.604	0.614	0.600	0.600	0.584	0.610
Average	0.614	0.613	0.616	0.610	0.613	0.606	0.615	0.600	0.611	0.589	0.612
Max	0.618	0.618	0.618	0.612	0.614	0.608	0.616	0.602	0.614	0.596	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

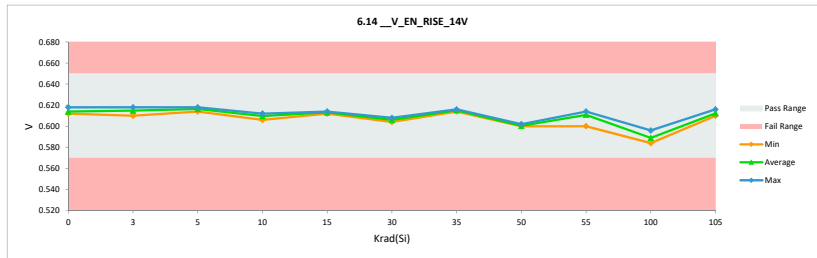


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6.14_V_EN_RISE_14V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			V	0.65	0.57
			V	0.65	0.57
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	0.612	0.612	0.000	
0	138	0.618	0.618	0.000	
0	139	0.612	0.612	0.000	
3	197	0.612	0.610	-0.002	
3	198	0.618	0.618	0.000	
3	199	0.618	0.616	-0.002	
3	202	0.614	0.614	0.000	
3	203	0.612	0.616	0.004	
5	204	0.618	0.616	-0.002	
5	205	0.614	0.614	0.000	
5	206	0.614	0.616	0.002	
5	208	0.618	0.618	0.000	
5	209	0.618	0.618	0.000	
10	210	0.616	0.612	-0.004	
10	211	0.610	0.606	-0.004	
10	212	0.616	0.612	-0.004	
10	213	0.614	0.610	-0.004	
10	214	0.616	0.608	-0.008	
10	215	0.612	0.612	0.000	
15	217	0.614	0.614	0.000	
15	219	0.614	0.612	-0.002	
15	220	0.614	0.614	0.000	
15	221	0.616	0.614	-0.002	
30	222	0.612	0.608	-0.004	
30	223	0.614	0.606	-0.008	
30	224	0.614	0.604	-0.010	
30	225	0.618	0.606	-0.012	
30	226	0.614	0.606	-0.008	
35	227	0.616	0.616	0.000	
35	228	0.616	0.614	-0.002	
35	229	0.618	0.616	-0.002	
35	230	0.614	0.614	0.000	
35	231	0.616	0.616	0.000	
50	170	0.614	0.602	-0.012	
50	172	0.614	0.600	-0.014	
50	173	0.614	0.600	-0.014	
50	175	0.616	0.600	-0.016	
50	176	0.616	0.600	-0.016	
55	177	0.618	0.614	-0.004	
55	178	0.612	0.600	-0.012	
55	179	0.616	0.612	-0.004	
55	180	0.614	0.614	0.000	
55	181	0.614	0.614	0.000	
100	154	0.616	0.592	-0.024	
100	155	0.612	0.586	-0.026	
100	156	0.618	0.592	-0.026	
100	157	0.614	0.588	-0.026	
100	158	0.616	0.586	-0.030	
100	159	0.616	0.588	-0.028	
100	160	0.612	0.590	-0.022	
100	161	0.608	0.584	-0.024	
100	162	0.612	0.588	-0.024	
100	163	0.614	0.590	-0.024	
100	164	0.612	0.592	-0.020	
100	165	0.612	0.588	-0.024	
100	166	0.614	0.588	-0.026	
100	168	0.620	0.596	-0.024	
100	169	0.618	0.586	-0.032	
100	182	0.616	0.590	-0.026	
100	183	0.612	0.588	-0.024	
100	184	0.614	0.590	-0.024	
100	185	0.616	0.592	-0.024	
100	186	0.612	0.586	-0.026	
100	187	0.616	0.590	-0.026	
100	188	0.616	0.588	-0.028	
100	189	0.614	0.588	-0.026	
100	190	0.614	0.590	-0.024	
105	191	0.612	0.612	0.000	
105	192	0.612	0.610	-0.002	
105	193	0.614	0.612	-0.002	
105	194	0.612	0.612	0.000	
105	195	0.614	0.612	-0.002	
105	196	0.616	0.616	0.000	
	Max	0.620	0.618	0.004	
	Average	0.615	0.604	-0.011	
	Min	0.608	0.584	-0.032	
	Std Dev	0.002	0.011	0.011	

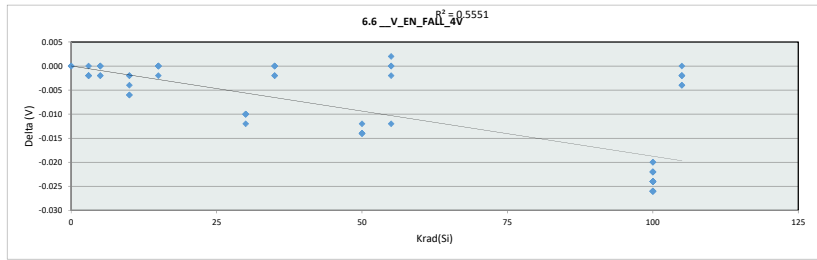


6.14_V_EN_RISE											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			0.65	0.57							
			V	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.610	0.614	0.606	0.612	0.604	0.614	0.600	0.600	0.584	0.610
Average	0.614	0.615	0.616	0.610	0.613	0.606	0.615	0.600	0.611	0.589	0.612
Max	0.618	0.618	0.618	0.612	0.614	0.608	0.616	0.602	0.614	0.596	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

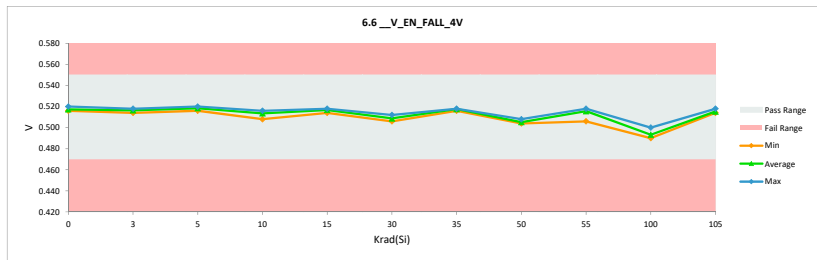


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		6.6_V_EN_FALL_4V		
Test Site				
Tester				
Test Number				
Unit		V	V	
Max Limit		0.55	0.55	
Min Limit		0.47	0.47	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.516	0.516	0.000
0	138	0.520	0.520	0.000
0	139	0.516	0.516	0.000
3	197	0.516	0.514	-0.002
3	198	0.518	0.518	0.000
3	199	0.520	0.518	-0.002
3	202	0.518	0.516	-0.002
3	203	0.518	0.516	-0.002
5	204	0.518	0.516	-0.002
5	205	0.518	0.516	-0.002
5	206	0.520	0.520	0.000
5	208	0.520	0.520	0.000
5	209	0.520	0.520	0.000
10	210	0.518	0.516	-0.002
10	211	0.512	0.508	-0.004
10	212	0.522	0.516	-0.006
10	213	0.516	0.514	-0.002
10	214	0.520	0.514	-0.006
15	215	0.518	0.518	0.000
15	217	0.518	0.518	0.000
15	219	0.516	0.514	-0.002
15	220	0.516	0.516	0.000
15	221	0.518	0.518	0.000
30	222	0.518	0.508	-0.010
30	223	0.518	0.508	-0.010
30	224	0.518	0.506	-0.012
30	225	0.522	0.512	-0.010
30	226	0.520	0.510	-0.010
35	227	0.518	0.518	0.000
35	228	0.518	0.516	-0.002
35	229	0.520	0.518	-0.002
35	230	0.518	0.518	0.000
35	231	0.518	0.518	0.000
50	170	0.518	0.504	-0.014
50	172	0.518	0.504	-0.014
50	173	0.518	0.504	-0.014
50	175	0.520	0.508	-0.012
50	176	0.520	0.506	-0.014
55	177	0.520	0.518	-0.002
55	178	0.518	0.506	-0.012
55	179	0.518	0.518	0.000
55	180	0.518	0.518	0.000
55	181	0.516	0.518	0.002
100	154	0.518	0.492	-0.026
100	155	0.516	0.494	-0.022
100	156	0.520	0.496	-0.024
100	157	0.518	0.492	-0.026
100	158	0.518	0.494	-0.024
100	159	0.520	0.496	-0.024
100	160	0.516	0.496	-0.020
100	161	0.514	0.490	-0.024
100	162	0.514	0.490	-0.024
100	163	0.520	0.494	-0.026
100	164	0.516	0.494	-0.022
100	165	0.514	0.492	-0.022
100	166	0.516	0.490	-0.026
100	168	0.524	0.500	-0.024
100	169	0.518	0.492	-0.026
100	182	0.518	0.492	-0.026
100	183	0.516	0.494	-0.022
100	184	0.516	0.494	-0.022
100	185	0.518	0.498	-0.020
100	186	0.516	0.490	-0.026
100	187	0.518	0.492	-0.026
100	188	0.518	0.494	-0.024
100	189	0.514	0.490	-0.024
100	190	0.516	0.492	-0.024
105	191	0.518	0.514	-0.004
105	192	0.518	0.514	-0.004
105	193	0.516	0.516	0.000
105	194	0.516	0.514	-0.002
105	195	0.518	0.516	-0.002
105	196	0.520	0.518	-0.002
	Max	0.524	0.520	0.002
	Average	0.518	0.507	-0.010
	Min	0.512	0.490	-0.026
	Std Dev	0.002	0.011	0.010

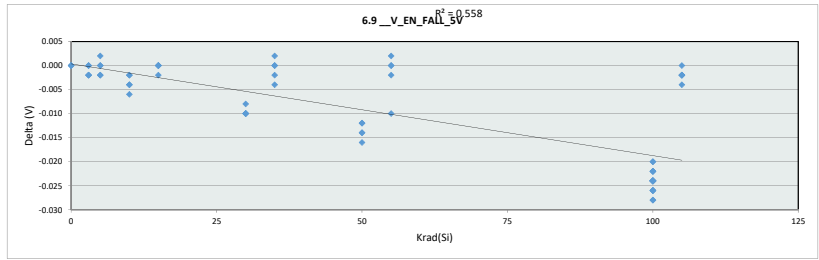


		6.6_V_EN_FALL											
Test Site													
Tester													
Test Number													
Max Limit		0.55	V										
Min Limit		0.47	V										
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105	
LL		0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	
Min		0.516	0.514	0.516	0.508	0.514	0.506	0.516	0.504	0.506	0.490	0.514	
Average		0.517	0.516	0.518	0.514	0.517	0.509	0.518	0.505	0.516	0.493	0.515	
Max		0.520	0.518	0.520	0.516	0.518	0.512	0.518	0.508	0.518	0.500	0.518	
UL		0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	

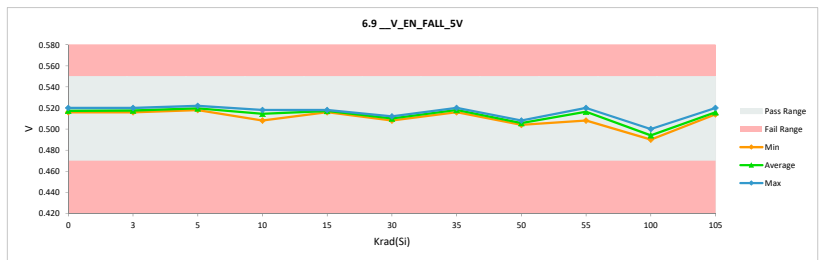


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6.9_V_EN_FALL_5V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			V	0.55	0.47
			V	0.55	0.47
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	0.516	0.516	0.000	
0	138	0.520	0.520	0.000	
0	139	0.516	0.516	0.000	
3	197	0.516	0.516	0.000	
3	198	0.520	0.520	0.000	
3	199	0.520	0.518	-0.002	
3	202	0.520	0.518	-0.002	
3	203	0.518	0.516	-0.002	
5	204	0.518	0.518	0.000	
5	205	0.520	0.518	-0.002	
5	206	0.520	0.520	0.000	
5	208	0.522	0.520	-0.002	
5	209	0.520	0.522	0.002	
10	210	0.518	0.516	-0.002	
10	211	0.512	0.508	-0.004	
10	212	0.522	0.518	-0.004	
10	213	0.518	0.516	-0.002	
10	214	0.520	0.514	-0.006	
10	215	0.518	0.518	0.000	
15	217	0.518	0.518	0.000	
15	219	0.518	0.516	-0.002	
15	220	0.516	0.516	0.000	
15	221	0.518	0.518	0.000	
30	222	0.518	0.508	-0.010	
30	223	0.518	0.508	-0.010	
30	224	0.518	0.510	-0.008	
30	225	0.522	0.512	-0.010	
30	226	0.522	0.512	-0.010	
35	227	0.518	0.520	0.002	
35	228	0.518	0.516	-0.002	
35	229	0.522	0.518	-0.004	
35	230	0.518	0.518	0.000	
35	231	0.518	0.518	0.000	
50	170	0.518	0.504	-0.014	
50	172	0.520	0.504	-0.016	
50	173	0.518	0.504	-0.014	
50	175	0.520	0.508	-0.012	
50	176	0.520	0.508	-0.012	
55	177	0.522	0.520	-0.002	
55	178	0.518	0.508	-0.010	
55	179	0.518	0.518	0.000	
55	180	0.518	0.518	0.000	
55	181	0.516	0.518	0.002	
100	154	0.518	0.494	-0.024	
100	155	0.516	0.494	-0.022	
100	156	0.520	0.496	-0.024	
100	157	0.518	0.494	-0.024	
100	158	0.518	0.494	-0.024	
100	159	0.522	0.496	-0.026	
100	160	0.516	0.496	-0.020	
100	161	0.514	0.490	-0.024	
100	162	0.514	0.490	-0.024	
100	163	0.520	0.496	-0.024	
100	164	0.516	0.494	-0.022	
100	165	0.518	0.496	-0.022	
100	166	0.518	0.492	-0.026	
100	168	0.526	0.500	-0.026	
100	169	0.520	0.492	-0.028	
100	182	0.520	0.492	-0.028	
100	183	0.518	0.494	-0.024	
100	184	0.516	0.494	-0.022	
100	185	0.518	0.498	-0.020	
100	186	0.516	0.492	-0.024	
100	187	0.520	0.494	-0.026	
100	188	0.518	0.494	-0.024	
100	189	0.516	0.492	-0.024	
100	190	0.516	0.492	-0.024	
105	191	0.518	0.516	-0.002	
105	192	0.518	0.514	-0.004	
105	193	0.516	0.516	0.000	
105	194	0.516	0.514	-0.002	
105	195	0.518	0.516	-0.002	
105	196	0.522	0.520	-0.002	
	Max	0.526	0.522	0.002	
	Average	0.518	0.508	-0.010	
	Min	0.512	0.490	-0.028	
	Std Dev	0.002	0.011	0.011	



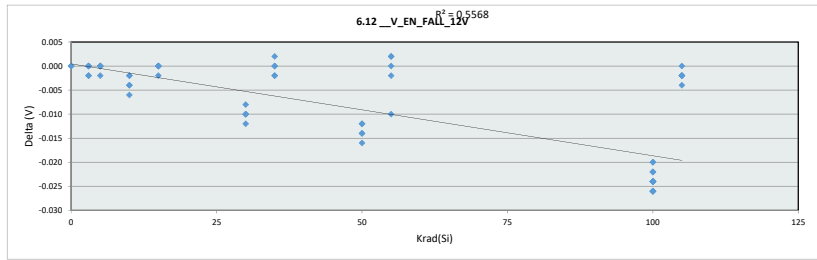
6.9_V_EN_FALL											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	0.55	0.47						
			V	0.55	0.47						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.516	0.516	0.518	0.508	0.516	0.508	0.516	0.504	0.508	0.490	0.514
Average	0.517	0.518	0.520	0.514	0.517	0.510	0.518	0.506	0.516	0.494	0.516
Max	0.520	0.520	0.522	0.518	0.518	0.512	0.520	0.508	0.520	0.500	0.520
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



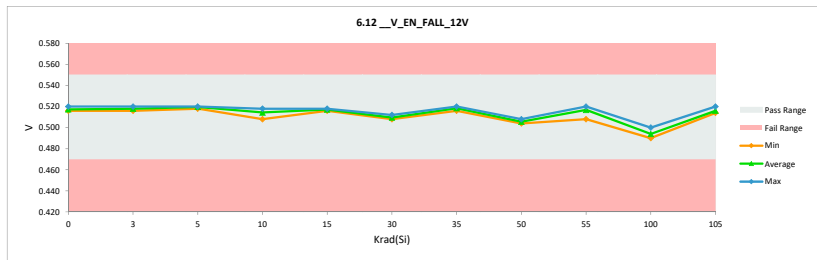


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6.12_V_EN_FALL_12V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			0.55	0.55
Min Limit			0.47	0.47
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.516	0.516	0.000
0	138	0.520	0.520	0.000
0	139	0.516	0.516	0.000
3	197	0.516	0.516	0.000
3	198	0.520	0.520	0.000
3	199	0.520	0.518	-0.002
3	202	0.520	0.518	-0.002
3	203	0.518	0.518	0.000
5	204	0.518	0.518	0.000
5	205	0.520	0.520	0.000
5	206	0.520	0.520	0.000
5	208	0.522	0.520	-0.002
5	209	0.520	0.520	0.000
10	210	0.518	0.516	-0.002
10	211	0.512	0.508	-0.004
10	212	0.522	0.518	-0.004
10	213	0.518	0.516	-0.002
10	214	0.520	0.514	-0.006
15	215	0.518	0.518	0.000
15	217	0.518	0.518	0.000
15	219	0.518	0.516	-0.002
15	220	0.516	0.516	0.000
15	221	0.518	0.518	0.000
30	222	0.518	0.508	-0.010
30	223	0.518	0.508	-0.010
30	224	0.518	0.510	-0.008
30	225	0.522	0.512	-0.010
30	226	0.522	0.510	-0.012
35	227	0.518	0.520	0.002
35	228	0.518	0.516	-0.002
35	229	0.522	0.520	-0.002
35	230	0.518	0.518	0.000
35	231	0.518	0.518	0.000
50	170	0.518	0.504	-0.014
50	172	0.520	0.504	-0.016
50	173	0.518	0.504	-0.014
50	175	0.520	0.508	-0.012
50	176	0.520	0.508	-0.012
55	177	0.522	0.520	-0.002
55	178	0.518	0.508	-0.010
55	179	0.518	0.518	0.000
55	180	0.518	0.520	0.002
55	181	0.516	0.518	0.002
100	154	0.518	0.494	-0.024
100	155	0.516	0.494	-0.022
100	156	0.520	0.496	-0.024
100	157	0.518	0.494	-0.024
100	158	0.518	0.494	-0.024
100	159	0.522	0.496	-0.026
100	160	0.516	0.496	-0.020
100	161	0.514	0.490	-0.024
100	162	0.514	0.490	-0.024
100	163	0.520	0.496	-0.024
100	164	0.516	0.494	-0.022
100	165	0.518	0.496	-0.022
100	166	0.518	0.492	-0.026
100	168	0.526	0.500	-0.026
100	169	0.518	0.492	-0.026
100	182	0.518	0.492	-0.026
100	183	0.518	0.494	-0.024
100	184	0.518	0.494	-0.024
100	185	0.518	0.498	-0.020
100	186	0.516	0.492	-0.024
100	187	0.520	0.494	-0.026
100	188	0.518	0.494	-0.024
100	189	0.516	0.492	-0.024
100	190	0.516	0.492	-0.024
105	191	0.518	0.516	-0.002
105	192	0.518	0.514	-0.004
105	193	0.516	0.516	0.000
105	194	0.516	0.514	-0.002
105	195	0.518	0.516	-0.002
105	196	0.522	0.520	-0.002
Max		0.526	0.520	0.002
Average		0.518	0.508	-0.010
Min		0.512	0.490	-0.026
Std Dev		0.002	0.011	0.011

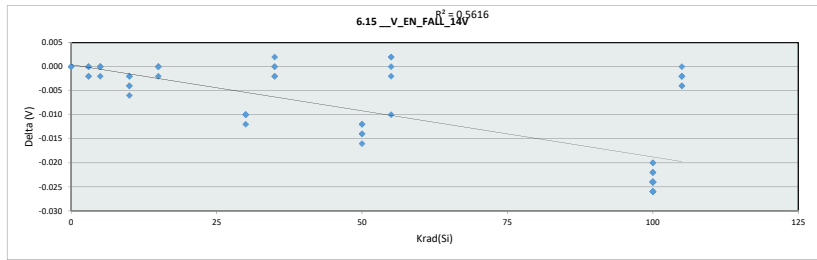


6.12_V_EN_FALL											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit			0.55	0.55							
Min Limit			0.47	0.47							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.516	0.516	0.518	0.508	0.516	0.508	0.516	0.504	0.508	0.490	0.514
Average	0.517	0.518	0.520	0.514	0.517	0.510	0.518	0.506	0.517	0.494	0.516
Max	0.520	0.520	0.520	0.518	0.518	0.512	0.520	0.508	0.520	0.500	0.520
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

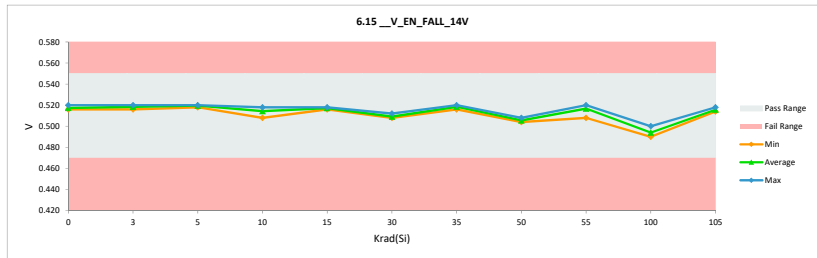


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6.15_V_EN_FALL_14V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			V	0.55	0.47
			V	0.55	0.47
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	0.516	0.516	0.000	
0	138	0.520	0.520	0.000	
0	139	0.516	0.516	0.000	
3	197	0.518	0.516	-0.002	
3	198	0.520	0.520	0.000	
3	199	0.520	0.518	-0.002	
3	202	0.520	0.520	0.000	
3	203	0.518	0.518	0.000	
5	204	0.518	0.518	0.000	
5	205	0.520	0.520	0.000	
5	206	0.520	0.520	0.000	
5	208	0.522	0.520	-0.002	
5	209	0.520	0.520	0.000	
10	210	0.518	0.516	-0.002	
10	211	0.512	0.508	-0.004	
10	212	0.522	0.518	-0.004	
10	213	0.518	0.516	-0.002	
10	214	0.520	0.514	-0.006	
15	215	0.518	0.518	0.000	
15	217	0.518	0.518	0.000	
15	219	0.518	0.516	-0.002	
15	220	0.516	0.516	0.000	
15	221	0.518	0.518	0.000	
30	222	0.518	0.508	-0.010	
30	223	0.518	0.508	-0.010	
30	224	0.518	0.508	-0.010	
30	225	0.522	0.512	-0.010	
30	226	0.522	0.510	-0.012	
35	227	0.518	0.520	0.002	
35	228	0.518	0.516	-0.002	
35	229	0.522	0.520	-0.002	
35	230	0.518	0.518	0.000	
35	231	0.518	0.518	0.000	
50	170	0.518	0.504	-0.014	
50	172	0.520	0.504	-0.016	
50	173	0.518	0.504	-0.014	
50	175	0.520	0.508	-0.012	
50	176	0.520	0.508	-0.012	
55	177	0.522	0.520	-0.002	
55	178	0.518	0.508	-0.010	
55	179	0.518	0.518	0.000	
55	180	0.518	0.520	0.002	
55	181	0.516	0.518	0.002	
100	154	0.518	0.494	-0.024	
100	155	0.516	0.494	-0.022	
100	156	0.520	0.496	-0.024	
100	157	0.518	0.494	-0.024	
100	158	0.518	0.494	-0.024	
100	159	0.522	0.496	-0.026	
100	160	0.516	0.496	-0.020	
100	161	0.514	0.490	-0.024	
100	162	0.514	0.490	-0.024	
100	163	0.520	0.496	-0.024	
100	164	0.516	0.494	-0.022	
100	165	0.518	0.496	-0.022	
100	166	0.518	0.492	-0.026	
100	168	0.526	0.500	-0.026	
100	169	0.518	0.492	-0.026	
100	182	0.518	0.492	-0.026	
100	183	0.518	0.494	-0.024	
100	184	0.518	0.494	-0.024	
100	185	0.518	0.498	-0.020	
100	186	0.516	0.492	-0.024	
100	187	0.520	0.494	-0.026	
100	188	0.518	0.494	-0.024	
100	189	0.516	0.492	-0.024	
100	190	0.518	0.492	-0.026	
105	191	0.518	0.516	-0.002	
105	192	0.518	0.514	-0.004	
105	193	0.516	0.516	0.000	
105	194	0.516	0.514	-0.002	
105	195	0.518	0.516	-0.002	
105	196	0.522	0.518	-0.004	
Max		0.526	0.520	0.002	
Average		0.518	0.508	-0.010	
Min		0.512	0.490	-0.026	
Std Dev		0.002	0.011	0.011	

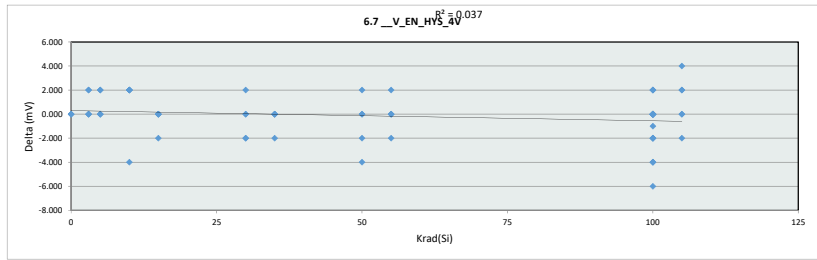


6.15_V_EN_FALL											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	0.55	0.47						
			V	0.55	0.47						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.516	0.516	0.518	0.508	0.516	0.508	0.516	0.504	0.508	0.490	0.514
Average	0.517	0.518	0.520	0.514	0.517	0.509	0.518	0.506	0.517	0.494	0.516
Max	0.520	0.520	0.520	0.518	0.518	0.512	0.520	0.508	0.520	0.500	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

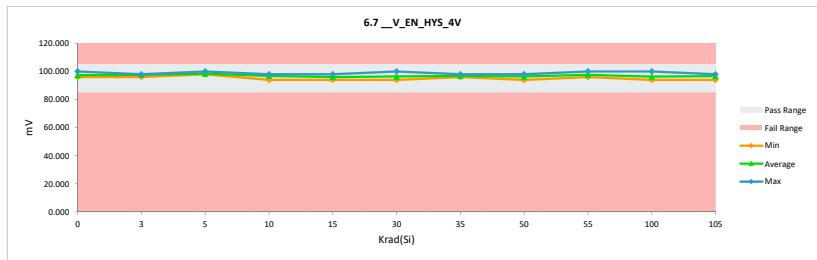


# HDR TID Report TPS7H5003-SP

6.7 __V_EN_HYS_4V				
Test Site	Tester	Test Number	Unit	
			mV	mV
Max Limit			105	105
Min Limit			85	85
Krad(Si)	Serial #	PRE	POST	Delta
0	137	100.000	100.000	0.000
0	138	96.000	96.000	0.000
0	139	96.000	96.000	0.000
3	197	94.000	96.000	2.000
3	198	98.000	98.000	0.000
3	199	98.000	98.000	0.000
3	202	96.000	98.000	2.000
3	203	98.000	98.000	0.000
5	204	98.000	98.000	0.000
5	205	96.000	98.000	2.000
5	206	98.000	100.000	2.000
5	208	98.000	98.000	0.000
5	209	98.000	98.000	0.000
10	210	98.000	94.000	-4.000
10	211	96.000	98.000	2.000
10	212	94.000	96.000	2.000
10	213	96.000	98.000	2.000
10	214	96.000	98.000	2.000
15	215	96.000	94.000	-2.000
15	217	96.000	96.000	0.000
15	219	98.000	98.000	0.000
15	220	96.000	96.000	0.000
15	221	96.000	96.000	0.000
30	222	98.000	100.000	2.000
30	223	96.000	96.000	0.000
30	224	96.000	96.000	0.000
30	225	96.000	94.000	-2.000
30	226	98.000	96.000	-2.000
35	227	98.000	98.000	0.000
35	228	96.000	96.000	0.000
35	229	98.000	98.000	0.000
35	230	96.000	96.000	0.000
35	231	98.000	96.000	-2.000
50	170	100.000	98.000	-2.000
50	172	98.000	94.000	-4.000
50	173	96.000	96.000	0.000
50	175	96.000	96.000	0.000
50	176	96.000	98.000	2.000
55	177	98.000	100.000	2.000
55	178	98.000	98.000	0.000
55	179	98.000	98.000	0.000
55	180	96.000	96.000	0.000
55	181	98.000	96.000	-2.000
100	154	98.000	100.000	2.000
100	155	96.000	96.000	0.000
100	156	98.000	96.000	-2.000
100	157	96.000	96.000	0.000
100	158	98.000	97.000	-1.000
100	159	98.000	96.000	-2.000
100	160	96.000	96.000	0.000
100	161	98.000	94.000	-4.000
100	162	98.000	98.000	0.000
100	163	94.000	96.000	2.000
100	164	96.000	96.000	0.000
100	165	98.000	98.000	0.000
100	166	98.000	98.000	0.000
100	168	96.000	96.000	0.000
100	169	98.000	98.000	0.000
100	182	98.000	98.000	0.000
100	183	100.000	94.000	-6.000
100	184	98.000	94.000	-4.000
100	185	96.000	94.000	-2.000
100	186	94.000	96.000	2.000
100	187	98.000	98.000	0.000
100	188	98.000	94.000	-4.000
100	189	98.000	98.000	0.000
100	190	98.000	96.000	-2.000
105	191	94.000	98.000	4.000
105	192	98.000	96.000	-2.000
105	193	96.000	96.000	0.000
105	194	96.000	98.000	2.000
105	195	94.000	94.000	0.000
105	196	96.000	98.000	2.000
Max		100.000	100.000	4.000
Average		96.959	96.781	-0.178
Min		94.000	94.000	-6.000
Std Dev		1.419	1.618	1.821

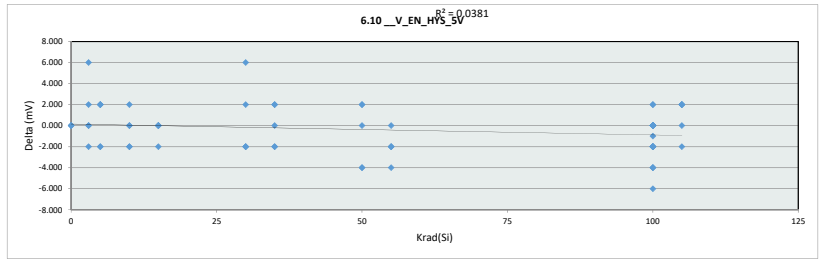


6.7 V EN HYS											
Test Site	Tester	Test Number	Unit								
			mV								
Max Limit			105	mV							
Min Limit			85	mV							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	96.000	96.000	98.000	94.000	94.000	94.000	96.000	94.000	96.000	94.000	94.000
Average	97.333	97.600	98.400	96.800	96.000	96.400	96.800	96.400	97.600	96.375	96.667
Max	100.000	98.000	100.000	98.000	98.000	100.000	98.000	98.000	100.000	100.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

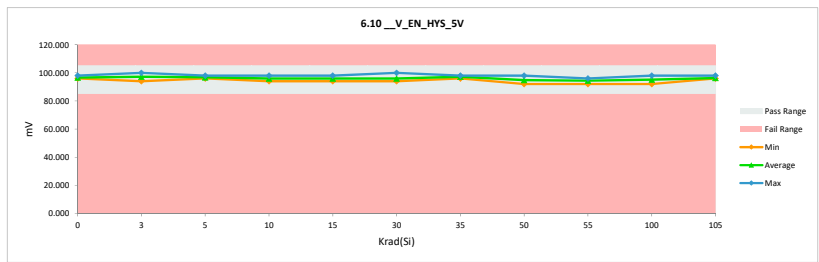


HDR TID Report  
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6.10_V_EN_HYS_5V				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	96.000	96.000	0.000
0	138	98.000	98.000	0.000
0	139	96.000	96.000	0.000
3	197	96.000	94.000	-2.000
3	198	98.000	98.000	0.000
3	199	98.000	98.000	0.000
3	202	94.000	96.000	2.000
3	203	94.000	100.000	6.000
5	204	100.000	98.000	-2.000
5	205	94.000	96.000	2.000
5	206	94.000	96.000	2.000
5	208	96.000	98.000	2.000
5	209	98.000	96.000	-2.000
10	210	98.000	96.000	-2.000
10	211	98.000	98.000	0.000
10	212	94.000	94.000	0.000
10	213	96.000	94.000	-2.000
10	214	96.000	98.000	2.000
15	215	94.000	94.000	0.000
15	217	96.000	96.000	0.000
15	219	96.000	96.000	0.000
15	220	98.000	98.000	0.000
15	221	98.000	96.000	-2.000
30	222	94.000	100.000	6.000
30	223	96.000	98.000	2.000
30	224	96.000	94.000	-2.000
30	225	96.000	94.000	-2.000
30	226	96.000	94.000	-2.000
35	227	98.000	96.000	-2.000
35	228	96.000	98.000	2.000
35	229	96.000	98.000	2.000
35	230	98.000	96.000	-2.000
35	231	98.000	98.000	0.000
50	170	96.000	98.000	2.000
50	172	94.000	96.000	2.000
50	173	96.000	96.000	0.000
50	175	96.000	92.000	-4.000
50	176	96.000	92.000	-4.000
55	177	96.000	94.000	-2.000
55	178	94.000	92.000	-2.000
55	179	98.000	94.000	-4.000
55	180	96.000	96.000	0.000
55	181	98.000	96.000	-2.000
100	154	98.000	98.000	0.000
100	155	96.000	92.000	-4.000
100	156	98.000	96.000	-2.000
100	157	96.000	94.000	-2.000
100	158	98.000	92.000	-6.000
100	159	94.000	94.000	0.000
100	160	96.000	94.000	-2.000
100	161	94.000	93.000	-1.000
100	162	98.000	98.000	0.000
100	163	94.000	94.000	0.000
100	164	96.000	96.000	0.000
100	165	94.000	92.000	-2.000
100	166	96.000	96.000	0.000
100	168	94.000	96.000	2.000
100	169	98.000	98.000	0.000
100	182	96.000	98.000	2.000
100	183	94.000	94.000	0.000
100	184	98.000	96.000	-2.000
100	185	98.000	94.000	-4.000
100	186	96.000	94.000	-2.000
100	187	96.000	96.000	0.000
100	188	98.000	94.000	-4.000
100	189	98.000	96.000	-2.000
100	190	98.000	98.000	0.000
105	191	94.000	96.000	2.000
105	192	94.000	96.000	2.000
105	193	98.000	96.000	-2.000
105	194	96.000	98.000	2.000
105	195	96.000	96.000	0.000
105	196	94.000	96.000	2.000
Max		100.000	100.000	6.000
Average		96.219	95.795	-0.425
Min		94.000	92.000	-6.000
Std Dev		1.583	1.965	2.185

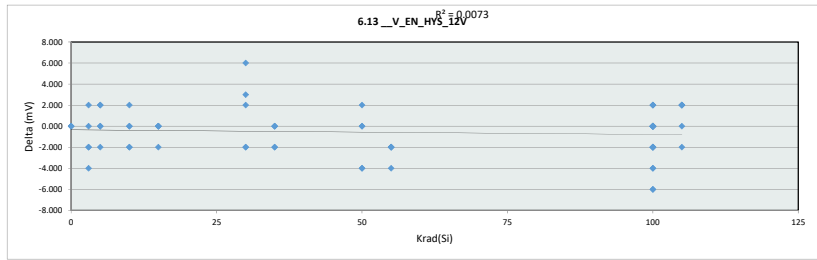


6.10_V_EN_HYS											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	96.000	94.000	96.000	94.000	94.000	94.000	96.000	92.000	92.000	92.000	96.000
Average	96.667	97.200	96.800	96.000	96.000	96.000	97.200	94.800	94.400	95.125	96.333
Max	98.000	100.000	98.000	98.000	98.000	100.000	98.000	98.000	96.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

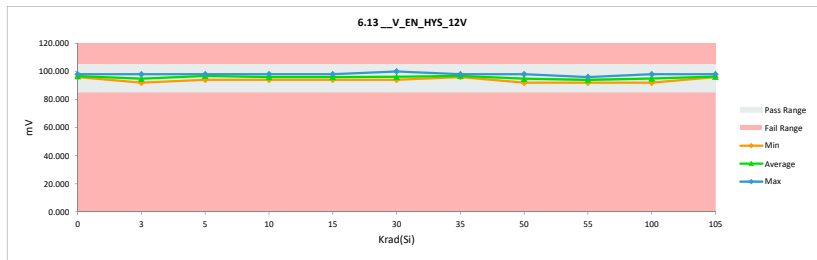


# HDR TID Report TPS7H5003-SP

6.13_V_EN_HYS_12V				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	96.000	96.000	0.000
0	138	98.000	98.000	0.000
0	139	96.000	96.000	0.000
3	197	96.000	94.000	-2.000
3	198	98.000	98.000	0.000
3	199	98.000	94.000	-4.000
3	202	94.000	96.000	2.000
3	203	94.000	92.000	-2.000
5	204	100.000	98.000	-2.000
5	205	94.000	94.000	0.000
5	206	94.000	96.000	2.000
5	208	96.000	98.000	2.000
5	209	98.000	98.000	0.000
10	210	98.000	96.000	-2.000
10	211	98.000	98.000	0.000
10	212	94.000	94.000	0.000
10	213	96.000	94.000	-2.000
10	214	96.000	98.000	2.000
15	215	94.000	94.000	0.000
15	217	96.000	96.000	0.000
15	219	96.000	96.000	0.000
15	220	98.000	98.000	0.000
15	221	98.000	96.000	-2.000
30	222	94.000	100.000	6.000
30	223	96.000	98.000	2.000
30	224	96.000	94.000	-2.000
30	225	96.000	94.000	-2.000
30	226	92.000	95.000	3.000
35	227	98.000	96.000	-2.000
35	228	98.000	98.000	0.000
35	229	96.000	96.000	0.000
35	230	98.000	96.000	-2.000
35	231	98.000	98.000	0.000
50	170	98.000	98.000	0.000
50	172	94.000	96.000	2.000
50	173	96.000	96.000	0.000
50	175	96.000	92.000	-4.000
50	176	96.000	92.000	-4.000
55	177	96.000	94.000	-2.000
55	178	94.000	92.000	-2.000
55	179	98.000	94.000	-4.000
55	180	96.000	94.000	-2.000
55	181	98.000	96.000	-2.000
100	154	98.000	98.000	0.000
100	155	96.000	92.000	-4.000
100	156	94.000	96.000	2.000
100	157	96.000	94.000	-2.000
100	158	98.000	92.000	-6.000
100	159	94.000	94.000	0.000
100	160	96.000	94.000	-2.000
100	161	94.000	94.000	0.000
100	162	98.000	98.000	0.000
100	163	94.000	94.000	0.000
100	164	96.000	98.000	2.000
100	165	94.000	92.000	-2.000
100	166	96.000	96.000	0.000
100	168	94.000	96.000	2.000
100	169	100.000	94.000	-6.000
100	182	98.000	98.000	0.000
100	183	94.000	94.000	0.000
100	184	96.000	96.000	0.000
100	185	98.000	94.000	-4.000
100	186	96.000	94.000	-2.000
100	187	96.000	96.000	0.000
100	188	94.000	94.000	0.000
100	189	98.000	96.000	-2.000
100	190	98.000	96.000	-2.000
105	191	94.000	96.000	2.000
105	192	94.000	96.000	2.000
105	193	98.000	96.000	-2.000
105	194	96.000	98.000	2.000
105	195	96.000	96.000	0.000
105	196	94.000	96.000	2.000
Max		100.000	100.000	6.000
Average		96.137	95.548	-0.589
Min		92.000	92.000	-6.000
Std Dev		1.743	1.922	2.120

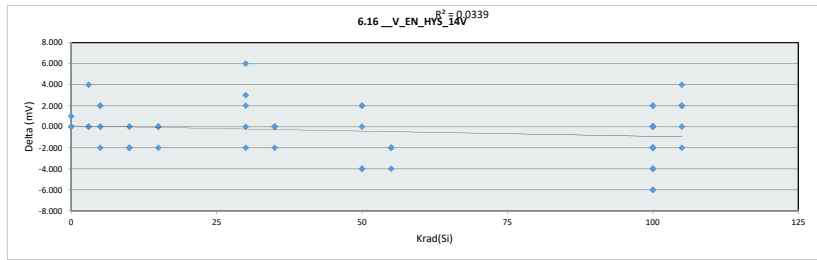


6.13_V_EN_HYS											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	96.000	92.000	94.000	94.000	94.000	94.000	96.000	92.000	92.000	92.000	96.000
Average	96.667	94.800	96.800	96.000	96.000	96.200	96.800	94.800	94.000	95.000	96.333
Max	98.000	98.000	98.000	98.000	98.000	100.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

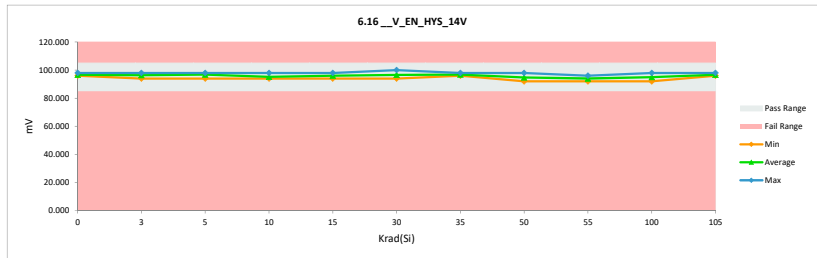


HDR TID Report  
TPS7H5003-SP

6.16_V_EN_HYS_14V				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	96.000	96.000	0.000
0	138	97.000	98.000	1.000
0	139	96.000	96.000	0.000
3	197	94.000	94.000	0.000
3	198	98.000	98.000	0.000
3	199	98.000	98.000	0.000
3	202	94.000	94.000	0.000
3	203	94.000	98.000	4.000
5	204	100.000	98.000	-2.000
5	205	94.000	94.000	0.000
5	206	94.000	96.000	2.000
5	208	96.000	98.000	2.000
5	209	98.000	98.000	0.000
10	210	98.000	96.000	-2.000
10	211	98.000	98.000	0.000
10	212	94.000	94.000	0.000
10	213	96.000	94.000	-2.000
10	214	96.000	94.000	-2.000
15	215	94.000	94.000	0.000
15	217	96.000	96.000	0.000
15	219	96.000	96.000	0.000
15	220	98.000	98.000	0.000
15	221	98.000	96.000	-2.000
30	222	94.000	100.000	6.000
30	223	96.000	98.000	2.000
30	224	96.000	96.000	0.000
30	225	96.000	94.000	-2.000
30	226	92.000	95.000	3.000
35	227	98.000	96.000	-2.000
35	228	98.000	98.000	0.000
35	229	96.000	96.000	0.000
35	230	96.000	96.000	0.000
35	231	98.000	98.000	0.000
50	170	96.000	98.000	2.000
50	172	94.000	96.000	2.000
50	173	96.000	96.000	0.000
50	175	96.000	92.000	-4.000
50	176	96.000	92.000	-4.000
55	177	96.000	94.000	-2.000
55	178	94.000	92.000	-2.000
55	179	98.000	94.000	-4.000
55	180	96.000	94.000	-2.000
55	181	98.000	96.000	-2.000
100	154	98.000	98.000	0.000
100	155	96.000	92.000	-4.000
100	156	98.000	96.000	-2.000
100	157	96.000	94.000	-2.000
100	158	98.000	92.000	-6.000
100	159	94.000	92.000	-2.000
100	160	96.000	94.000	-2.000
100	161	94.000	94.000	0.000
100	162	98.000	98.000	0.000
100	163	94.000	94.000	0.000
100	164	96.000	98.000	2.000
100	165	94.000	92.000	-2.000
100	166	96.000	96.000	0.000
100	168	94.000	96.000	2.000
100	169	100.000	94.000	-6.000
100	182	98.000	98.000	0.000
100	183	94.000	94.000	0.000
100	184	96.000	96.000	0.000
100	185	98.000	94.000	-4.000
100	186	96.000	94.000	-2.000
100	187	96.000	96.000	0.000
100	188	98.000	94.000	-4.000
100	189	98.000	96.000	-2.000
100	190	96.000	98.000	2.000
105	191	94.000	96.000	2.000
105	192	94.000	96.000	2.000
105	193	98.000	96.000	-2.000
105	194	96.000	98.000	2.000
105	195	96.000	96.000	0.000
105	196	94.000	98.000	4.000
Max		100.000	100.000	6.000
Average		96.123	95.658	-0.466
Min		92.000	92.000	-6.000
Std Dev		1.699	1.974	2.205

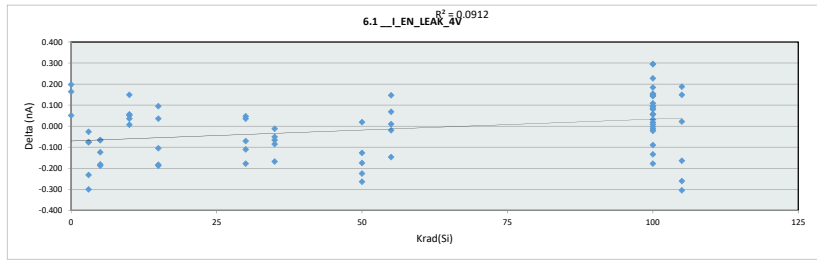


6.16_V_EN_HYS											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	96.000	94.000	94.000	94.000	94.000	94.000	96.000	92.000	92.000	92.000	96.000
Average	96.667	96.400	96.800	95.200	96.000	96.600	96.800	94.800	94.000	95.000	96.667
Max	98.000	98.000	98.000	98.000	98.000	100.000	98.000	98.000	96.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

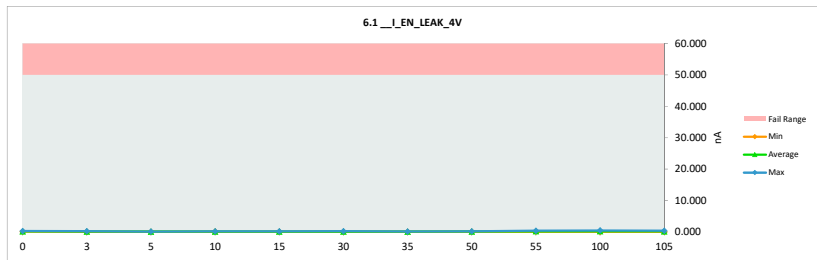


# HDR TID Report TPS7H5003-SP

6.1_I_EN_LEAK_4V				
Test Site				
Tester				
Test Number				
Unit	nA	nA		
Max Limit	50	50		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.095	0.293	0.198
0	138	0.027	0.078	0.051
0	139	0.002	0.166	0.164
3	197	0.303	0.227	-0.076
3	198	0.244	0.012	-0.232
3	199	0.478	0.178	-0.300
3	202	0.185	0.110	-0.075
3	203	0.117	0.090	-0.027
5	204	0.200	0.012	-0.188
5	205	0.229	0.105	-0.124
5	206	0.132	0.066	-0.066
5	208	0.112	0.046	-0.066
5	209	0.219	0.037	-0.182
10	210	0.132	0.139	0.007
10	211	0.005	0.154	0.149
10	212	0.132	0.168	0.036
10	213	0.083	0.134	0.051
10	214	0.044	0.100	0.056
15	215	0.073	0.168	0.095
15	217	0.239	0.134	-0.105
15	219	0.054	0.090	0.036
15	220	0.317	0.134	-0.183
15	221	0.288	0.100	-0.188
30	222	0.180	0.002	-0.178
30	223	0.278	0.168	-0.110
30	224	0.180	0.227	0.047
30	225	0.054	0.090	0.036
30	226	0.093	0.022	-0.071
35	227	0.112	0.061	-0.051
35	228	0.083	0.017	-0.066
35	229	0.229	0.144	-0.085
35	230	0.083	0.071	-0.012
35	231	0.219	0.051	-0.168
50	170	0.342	0.117	-0.225
50	172	0.312	0.137	-0.175
50	173	0.459	0.195	-0.264
50	175	0.166	0.185	0.019
50	176	0.225	0.098	-0.127
55	177	0.137	0.117	-0.020
55	178	0.332	0.185	-0.147
55	179	0.234	0.244	0.010
55	180	0.146	0.293	0.147
55	181	0.303	0.371	0.068
100	154	0.083	0.227	0.144
100	155	0.102	0.286	0.184
100	156	0.024	0.120	0.096
100	157	0.073	0.081	0.008
100	158	0.044	0.022	-0.022
100	159	0.034	0.129	0.095
100	160	0.102	0.100	-0.002
100	161	0.142	0.129	-0.013
100	162	0.083	0.237	0.154
100	163	0.220	0.276	0.056
100	164	0.239	0.149	-0.090
100	165	0.063	0.081	0.018
100	166	0.005	0.090	0.085
100	168	0.083	0.139	0.056
100	169	0.132	0.276	0.144
100	182	0.088	0.120	0.032
100	183	0.059	0.354	0.295
100	184	0.166	0.461	0.295
100	185	0.185	0.334	0.149
100	186	0.195	0.017	-0.178
100	187	0.293	0.159	-0.134
100	188	0.225	0.305	0.080
100	189	0.117	0.344	0.227
100	190	0.342	0.451	0.109
105	191	0.117	0.139	0.022
105	192	0.303	0.139	-0.164
105	193	0.185	0.373	0.188
105	194	0.293	0.032	-0.261
105	195	0.371	0.066	-0.305
105	196	0.107	0.256	0.149
Max		0.478	0.461	0.295
Average		0.169	0.157	-0.013
Min		0.002	0.002	-0.305
Std Dev		0.108	0.106	0.141

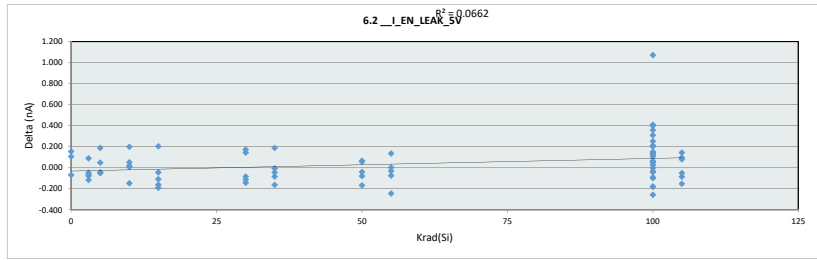


6.1_I_EN_LEAK											
Test Site											
Tester											
Test Number											
Max Limit	50	nA									
Min Limit		nA									
LL											
Min	0.078	0.012	0.012	0.100	0.090	0.002	0.017	0.098	0.117	0.017	0.032
Average	0.179	0.123	0.053	0.139	0.125	0.102	0.069	0.146	0.242	0.204	0.168
Max	0.293	0.227	0.105	0.168	0.168	0.227	0.144	0.195	0.371	0.461	0.373
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000

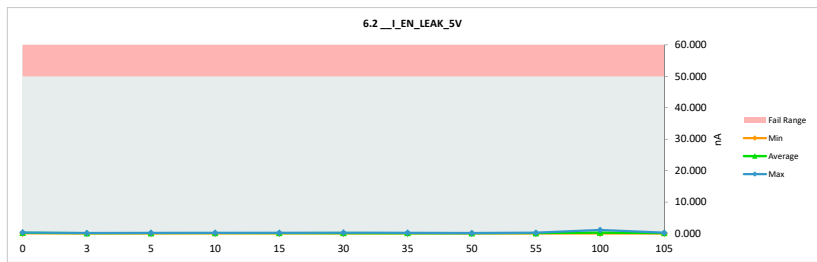


HDR TID Report  
TPS7H5003-SP

6.2_I_EN_LEAK_5V				
Test Site				
Tester				
Test Number				
Unit	nA	nA		
Max Limit	50	50		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.137	0.242	0.105
0	138	0.107	0.037	-0.070
0	139	0.156	0.310	0.154
3	197	0.205	0.137	-0.068
3	198	0.029	0.117	0.088
3	199	0.127	0.078	-0.049
3	202	0.146	0.068	-0.078
3	203	0.146	0.029	-0.117
5	204	0.046	0.000	-0.046
5	205	0.066	0.020	-0.046
5	206	0.090	0.137	0.047
5	208	0.110	0.059	-0.051
5	209	0.046	0.234	0.188
10	210	0.061	0.078	0.017
10	211	0.149	0.156	0.007
10	212	0.168	0.029	-0.148
10	213	0.007	0.205	0.198
10	214	0.144	0.195	0.051
15	215	0.012	0.215	0.203
15	217	0.241	0.078	-0.163
15	219	0.212	0.166	-0.046
15	220	0.168	0.059	-0.109
15	221	0.241	0.049	-0.192
30	222	0.193	0.049	-0.144
30	223	0.134	0.049	-0.085
30	224	0.202	0.088	-0.114
30	225	0.100	0.244	0.144
30	226	0.100	0.273	0.173
35	227	0.193	0.029	-0.164
35	228	0.027	0.215	0.188
35	229	0.124	0.117	-0.007
35	230	0.202	0.156	-0.046
35	231	0.212	0.127	-0.085
50	170	0.185	0.017	-0.168
50	172	0.068	0.027	-0.041
50	173	0.010	0.076	0.066
50	175	0.020	0.076	0.056
50	176	0.205	0.124	-0.081
55	177	0.039	0.007	-0.032
55	178	0.107	0.242	0.135
55	179	0.146	0.071	-0.075
55	180	0.010	0.012	0.002
55	181	0.312	0.066	-0.246
100	154	0.012	0.320	0.308
100	155	0.198	0.017	-0.181
100	156	0.115	0.251	0.136
100	157	0.237	0.300	0.063
100	158	0.266	0.222	-0.044
100	159	0.315	0.134	-0.181
100	160	0.061	0.212	0.151
100	161	0.100	0.007	-0.093
100	162	0.256	0.612	0.356
100	163	0.022	0.163	0.141
100	164	0.276	0.017	-0.259
100	165	0.061	0.271	0.210
100	166	0.139	0.163	0.024
100	168	0.256	0.378	0.122
100	169	0.198	0.163	-0.035
100	182	0.039	1.110	1.071
100	183	0.156	0.203	0.047
100	184	0.098	0.300	0.202
100	185	0.156	0.261	0.105
100	186	0.000	0.251	0.251
100	187	0.156	0.056	-0.100
100	188	0.068	0.476	0.408
100	189	0.098	0.095	-0.003
100	190	0.146	0.544	0.398
105	191	0.137	0.051	-0.086
105	192	0.068	0.017	-0.051
105	193	0.010	0.090	0.080
105	194	0.088	0.232	0.144
105	195	0.146	0.242	0.096
105	196	0.166	0.012	-0.154
Max		0.315	1.110	1.071
Average		0.129	0.163	0.034
Min		0.000	0.000	-0.259
Std Dev		0.080	0.169	0.193



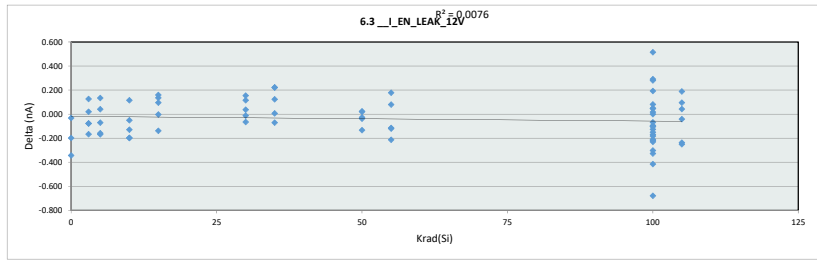
6.2_I_EN_LEAK											
Test Site											
Tester											
Test Number											
Max Limit	50	nA									
Min Limit		nA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.037	0.029	0.000	0.020	0.049	0.049	0.029	0.017	0.007	0.007	0.012
Min	0.196	0.086	0.090	0.131	0.113	0.141	0.129	0.064	0.080	0.272	0.107
Average	0.310	0.137	0.234	0.205	0.215	0.273	0.215	0.124	0.242	1.110	0.242
Max	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



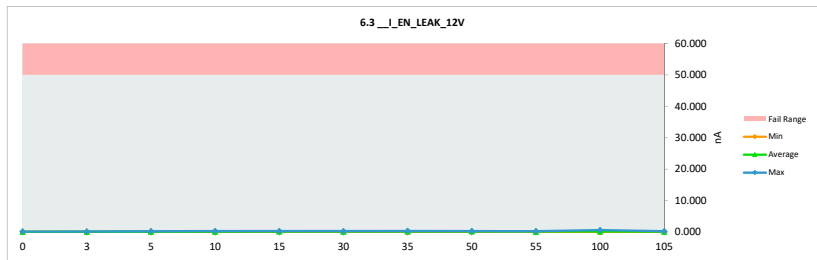


# HDR TID Report TPS7H5003-SP

6.3 I EN LEAK 12V				
Test Site	Tester	Test Number	Unit	Max Limit
			nA	nA
			50	50
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.049	0.017	-0.032
0	138	0.303	0.105	-0.198
0	139	0.390	0.046	-0.344
3	197	0.107	0.029	-0.078
3	198	0.107	0.029	-0.078
3	199	0.020	0.146	0.126
3	202	0.059	0.078	0.019
3	203	0.244	0.078	-0.166
5	204	0.051	0.185	0.134
5	205	0.236	0.068	-0.168
5	206	0.295	0.224	-0.071
5	208	0.188	0.029	-0.159
5	209	0.066	0.107	0.041
10	210	0.207	0.010	-0.197
10	211	0.158	0.273	0.115
10	212	0.276	0.078	-0.198
10	213	0.178	0.127	-0.051
10	214	0.188	0.059	-0.129
15	215	0.207	0.068	-0.139
15	217	0.017	0.176	0.159
15	219	0.168	0.303	0.135
15	220	0.022	0.117	0.095
15	221	0.227	0.224	-0.003
30	222	0.022	0.010	-0.012
30	223	0.115	0.049	-0.066
30	224	0.080	0.234	0.154
30	225	0.051	0.166	0.115
30	226	0.197	0.234	0.037
35	227	0.139	0.068	-0.071
35	228	0.022	0.244	0.222
35	229	0.110	0.332	0.222
35	230	0.022	0.029	0.007
35	231	0.032	0.156	0.124
50	170	0.303	0.276	-0.027
50	172	0.225	0.246	0.021
50	173	0.361	0.227	-0.134
50	175	0.254	0.217	-0.037
50	176	0.049	0.071	0.022
55	177	0.010	0.090	0.080
55	178	0.156	0.041	-0.115
55	179	0.127	0.007	-0.120
55	180	0.059	0.237	0.178
55	181	0.264	0.051	-0.213
100	154	0.315	0.212	-0.103
100	155	0.305	0.085	-0.220
100	156	0.334	0.032	-0.302
100	157	0.227	0.056	-0.171
100	158	0.100	0.032	-0.068
100	159	0.559	0.144	-0.415
100	160	0.110	0.007	-0.103
100	161	0.364	0.037	-0.327
100	162	0.373	0.163	-0.210
100	163	0.110	0.159	0.049
100	164	0.149	0.149	0.000
100	165	0.393	0.242	-0.151
100	166	0.725	0.046	-0.679
100	168	0.354	0.124	-0.230
100	169	0.305	0.178	-0.127
100	182	0.078	0.593	0.515
100	183	0.332	0.378	0.046
100	184	0.146	0.163	0.017
100	185	0.068	0.149	0.081
100	186	0.205	0.110	-0.095
100	187	0.088	0.281	0.193
100	188	0.088	0.368	0.280
100	189	0.195	0.012	-0.183
100	190	0.156	0.447	0.291
105	191	0.371	0.120	-0.251
105	192	0.185	0.227	0.042
105	193	0.283	0.046	-0.237
105	194	0.010	0.198	0.188
105	195	0.059	0.154	0.095
105	196	0.185	0.144	-0.041
Max		0.725	0.593	0.515
Average		0.185	0.145	-0.040
Min		0.010	0.007	-0.679
Std Dev		0.136	0.114	0.183

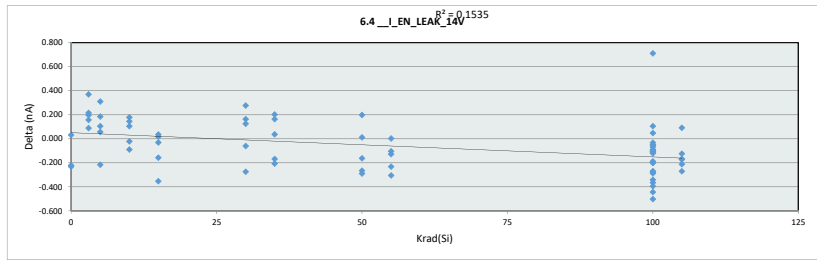


6.3 I EN LEAK																	
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit	LL	0	3	5	10	15	30	35	50	55	100	105
			nA	nA			0.017	0.029	0.029	0.010	0.068	0.010	0.029	0.071	0.007	0.007	0.046
						Min	0.056	0.072	0.123	0.109	0.178	0.139	0.166	0.207	0.085	0.174	0.148
						Average	0.105	0.146	0.224	0.273	0.303	0.234	0.332	0.276	0.237	0.593	0.227
						Max	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000
						UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000

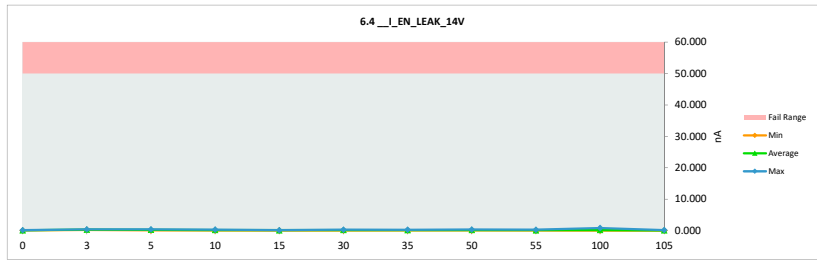


# HDR TID Report TPS7H5003-SP

6.4_I_EN_LEAK_14V				
Test Site				
Tester				
Test Number				
Unit	nA	nA		
Max Limit	50	50		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.088	0.120	0.032
0	138	0.254	0.032	-0.222
0	139	0.322	0.090	-0.232
3	197	0.156	0.244	0.088
3	198	0.107	0.303	0.196
3	199	0.176	0.332	0.156
3	202	0.000	0.215	0.215
3	203	0.020	0.390	0.370
5	204	0.090	0.400	0.310
5	205	0.158	0.342	0.184
5	206	0.266	0.049	-0.217
5	208	0.090	0.195	0.105
5	209	0.012	0.068	0.056
10	210	0.207	0.312	0.105
10	211	0.305	0.215	-0.090
10	212	0.032	0.176	0.144
10	213	0.061	0.039	-0.022
10	214	0.037	0.215	0.178
15	215	0.110	0.146	0.036
15	217	0.051	0.020	-0.031
15	219	0.207	0.049	-0.158
15	220	0.071	0.088	0.017
15	221	0.383	0.029	-0.354
30	222	0.110	0.234	0.124
30	223	0.007	0.283	0.276
30	224	0.256	0.195	-0.061
30	225	0.324	0.049	-0.275
30	226	0.032	0.195	0.163
35	227	0.246	0.039	-0.207
35	228	0.129	0.166	0.037
35	229	0.188	0.020	-0.168
35	230	0.032	0.234	0.202
35	231	0.110	0.273	0.163
50	170	0.156	0.354	0.198
50	172	0.390	0.227	-0.163
50	173	0.322	0.032	-0.290
50	175	0.322	0.056	-0.266
50	176	0.127	0.139	0.012
55	177	0.293	0.295	0.002
55	178	0.156	0.027	-0.129
55	179	0.283	0.051	-0.232
55	180	0.332	0.027	-0.305
55	181	0.195	0.090	-0.105
100	154	0.354	0.154	-0.200
100	155	0.217	0.149	-0.068
100	156	0.451	0.110	-0.341
100	157	0.549	0.046	-0.503
100	158	0.334	0.237	-0.097
100	159	0.471	0.027	-0.444
100	160	0.286	0.173	-0.113
100	161	0.354	0.076	-0.278
100	162	0.286	0.183	-0.103
100	163	0.539	0.251	-0.288
100	164	0.246	0.046	-0.200
100	165	0.481	0.115	-0.366
100	166	0.364	0.095	-0.269
100	168	0.217	0.095	-0.122
100	169	0.451	0.056	-0.395
100	182	0.107	0.817	0.710
100	183	0.166	0.115	-0.051
100	184	0.244	0.212	-0.032
100	185	0.068	0.017	-0.051
100	186	0.264	0.076	-0.188
100	187	0.283	0.193	-0.090
100	188	0.068	0.115	0.047
100	189	0.234	0.129	-0.105
100	190	0.185	0.290	0.105
105	191	0.254	0.081	-0.173
105	192	0.068	0.159	0.091
105	193	0.176	0.007	-0.169
105	194	0.332	0.061	-0.271
105	195	0.293	0.081	-0.212
105	196	0.185	0.061	-0.124
Max		0.549	0.817	0.710
Average		0.216	0.155	-0.061
Min		0.000	0.007	-0.503
Std Dev		0.134	0.129	0.212

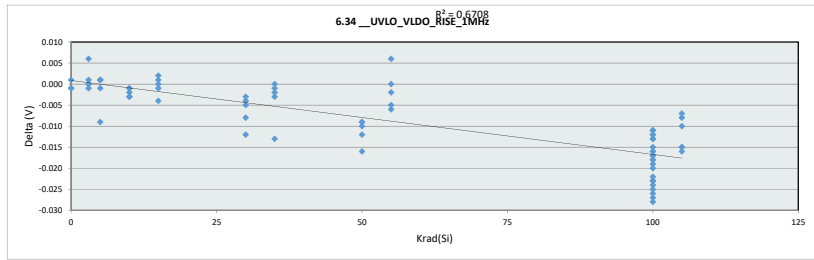


6.4_I_EN_LEAK											
Test Site											
Tester											
Test Number											
Max Limit	50	nA									
Min Limit		nA									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	0.032	0.215	0.049	0.039	0.020	0.049	0.020	0.032	0.027	0.017	0.007
Average	0.081	0.297	0.211	0.191	0.066	0.191	0.146	0.162	0.098	0.157	0.075
Max	0.120	0.390	0.400	0.312	0.146	0.283	0.273	0.354	0.295	0.817	0.159
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000

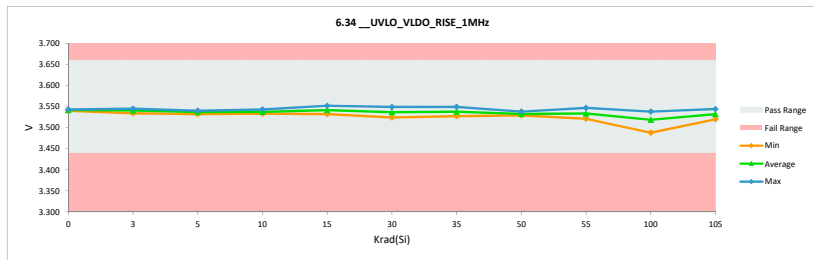


HDR TID Report  
TPS7H5003-SP

6.34 UVLO_VLDO_RISE_1MHZ				
Test Site	Tester	Test Number	Unit	Max Limit
			V	3.66
			V	3.44
Krad(Si)	Serial #	PRE	POST	Delta
0	137	3.542	3.543	0.001
0	138	3.541	3.540	-0.001
0	139	3.543	3.542	-0.001
3	197	3.534	3.534	0.000
3	198	3.544	3.543	-0.001
3	199	3.543	3.544	0.001
3	202	3.539	3.545	0.006
3	203	3.538	3.538	0.000
5	204	3.539	3.538	-0.001
5	205	3.535	3.536	0.001
5	206	3.531	3.532	0.001
5	208	3.538	3.539	0.001
5	209	3.549	3.540	-0.009
10	210	3.544	3.543	-0.001
10	211	3.535	3.534	-0.001
10	212	3.546	3.543	-0.003
10	213	3.535	3.533	-0.002
10	214	3.536	3.533	-0.003
15	215	3.544	3.546	0.002
15	217	3.551	3.552	0.001
15	219	3.547	3.543	-0.004
15	220	3.533	3.532	-0.001
15	221	3.534	3.534	0.000
30	222	3.536	3.524	-0.012
30	223	3.536	3.533	-0.003
30	224	3.542	3.534	-0.008
30	225	3.547	3.543	-0.004
30	226	3.554	3.549	-0.005
35	227	3.542	3.529	-0.013
35	228	3.552	3.549	-0.003
35	229	3.549	3.547	-0.002
35	230	3.527	3.527	0.000
35	231	3.537	3.536	-0.001
50	170	3.538	3.529	-0.009
50	172	3.540	3.530	-0.010
50	173	3.547	3.535	-0.012
50	175	3.547	3.538	-0.009
50	176	3.546	3.530	-0.016
55	177	3.543	3.543	0.000
55	178	3.523	3.529	0.006
55	179	3.534	3.528	-0.006
55	180	3.526	3.521	-0.005
55	181	3.549	3.547	-0.002
100	154	3.548	3.535	-0.013
100	155	3.536	3.519	-0.017
100	156	3.543	3.531	-0.012
100	157	3.545	3.517	-0.028
100	158	3.527	3.516	-0.011
100	159	3.526	3.509	-0.017
100	160	3.528	3.512	-0.016
100	161	3.537	3.526	-0.011
100	162	3.542	3.519	-0.023
100	163	3.543	3.517	-0.026
100	164	3.535	3.517	-0.018
100	165	3.534	3.510	-0.024
100	166	3.527	3.509	-0.018
100	168	3.557	3.538	-0.019
100	169	3.546	3.526	-0.020
100	182	3.541	3.519	-0.022
100	183	3.535	3.523	-0.012
100	184	3.530	3.517	-0.013
100	185	3.515	3.488	-0.027
100	186	3.535	3.512	-0.023
100	187	3.550	3.527	-0.023
100	188	3.531	3.506	-0.025
100	189	3.538	3.523	-0.015
100	190	3.548	3.532	-0.016
105	191	3.551	3.543	-0.008
105	192	3.539	3.532	-0.007
105	193	3.537	3.521	-0.016
105	194	3.535	3.520	-0.015
105	195	3.547	3.532	-0.015
105	196	3.554	3.544	-0.010
Max		3.557	3.552	0.006
Average		3.540	3.531	-0.009
Min		3.515	3.488	-0.028
Std Dev		0.008	0.012	0.009

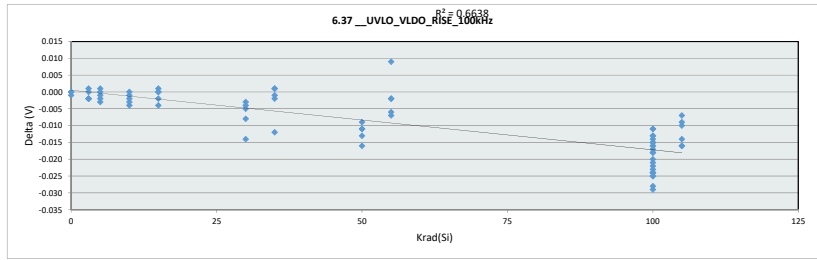


6.34 UVLO_VLD										
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit					
			V	3.66	3.44					
			V	3.66	3.44					
Krad(Si)	LL	Min	Average	Max	UL					
0	3.440	3.540	3.542	3.543	3.660					
3	3.440	3.534	3.541	3.545	3.660					
5	3.440	3.532	3.537	3.540	3.660					
10	3.440	3.533	3.537	3.543	3.660					
15	3.440	3.532	3.541	3.543	3.660					
30	3.440	3.524	3.537	3.549	3.660					
35	3.440	3.527	3.538	3.549	3.660					
50	3.440	3.529	3.532	3.549	3.660					
55	3.440	3.521	3.534	3.538	3.660					
100	3.440	3.488	3.519	3.547	3.660					
105	3.440	3.520	3.532	3.538	3.660					

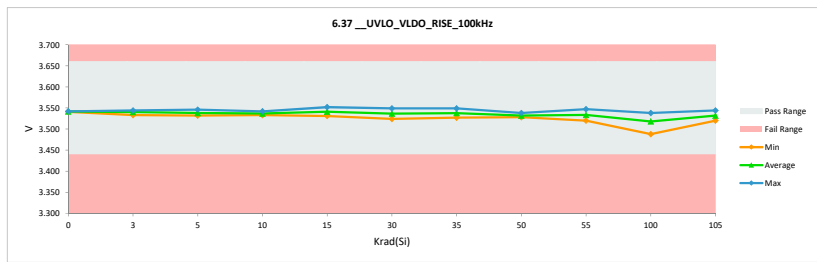


# HDR TID Report TPS7H5003-SP

6.37_UVLO_VLDO_RISE_100kHz				
Test Site				
Tester				
Test Number				
Unit		V	V	
Max Limit		3.66	3.66	
Min Limit		3.44	3.44	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	3.542	3.542	0.000
0	138	3.541	3.541	0.000
0	139	3.543	3.542	-0.001
3	197	3.535	3.533	-0.002
3	198	3.544	3.542	-0.002
3	199	3.543	3.543	0.000
3	202	3.546	3.544	-0.002
3	203	3.538	3.539	0.001
5	204	3.540	3.538	-0.002
5	205	3.536	3.535	-0.001
5	206	3.532	3.532	0.000
5	208	3.538	3.539	0.001
5	209	3.549	3.546	-0.003
10	210	3.543	3.542	-0.001
10	211	3.534	3.534	0.000
10	212	3.545	3.542	-0.003
10	213	3.535	3.533	-0.002
10	214	3.537	3.533	-0.004
15	215	3.545	3.545	0.000
15	217	3.551	3.552	0.001
15	219	3.547	3.543	-0.004
15	220	3.533	3.531	-0.002
15	221	3.534	3.534	0.000
30	222	3.538	3.524	-0.014
30	223	3.537	3.533	-0.004
30	224	3.542	3.534	-0.008
30	225	3.546	3.543	-0.003
30	226	3.554	3.549	-0.005
35	227	3.541	3.529	-0.012
35	228	3.548	3.549	0.001
35	229	3.549	3.547	-0.002
35	230	3.526	3.527	0.001
35	231	3.538	3.537	-0.001
50	170	3.539	3.528	-0.011
50	172	3.540	3.529	-0.011
50	173	3.548	3.535	-0.013
50	175	3.547	3.538	-0.009
50	176	3.546	3.530	-0.016
55	177	3.544	3.542	-0.002
55	178	3.522	3.531	0.009
55	179	3.534	3.527	-0.007
55	180	3.526	3.520	-0.006
55	181	3.549	3.547	-0.002
100	154	3.548	3.533	-0.015
100	155	3.535	3.518	-0.017
100	156	3.544	3.531	-0.013
100	157	3.546	3.517	-0.029
100	158	3.526	3.515	-0.011
100	159	3.526	3.508	-0.018
100	160	3.528	3.512	-0.016
100	161	3.536	3.525	-0.011
100	162	3.542	3.519	-0.023
100	163	3.544	3.516	-0.028
100	164	3.534	3.516	-0.018
100	165	3.534	3.509	-0.025
100	166	3.532	3.508	-0.024
100	168	3.558	3.538	-0.020
100	169	3.546	3.525	-0.021
100	182	3.541	3.519	-0.022
100	183	3.536	3.523	-0.013
100	184	3.531	3.516	-0.015
100	185	3.513	3.488	-0.025
100	186	3.535	3.511	-0.024
100	187	3.550	3.526	-0.024
100	188	3.531	3.507	-0.024
100	189	3.538	3.522	-0.016
100	190	3.547	3.533	-0.014
105	191	3.551	3.542	-0.009
105	192	3.540	3.533	-0.007
105	193	3.536	3.522	-0.014
105	194	3.536	3.520	-0.016
105	195	3.547	3.531	-0.016
105	196	3.554	3.544	-0.010
Max		3.558	3.552	0.009
Average		3.540	3.531	-0.009
Min		3.513	3.488	-0.029
Std Dev		0.008	0.012	0.009

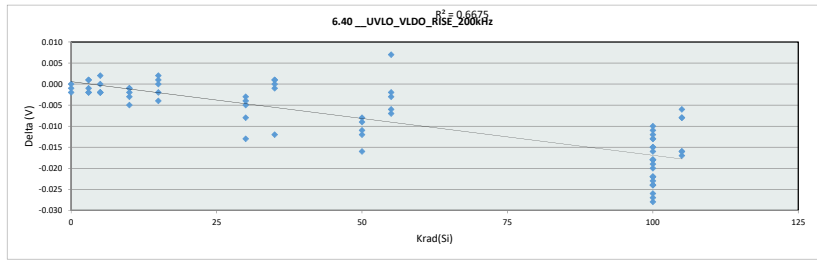


6.37_UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit		3.66	V								
Min Limit		3.44	V								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.541	3.533	3.532	3.533	3.531	3.524	3.527	3.528	3.520	3.488	3.520
Average	3.542	3.540	3.538	3.537	3.541	3.537	3.538	3.532	3.533	3.518	3.532
Max	3.542	3.544	3.546	3.542	3.552	3.549	3.549	3.538	3.547	3.538	3.544
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

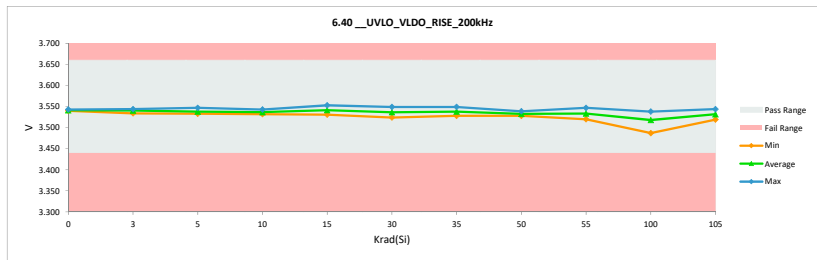


# HDR TID Report TPS7H5003-SP

6.40 UVLO_VLDO_RISE_200kHz					
Test Site					
Tester					
Test Number					
Unit		V	V		
Max Limit		3.66	3.66		
Min Limit		3.44	3.44		
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	3.542	3.541	-0.001	
0	138	3.542	3.540	-0.002	
0	139	3.543	3.543	0.000	
3	197	3.535	3.534	-0.001	
3	198	3.544	3.542	-0.002	
3	199	3.543	3.544	0.001	
3	202	3.546	3.544	-0.002	
3	203	3.538	3.539	0.001	
5	204	3.540	3.538	-0.002	
5	205	3.535	3.535	0.000	
5	206	3.531	3.533	0.002	
5	208	3.540	3.538	-0.002	
5	209	3.549	3.547	-0.002	
10	210	3.543	3.542	-0.001	
10	211	3.535	3.534	-0.001	
10	212	3.545	3.543	-0.002	
10	213	3.536	3.533	-0.003	
10	214	3.537	3.532	-0.005	
10	215	3.544	3.546	0.002	
15	217	3.552	3.553	0.001	
15	219	3.547	3.543	-0.004	
15	220	3.533	3.531	-0.002	
15	221	3.534	3.534	0.000	
30	222	3.537	3.524	-0.013	
30	223	3.537	3.532	-0.005	
30	224	3.542	3.534	-0.008	
30	225	3.547	3.544	-0.003	
30	226	3.553	3.549	-0.004	
35	227	3.541	3.529	-0.012	
35	228	3.548	3.549	0.001	
35	229	3.549	3.548	-0.001	
35	230	3.527	3.528	0.001	
35	231	3.537	3.537	0.000	
50	170	3.539	3.528	-0.011	
50	172	3.540	3.531	-0.009	
50	173	3.547	3.535	-0.012	
50	175	3.547	3.539	-0.008	
50	176	3.546	3.530	-0.016	
55	177	3.544	3.542	-0.002	
55	178	3.523	3.530	0.007	
55	179	3.534	3.528	-0.006	
55	180	3.527	3.520	-0.007	
55	181	3.550	3.547	-0.003	
100	154	3.548	3.535	-0.013	
100	155	3.536	3.518	-0.018	
100	156	3.543	3.531	-0.012	
100	157	3.544	3.517	-0.027	
100	158	3.526	3.515	-0.011	
100	159	3.526	3.508	-0.018	
100	160	3.528	3.512	-0.016	
100	161	3.536	3.526	-0.010	
100	162	3.543	3.519	-0.024	
100	163	3.544	3.516	-0.028	
100	164	3.534	3.516	-0.018	
100	165	3.533	3.509	-0.024	
100	166	3.527	3.509	-0.018	
100	168	3.558	3.538	-0.020	
100	169	3.545	3.526	-0.019	
100	182	3.541	3.519	-0.022	
100	183	3.536	3.523	-0.013	
100	184	3.531	3.509	-0.022	
100	185	3.513	3.487	-0.026	
100	186	3.535	3.512	-0.023	
100	187	3.549	3.527	-0.022	
100	188	3.530	3.506	-0.024	
100	189	3.537	3.522	-0.015	
100	190	3.547	3.532	-0.015	
105	191	3.551	3.543	-0.008	
105	192	3.539	3.533	-0.006	
105	193	3.537	3.521	-0.016	
105	194	3.536	3.519	-0.017	
105	195	3.547	3.531	-0.016	
105	196	3.552	3.544	-0.008	
Max		3.558	3.553	0.007	
Average		3.540	3.531	-0.009	
Min		3.513	3.487	-0.028	
Std Dev		0.008	0.013	0.009	

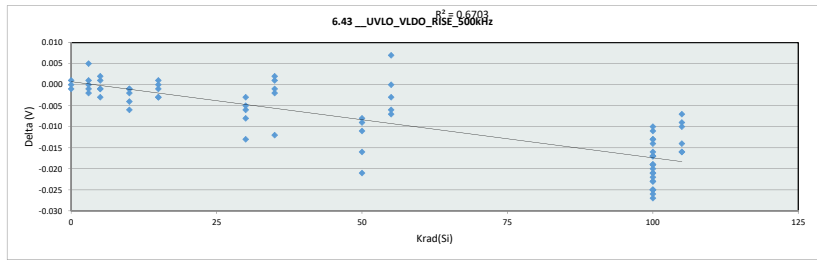


6.40 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit		3.66	V								
Min Limit		3.44	V								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.540	3.534	3.533	3.532	3.531	3.524	3.528	3.528	3.520	3.487	3.519
Average	3.541	3.541	3.538	3.537	3.541	3.537	3.538	3.533	3.533	3.518	3.532
Max	3.543	3.544	3.547	3.543	3.553	3.549	3.549	3.539	3.547	3.538	3.544
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

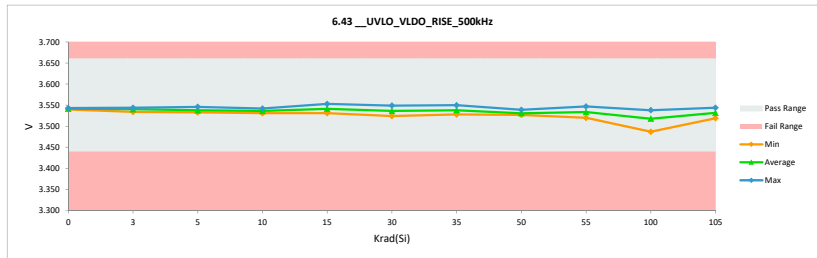


HDR TID Report  
TPS7H5003-SP

6.43 UVLO_VLDO_RISE_500kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	3.541	3.542	0.001
0	138	3.541	3.540	-0.001
0	139	3.543	3.543	0.000
3	197	3.535	3.534	-0.001
3	198	3.545	3.543	-0.002
3	199	3.543	3.544	0.001
3	202	3.539	3.544	0.005
3	203	3.538	3.538	0.000
5	204	3.538	3.537	-0.001
5	205	3.534	3.535	0.001
5	206	3.531	3.533	0.002
5	208	3.539	3.538	-0.001
5	209	3.549	3.546	-0.003
10	210	3.543	3.542	-0.001
10	211	3.535	3.534	-0.001
10	212	3.545	3.541	-0.004
10	213	3.535	3.533	-0.002
10	214	3.537	3.531	-0.006
15	215	3.545	3.545	0.000
15	217	3.552	3.553	0.001
15	219	3.547	3.544	-0.003
15	220	3.534	3.531	-0.003
15	221	3.535	3.534	-0.001
30	222	3.537	3.524	-0.013
30	223	3.537	3.531	-0.006
30	224	3.542	3.534	-0.008
30	225	3.547	3.544	-0.003
30	226	3.554	3.549	-0.005
35	227	3.541	3.529	-0.012
35	228	3.548	3.550	0.002
35	229	3.549	3.547	-0.002
35	230	3.527	3.528	0.001
35	231	3.537	3.536	-0.001
50	170	3.538	3.527	-0.011
50	172	3.540	3.531	-0.009
50	173	3.548	3.527	-0.021
50	175	3.547	3.539	-0.008
50	176	3.546	3.530	-0.016
55	177	3.543	3.543	0.000
55	178	3.523	3.530	0.007
55	179	3.534	3.528	-0.006
55	180	3.527	3.520	-0.007
55	181	3.550	3.547	-0.003
100	154	3.549	3.535	-0.014
100	155	3.536	3.519	-0.017
100	156	3.544	3.531	-0.013
100	157	3.543	3.517	-0.026
100	158	3.526	3.515	-0.011
100	159	3.527	3.508	-0.019
100	160	3.528	3.512	-0.016
100	161	3.536	3.526	-0.010
100	162	3.543	3.518	-0.025
100	163	3.544	3.517	-0.027
100	164	3.535	3.516	-0.019
100	165	3.533	3.508	-0.025
100	166	3.527	3.508	-0.019
100	168	3.558	3.538	-0.020
100	169	3.546	3.525	-0.021
100	182	3.541	3.519	-0.022
100	183	3.536	3.523	-0.013
100	184	3.531	3.510	-0.021
100	185	3.512	3.487	-0.025
100	186	3.535	3.512	-0.023
100	187	3.550	3.527	-0.023
100	188	3.531	3.506	-0.025
100	189	3.538	3.521	-0.017
100	190	3.548	3.531	-0.017
105	191	3.551	3.542	-0.009
105	192	3.540	3.533	-0.007
105	193	3.536	3.522	-0.014
105	194	3.535	3.519	-0.016
105	195	3.547	3.531	-0.016
105	196	3.554	3.544	-0.010
Max		3.558	3.553	0.007
Average		3.540	3.530	-0.009
Min		3.512	3.487	-0.027
Std Dev		0.008	0.013	0.009

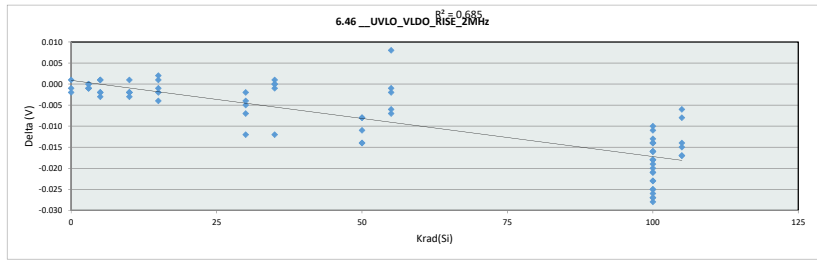


6.43 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.540	3.534	3.533	3.531	3.531	3.524	3.528	3.527	3.520	3.487	3.519
Average	3.542	3.541	3.538	3.536	3.541	3.536	3.538	3.531	3.534	3.518	3.532
Max	3.543	3.544	3.546	3.542	3.553	3.549	3.550	3.539	3.547	3.538	3.544
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

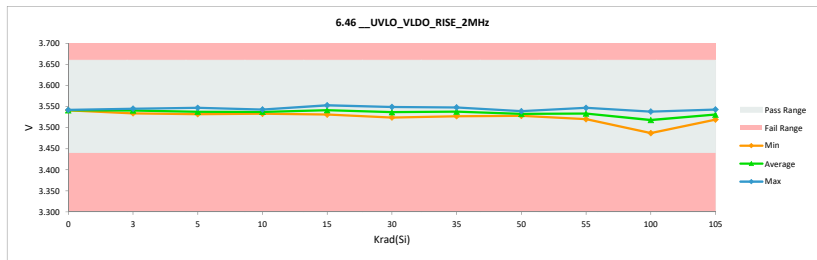


# HDR TID Report TPS7H5003-SP

6.46 UVLO_VLDO_RISE_2MHz				
Test Site	Tester	Test Number	Unit	Max Limit
			V	3.66
			V	3.44
Krad(Si)	Serial #	PRE	POST	Delta
0	137	3.542	3.541	-0.001
0	138	3.540	3.541	0.001
0	139	3.544	3.542	-0.002
3	197	3.535	3.534	-0.001
3	198	3.544	3.543	-0.001
3	199	3.544	3.544	0.000
3	202	3.546	3.545	-0.001
3	203	3.538	3.538	0.000
5	204	3.540	3.537	-0.003
5	205	3.534	3.535	0.001
5	206	3.531	3.532	0.001
5	208	3.540	3.538	-0.002
5	209	3.549	3.547	-0.002
10	210	3.544	3.542	-0.002
10	211	3.534	3.535	0.001
10	212	3.545	3.543	-0.002
10	213	3.536	3.534	-0.002
10	214	3.536	3.533	-0.003
10	215	3.545	3.546	0.001
15	217	3.551	3.553	0.002
15	219	3.548	3.544	-0.004
15	220	3.533	3.531	-0.002
15	221	3.534	3.533	-0.001
30	222	3.536	3.524	-0.012
30	223	3.537	3.532	-0.005
30	224	3.541	3.534	-0.007
30	225	3.547	3.545	-0.002
30	226	3.553	3.549	-0.004
35	227	3.542	3.530	-0.012
35	228	3.548	3.548	0.000
35	229	3.549	3.548	-0.001
35	230	3.526	3.527	0.001
35	231	3.537	3.537	0.000
50	170	3.539	3.528	-0.011
50	172	3.539	3.531	-0.008
50	173	3.548	3.534	-0.014
50	175	3.547	3.539	-0.008
50	176	3.545	3.531	-0.014
55	177	3.543	3.542	-0.001
55	178	3.523	3.531	0.008
55	179	3.534	3.527	-0.007
55	180	3.526	3.520	-0.006
55	181	3.549	3.547	-0.002
100	154	3.548	3.534	-0.014
100	155	3.535	3.519	-0.016
100	156	3.544	3.530	-0.014
100	157	3.544	3.517	-0.027
100	158	3.526	3.515	-0.011
100	159	3.526	3.508	-0.018
100	160	3.528	3.512	-0.016
100	161	3.536	3.526	-0.010
100	162	3.543	3.518	-0.025
100	163	3.544	3.516	-0.028
100	164	3.535	3.517	-0.018
100	165	3.534	3.508	-0.026
100	166	3.527	3.509	-0.018
100	168	3.557	3.538	-0.019
100	169	3.546	3.525	-0.021
100	182	3.541	3.520	-0.021
100	183	3.536	3.523	-0.013
100	184	3.530	3.510	-0.020
100	185	3.514	3.487	-0.027
100	186	3.535	3.512	-0.023
100	187	3.549	3.526	-0.023
100	188	3.531	3.506	-0.025
100	189	3.538	3.522	-0.016
100	190	3.547	3.533	-0.014
105	191	3.551	3.543	-0.008
105	192	3.539	3.533	-0.006
105	193	3.537	3.522	-0.015
105	194	3.536	3.519	-0.017
105	195	3.547	3.530	-0.017
105	196	3.553	3.539	-0.014
	Max	3.557	3.553	0.008
	Average	3.540	3.531	-0.009
	Min	3.514	3.487	-0.028
	Std Dev	0.008	0.013	0.009

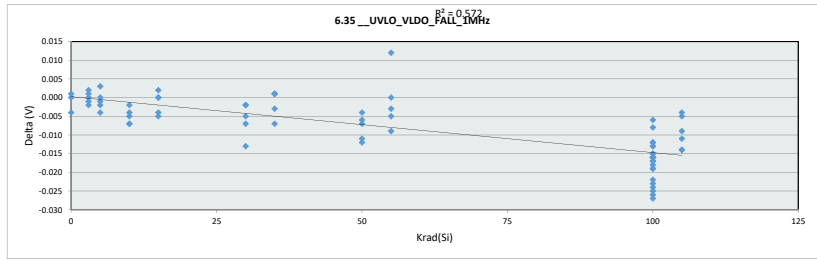


6.46 UVLO_VLD											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			3.66	3.44							
			V	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.541	3.534	3.532	3.533	3.531	3.524	3.527	3.528	3.520	3.487	3.519
Average	3.541	3.541	3.538	3.537	3.541	3.537	3.538	3.533	3.533	3.518	3.531
Max	3.542	3.545	3.547	3.543	3.553	3.549	3.548	3.539	3.547	3.538	3.543
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

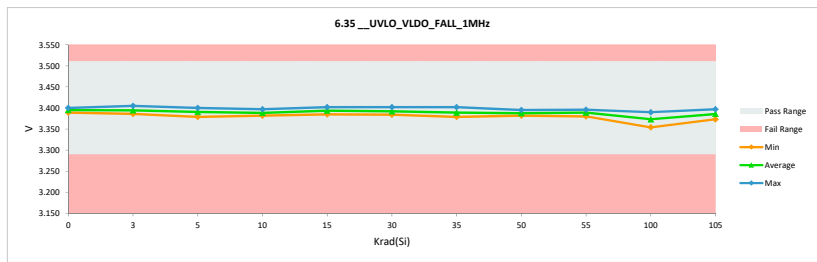


# HDR TID Report TPS7H5003-SP

6.35_UVLO_VLDO_FALL_1MHz				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			3.51	3.51
Min Limit			3.29	3.29
Krad(Si)	Serial #	PRE	POST	Delta
0	137	3.393	3.389	-0.004
0	138	3.396	3.396	0.000
0	139	3.399	3.400	0.001
3	197	3.385	3.386	0.001
3	198	3.403	3.405	0.002
3	199	3.392	3.392	0.000
3	202	3.397	3.395	-0.002
3	203	3.395	3.394	-0.001
5	204	3.389	3.388	-0.001
5	205	3.391	3.389	-0.002
5	206	3.383	3.379	-0.004
5	208	3.397	3.400	0.003
5	209	3.397	3.397	0.000
10	210	3.397	3.392	-0.005
10	211	3.391	3.384	-0.007
10	212	3.399	3.397	-0.002
10	213	3.396	3.382	-0.004
10	214	3.394	3.387	-0.007
15	215	3.393	3.395	0.002
15	217	3.402	3.402	0.000
15	219	3.405	3.400	-0.005
15	220	3.385	3.385	0.000
15	221	3.391	3.387	-0.004
30	222	3.401	3.388	-0.013
30	223	3.391	3.384	-0.007
30	224	3.393	3.388	-0.005
30	225	3.401	3.399	-0.002
30	226	3.404	3.402	-0.002
35	227	3.389	3.382	-0.007
35	228	3.401	3.402	0.001
35	229	3.399	3.396	-0.003
35	230	3.378	3.379	0.001
35	231	3.385	3.386	0.001
50	170	3.386	3.382	-0.004
50	172	3.398	3.392	-0.006
50	173	3.397	3.385	-0.012
50	175	3.402	3.395	-0.007
50	176	3.395	3.384	-0.011
55	177	3.398	3.395	-0.003
55	178	3.371	3.383	0.012
55	179	3.385	3.380	-0.005
55	180	3.391	3.391	0.000
55	181	3.405	3.396	-0.009
100	154	3.402	3.385	-0.017
100	155	3.383	3.368	-0.015
100	156	3.395	3.380	-0.015
100	157	3.403	3.377	-0.026
100	158	3.380	3.372	-0.008
100	159	3.385	3.358	-0.027
100	160	3.377	3.361	-0.016
100	161	3.386	3.380	-0.006
100	162	3.399	3.387	-0.012
100	163	3.394	3.371	-0.023
100	164	3.382	3.366	-0.016
100	165	3.387	3.371	-0.016
100	166	3.381	3.359	-0.022
100	168	3.405	3.389	-0.016
100	169	3.396	3.379	-0.017
100	182	3.409	3.390	-0.019
100	183	3.397	3.381	-0.016
100	184	3.380	3.367	-0.013
100	185	3.367	3.354	-0.013
100	186	3.390	3.366	-0.024
100	187	3.399	3.381	-0.018
100	188	3.383	3.358	-0.025
100	189	3.386	3.374	-0.012
100	190	3.401	3.382	-0.019
105	191	3.404	3.390	-0.014
105	192	3.390	3.385	-0.005
105	193	3.384	3.373	-0.011
105	194	3.390	3.376	-0.014
105	195	3.398	3.394	-0.004
105	196	3.406	3.397	-0.009
Max		3.409	3.405	0.012
Average		3.393	3.385	-0.008
Min		3.367	3.354	-0.027
Std Dev		0.009	0.012	0.008



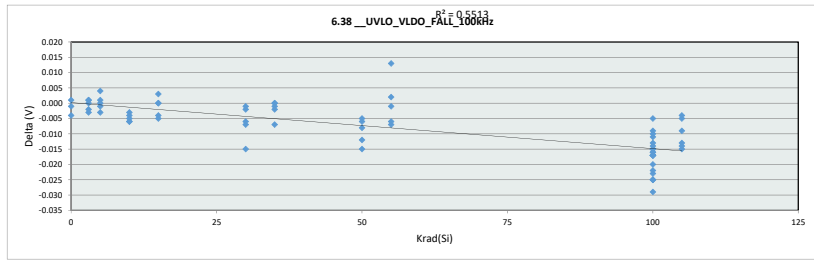
6.35_UVLO_VLD											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit			3.51	3.51							
Min Limit			3.29	3.29							
Krad(Si)	LL	Min	Average	Max	UL						
0	3.290	3.389	3.395	3.400	3.510						
3	3.290	3.386	3.394	3.405	3.510						
5	3.290	3.379	3.391	3.400	3.510						
10	3.290	3.382	3.388	3.399	3.510						
15	3.290	3.385	3.394	3.402	3.510						
30	3.290	3.384	3.392	3.402	3.510						
35	3.290	3.379	3.389	3.402	3.510						
50	3.290	3.382	3.388	3.402	3.510						
55	3.290	3.380	3.389	3.402	3.510						
100	3.290	3.354	3.373	3.396	3.510						
105	3.290	3.373	3.386	3.397	3.510						



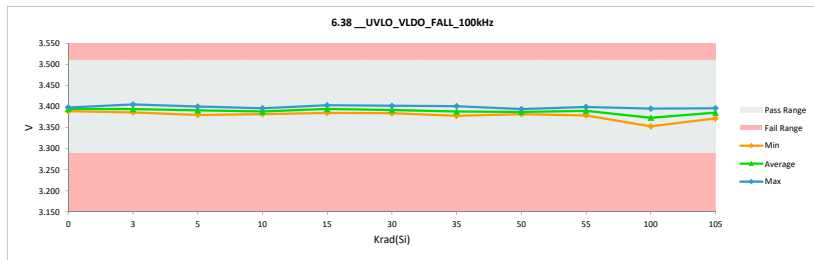


HDR TID Report  
TPS7H5003-SP

6.38 UVLO_VLDO_FALL_100kHz				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			3.51	3.51
Min Limit			3.29	3.29
Krad(Si)	Serial #	PRE	POST	Delta
0	137	3.393	3.389	-0.004
0	138	3.395	3.396	0.001
0	139	3.399	3.398	-0.001
3	197	3.385	3.386	0.001
3	198	3.404	3.405	0.001
3	199	3.392	3.392	0.000
3	202	3.396	3.394	-0.002
3	203	3.396	3.393	-0.003
5	204	3.388	3.388	0.000
5	205	3.391	3.390	-0.001
5	206	3.383	3.380	-0.003
5	208	3.396	3.400	0.004
5	209	3.396	3.397	0.001
10	210	3.397	3.392	-0.005
10	211	3.390	3.384	-0.006
10	212	3.399	3.396	-0.003
10	213	3.396	3.382	-0.004
10	214	3.393	3.387	-0.006
15	215	3.393	3.396	0.003
15	217	3.403	3.403	0.000
15	219	3.405	3.401	-0.004
15	220	3.385	3.385	0.000
15	221	3.392	3.387	-0.005
30	222	3.402	3.387	-0.015
30	223	3.390	3.384	-0.006
30	224	3.394	3.387	-0.007
30	225	3.401	3.399	-0.002
30	226	3.403	3.402	-0.001
35	227	3.389	3.382	-0.007
35	228	3.402	3.401	-0.001
35	229	3.398	3.396	-0.002
35	230	3.378	3.378	0.000
35	231	3.385	3.385	0.000
50	170	3.387	3.382	-0.005
50	172	3.398	3.392	-0.006
50	173	3.400	3.385	-0.015
50	175	3.402	3.394	-0.008
50	176	3.395	3.383	-0.012
55	177	3.397	3.399	0.002
55	178	3.371	3.384	0.013
55	179	3.385	3.379	-0.006
55	180	3.392	3.391	-0.001
55	181	3.404	3.397	-0.007
100	154	3.402	3.385	-0.017
100	155	3.385	3.368	-0.017
100	156	3.396	3.380	-0.016
100	157	3.405	3.376	-0.029
100	158	3.381	3.372	-0.009
100	159	3.385	3.360	-0.025
100	160	3.376	3.361	-0.015
100	161	3.386	3.381	-0.005
100	162	3.398	3.388	-0.010
100	163	3.394	3.371	-0.023
100	164	3.382	3.365	-0.017
100	165	3.387	3.370	-0.017
100	166	3.381	3.359	-0.022
100	168	3.405	3.389	-0.016
100	169	3.396	3.379	-0.017
100	182	3.408	3.395	-0.013
100	183	3.397	3.382	-0.015
100	184	3.381	3.367	-0.014
100	185	3.368	3.353	-0.015
100	186	3.390	3.365	-0.025
100	187	3.399	3.382	-0.017
100	188	3.382	3.357	-0.025
100	189	3.385	3.374	-0.011
100	190	3.401	3.381	-0.020
105	191	3.404	3.390	-0.014
105	192	3.390	3.385	-0.005
105	193	3.385	3.372	-0.013
105	194	3.390	3.375	-0.015
105	195	3.399	3.395	-0.004
105	196	3.405	3.396	-0.009
Max		3.408	3.405	0.013
Average		3.393	3.385	-0.008
Min		3.368	3.353	-0.029
Std Dev		0.009	0.012	0.008

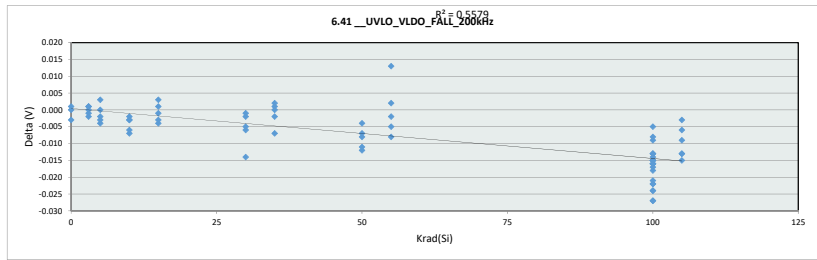


6.38 UVLO_VLD											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit			3.51	3.51							
Min Limit			3.29	3.29							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.389	3.386	3.380	3.382	3.385	3.384	3.378	3.382	3.379	3.353	3.372
Average	3.394	3.394	3.391	3.388	3.394	3.392	3.388	3.387	3.390	3.373	3.386
Max	3.398	3.405	3.400	3.396	3.403	3.402	3.401	3.394	3.399	3.395	3.396
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

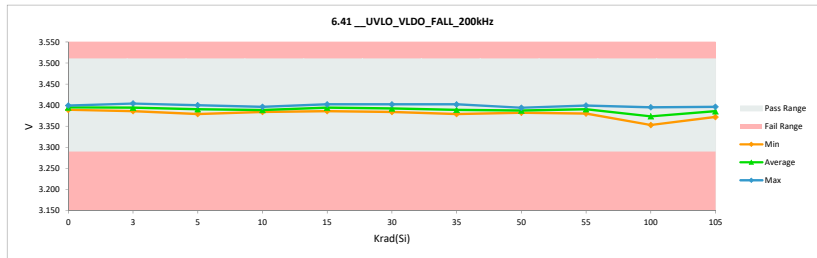


# HDR TID Report TPS7H5003-SP

6.41 UVLO_VLDO_FALL_200kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	3.392	3.389	-0.003
0	138	3.395	3.396	0.001
0	139	3.399	3.399	0.000
3	197	3.385	3.386	0.001
3	198	3.403	3.404	0.001
3	199	3.392	3.392	0.000
3	202	3.396	3.395	-0.001
3	203	3.396	3.394	-0.002
5	204	3.390	3.387	-0.003
5	205	3.391	3.389	-0.002
5	206	3.383	3.379	-0.004
5	208	3.397	3.400	0.003
5	209	3.397	3.397	0.000
10	210	3.395	3.392	-0.003
10	211	3.390	3.384	-0.006
10	212	3.399	3.396	-0.003
10	213	3.386	3.384	-0.002
10	214	3.393	3.386	-0.007
15	215	3.392	3.395	0.003
15	217	3.403	3.402	-0.001
15	219	3.404	3.400	-0.004
15	220	3.385	3.386	0.001
15	221	3.391	3.388	-0.003
30	222	3.402	3.388	-0.014
30	223	3.390	3.384	-0.006
30	224	3.393	3.388	-0.005
30	225	3.401	3.399	-0.002
30	226	3.403	3.402	-0.001
35	227	3.389	3.382	-0.007
35	228	3.401	3.402	0.001
35	229	3.398	3.396	-0.002
35	230	3.377	3.379	0.002
35	231	3.385	3.385	0.000
50	170	3.386	3.382	-0.004
50	172	3.399	3.392	-0.007
50	173	3.397	3.385	-0.012
50	175	3.402	3.394	-0.008
50	176	3.395	3.384	-0.011
55	177	3.397	3.399	0.002
55	178	3.372	3.385	0.013
55	179	3.385	3.380	-0.005
55	180	3.393	3.391	-0.002
55	181	3.405	3.397	-0.008
100	154	3.401	3.385	-0.016
100	155	3.383	3.368	-0.015
100	156	3.395	3.380	-0.015
100	157	3.404	3.377	-0.027
100	158	3.381	3.372	-0.009
100	159	3.386	3.359	-0.027
100	160	3.377	3.361	-0.016
100	161	3.386	3.381	-0.005
100	162	3.397	3.389	-0.008
100	163	3.394	3.372	-0.022
100	164	3.383	3.367	-0.016
100	165	3.388	3.370	-0.018
100	166	3.381	3.359	-0.022
100	168	3.405	3.389	-0.016
100	169	3.396	3.381	-0.015
100	182	3.408	3.395	-0.013
100	183	3.396	3.382	-0.014
100	184	3.380	3.367	-0.013
100	185	3.368	3.353	-0.015
100	186	3.389	3.365	-0.024
100	187	3.399	3.382	-0.017
100	188	3.382	3.358	-0.024
100	189	3.387	3.374	-0.013
100	190	3.401	3.380	-0.021
105	191	3.404	3.391	-0.013
105	192	3.391	3.385	-0.006
105	193	3.385	3.372	-0.013
105	194	3.390	3.375	-0.015
105	195	3.398	3.395	-0.003
105	196	3.405	3.396	-0.009
Max		3.408	3.404	0.013
Average		3.393	3.385	-0.008
Min		3.368	3.353	-0.027
Std Dev		0.008	0.012	0.008

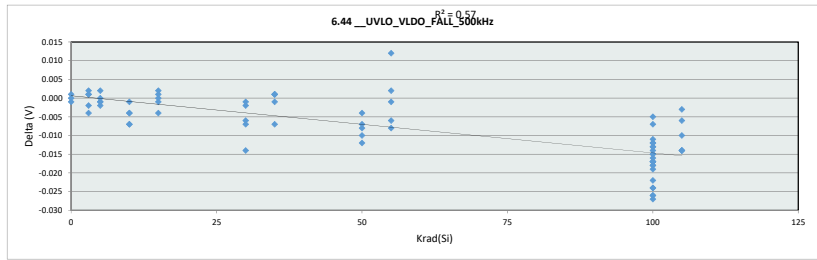


6.41 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.389	3.386	3.379	3.384	3.386	3.384	3.379	3.382	3.380	3.353	3.372
Average	3.395	3.394	3.390	3.388	3.394	3.392	3.389	3.387	3.390	3.374	3.386
Max	3.399	3.404	3.400	3.396	3.402	3.402	3.402	3.394	3.399	3.395	3.396
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

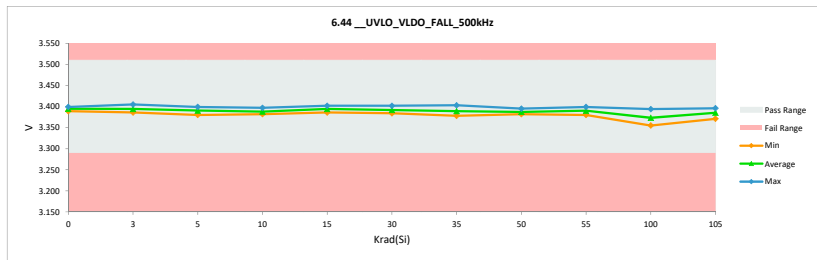


# HDR TID Report TPS7H5003-SP

6.44 UVLO_VLDO_FALL_500kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	3.390	3.389	-0.001
0	138	3.395	3.396	0.001
0	139	3.399	3.399	0.000
3	197	3.385	3.386	0.001
3	198	3.403	3.405	0.002
3	199	3.392	3.393	0.001
3	202	3.397	3.395	-0.002
3	203	3.396	3.392	-0.004
5	204	3.388	3.387	-0.001
5	205	3.391	3.389	-0.002
5	206	3.381	3.380	-0.001
5	208	3.397	3.399	0.002
5	209	3.397	3.397	0.000
10	210	3.396	3.392	-0.004
10	211	3.390	3.383	-0.007
10	212	3.398	3.397	-0.001
10	213	3.386	3.382	-0.004
10	214	3.393	3.386	-0.007
15	215	3.393	3.395	0.002
15	217	3.403	3.402	-0.001
15	219	3.400	3.401	0.001
15	220	3.386	3.386	0.000
15	221	3.391	3.387	-0.004
30	222	3.401	3.387	-0.014
30	223	3.391	3.384	-0.007
30	224	3.393	3.387	-0.006
30	225	3.401	3.399	-0.002
30	226	3.403	3.402	-0.001
35	227	3.390	3.383	-0.007
35	228	3.402	3.403	0.001
35	229	3.397	3.396	-0.001
35	230	3.377	3.378	0.001
35	231	3.385	3.386	0.001
50	170	3.386	3.382	-0.004
50	172	3.399	3.391	-0.008
50	173	3.396	3.384	-0.012
50	175	3.402	3.395	-0.007
50	176	3.394	3.384	-0.010
55	177	3.397	3.399	0.002
55	178	3.372	3.384	0.012
55	179	3.386	3.380	-0.006
55	180	3.392	3.391	-0.001
55	181	3.405	3.397	-0.008
100	154	3.402	3.384	-0.018
100	155	3.383	3.367	-0.016
100	156	3.394	3.380	-0.014
100	157	3.405	3.378	-0.027
100	158	3.380	3.373	-0.007
100	159	3.385	3.359	-0.026
100	160	3.377	3.360	-0.017
100	161	3.386	3.381	-0.005
100	162	3.398	3.387	-0.011
100	163	3.395	3.371	-0.024
100	164	3.382	3.365	-0.017
100	165	3.388	3.370	-0.018
100	166	3.381	3.359	-0.022
100	168	3.406	3.389	-0.017
100	169	3.396	3.381	-0.015
100	182	3.407	3.394	-0.013
100	183	3.396	3.381	-0.015
100	184	3.380	3.368	-0.012
100	185	3.367	3.355	-0.012
100	186	3.389	3.365	-0.024
100	187	3.399	3.382	-0.017
100	188	3.383	3.357	-0.026
100	189	3.386	3.373	-0.013
100	190	3.400	3.381	-0.019
105	191	3.405	3.391	-0.014
105	192	3.390	3.384	-0.006
105	193	3.385	3.371	-0.014
105	194	3.390	3.376	-0.014
105	195	3.397	3.394	-0.003
105	196	3.406	3.396	-0.010
	Max	3.407	3.405	0.012
	Average	3.393	3.385	-0.008
	Min	3.367	3.355	-0.027
	Std Dev	0.009	0.012	0.008

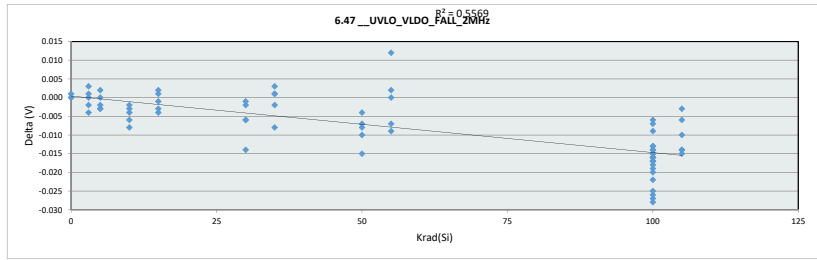


6.44 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.389	3.386	3.380	3.382	3.386	3.384	3.378	3.382	3.380	3.355	3.371
Average	3.395	3.394	3.390	3.388	3.394	3.392	3.389	3.387	3.390	3.373	3.385
Max	3.399	3.405	3.399	3.397	3.402	3.402	3.403	3.395	3.399	3.394	3.396
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

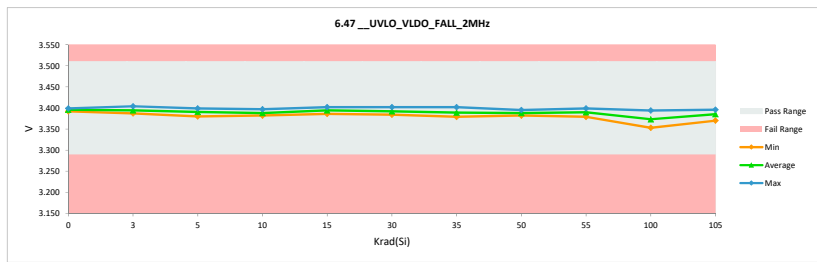


# HDR TID Report TPS7H5003-SP

6.47_UVLO_VLDO_FALL_2MHz				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			3.51	3.51
Min Limit			3.29	3.29
Krad(Si)	Serial #	PRE	POST	Delta
0	137	3.392	3.392	0.000
0	138	3.395	3.396	0.001
0	139	3.399	3.399	0.000
3	197	3.384	3.387	0.003
3	198	3.403	3.404	0.001
3	199	3.392	3.392	0.000
3	202	3.397	3.395	-0.002
3	203	3.397	3.393	-0.004
5	204	3.390	3.387	-0.003
5	205	3.391	3.389	-0.002
5	206	3.383	3.380	-0.003
5	208	3.397	3.399	0.002
5	209	3.397	3.397	0.000
10	210	3.396	3.392	-0.004
10	211	3.390	3.384	-0.006
10	212	3.399	3.397	-0.002
10	213	3.385	3.382	-0.003
10	214	3.393	3.385	-0.008
15	215	3.393	3.395	0.002
15	217	3.403	3.402	-0.001
15	219	3.405	3.401	-0.004
15	220	3.385	3.386	0.001
15	221	3.391	3.388	-0.003
30	222	3.402	3.388	-0.014
30	223	3.390	3.384	-0.006
30	224	3.393	3.387	-0.006
30	225	3.401	3.399	-0.002
30	226	3.403	3.402	-0.001
35	227	3.390	3.382	-0.008
35	228	3.401	3.402	0.001
35	229	3.398	3.396	-0.002
35	230	3.376	3.379	0.003
35	231	3.385	3.386	0.001
50	170	3.386	3.382	-0.004
50	172	3.399	3.392	-0.007
50	173	3.400	3.385	-0.015
50	175	3.403	3.395	-0.008
50	176	3.395	3.385	-0.010
55	177	3.397	3.399	0.002
55	178	3.372	3.384	0.012
55	179	3.386	3.379	-0.007
55	180	3.391	3.391	0.000
55	181	3.405	3.396	-0.009
100	154	3.401	3.385	-0.016
100	155	3.383	3.368	-0.015
100	156	3.395	3.380	-0.015
100	157	3.405	3.378	-0.027
100	158	3.380	3.373	-0.007
100	159	3.386	3.358	-0.028
100	160	3.377	3.361	-0.016
100	161	3.386	3.380	-0.006
100	162	3.397	3.388	-0.009
100	163	3.394	3.372	-0.022
100	164	3.383	3.366	-0.017
100	165	3.388	3.370	-0.018
100	166	3.380	3.360	-0.020
100	168	3.406	3.389	-0.017
100	169	3.396	3.380	-0.016
100	182	3.408	3.394	-0.014
100	183	3.396	3.381	-0.015
100	184	3.380	3.367	-0.013
100	185	3.367	3.353	-0.014
100	186	3.390	3.365	-0.025
100	187	3.398	3.381	-0.017
100	188	3.382	3.356	-0.026
100	189	3.386	3.373	-0.013
100	190	3.400	3.381	-0.019
105	191	3.405	3.391	-0.014
105	192	3.391	3.385	-0.006
105	193	3.385	3.370	-0.015
105	194	3.390	3.376	-0.014
105	195	3.397	3.394	-0.003
105	196	3.406	3.396	-0.010
Max		3.408	3.404	0.012
Average		3.393	3.385	-0.008
Min		3.367	3.353	-0.028
Std Dev		0.009	0.012	0.008

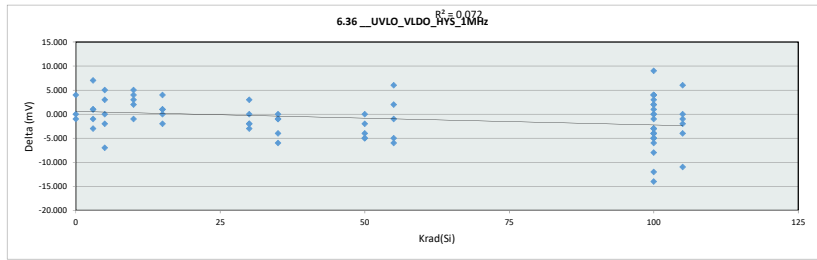


6.47_UVLO_VLDO											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit			3.51	3.51							
Min Limit			3.29	3.29							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.392	3.387	3.380	3.382	3.386	3.384	3.379	3.382	3.379	3.353	3.370
Average	3.396	3.394	3.390	3.388	3.394	3.392	3.389	3.388	3.390	3.373	3.385
Max	3.399	3.404	3.399	3.397	3.402	3.402	3.402	3.395	3.399	3.394	3.396
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

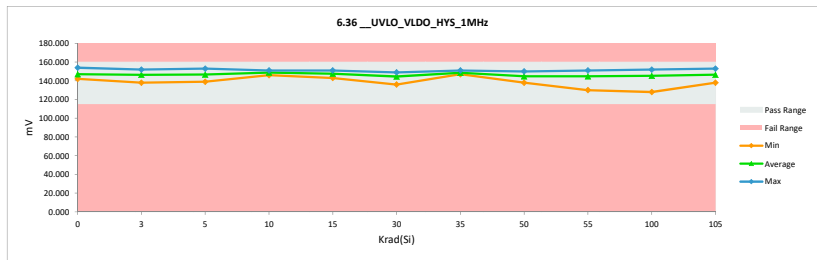


# HDR TID Report TPS7H5003-SP

6.36 UVLO_VLDO_HYS_1MHz				
Test Site	Tester	PRE	POST	Delta
0	137	150.000	154.000	4.000
0	138	145.000	145.000	0.000
0	139	143.000	142.000	-1.000
3	197	149.000	148.000	-1.000
3	198	141.000	138.000	-3.000
3	199	151.000	152.000	1.000
3	202	143.000	150.000	7.000
3	203	143.000	144.000	1.000
5	204	150.000	150.000	0.000
5	205	144.000	147.000	3.000
5	206	148.000	153.000	5.000
5	208	141.000	139.000	-2.000
5	209	151.000	144.000	-7.000
10	210	147.000	151.000	4.000
10	211	145.000	150.000	5.000
10	212	147.000	146.000	-1.000
10	213	149.000	151.000	2.000
10	214	143.000	146.000	3.000
15	215	151.000	151.000	0.000
15	217	149.000	150.000	1.000
15	219	142.000	143.000	1.000
15	220	149.000	147.000	-2.000
15	221	143.000	147.000	4.000
30	222	136.000	136.000	0.000
30	223	146.000	149.000	3.000
30	224	149.000	146.000	-3.000
30	225	146.000	144.000	-2.000
30	226	150.000	148.000	-2.000
35	227	153.000	147.000	-6.000
35	228	151.000	147.000	-4.000
35	229	150.000	150.000	0.000
35	230	149.000	148.000	-1.000
35	231	152.000	151.000	-1.000
50	170	152.000	147.000	-5.000
50	172	142.000	138.000	-4.000
50	173	150.000	150.000	0.000
50	175	145.000	143.000	-2.000
50	176	151.000	146.000	-5.000
55	177	146.000	148.000	2.000
55	178	153.000	147.000	-6.000
55	179	149.000	148.000	-1.000
55	180	135.000	130.000	-5.000
55	181	145.000	151.000	6.000
100	154	146.000	150.000	4.000
100	155	153.000	150.000	-3.000
100	156	149.000	151.000	2.000
100	157	142.000	139.000	-3.000
100	158	147.000	143.000	-4.000
100	159	142.000	151.000	9.000
100	160	150.000	151.000	1.000
100	161	151.000	146.000	-5.000
100	162	144.000	132.000	-12.000
100	163	150.000	146.000	-4.000
100	164	153.000	152.000	-1.000
100	165	147.000	139.000	-8.000
100	166	146.000	150.000	4.000
100	168	152.000	148.000	-4.000
100	169	151.000	146.000	-5.000
100	182	133.000	128.000	-5.000
100	183	138.000	142.000	4.000
100	184	150.000	150.000	0.000
100	185	148.000	134.000	-14.000
100	186	145.000	147.000	2.000
100	187	151.000	145.000	-6.000
100	188	148.000	148.000	0.000
100	189	152.000	149.000	-3.000
100	190	147.000	150.000	3.000
105	191	147.000	153.000	6.000
105	192	149.000	148.000	-1.000
105	193	152.000	148.000	-4.000
105	194	146.000	144.000	-2.000
105	195	149.000	138.000	-11.000
105	196	148.000	148.000	0.000
Max		153.000	154.000	9.000
Average		147.123	146.137	-0.986
Min		133.000	128.000	-14.000
Std Dev		4.330	5.414	4.312

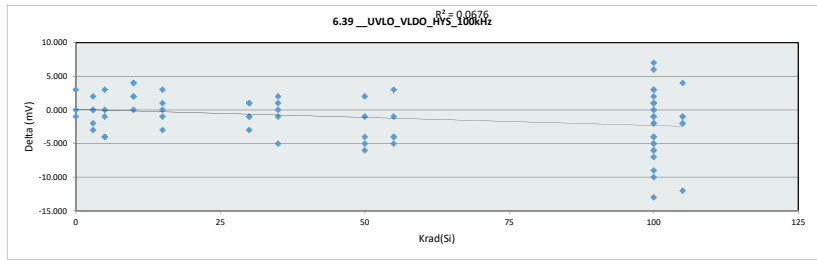


6.36 UVLO_VLD														
Test Site	Tester	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
LL		160	115	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min		160	115	142.000	138.000	139.000	146.000	143.000	136.000	147.000	138.000	130.000	128.000	138.000
Average		160	115	147.000	146.400	146.600	148.800	147.600	144.600	148.600	144.800	144.800	145.292	146.500
Max		160	115	154.000	152.000	153.000	151.000	151.000	149.000	151.000	150.000	151.000	152.000	153.000
UL		160	115	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

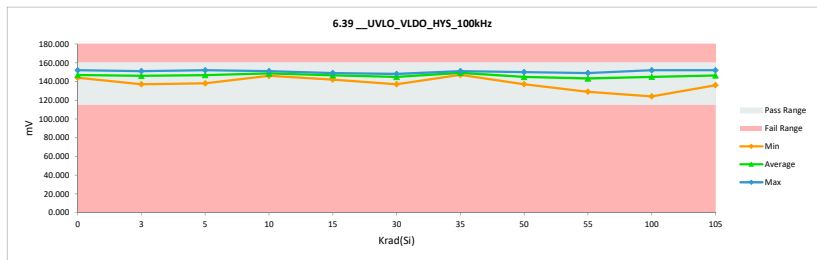


# HDR TID Report TPS7H5003-SP

6.39 UVLO_VLDO_HYS_100kHz				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	160	160		
Min Limit	115	115		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	149.000	152.000	3.000
0	138	146.000	145.000	-1.000
0	139	144.000	144.000	0.000
3	197	149.000	147.000	-2.000
3	198	140.000	137.000	-3.000
3	199	151.000	151.000	0.000
3	202	150.000	150.000	0.000
3	203	143.000	145.000	2.000
5	204	151.000	150.000	-1.000
5	205	145.000	145.000	0.000
5	206	149.000	152.000	3.000
5	208	142.000	138.000	-4.000
5	209	153.000	149.000	-4.000
10	210	147.000	151.000	4.000
10	211	145.000	149.000	4.000
10	212	146.000	146.000	0.000
10	213	149.000	151.000	2.000
10	214	144.000	146.000	2.000
15	215	152.000	149.000	-3.000
15	217	148.000	149.000	1.000
15	219	142.000	142.000	0.000
15	220	148.000	147.000	-1.000
15	221	143.000	146.000	3.000
30	222	136.000	137.000	1.000
30	223	147.000	148.000	1.000
30	224	148.000	147.000	-1.000
30	225	145.000	144.000	-1.000
30	226	151.000	148.000	-3.000
35	227	152.000	147.000	-5.000
35	228	146.000	148.000	2.000
35	229	151.000	151.000	0.000
35	230	148.000	149.000	1.000
35	231	152.000	151.000	-1.000
50	170	152.000	146.000	-6.000
50	172	142.000	137.000	-5.000
50	173	148.000	150.000	2.000
50	175	145.000	144.000	-1.000
50	176	151.000	147.000	-4.000
55	177	147.000	143.000	-4.000
55	178	151.000	147.000	-4.000
55	179	149.000	148.000	-1.000
55	180	134.000	129.000	-5.000
55	181	146.000	149.000	3.000
100	154	147.000	148.000	1.000
100	155	150.000	150.000	0.000
100	156	148.000	151.000	3.000
100	157	140.000	140.000	0.000
100	158	145.000	143.000	-2.000
100	159	142.000	149.000	7.000
100	160	151.000	152.000	1.000
100	161	150.000	145.000	-5.000
100	162	144.000	131.000	-13.000
100	163	150.000	144.000	-6.000
100	164	152.000	151.000	-1.000
100	165	146.000	139.000	-7.000
100	166	151.000	149.000	-2.000
100	168	153.000	149.000	-4.000
100	169	150.000	146.000	-4.000
100	182	133.000	124.000	-9.000
100	183	139.000	142.000	3.000
100	184	150.000	149.000	-1.000
100	185	145.000	135.000	-10.000
100	186	145.000	146.000	1.000
100	187	151.000	145.000	-6.000
100	188	148.000	150.000	2.000
100	189	153.000	148.000	-5.000
100	190	146.000	152.000	6.000
105	191	148.000	152.000	4.000
105	192	150.000	148.000	-2.000
105	193	152.000	150.000	-2.000
105	194	146.000	145.000	-1.000
105	195	148.000	136.000	-12.000
105	196	149.000	148.000	-1.000
Max		153.000	152.000	7.000
Average		147.110	145.863	-1.247
Min		133.000	124.000	-13.000
Std Dev		4.313	5.603	3.822

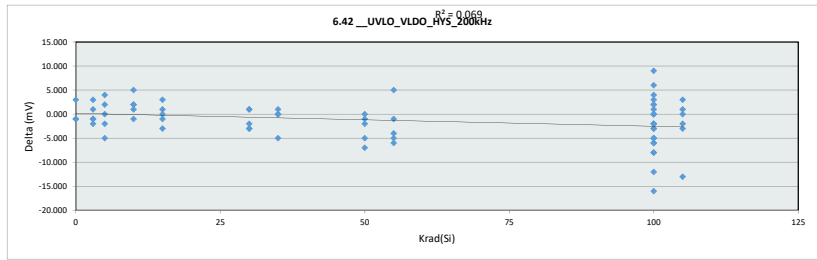


6.39 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	160	mV									
Min Limit	115	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	144.000	137.000	138.000	146.000	142.000	137.000	147.000	137.000	129.000	124.000	136.000
Average	147.000	146.000	146.800	148.600	146.600	144.800	149.200	144.800	143.200	144.917	146.500
Max	152.000	151.000	152.000	151.000	149.000	148.000	151.000	150.000	149.000	152.000	152.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

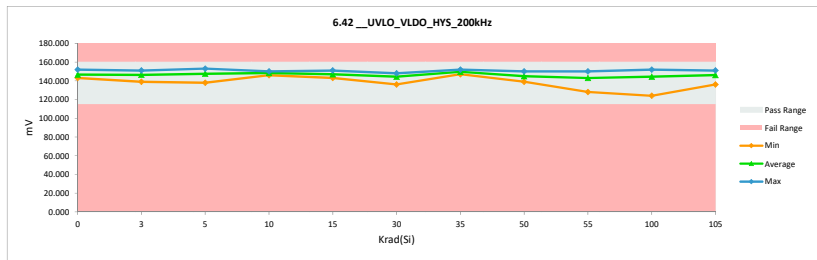


# HDR TID Report TPS7H5003-SP

6.42 UVLO_VLDO_HYS_200kHz				
Test Site	Tester	Test Number	Unit	Max Limit
			mV	160
			mV	115
Krad(Si)	Serial #	PRE	POST	Delta
0	137	149.000	152.000	3.000
0	138	146.000	145.000	-1.000
0	139	144.000	143.000	-1.000
3	197	149.000	148.000	-1.000
3	198	141.000	139.000	-2.000
3	199	150.000	151.000	1.000
3	202	150.000	149.000	-1.000
3	203	142.000	145.000	3.000
5	204	151.000	151.000	0.000
5	205	144.000	146.000	2.000
5	206	149.000	153.000	4.000
5	208	143.000	138.000	-5.000
5	209	152.000	150.000	-2.000
10	210	148.000	150.000	2.000
10	211	145.000	150.000	5.000
10	212	146.000	147.000	1.000
10	213	150.000	149.000	-1.000
10	214	144.000	146.000	2.000
15	215	152.000	151.000	-1.000
15	217	149.000	150.000	1.000
15	219	143.000	143.000	0.000
15	220	148.000	145.000	-3.000
15	221	143.000	146.000	3.000
30	222	135.000	136.000	1.000
30	223	147.000	148.000	1.000
30	224	149.000	146.000	-3.000
30	225	147.000	144.000	-3.000
30	226	150.000	148.000	-2.000
35	227	152.000	147.000	-5.000
35	228	147.000	147.000	0.000
35	229	152.000	152.000	0.000
35	230	149.000	150.000	1.000
35	231	152.000	152.000	0.000
50	170	153.000	146.000	-7.000
50	172	141.000	139.000	-2.000
50	173	150.000	150.000	0.000
50	175	145.000	144.000	-1.000
50	176	151.000	146.000	-5.000
55	177	147.000	143.000	-4.000
55	178	151.000	146.000	-5.000
55	179	149.000	148.000	-1.000
55	180	134.000	128.000	-6.000
55	181	145.000	150.000	5.000
100	154	147.000	150.000	3.000
100	155	153.000	150.000	-3.000
100	156	149.000	151.000	2.000
100	157	140.000	140.000	0.000
100	158	145.000	143.000	-2.000
100	159	140.000	149.000	9.000
100	160	151.000	151.000	0.000
100	161	150.000	145.000	-5.000
100	162	146.000	130.000	-16.000
100	163	150.000	144.000	-6.000
100	164	151.000	149.000	-2.000
100	165	145.000	139.000	-6.000
100	166	146.000	150.000	4.000
100	168	152.000	149.000	-3.000
100	169	150.000	145.000	-5.000
100	182	132.000	124.000	-8.000
100	183	140.000	141.000	1.000
100	184	150.000	142.000	-8.000
100	185	146.000	134.000	-12.000
100	186	145.000	147.000	2.000
100	187	150.000	145.000	-5.000
100	188	148.000	148.000	0.000
100	189	150.000	148.000	-2.000
100	190	146.000	152.000	6.000
105	191	148.000	151.000	3.000
105	192	149.000	149.000	0.000
105	193	152.000	149.000	-3.000
105	194	146.000	144.000	-2.000
105	195	149.000	136.000	-13.000
105	196	147.000	148.000	1.000
Max		153.000	153.000	9.000
Average		147.082	145.753	-1.329
Min		132.000	124.000	-16.000
Std Dev		4.342	5.710	4.220

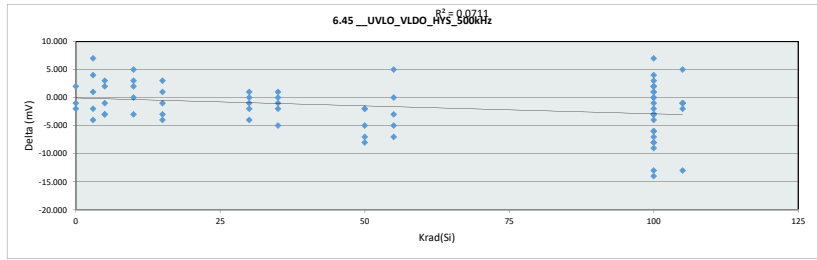


6.42 UVLO_VLD										
Test Site	Tester	Test Number	Max Limit	Min Limit						
			160 mV	115 mV						
Krad(Si)	LL	Min	Average	Max	UL					
0	115.000	143.000	146.667	152.000	160.000					
3	115.000	139.000	146.400	151.000	160.000					
5	115.000	138.000	147.600	153.000	160.000					
10	115.000	146.000	148.400	150.000	160.000					
15	115.000	143.000	147.000	151.000	160.000					
30	115.000	136.000	144.400	148.000	160.000					
35	115.000	139.000	145.600	150.000	160.000					
50	115.000	128.000	145.000	148.000	160.000					
55	115.000	128.000	143.000	150.000	160.000					
100	115.000	124.000	144.417	152.000	160.000					
105	115.000	136.000	146.167	151.000	160.000					

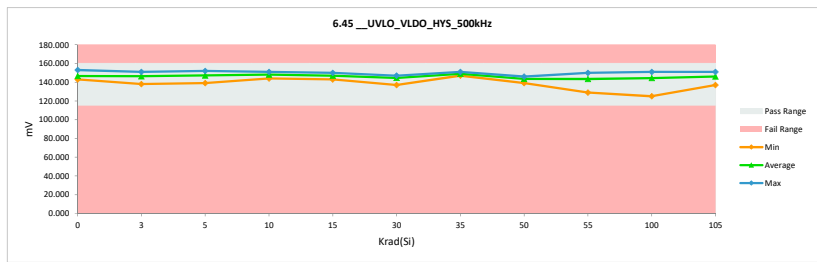


# HDR TID Report TPS7H5003-SP

6.45 UVLO_VLDO_HYS_500kHz				
Test Site	Tester	Test Number	Unit	Max Limit
			mV	160
			mV	115
Krad(Si)	Serial #	PRE	POST	Delta
0	137	151.000	153.000	2.000
0	138	146.000	144.000	-2.000
0	139	144.000	143.000	-1.000
3	197	150.000	148.000	-2.000
3	198	142.000	138.000	-4.000
3	199	150.000	151.000	1.000
3	202	142.000	149.000	7.000
3	203	142.000	146.000	4.000
5	204	151.000	150.000	-1.000
5	205	143.000	146.000	3.000
5	206	150.000	152.000	2.000
5	208	142.000	139.000	-3.000
5	209	152.000	149.000	-3.000
10	210	147.000	150.000	3.000
10	211	146.000	151.000	5.000
10	212	147.000	144.000	-3.000
10	213	150.000	150.000	0.000
10	214	144.000	146.000	2.000
15	215	151.000	150.000	-1.000
15	217	149.000	150.000	1.000
15	219	147.000	143.000	-4.000
15	220	148.000	145.000	-3.000
15	221	144.000	147.000	3.000
30	222	136.000	137.000	1.000
30	223	147.000	147.000	0.000
30	224	149.000	147.000	-2.000
30	225	146.000	145.000	-1.000
30	226	151.000	147.000	-4.000
35	227	152.000	147.000	-5.000
35	228	146.000	147.000	1.000
35	229	152.000	151.000	-1.000
35	230	150.000	150.000	0.000
35	231	152.000	150.000	-2.000
50	170	152.000	145.000	-7.000
50	172	141.000	139.000	-2.000
50	173	152.000	144.000	-8.000
50	175	146.000	144.000	-2.000
50	176	151.000	146.000	-5.000
55	177	147.000	144.000	-3.000
55	178	151.000	146.000	-5.000
55	179	148.000	148.000	0.000
55	180	136.000	129.000	-7.000
55	181	145.000	150.000	5.000
100	154	147.000	151.000	4.000
100	155	152.000	151.000	-1.000
100	156	149.000	151.000	2.000
100	157	138.000	138.000	0.000
100	158	146.000	142.000	-4.000
100	159	142.000	149.000	7.000
100	160	151.000	151.000	0.000
100	161	150.000	144.000	-6.000
100	162	145.000	131.000	-14.000
100	163	149.000	146.000	-3.000
100	164	153.000	151.000	-2.000
100	165	146.000	138.000	-8.000
100	166	146.000	149.000	3.000
100	168	152.000	149.000	-3.000
100	169	151.000	144.000	-7.000
100	182	134.000	125.000	-9.000
100	183	140.000	142.000	2.000
100	184	151.000	143.000	-8.000
100	185	145.000	132.000	-13.000
100	186	146.000	147.000	1.000
100	187	151.000	145.000	-6.000
100	188	148.000	149.000	1.000
100	189	152.000	149.000	-3.000
100	190	148.000	150.000	2.000
105	191	146.000	151.000	5.000
105	192	150.000	149.000	-1.000
105	193	151.000	150.000	-1.000
105	194	145.000	143.000	-2.000
105	195	150.000	137.000	-13.000
105	196	148.000	147.000	-1.000
Max		153.000	153.000	7.000
Average		147.260	145.630	-1.630
Min		134.000	125.000	-14.000
Std Dev		4.243	5.591	4.338



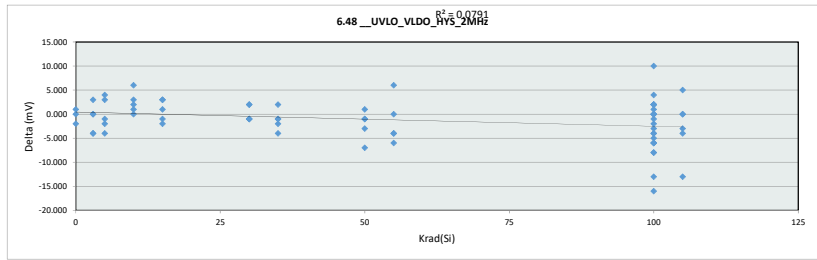
6.45 UVLO_VLD											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			mV	160	115						
			mV	160	115						
Krad(Si)	LL	Min	Average	Max	UL						
0	115.000	143.000	146.667	153.000	160.000						
3	115.000	138.000	146.400	151.000	160.000						
5	115.000	139.000	147.200	152.000	160.000						
10	115.000	144.000	148.200	151.000	160.000						
15	115.000	143.000	147.000	150.000	160.000						
30	115.000	137.000	144.600	147.000	160.000						
35	115.000	139.000	149.000	151.000	160.000						
50	115.000	129.000	143.600	146.000	160.000						
55	115.000	129.000	143.400	146.000	160.000						
100	115.000	125.000	144.458	151.000	160.000						
105	115.000	137.000	146.167	151.000	160.000						



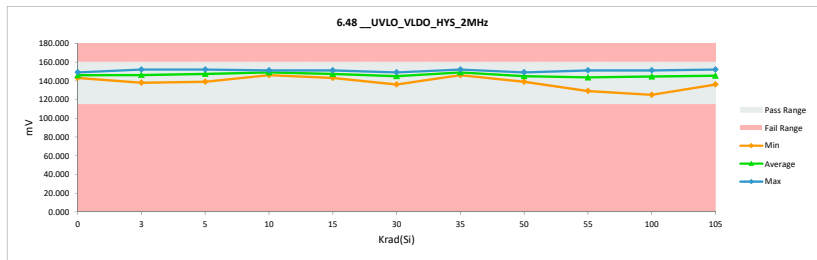


# HDR TID Report TPS7H5003-SP

6.48 UVLO_VLDO_HYS_2MHz				
Test Site	Tester	Unit	Max Limit	Min Limit
		mV	160	115
Krad(Si)	Serial #	PRE	POST	Delta
0	137	149.000	149.000	0.000
0	138	145.000	146.000	1.000
0	139	145.000	143.000	-2.000
3	197	151.000	147.000	-4.000
3	198	142.000	138.000	-4.000
3	199	152.000	152.000	0.000
3	202	149.000	149.000	0.000
3	203	142.000	145.000	3.000
5	204	151.000	150.000	-1.000
5	205	143.000	146.000	3.000
5	206	148.000	152.000	4.000
5	208	143.000	139.000	-4.000
5	209	152.000	150.000	-2.000
10	210	148.000	150.000	2.000
10	211	145.000	151.000	6.000
10	212	146.000	146.000	0.000
10	213	150.000	151.000	1.000
10	214	144.000	147.000	3.000
15	215	152.000	151.000	-1.000
15	217	148.000	151.000	3.000
15	219	142.000	143.000	1.000
15	220	148.000	146.000	-2.000
15	221	143.000	146.000	3.000
30	222	134.000	136.000	2.000
30	223	147.000	149.000	2.000
30	224	148.000	147.000	-1.000
30	225	146.000	145.000	-1.000
30	226	149.000	148.000	-1.000
35	227	152.000	148.000	-4.000
35	228	147.000	146.000	-1.000
35	229	150.000	152.000	2.000
35	230	150.000	148.000	-2.000
35	231	152.000	151.000	-1.000
50	170	153.000	146.000	-7.000
50	172	140.000	139.000	-1.000
50	173	148.000	149.000	1.000
50	175	145.000	144.000	-1.000
50	176	150.000	147.000	-3.000
55	177	147.000	143.000	-4.000
55	178	151.000	147.000	-4.000
55	179	148.000	148.000	0.000
55	180	135.000	129.000	-6.000
55	181	145.000	151.000	6.000
100	154	147.000	149.000	2.000
100	155	152.000	151.000	-1.000
100	156	149.000	151.000	2.000
100	157	139.000	139.000	0.000
100	158	146.000	143.000	-3.000
100	159	140.000	150.000	10.000
100	160	151.000	151.000	0.000
100	161	150.000	146.000	-4.000
100	162	146.000	130.000	-16.000
100	163	150.000	144.000	-6.000
100	164	151.000	151.000	0.000
100	165	146.000	138.000	-8.000
100	166	147.000	149.000	2.000
100	168	151.000	149.000	-2.000
100	169	150.000	145.000	-5.000
100	182	133.000	125.000	-8.000
100	183	140.000	141.000	1.000
100	184	150.000	144.000	-6.000
100	185	147.000	134.000	-13.000
100	186	145.000	147.000	2.000
100	187	151.000	145.000	-6.000
100	188	149.000	149.000	0.000
100	189	153.000	149.000	-4.000
100	190	147.000	151.000	4.000
105	191	147.000	152.000	5.000
105	192	148.000	148.000	0.000
105	193	151.000	151.000	0.000
105	194	146.000	143.000	-3.000
105	195	149.000	136.000	-13.000
105	196	147.000	143.000	-4.000
Max		153.000	152.000	10.000
Average		147.027	145.822	-1.205
Min		133.000	125.000	-16.000
Std Dev		4.308	5.668	4.324

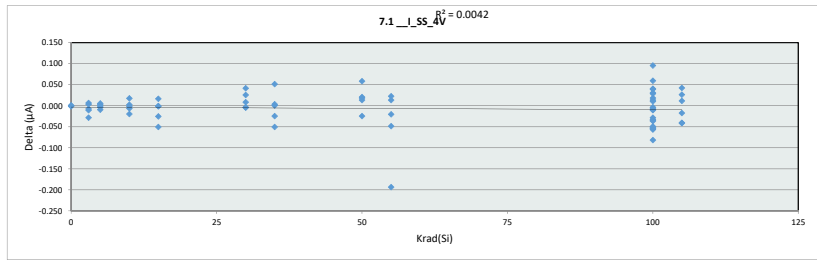


6.48 UVLO_VLD											
Test Site	Tester	Unit	Max Limit	Min Limit							
		mV	160	115							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	143.000	138.000	139.000	146.000	143.000	136.000	146.000	139.000	129.000	125.000	136.000
Average	146.000	146.200	147.400	149.000	147.400	145.000	149.000	145.000	143.600	144.625	145.500
Max	149.000	152.000	152.000	151.000	151.000	149.000	152.000	149.000	151.000	151.000	152.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

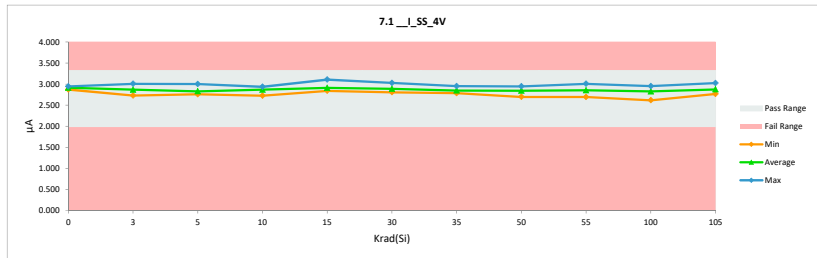


HDR TID Report  
TPS7H5003-SP

		7.1_I_SS_4V		
Test Site				
Tester				
Test Number				
Unit				
Max Limit		3.32	3.32	
Min Limit		1.98	1.98	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	2.926	2.926	0.000
0	138	2.946	2.944	-0.002
0	139	2.871	2.871	0.000
3	197	2.849	2.855	0.006
3	198	2.903	2.892	-0.011
3	199	2.735	2.727	-0.008
3	202	2.854	2.857	0.003
3	203	3.036	3.007	-0.029
5	204	2.757	2.759	0.002
5	205	3.002	3.001	-0.001
5	206	2.856	2.846	-0.010
5	208	2.767	2.772	0.005
5	209	2.772	2.768	-0.004
10	210	2.727	2.725	-0.002
10	211	2.925	2.918	-0.007
10	212	2.932	2.934	0.002
10	213	2.844	2.824	-0.020
10	214	2.919	2.936	0.017
15	215	2.886	2.860	-0.026
15	217	2.926	2.875	-0.051
15	219	2.866	2.865	-0.001
15	220	2.824	2.840	0.016
15	221	3.109	3.107	-0.002
30	222	2.889	2.930	0.041
30	223	2.810	2.806	-0.004
30	224	2.833	2.828	-0.005
30	225	2.835	2.843	0.008
30	226	3.002	3.027	0.025
35	227	2.733	2.784	0.051
35	228	2.844	2.793	-0.051
35	229	2.952	2.952	0.000
35	230	2.902	2.905	0.003
35	231	2.830	2.805	-0.025
50	170	2.799	2.774	-0.025
50	172	2.872	2.892	0.020
50	173	2.856	2.914	0.058
50	175	2.933	2.946	0.013
50	176	2.678	2.696	0.018
55	177	2.825	2.776	-0.049
55	178	2.888	2.694	-0.194
55	179	2.923	2.902	-0.021
55	180	2.994	3.007	0.013
55	181	2.867	2.889	0.022
100	154	2.737	2.796	0.059
100	155	2.892	2.910	0.018
100	156	2.954	2.904	-0.050
100	157	2.856	2.951	0.095
100	158	2.739	2.702	-0.037
100	159	2.953	2.947	-0.006
100	160	2.874	2.904	0.030
100	161	2.952	2.917	-0.035
100	162	2.874	2.864	-0.010
100	163	2.900	2.889	-0.011
100	164	2.844	2.857	0.013
100	165	2.800	2.810	0.010
100	166	2.735	2.678	-0.057
100	168	2.949	2.867	-0.082
100	169	2.868	2.817	-0.051
100	182	2.670	2.615	-0.055
100	183	2.851	2.847	-0.004
100	184	2.934	2.880	-0.054
100	185	2.889	2.917	0.028
100	186	2.881	2.828	-0.053
100	187	2.792	2.763	-0.029
100	188	2.625	2.664	0.039
100	189	2.772	2.739	-0.033
100	190	2.727	2.767	0.040
105	191	2.814	2.796	-0.018
105	192	2.907	2.918	0.011
105	193	2.723	2.765	0.042
105	194	2.870	2.829	-0.041
105	195	2.955	2.913	-0.042
105	196	2.997	3.023	0.026
	Max	3.109	3.107	0.095
	Average	2.861	2.854	-0.007
	Min	2.625	2.615	-0.194
	Std Dev	0.091	0.094	0.039

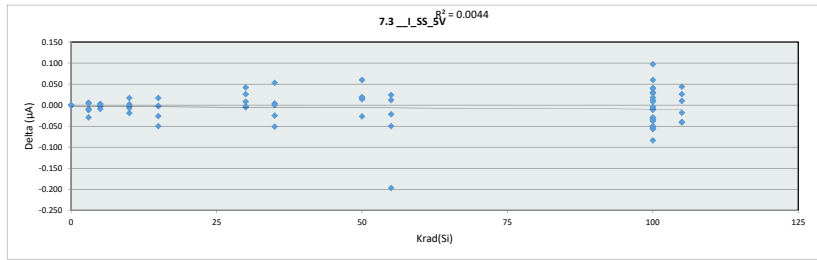


		7.1_I_SS_4V										
Test Site												
Tester												
Test Number												
Max Limit		3.32	µA									
Min Limit		1.98	µA									
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min		2.871	2.727	2.759	2.725	2.840	2.806	2.784	2.696	2.694	2.615	2.765
Average		2.914	2.868	2.829	2.867	2.909	2.887	2.848	2.844	2.854	2.826	2.874
Max		2.944	3.007	3.001	2.936	3.107	3.027	2.952	2.946	3.007	2.951	3.023
UL		3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320



# HDR TID Report TPS7H5003-SP

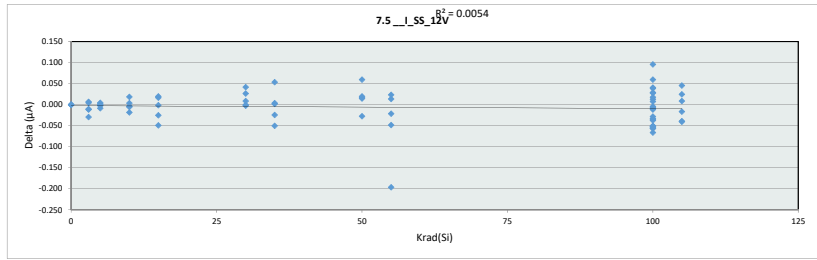
		7.3_I_SS_5V		
Test Site	Tester			
Test Number	Unit	µA	µA	
Max Limit		3.32	3.32	
Min Limit		1.98	1.98	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	2.950	2.950	0.000
0	138	2.968	2.967	-0.001
0	139	2.895	2.895	0.000
3	197	2.871	2.877	0.006
3	198	2.927	2.915	-0.012
3	199	2.755	2.746	-0.009
3	202	2.875	2.879	0.004
3	203	3.059	3.030	-0.029
5	204	2.779	2.781	0.002
5	205	3.026	3.024	-0.002
5	206	2.879	2.870	-0.009
5	208	2.791	2.794	0.003
5	209	2.795	2.792	-0.003
10	210	2.749	2.746	-0.003
10	211	2.949	2.942	-0.007
10	212	2.955	2.957	0.002
10	213	2.867	2.848	-0.019
10	214	2.941	2.958	0.017
15	215	2.908	2.882	-0.026
15	217	2.949	2.899	-0.050
15	219	2.889	2.887	-0.002
15	220	2.846	2.863	0.017
15	221	3.135	3.133	-0.002
30	222	2.912	2.954	0.042
30	223	2.831	2.828	-0.003
30	224	2.855	2.850	-0.005
30	225	2.857	2.865	0.008
30	226	3.026	3.052	0.026
35	227	2.754	2.807	0.053
35	228	2.865	2.814	-0.051
35	229	2.975	2.975	0.000
35	230	2.925	2.929	0.004
35	231	2.853	2.828	-0.025
50	170	2.821	2.794	-0.027
50	172	2.895	2.914	0.019
50	173	2.876	2.936	0.060
50	175	2.956	2.970	0.014
50	176	2.701	2.718	0.017
55	177	2.847	2.797	-0.050
55	178	2.912	2.715	-0.197
55	179	2.946	2.924	-0.022
55	180	3.018	3.030	0.012
55	181	2.889	2.913	0.024
100	154	2.759	2.819	0.060
100	155	2.915	2.933	0.018
100	156	2.976	2.926	-0.050
100	157	2.879	2.976	0.097
100	158	2.760	2.722	-0.038
100	159	2.977	2.970	-0.007
100	160	2.896	2.926	0.030
100	161	2.976	2.941	-0.035
100	162	2.896	2.885	-0.011
100	163	2.922	2.912	-0.010
100	164	2.866	2.878	0.012
100	165	2.822	2.830	0.008
100	166	2.756	2.699	-0.057
100	168	2.972	2.888	-0.084
100	169	2.890	2.839	-0.051
100	182	2.691	2.636	-0.055
100	183	2.873	2.869	-0.004
100	184	2.958	2.902	-0.056
100	185	2.911	2.939	0.028
100	186	2.903	2.850	-0.053
100	187	2.814	2.785	-0.029
100	188	2.646	2.685	0.039
100	189	2.794	2.761	-0.033
100	190	2.747	2.788	0.041
105	191	2.836	2.818	-0.018
105	192	2.930	2.940	0.010
105	193	2.742	2.786	0.044
105	194	2.891	2.851	-0.040
105	195	2.980	2.939	-0.041
105	196	3.022	3.048	0.026
Max		3.135	3.133	0.097
Average		2.883	2.877	-0.007
Min		2.646	2.636	-0.197
Std Dev		0.091	0.095	0.040



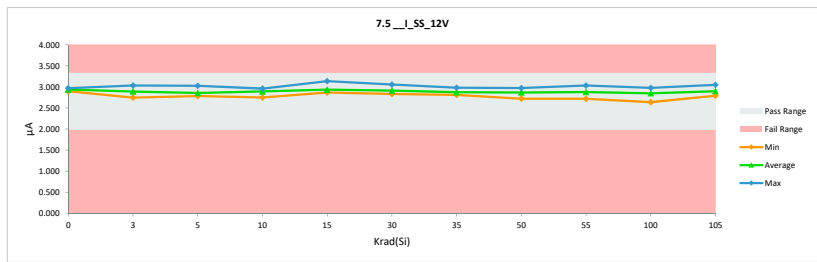
		7.3_I_SS_5V															
Test Site	Tester																
Test Number	Unit	µA	µA														
Max Limit		3.32	µA														
Min Limit		1.98	µA														
Krad(Si)	LL	Min	Average	Max	UL	0	3	5	10	15	30	35	50	55	100	105	
		1.980	2.895	2.937	3.320	1.980	2.746	2.889	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2.848	2.897	3.048
		1.980	2.746	2.889	3.320	1.980	2.871	2.852	2.890	2.933	2.910	2.871	2.866	2.876	2		

# HDR TID Report TPS7H5003-SP

7.5_I_SS_12V				
Test Site				
Tester				
Test Number				
Unit		µA	µA	
Max Limit		3.32	3.32	
Min Limit		1.98	1.98	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	2.951	2.951	0.000
0	138	2.970	2.969	-0.001
0	139	2.897	2.896	-0.001
3	197	2.871	2.877	0.006
3	198	2.927	2.915	-0.012
3	199	2.757	2.746	-0.011
3	202	2.876	2.880	0.004
3	203	3.062	3.032	-0.030
5	204	2.780	2.781	0.001
5	205	3.027	3.026	-0.001
5	206	2.880	2.871	-0.009
5	208	2.791	2.795	0.004
5	209	2.796	2.793	-0.003
10	210	2.751	2.747	-0.004
10	211	2.949	2.943	-0.006
10	212	2.956	2.959	0.003
10	213	2.868	2.849	-0.019
10	214	2.941	2.959	0.018
15	215	2.909	2.883	-0.026
15	217	2.950	2.900	-0.050
15	219	2.873	2.889	0.016
15	220	2.846	2.865	0.019
15	221	3.137	3.135	-0.002
30	222	2.913	2.954	0.041
30	223	2.831	2.830	-0.001
30	224	2.856	2.853	-0.003
30	225	2.858	2.866	0.008
30	226	3.028	3.054	0.026
35	227	2.756	2.809	0.053
35	228	2.865	2.814	-0.051
35	229	2.977	2.978	0.001
35	230	2.926	2.929	0.003
35	231	2.854	2.829	-0.025
50	170	2.823	2.795	-0.028
50	172	2.897	2.916	0.019
50	173	2.877	2.936	0.059
50	175	2.957	2.971	0.014
50	176	2.703	2.719	0.016
55	177	2.848	2.799	-0.049
55	178	2.914	2.717	-0.197
55	179	2.947	2.925	-0.022
55	180	3.019	3.032	0.013
55	181	2.890	2.913	0.023
100	154	2.760	2.819	0.059
100	155	2.917	2.934	0.017
100	156	2.977	2.926	-0.051
100	157	2.880	2.975	0.095
100	158	2.761	2.723	-0.038
100	159	2.979	2.972	-0.007
100	160	2.898	2.926	0.028
100	161	2.976	2.941	-0.035
100	162	2.897	2.885	-0.012
100	163	2.923	2.913	-0.010
100	164	2.866	2.878	0.012
100	165	2.823	2.830	0.007
100	166	2.757	2.700	-0.057
100	168	2.957	2.890	-0.067
100	169	2.891	2.839	-0.052
100	182	2.692	2.637	-0.055
100	183	2.873	2.868	-0.005
100	184	2.959	2.902	-0.057
100	185	2.913	2.940	0.027
100	186	2.904	2.852	-0.052
100	187	2.815	2.786	-0.029
100	188	2.647	2.685	0.038
100	189	2.795	2.761	-0.034
100	190	2.748	2.788	0.040
105	191	2.837	2.820	-0.017
105	192	2.933	2.941	0.008
105	193	2.744	2.789	0.045
105	194	2.892	2.852	-0.040
105	195	2.980	2.939	-0.041
105	196	3.024	3.048	0.024
	Max	3.137	3.135	0.095
	Average	2.884	2.878	-0.006
	Min	2.647	2.637	-0.197
	Std Dev	0.091	0.095	0.039

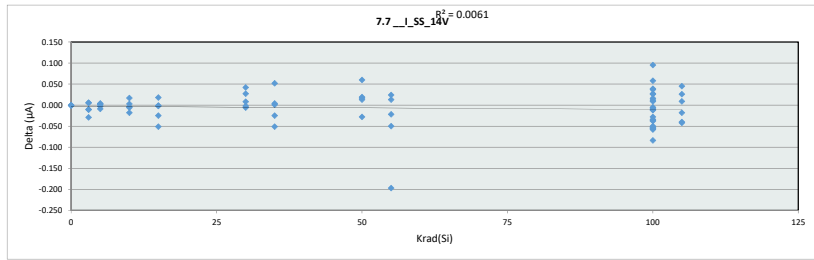


7.5_I_SS_12V											
Test Site											
Tester											
Test Number											
Max Limit		3.32		µA							
Min Limit		1.98		µA							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.896	2.746	2.781	2.747	2.865	2.830	2.809	2.719	2.717	2.637	2.789
Average	2.939	2.890	2.853	2.891	2.934	2.911	2.872	2.867	2.877	2.849	2.898
Max	2.969	3.032	3.026	2.959	3.135	3.054	2.978	2.971	3.032	2.975	3.048
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

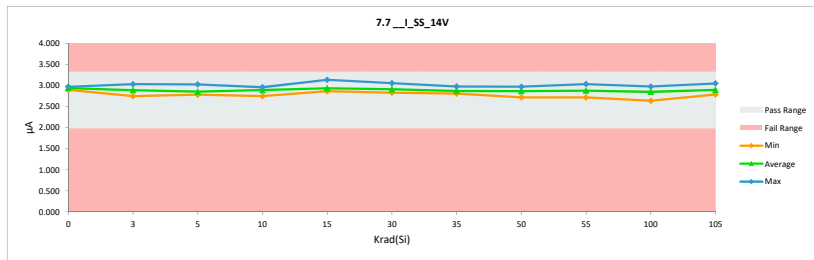


# HDR TID Report TPS7H5003-SP

7.7_I_SS_14V				
Test Site	Tester	Test Number	Unit	
			µA	µA
Max Limit			3.32	3.32
Min Limit			1.98	1.98
Krad(Si)	Serial #	PRE	POST	Delta
0	137	2.951	2.951	0.000
0	138	2.970	2.969	-0.001
0	139	2.897	2.896	-0.001
3	197	2.872	2.878	0.006
3	198	2.927	2.916	-0.011
3	199	2.757	2.747	-0.010
3	202	2.875	2.880	0.005
3	203	3.061	3.032	-0.029
5	204	2.779	2.782	0.003
5	205	3.026	3.027	0.001
5	206	2.880	2.871	-0.009
5	208	2.791	2.795	0.004
5	209	2.796	2.793	-0.003
10	210	2.751	2.747	-0.004
10	211	2.949	2.943	-0.006
10	212	2.956	2.959	0.003
10	213	2.868	2.850	-0.018
10	214	2.942	2.959	0.017
15	215	2.908	2.883	-0.025
15	217	2.950	2.899	-0.051
15	219	2.890	2.889	-0.001
15	220	2.847	2.865	0.018
15	221	3.137	3.135	-0.002
30	222	2.913	2.955	0.042
30	223	2.831	2.829	-0.002
30	224	2.857	2.851	-0.006
30	225	2.858	2.866	0.008
30	226	3.028	3.055	0.027
35	227	2.756	2.808	0.052
35	228	2.865	2.814	-0.051
35	229	2.977	2.978	0.001
35	230	2.926	2.930	0.004
35	231	2.855	2.830	-0.025
50	170	2.823	2.795	-0.028
50	172	2.897	2.916	0.019
50	173	2.876	2.936	0.060
50	175	2.958	2.971	0.013
50	176	2.703	2.720	0.017
55	177	2.848	2.798	-0.050
55	178	2.914	2.717	-0.197
55	179	2.947	2.925	-0.022
55	180	3.020	3.033	0.013
55	181	2.890	2.914	0.024
100	154	2.761	2.819	0.058
100	155	2.917	2.933	0.016
100	156	2.977	2.927	-0.050
100	157	2.880	2.975	0.095
100	158	2.761	2.723	-0.038
100	159	2.980	2.973	-0.007
100	160	2.897	2.923	0.026
100	161	2.977	2.941	-0.036
100	162	2.897	2.885	-0.012
100	163	2.924	2.913	-0.011
100	164	2.866	2.878	0.012
100	165	2.823	2.832	0.009
100	166	2.757	2.701	-0.056
100	168	2.974	2.890	-0.084
100	169	2.891	2.839	-0.052
100	182	2.692	2.637	-0.055
100	183	2.873	2.868	-0.005
100	184	2.960	2.902	-0.058
100	185	2.912	2.939	0.027
100	186	2.904	2.851	-0.053
100	187	2.815	2.787	-0.028
100	188	2.647	2.684	0.037
100	189	2.794	2.760	-0.034
100	190	2.748	2.787	0.039
105	191	2.837	2.819	-0.018
105	192	2.932	2.941	0.009
105	193	2.743	2.788	0.045
105	194	2.892	2.852	-0.040
105	195	2.980	2.938	-0.042
105	196	3.023	3.049	0.026
Max		3.137	3.135	0.095
Average		2.884	2.878	-0.007
Min		2.647	2.637	-0.197
Std Dev		0.092	0.095	0.039

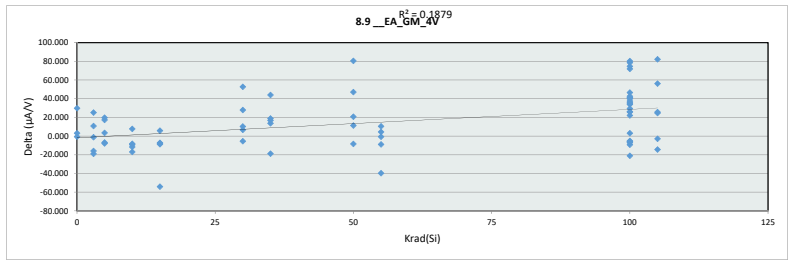


7.7_I_SS_14V											
Test Site	Tester	Test Number	Unit								
			µA								
Max Limit			3.32								
Min Limit			1.98								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.896	2.747	2.782	2.747	2.865	2.829	2.808	2.720	2.717	2.637	2.788
Average	2.939	2.891	2.854	2.892	2.934	2.911	2.872	2.868	2.877	2.849	2.898
Max	2.969	3.032	3.027	2.959	3.135	3.055	2.978	2.971	3.033	2.975	3.049
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

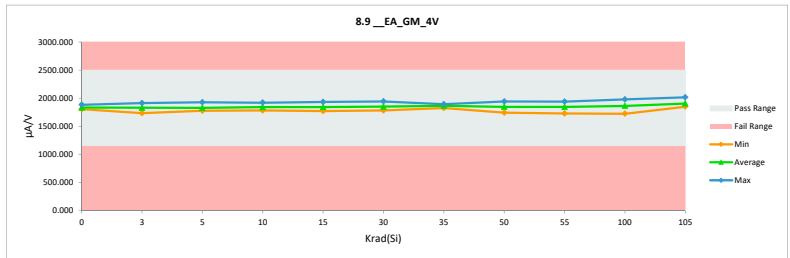


# HDR TID Report TPS7H5003-SP

		8.9_EA_GM_4V		
Test Site	Tester			
Test Number	Unit	µA/V	µA/V	
Max Limit	2500	2500		
Min Limit	1150	1150		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1810.699	1809.889	-0.810
0	138	1852.311	1881.909	29.598
0	139	1801.329	1804.176	2.847
3	197	1829.307	1813.214	-16.093
3	198	1912.467	1911.084	-1.383
3	199	1707.422	1732.459	25.037
3	202	1789.178	1799.754	10.576
3	203	1904.900	1885.653	-19.247
5	204	1830.704	1850.197	19.493
5	205	1932.035	1924.942	-7.093
5	206	1805.458	1797.334	-8.124
5	208	1757.034	1774.203	17.169
5	209	1784.506	1787.672	3.166
10	210	1833.545	1816.316	-17.229
10	211	1911.095	1918.679	7.584
10	212	1791.748	1779.897	-11.851
10	213	1879.347	1869.953	-9.394
10	214	1826.449	1818.008	-8.441
15	215	1871.382	1864.042	-7.340
15	217	1889.369	1835.009	-54.360
15	219	1807.880	1799.802	-8.078
15	220	1763.174	1768.872	5.698
15	221	1941.021	1932.035	-8.986
30	222	1913.907	1941.684	27.777
30	223	1733.130	1785.741	52.611
30	224	1816.050	1822.501	6.451
30	225	1786.854	1781.229	-5.625
30	226	1908.595	1918.679	10.084
35	227	1779.859	1823.640	43.781
35	228	1876.499	1889.965	13.466
35	229	1871.966	1890.636	18.670
35	230	1853.723	1870.276	16.553
35	231	1867.673	1848.556	-19.117
50	170	1748.811	1740.140	-8.671
50	172	1835.009	1881.909	46.900
50	173	1774.212	1854.441	80.229
50	175	1918.690	1939.226	20.536
50	176	1795.843	1806.867	11.024
55	177	1809.235	1813.439	4.204
55	178	1852.321	1812.411	-39.910
55	179	1927.330	1937.614	10.284
55	180	1925.848	1924.964	-0.884
55	181	1734.787	1725.660	-9.127
100	154	1768.881	1847.263	78.382
100	155	1843.356	1884.085	40.729
100	156	1942.853	1976.627	33.774
100	157	1902.168	1976.639	74.471
100	158	1753.600	1744.120	-9.480
100	159	1875.912	1913.143	37.231
100	160	1727.384	1753.087	25.703
100	161	1894.008	1934.787	40.779
100	162	1858.158	1883.916	25.758
100	163	1801.338	1835.762	34.424
100	164	1897.693	1933.449	35.756
100	165	1822.422	1902.556	80.134
100	166	1823.640	1869.963	46.323
100	168	1924.964	1918.679	-6.285
100	169	1858.765	1853.365	-5.400
100	182	1866.998	1869.953	2.955
100	183	1851.320	1893.719	42.399
100	184	1912.457	1906.264	-6.193
100	185	1829.317	1851.156	21.839
100	186	1871.183	1864.042	-7.141
100	187	1743.504	1722.330	-21.174
100	188	1692.642	1764.370	71.728
100	189	1744.873	1774.203	29.330
100	190	1767.204	1806.858	39.654
105	191	1822.244	1847.762	25.518
105	192	1866.665	1891.051	24.386
105	193	1823.640	1905.711	82.071
105	194	1887.944	1884.911	-3.033
105	195	1900.670	1886.119	-14.551
105	196	1958.811	2014.725	55.914
Max		1958.811	2014.725	82.071
Average		1838.319	1853.360	15.041
Min		1692.642	1722.330	-54.360
Std Dev		63.735	65.317	28.872

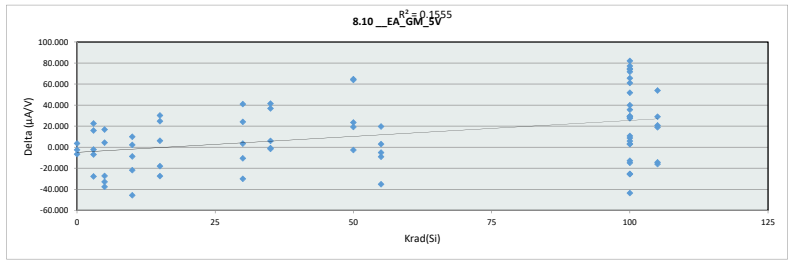


		8.9_EA_GM_4V											
Test Site	Tester												
Test Number	Unit	µA/V	µA/V										
Max Limit	2500	2500											
Min Limit	1150	1150											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1804.176	1732.459	1774.203	1779.897	1768.872	1781.229	1823.640	1740.140	1725.660	1722.330	1847.762		
Average	1831.991	1828.433	1826.870	1840.571	1839.952	1849.967	1864.615	1844.517	1842.818	1861.681	1905.047		
Max	1881.909	1911.084	1924.942	1918.679	1932.035	1941.684	1890.636	1939.226	1937.614	1976.639	2014.725		
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000

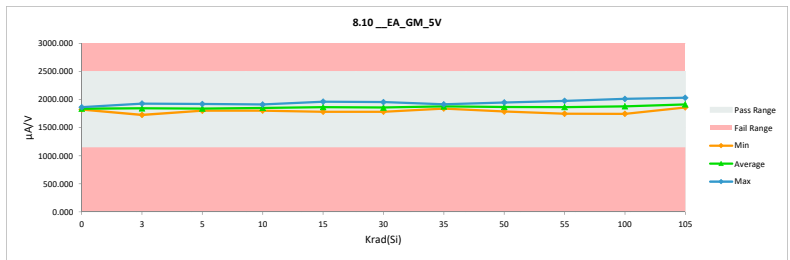


# HDR TID Report TPS7H5003-SP

8.10_EA_GM_5V				
Test Site	Tester	Test Number	Unit	
			µA/V	µA/V
Max Limit	2500		2500	
Min Limit	1150		1150	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1822.204	1825.754	3.550
0	138	1866.001	1863.514	-2.487
0	139	1829.307	1822.689	-6.618
3	197	1848.688	1846.501	-2.187
3	198	1935.077	1928.116	-6.961
3	199	1710.512	1726.362	15.850
3	202	1784.962	1807.471	22.509
3	203	1940.808	1913.023	-27.785
5	204	1893.623	1866.375	-27.248
5	205	1958.800	1921.195	-37.605
5	206	1836.395	1803.527	-32.868
5	208	1784.516	1801.329	16.813
5	209	1802.587	1806.848	4.261
10	210	1838.045	1829.297	-8.748
10	211	1934.252	1912.457	-21.795
10	212	1800.633	1802.829	2.196
10	213	1904.857	1859.136	-45.721
10	214	1839.688	1849.718	10.030
15	215	1882.553	1864.570	-17.983
15	217	1915.371	1887.934	-27.437
15	219	1820.642	1826.728	6.086
15	220	1757.623	1782.260	24.637
15	221	1932.737	1962.982	30.245
30	222	1914.202	1955.159	40.957
30	223	1757.697	1781.844	24.147
30	224	1853.723	1823.640	-30.083
30	225	1802.819	1806.284	3.465
30	226	1935.557	1924.953	-10.604
35	227	1802.713	1839.587	36.874
35	228	1886.936	1886.119	-0.817
35	229	1908.953	1914.945	5.992
35	230	1842.142	1883.620	41.478
35	231	1870.318	1868.755	-1.563
50	170	1767.828	1786.996	19.168
50	172	1850.166	1914.803	64.637
50	173	1801.338	1865.337	63.999
50	175	1950.454	1947.770	-2.684
50	176	1801.338	1824.771	23.433
55	177	1823.095	1818.008	-5.087
55	178	1875.912	1840.737	-35.175
55	179	1956.926	1976.639	19.713
55	180	1956.116	1947.036	-9.080
55	181	1744.401	1747.308	2.907
100	154	1780.652	1862.738	82.086
100	155	1864.042	1935.792	71.750
100	156	1983.287	2012.091	28.804
100	157	1929.547	2003.525	73.978
100	158	1789.837	1746.326	-43.511
100	159	1887.944	1917.440	29.496
100	160	1750.115	1753.087	2.972
100	161	1929.181	1956.572	27.391
100	162	1892.790	1879.990	-12.800
100	163	1835.009	1843.711	8.702
100	164	1901.683	1953.474	51.791
100	165	1847.680	1922.273	74.593
100	166	1829.307	1890.369	61.062
100	168	1956.937	1931.268	-25.669
100	169	1868.256	1897.403	29.147
100	182	1875.912	1886.607	10.695
100	183	1875.912	1911.585	35.673
100	184	1906.274	1909.355	3.081
100	185	1858.168	1864.052	5.884
100	186	1886.331	1861.085	-25.246
100	187	1762.655	1747.891	-14.764
100	188	1717.305	1794.470	77.165
100	189	1766.981	1806.867	39.886
100	190	1773.621	1839.163	65.542
105	191	1840.576	1859.435	18.859
105	192	1875.822	1904.868	28.946
105	193	1861.095	1881.919	20.824
105	194	1918.679	1902.567	-16.112
105	195	1912.457	1898.068	-14.389
105	196	1978.457	2032.333	53.876
Max		1983.287	2032.333	82.086
Average		1857.522	1869.469	11.947
Min		1710.512	1726.362	-45.721
Std Dev		66.154	65.970	31.635

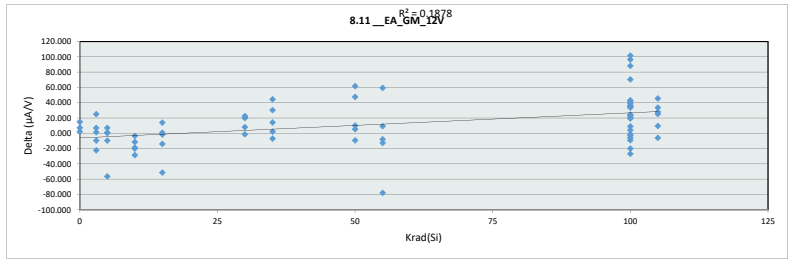


8.10_EA_GM_5V											
Test Site	Tester	Test Number	Unit								
			µA/V	µA/V							
Max Limit	2500		2500								
Min Limit	1150		1150								
Krad(Si)	0	5	10	15	30	35	50	55	100	105	
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1822.689	1726.362	1801.329	1802.829	1782.260	1781.844	1839.587	1786.996	1747.308	1746.326	1859.435
Average	1837.319	1844.295	1839.855	1850.687	1864.895	1858.376	1878.605	1867.935	1865.946	1880.297	1913.198
Max	1863.514	1928.116	1921.195	1912.457	1962.982	1955.159	1914.945	1947.770	1976.639	2012.091	2032.333
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000

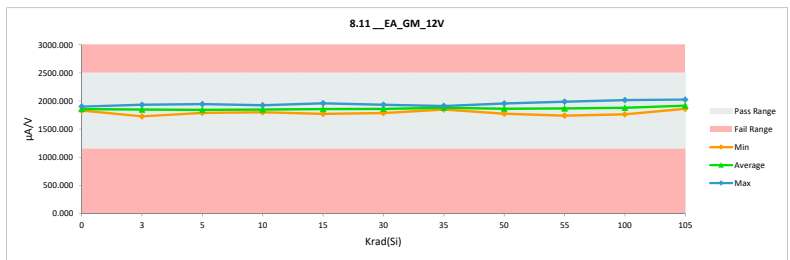


# HDR TID Report TPS7H5003-SP

8.11_EA_GM_12V				
Test Site	Tester	Test Number	Unit	
			µA/V	µA/V
Max Limit			2500	2500
Min Limit			1150	1150
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1832.874	1834.999	2.125
0	138	1885.186	1900.121	14.935
0	139	1821.581	1828.679	7.098
3	197	1839.577	1846.501	6.924
3	198	1940.808	1931.268	-9.540
3	199	1724.401	1725.687	1.286
3	202	1795.843	1820.810	24.967
3	203	1939.450	1917.111	-22.339
5	204	1875.733	1866.188	-9.545
5	205	1936.965	1944.012	7.047
5	206	1860.889	1804.399	-56.490
5	208	1785.418	1786.502	1.084
5	209	1805.778	1806.119	0.341
10	210	1844.765	1826.082	-18.683
10	211	1935.144	1923.695	-11.449
10	212	1803.255	1799.677	-3.578
10	213	1889.369	1869.286	-20.083
10	214	1851.606	1823.006	-28.600
15	215	1884.022	1869.953	-14.069
15	217	1910.105	1858.683	-51.422
15	219	1826.072	1824.354	-1.718
15	220	1768.126	1768.872	0.746
15	221	1945.500	1959.303	13.803
30	222	1908.834	1931.257	22.423
30	223	1764.927	1785.770	20.843
30	224	1828.181	1836.144	7.963
30	225	1802.732	1801.319	-1.413
30	226	1911.411	1931.257	19.846
35	227	1802.083	1846.511	44.428
35	228	1884.858	1877.707	-7.151
35	229	1896.974	1911.073	14.099
35	230	1861.529	1891.776	30.247
35	231	1866.115	1868.339	2.224
50	170	1764.184	1774.212	10.028
50	172	1848.434	1896.041	47.607
50	173	1801.338	1862.924	61.586
50	175	1963.465	1954.031	-9.434
50	176	1821.126	1826.449	5.323
55	177	1820.079	1829.307	9.228
55	178	1901.650	1823.640	-78.010
55	179	1926.533	1985.716	59.183
55	180	1964.764	1952.076	-12.688
55	181	1747.381	1739.446	-7.935
100	154	1784.962	1855.240	70.278
100	155	1869.963	1905.711	35.748
100	156	1982.560	2001.827	19.267
100	157	1918.690	2014.942	96.252
100	158	1789.474	1762.535	-26.939
100	159	1912.467	1911.422	-1.045
100	160	1724.640	1763.591	38.951
100	161	1922.438	1956.937	34.499
100	162	1869.963	1890.359	20.396
100	163	1818.018	1841.141	23.123
100	164	1910.671	1947.228	36.557
100	165	1822.709	1910.779	88.070
100	166	1846.511	1887.944	41.433
100	168	1972.676	1963.465	-9.211
100	169	1862.790	1867.123	4.333
100	182	1881.909	1875.912	-5.997
100	183	1869.953	1878.916	8.963
100	184	1912.457	1909.638	-2.819
100	185	1835.009	1868.474	33.465
100	186	1889.965	1869.963	-20.002
100	187	1739.284	1763.591	24.307
100	188	1697.515	1799.136	101.621
100	189	1760.704	1783.975	23.271
100	190	1778.095	1821.225	43.130
105	191	1833.576	1861.467	27.891
105	192	1894.008	1887.944	-6.064
105	193	1852.321	1885.748	33.427
105	194	1887.392	1912.457	25.065
105	195	1919.480	1928.903	9.423
105	196	1978.784	2024.180	45.396
Max		1982.560	2024.180	101.621
Average		1856.549	1868.659	12.110
Min		1697.515	1725.687	-78.010
Std Dev		66.470	66.379	31.282



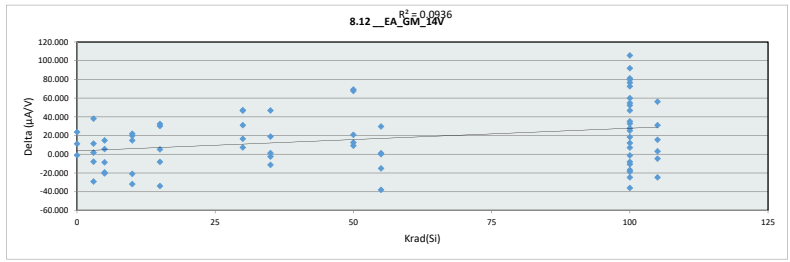
8.11_EA_GM_12											
Test Site	Tester	Test Number	Unit								
			µA/V								
Max Limit			2500								
Min Limit			1150								
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1828.679	1725.687	1786.502	1799.677	1768.872	1785.770	1846.511	1774.212	1739.446	1762.535	1861.467
Average	1854.600	1848.275	1841.444	1848.349	1856.233	1857.149	1879.081	1862.731	1866.037	1877.128	1916.783
Max	1900.121	1931.268	1944.012	1923.695	1959.303	1931.257	1911.073	1954.031	1985.716	2014.942	2024.180
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



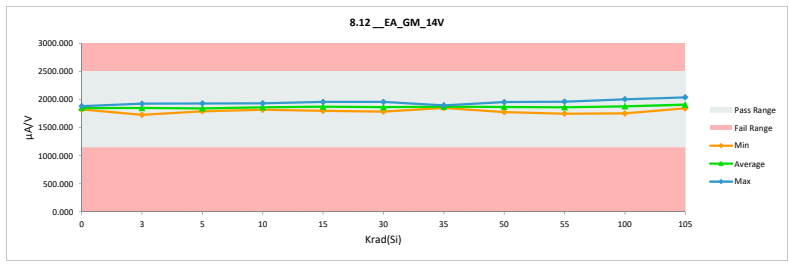


# HDR TID Report TPS7H5003-SP

8.12_EA_GM_14V				
Test Site				
Tester				
Test Number				
Unit	µA/V	µA/V		
Max Limit	2500	2500		
Min Limit	1150	1150		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1826.728	1837.874	11.146
0	138	1858.168	1881.909	23.741
0	139	1825.525	1824.324	-1.201
3	197	1829.307	1840.737	11.430
3	198	1934.899	1926.832	-8.067
3	199	1726.158	1727.944	1.786
3	202	1801.329	1839.305	37.976
3	203	1942.752	1913.591	-29.161
5	204	1866.105	1880.812	14.707
5	205	1949.592	1930.113	-19.479
5	206	1829.357	1808.874	-20.483
5	208	1785.409	1790.878	5.469
5	209	1807.169	1798.567	-8.602
10	210	1844.309	1823.274	-21.035
10	211	1913.907	1933.672	19.765
10	212	1803.313	1818.018	14.705
10	213	1903.625	1871.716	-31.909
10	214	1845.019	1866.894	21.875
15	215	1886.787	1878.611	-8.176
15	217	1919.096	1885.080	-34.016
15	219	1812.284	1842.385	30.101
15	220	1765.326	1797.671	32.345
15	221	1951.929	1957.063	5.134
30	222	1910.660	1957.588	46.928
30	223	1754.416	1785.352	30.936
30	224	1821.897	1838.428	16.531
30	225	1791.748	1799.069	7.321
30	226	1890.881	1937.614	46.733
35	227	1802.713	1849.391	46.678
35	228	1891.776	1880.401	-11.375
35	229	1895.452	1896.781	1.329
35	230	1856.965	1875.912	18.947
35	231	1869.203	1866.728	-2.475
50	170	1762.248	1774.822	12.574
50	172	1842.091	1911.334	69.243
50	173	1812.421	1880.116	67.695
50	175	1933.795	1954.578	20.783
50	176	1806.867	1815.913	9.046
55	177	1845.455	1830.265	-15.190
55	178	1861.106	1823.085	-38.021
55	179	1950.454	1950.454	0.000
55	180	1932.670	1962.259	29.589
55	181	1746.844	1747.891	1.047
100	154	1768.881	1860.765	91.884
100	155	1852.321	1924.953	72.632
100	156	1971.239	1989.637	18.398
100	157	1924.953	2004.770	79.817
100	158	1769.357	1752.437	-16.920
100	159	1924.964	1906.274	-18.690
100	160	1724.490	1784.469	59.979
100	161	1912.446	1937.614	25.168
100	162	1900.121	1889.475	-10.646
100	163	1818.018	1845.810	27.792
100	164	1906.838	1959.097	52.259
100	165	1823.640	1900.111	76.471
100	166	1826.459	1907.662	81.203
100	168	1973.720	1937.614	-36.106
100	169	1865.109	1863.711	-1.398
100	182	1881.898	1894.008	12.110
100	183	1859.765	1914.573	54.808
100	184	1905.387	1912.457	7.070
100	185	1835.009	1867.736	32.727
100	186	1887.350	1862.562	-24.788
100	187	1761.240	1753.087	-8.153
100	188	1707.353	1812.920	105.567
100	189	1760.667	1795.853	35.186
100	190	1776.880	1823.640	46.760
105	191	1840.747	1843.944	3.197
105	192	1883.419	1878.621	-4.798
105	193	1874.141	1905.073	30.932
105	194	1881.909	1897.339	15.430
105	195	1912.467	1887.785	-24.682
105	196	1982.056	2038.197	56.141
Max		1982.056	2038.197	105.567
Average		1853.707	1870.771	17.065
Min		1707.353	1727.944	-38.021
Std Dev		65.584	63.696	32.683

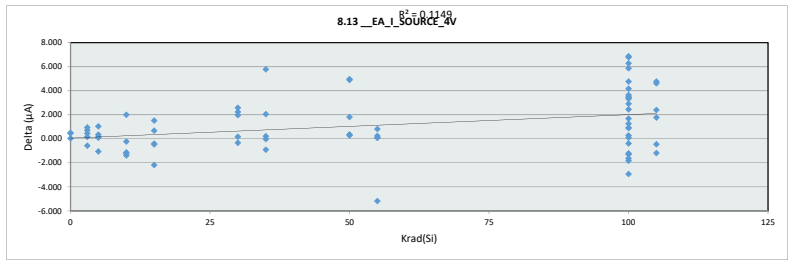


8.12_EA_GM_14											
Test Site											
Tester											
Test Number											
Max Limit	2500	µA/V									
Min Limit	1150	µA/V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1824.324	1727.944	1790.878	1818.018	1797.671	1785.352	1849.391	1774.822	1747.891	1752.437	1843.944
Average	1848.036	1849.682	1841.849	1862.715	1872.162	1863.610	1873.843	1867.353	1862.791	1879.218	1908.493
Max	1881.909	1926.832	1930.113	1933.672	1957.063	1957.588	1896.781	1954.578	1962.259	2004.770	2038.197
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000

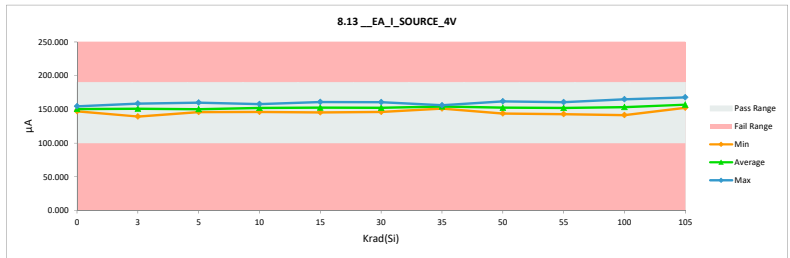


HDR TID Report  
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8.13 EA_I_SOURCE_4V				
Test Site	Tester	Test Number	Unit	
			µA	µA
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	150.061	150.080	0.019
0	138	154.053	154.523	0.470
0	139	146.746	147.196	0.450
3	197	150.025	150.951	0.926
3	198	158.324	158.466	0.142
3	199	138.945	139.372	0.427
3	202	147.694	148.387	0.693
3	203	158.119	157.531	-0.588
5	204	152.593	152.680	0.087
5	205	159.575	159.903	0.328
5	206	147.506	146.434	-1.072
5	208	145.170	146.184	1.014
5	209	145.874	145.977	0.103
10	210	150.450	149.301	-1.149
10	211	158.099	157.847	-0.252
10	212	147.508	146.278	-1.230
10	213	156.243	154.835	-1.408
10	214	150.766	152.739	1.973
15	215	154.723	154.244	-0.479
15	217	156.288	154.080	-2.208
15	219	147.976	148.634	0.658
15	220	143.904	145.407	1.503
15	221	161.411	160.991	-0.420
30	222	158.054	160.623	2.569
30	223	144.301	146.253	1.952
30	224	150.430	150.583	0.153
30	225	147.718	147.359	-0.359
30	226	155.015	157.236	2.221
35	227	145.536	151.298	5.762
35	228	155.881	154.960	-0.921
35	229	155.880	156.067	0.187
35	230	153.513	155.541	2.028
35	231	153.193	153.150	-0.043
50	170	143.612	143.954	0.342
50	172	150.779	155.663	4.884
50	173	146.415	151.363	4.948
50	175	161.714	161.977	0.263
50	176	147.768	149.559	1.791
55	177	148.135	148.359	0.224
55	178	154.712	149.514	-5.198
55	179	160.585	160.662	0.077
55	180	158.976	159.767	0.791
55	181	142.770	142.848	0.078
100	154	146.024	151.880	5.856
100	155	153.399	158.149	4.750
100	156	162.237	163.119	0.882
100	157	158.017	164.864	6.847
100	158	144.271	142.633	-1.638
100	159	154.976	156.204	1.228
100	160	140.457	143.767	3.310
100	161	158.940	161.365	2.425
100	162	153.607	153.867	0.260
100	163	150.791	152.455	1.664
100	164	156.939	160.354	3.415
100	165	149.683	155.950	6.267
100	166	149.097	152.561	3.464
100	168	161.700	158.757	-2.943
100	169	152.703	152.782	0.079
100	182	155.110	153.800	-1.310
100	183	152.018	156.169	4.151
100	184	156.557	156.156	-0.401
100	185	151.209	152.101	0.892
100	186	154.919	153.076	-1.843
100	187	142.613	141.385	-1.228
100	188	137.107	143.882	6.775
100	189	143.824	147.453	3.629
100	190	144.417	147.314	2.897
105	191	150.023	152.399	2.376
105	192	153.595	155.341	1.746
105	193	150.606	155.214	4.608
105	194	155.729	155.252	-0.477
105	195	156.934	155.725	-1.209
105	196	163.107	167.849	4.742
Max		163.107	167.849	6.847
Average		151.749	152.885	1.136
Min		137.107	139.372	-5.198
Std Dev		6.003	5.911	2.376

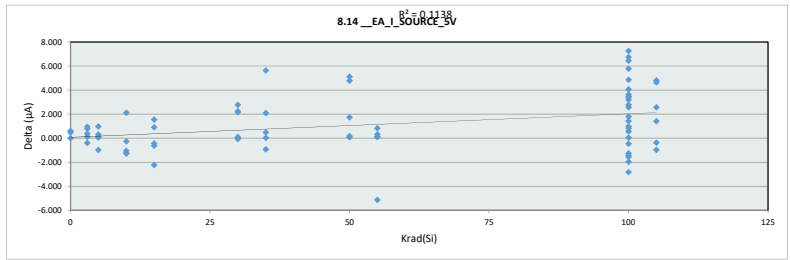


8.13 EA_I_SOURCE_4V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	147.196	139.372	145.977	146.278	145.407	146.253	151.298	143.954	142.848	141.385	152.399
Average	150.600	150.941	150.236	152.200	152.671	152.411	154.203	152.503	152.230	153.335	156.963
Max	154.523	158.466	159.903	157.847	160.991	160.623	156.067	161.977	160.662	164.864	167.849
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

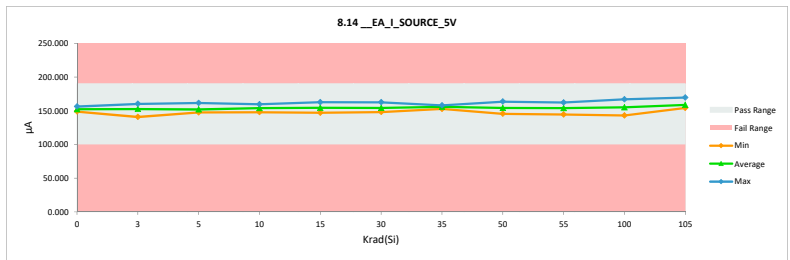


HDR TID Report  
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8.14_EA_I_SOURCE_5V				
Test Site	Tester	Test Number	Unit	
			µA	µA
Max Limit			190	190
Min Limit			100	100
Krad(Si)	Serial #	PRE	POST	Delta
0	137	151.668	151.658	-0.010
0	138	155.617	156.203	0.586
0	139	148.349	148.803	0.454
3	197	151.645	152.574	0.929
3	198	160.025	160.130	0.105
3	199	140.385	140.740	0.355
3	202	149.345	150.120	0.775
3	203	159.769	159.357	-0.412
5	204	154.436	154.471	0.035
5	205	161.229	161.520	0.291
5	206	149.211	148.214	-0.997
5	208	146.804	147.760	0.956
5	209	147.363	147.454	0.091
10	210	152.015	150.946	-1.069
10	211	159.833	159.552	-0.281
10	212	149.073	147.826	-1.247
10	213	157.942	156.650	-1.292
10	214	152.238	154.340	2.102
15	215	156.343	155.706	-0.637
15	217	157.962	155.709	-2.253
15	219	149.495	150.386	0.891
15	220	145.452	146.980	1.528
15	221	163.101	162.645	-0.456
30	222	159.794	162.548	2.754
30	223	145.880	148.034	2.154
30	224	152.174	152.253	0.079
30	225	149.233	149.148	-0.085
30	226	156.636	158.860	2.224
35	227	147.084	152.706	5.622
35	228	157.524	156.579	-0.945
35	229	157.391	157.864	0.473
35	230	155.083	157.156	2.073
35	231	154.790	154.811	0.021
50	170	145.292	145.447	0.155
50	172	152.468	157.228	4.760
50	173	147.862	152.962	5.100
50	175	163.401	163.474	0.073
50	176	149.309	151.026	1.717
55	177	149.744	150.054	0.310
55	178	156.341	151.193	-5.148
55	179	162.120	162.213	0.093
55	180	160.585	161.395	0.810
55	181	144.335	144.435	0.100
100	154	147.690	153.464	5.765
100	155	155.068	159.906	4.838
100	156	163.811	164.776	0.965
100	157	159.683	166.919	7.236
100	158	145.893	144.320	-1.573
100	159	156.639	158.055	1.416
100	160	141.964	145.351	3.387
100	161	160.531	163.082	2.551
100	162	155.136	155.689	0.553
100	163	152.344	154.133	1.789
100	164	158.585	162.023	3.438
100	165	151.401	157.833	6.432
100	166	150.649	153.814	3.165
100	168	163.503	160.676	-2.827
100	169	154.248	154.274	0.026
100	182	156.726	155.281	-1.445
100	183	153.722	157.769	4.047
100	184	158.140	157.664	-0.476
100	185	152.892	153.706	0.814
100	186	156.646	154.661	-1.985
100	187	144.221	142.945	-1.276
100	188	138.585	145.322	6.737
100	189	145.506	149.118	3.612
100	190	145.903	148.665	2.762
105	191	151.605	154.163	2.558
105	192	155.412	156.810	1.398
105	193	152.058	156.852	4.794
105	194	157.355	156.975	-0.380
105	195	158.532	157.550	-0.982
105	196	164.887	169.522	4.635
Max		164.887	169.522	7.236
Average		153.366	154.527	1.161
Min		138.585	140.740	-5.148
Std Dev		6.043	5.963	2.383

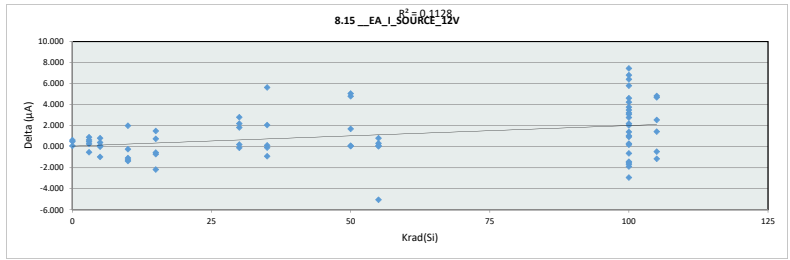


8.14_EA_I_SOURCE_5V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	148.803	140.740	147.454	147.826	146.980	148.034	152.706	145.447	144.435	142.945	154.163
Average	152.221	152.584	151.884	153.863	154.285	154.169	155.823	154.027	153.858	154.977	158.645
Max	156.203	160.130	161.520	159.552	162.645	162.548	157.864	163.474	162.213	166.919	169.522
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

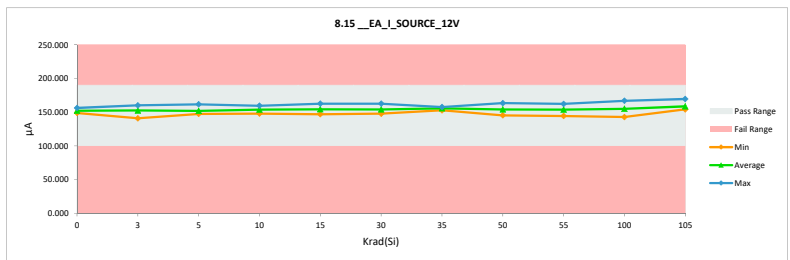


HDR TID Report  
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8.15_EA_I_SOURCE_12V				
Test Site	Tester	Test Number	Unit	
			µA	µA
Max Limit	190		190	
Min Limit	100		100	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	151.616	151.695	0.079
0	138	155.673	156.290	0.617
0	139	148.397	148.880	0.483
3	197	151.629	152.526	0.897
3	198	159.908	160.158	0.250
3	199	140.452	140.914	0.462
3	202	149.468	150.099	0.631
3	203	159.899	159.357	-0.542
5	204	154.529	154.521	-0.008
5	205	161.277	161.676	0.399
5	206	149.261	148.278	-0.983
5	208	146.810	147.613	0.803
5	209	147.457	147.549	0.092
10	210	152.163	151.085	-1.078
10	211	159.850	159.606	-0.244
10	212	149.330	147.957	-1.373
10	213	158.019	156.760	-1.259
10	214	152.389	154.381	1.992
15	215	156.424	155.701	-0.723
15	217	157.988	155.808	-2.180
15	219	149.667	150.411	0.744
15	220	145.576	147.058	1.482
15	221	163.243	162.675	-0.568
30	222	159.847	162.640	2.793
30	223	146.000	147.821	1.821
30	224	152.035	152.228	0.193
30	225	149.316	149.204	-0.112
30	226	156.798	158.989	2.191
35	227	147.193	152.821	5.628
35	228	157.479	156.564	-0.915
35	229	157.527	157.647	0.120
35	230	155.114	157.162	2.048
35	231	154.895	154.798	-0.097
50	170	145.285	145.381	0.096
50	172	152.576	157.364	4.788
50	173	147.924	152.975	5.051
50	175	163.466	163.512	0.046
50	176	149.482	151.170	1.688
55	177	149.881	150.193	0.312
55	178	156.415	151.357	-5.058
55	179	162.183	162.293	0.110
55	180	160.692	161.492	0.800
55	181	144.324	144.352	0.028
100	154	147.810	153.612	5.793
100	155	155.215	159.829	4.614
100	156	163.834	164.867	1.033
100	157	159.575	167.009	7.434
100	158	145.990	144.313	-1.677
100	159	156.712	158.116	1.404
100	160	142.089	145.579	3.490
100	161	160.626	162.819	2.193
100	162	155.379	155.667	0.288
100	163	152.368	154.391	2.023
100	164	158.590	161.797	3.207
100	165	151.409	157.814	6.405
100	166	150.761	153.829	3.068
100	168	163.679	160.741	-2.938
100	169	154.156	154.343	0.187
100	182	156.792	155.361	-1.431
100	183	153.676	157.909	4.233
100	184	158.230	157.586	-0.644
100	185	152.824	153.748	0.924
100	186	156.527	154.642	-1.885
100	187	144.403	142.857	-1.546
100	188	138.667	145.469	6.802
100	189	145.479	149.231	3.752
100	190	145.948	148.716	2.768
105	191	151.657	154.201	2.544
105	192	155.464	156.897	1.433
105	193	152.207	157.035	4.828
105	194	157.335	156.854	-0.481
105	195	158.611	157.445	-1.166
105	196	164.876	169.567	4.691
Max		164.876	169.567	7.434
Average		153.430	154.564	1.135
Min		138.667	140.914	-5.058
Std Dev		6.032	5.955	2.400

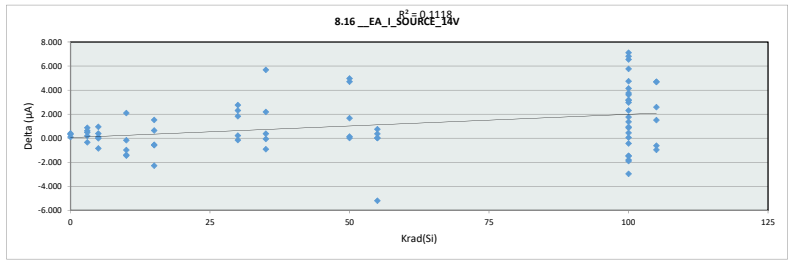


8.15_EA_I_SOURCE_12V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	148.880	140.914	147.549	147.957	147.058	147.821	152.821	145.381	144.352	142.857	154.201
Average	152.288	152.611	151.927	153.958	154.331	154.176	155.798	154.080	153.937	155.010	158.667
Max	156.290	160.158	161.676	159.606	162.675	162.640	157.647	163.512	162.293	167.009	169.567
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

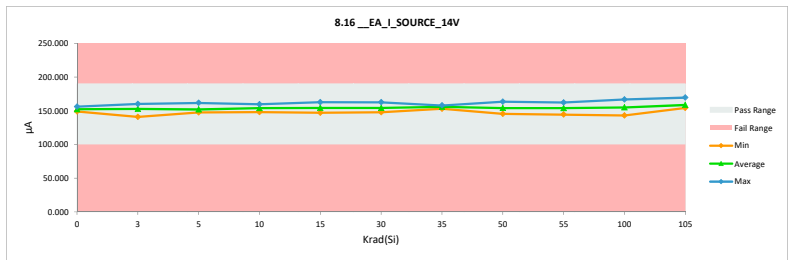


HDR TID Report  
TPS7H5003-SP

8.16_EA_I_SOURCE_14V				
Test Site	Tester	Test Number	Unit	
			µA	µA
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	151.604	151.688	0.084
0	138	155.627	156.009	0.382
0	139	148.511	148.812	0.301
3	197	151.631	152.485	0.854
3	198	159.919	160.087	0.168
3	199	140.412	140.881	0.469
3	202	149.423	150.022	0.599
3	203	159.744	159.389	-0.355
5	204	154.439	154.420	-0.019
5	205	161.193	161.565	0.372
5	206	149.153	148.285	-0.868
5	208	146.792	147.724	0.932
5	209	147.439	147.526	0.087
10	210	152.110	151.111	-0.999
10	211	159.721	159.541	-0.180
10	212	149.303	147.847	-1.456
10	213	158.957	156.630	-1.407
10	214	152.263	154.339	2.076
15	215	156.281	155.687	-0.594
15	217	158.006	155.706	-2.300
15	219	149.561	150.187	0.626
15	220	145.429	146.938	1.509
15	221	163.109	162.556	-0.553
30	222	159.771	162.511	2.740
30	223	145.956	147.761	1.805
30	224	151.947	152.150	0.203
30	225	149.271	149.093	-0.178
30	226	156.666	158.956	2.290
35	227	147.141	152.810	5.669
35	228	157.480	156.555	-0.925
35	229	157.377	157.743	0.366
35	230	155.025	157.193	2.168
35	231	154.768	154.689	-0.079
50	170	145.253	145.375	0.122
50	172	152.511	157.193	4.682
50	173	147.834	152.778	4.944
50	175	163.405	163.405	0.000
50	176	149.408	151.053	1.645
55	177	149.748	150.089	0.341
55	178	156.411	151.203	-5.208
55	179	162.082	162.086	0.004
55	180	160.618	161.352	0.734
55	181	144.221	144.221	0.000
100	154	147.761	153.513	5.752
100	155	155.144	159.865	4.721
100	156	163.734	164.589	0.855
100	157	159.667	166.754	7.087
100	158	145.947	144.126	-1.821
100	159	156.669	158.016	1.347
100	160	141.939	145.532	3.593
100	161	160.501	162.788	2.287
100	162	155.272	155.704	0.432
100	163	152.393	154.135	1.742
100	164	158.654	161.780	3.126
100	165	151.395	157.927	6.532
100	166	150.650	153.821	3.171
100	168	163.601	160.623	-2.978
100	169	154.203	154.247	0.044
100	182	156.723	155.249	-1.474
100	183	153.687	157.818	4.131
100	184	158.079	157.630	-0.449
100	185	152.755	153.674	0.919
100	186	156.436	154.517	-1.919
100	187	144.345	142.849	-1.496
100	188	138.676	145.471	6.795
100	189	145.492	149.230	3.738
100	190	145.808	148.768	2.960
105	191	151.574	154.145	2.571
105	192	155.376	156.864	1.488
105	193	152.210	156.868	4.658
105	194	157.332	156.696	-0.636
105	195	158.424	157.439	-0.985
105	196	164.761	169.451	4.690
Max		164.761	169.451	7.087
Average		153.367	154.490	1.122
Min		138.676	140.881	-5.208
Std Dev		6.024	5.936	2.397

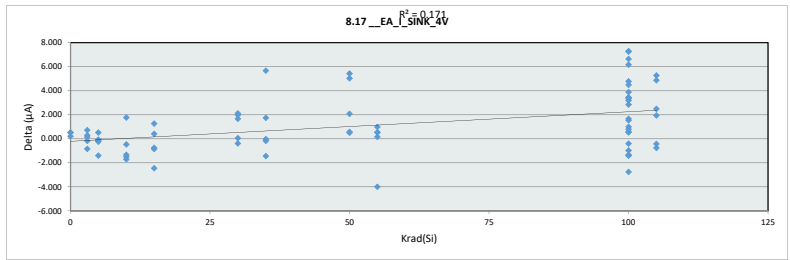


8.16_EA_I_SOURCE_14V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	148.812	140.881	147.526	147.847	146.938	147.761	152.810	145.375	144.221	142.849	154.145
Average	152.170	152.573	151.904	153.894	154.215	154.094	155.798	153.961	153.790	154.943	158.577
Max	156.009	160.087	161.565	159.541	162.556	162.511	157.743	163.405	162.086	166.754	169.451
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

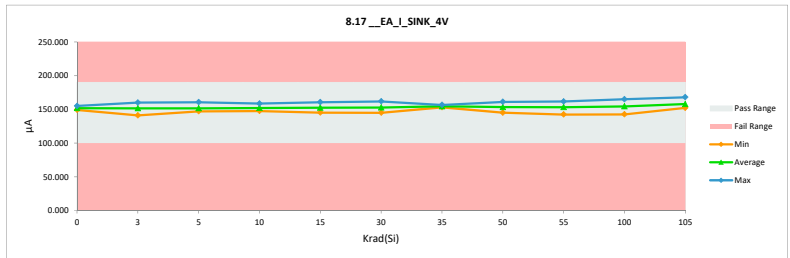


# HDR TID Report TPS7H5003-SP

8.17_EA_I_SINK_4V				
Test Site	Tester	Test Number	Unit	
			µA	µA
Max Limit	190		190	
Min Limit	100		100	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	149.041	149.228	0.187
0	138	154.554	155.054	0.500
0	139	150.639	151.155	0.516
3	197	151.469	152.171	0.702
3	198	160.270	160.078	-0.192
3	199	140.984	141.108	0.124
3	202	147.595	147.865	0.270
3	203	157.937	157.081	-0.856
5	204	153.394	153.140	-0.254
5	205	160.790	160.706	-0.084
5	206	150.340	148.926	-1.414
5	208	146.704	147.202	0.498
5	209	148.209	148.057	-0.152
10	210	149.911	148.557	-1.354
10	211	159.111	158.624	-0.487
10	212	149.099	147.594	-1.505
10	213	156.951	154.329	-1.722
10	214	149.792	151.545	1.753
15	215	154.594	153.719	-0.875
15	217	157.025	154.554	-2.471
15	219	148.659	149.056	0.397
15	220	144.063	145.310	1.247
15	221	161.360	160.595	-0.765
30	222	158.135	160.236	2.101
30	223	143.371	145.016	1.645
30	224	150.150	150.187	0.037
30	225	147.642	147.242	-0.400
30	226	159.877	161.840	1.963
35	227	147.436	153.096	5.660
35	228	155.515	154.063	-1.452
35	229	156.629	156.586	-0.043
35	230	153.523	155.246	1.723
35	231	154.117	153.930	-0.187
50	170	144.602	145.159	0.557
50	172	151.631	156.643	5.012
50	173	148.976	154.386	5.410
50	175	160.636	161.123	0.487
50	176	147.875	149.936	2.061
55	177	151.046	151.557	0.511
55	178	153.942	149.923	-4.019
55	179	160.087	160.619	0.532
55	180	160.796	161.758	0.962
55	181	143.323	142.463	-0.140
100	154	145.579	151.728	6.149
100	155	153.066	157.827	4.761
100	156	164.122	165.096	0.974
100	157	156.813	164.091	7.278
100	158	145.241	143.824	-1.417
100	159	158.516	160.040	1.524
100	160	140.853	144.289	3.436
100	161	158.029	160.863	2.834
100	162	155.260	155.825	0.565
100	163	148.727	150.372	1.645
100	164	157.091	160.447	3.356
100	165	151.199	157.827	6.628
100	166	151.929	155.316	3.387
100	168	161.905	159.130	-2.775
100	169	154.147	154.658	0.511
100	182	157.012	155.653	-1.359
100	183	153.125	157.589	4.464
100	184	160.318	159.896	-0.422
100	185	150.492	151.278	0.786
100	186	155.976	154.602	-1.374
100	187	143.677	142.688	-0.989
100	188	140.630	147.866	7.236
100	189	142.595	146.466	3.871
100	190	146.408	149.590	3.182
105	191	149.779	152.249	2.470
105	192	155.861	157.781	1.920
105	193	151.972	157.222	5.250
105	194	157.508	157.063	-0.445
105	195	156.180	155.396	-0.784
105	196	163.152	168.005	4.853
Max		164.122	168.005	7.278
Average		152.562	153.717	1.154
Min		140.630	141.108	-4.019
Std Dev		5.930	5.892	2.451

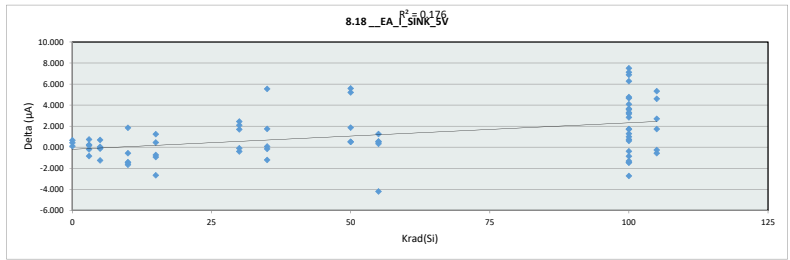


8.17_EA_I_SINK															
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
LL			190	100	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min					149.228	141.108	147.202	147.594	145.310	145.016	153.096	145.159	142.463	142.688	152.249
Average					151.812	151.661	151.606	152.130	152.647	152.904	154.584	153.449	153.264	154.457	157.953
Max					155.054	160.078	160.706	158.624	160.595	161.840	156.586	161.123	161.758	165.096	168.005
UL			190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

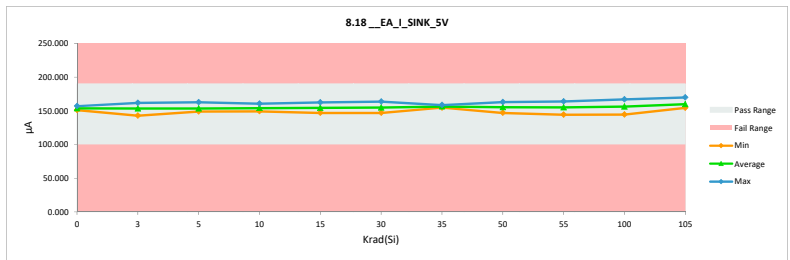


HDR TID Report  
TPS7H5003-SP

8.18_EA_I_SINK_5V				
Test Site				
Tester				
Test Number				
Unit		µA	µA	
Max Limit		190	190	
Min Limit		100	100	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	150.799	150.884	0.085
0	138	156.207	156.849	0.642
0	139	152.434	152.841	0.407
3	197	153.253	153.978	0.725
3	198	161.946	161.719	-0.227
3	199	142.495	142.642	0.147
3	202	149.475	149.692	0.217
3	203	159.712	158.850	-0.862
5	204	155.205	155.082	-0.123
5	205	162.543	162.561	0.018
5	206	152.077	150.805	-1.272
5	208	148.263	148.939	0.676
5	209	149.883	149.751	-0.132
10	210	151.800	150.362	-1.438
10	211	160.966	160.383	-0.583
10	212	150.895	149.218	-1.677
10	213	157.946	156.453	-1.493
10	214	151.485	153.317	1.832
15	215	156.251	155.297	-0.954
15	217	158.938	156.251	-2.687
15	219	150.422	150.851	0.429
15	220	145.607	146.837	1.230
15	221	163.202	162.452	-0.750
30	222	159.850	162.268	2.418
30	223	145.147	146.823	1.676
30	224	151.841	151.723	-0.118
30	225	149.407	148.993	-0.414
30	226	161.526	163.602	2.076
35	227	149.109	154.635	5.526
35	228	157.137	155.913	-1.224
35	229	158.406	158.466	0.060
35	230	155.247	156.974	1.727
35	231	155.867	155.696	-0.171
50	170	146.344	146.836	0.492
50	172	153.235	158.432	5.197
50	173	150.639	156.214	5.575
50	175	162.388	162.880	0.492
50	176	149.637	151.485	1.848
55	177	152.871	153.345	0.474
55	178	155.783	151.568	-4.215
55	179	161.762	162.296	0.534
55	180	162.466	163.698	1.232
55	181	143.818	144.130	0.312
100	154	147.418	153.667	6.249
100	155	154.899	159.657	4.758
100	156	165.693	166.961	1.268
100	157	158.633	166.123	7.490
100	158	146.994	145.516	-1.478
100	159	160.330	162.031	1.701
100	160	142.427	146.072	3.645
100	161	159.848	162.670	2.822
100	162	156.918	157.639	0.721
100	163	150.346	152.060	1.714
100	164	158.748	162.306	3.558
100	165	152.981	159.842	6.861
100	166	153.624	156.901	3.277
100	168	163.784	161.041	-2.743
100	169	155.796	156.383	0.587
100	182	158.730	157.403	-1.327
100	183	154.817	159.463	4.646
100	184	162.179	161.781	-0.398
100	185	152.190	153.168	0.978
100	186	157.810	156.355	-1.455
100	187	145.290	144.416	-0.874
100	188	142.237	149.350	7.113
100	189	144.286	148.357	4.071
100	190	147.940	151.096	3.156
105	191	151.529	154.215	2.686
105	192	157.719	159.428	1.709
105	193	153.726	159.037	5.311
105	194	159.142	158.863	-0.279
105	195	157.910	157.320	-0.590
105	196	165.106	169.685	4.579
Max		165.693	169.685	7.490
Average		154.292	155.490	1.198
Min		142.237	142.642	-4.215
Std Dev		5.970	5.948	2.479

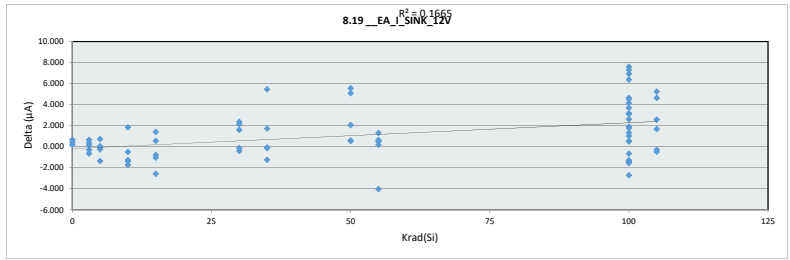


8.18_EA_I_SINK											
Test Site											
Tester											
Test Number											
Max Limit		190	µA								
Min Limit		100	µA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	150.884	142.642	148.939	149.218	146.837	146.823	154.635	146.836	144.130	144.416	154.215
Average	153.525	153.376	153.428	153.947	154.338	154.682	156.337	155.169	155.007	156.261	159.758
Max	156.849	161.719	162.561	160.383	162.452	163.602	158.466	162.880	163.698	166.961	169.685
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

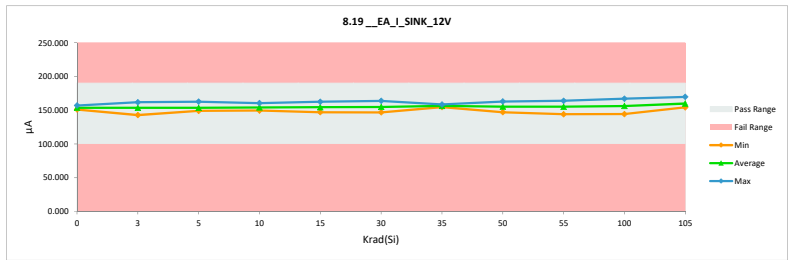


HDR TID Report  
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8.19_EA_I_SINK_12V				
Test Site				
Tester				
Test Number				
Unit		µA	µA	
Max Limit		190	190	
Min Limit		100	100	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	150.854	151.036	0.182
0	138	156.298	156.936	0.638
0	139	152.543	152.956	0.413
3	197	153.295	153.929	0.634
3	198	162.115	161.797	-0.318
3	199	142.628	142.751	0.123
3	202	149.407	149.750	0.343
3	203	159.845	159.176	-0.669
5	204	155.348	155.244	-0.104
5	205	162.662	162.686	0.024
5	206	152.216	150.824	-1.392
5	208	148.330	149.033	0.703
5	209	150.025	149.751	-0.274
10	210	151.744	150.458	-1.286
10	211	161.003	160.480	-0.523
10	212	159.911	149.495	-1.416
10	213	158.166	156.416	-1.750
10	214	151.597	153.414	1.817
15	215	156.400	155.330	-1.070
15	217	159.006	156.408	-2.598
15	219	150.467	150.993	0.526
15	220	145.640	147.020	1.380
15	221	163.292	162.479	-0.813
30	222	159.966	162.313	2.347
30	223	145.141	146.723	1.582
30	224	152.031	151.891	-0.140
30	225	149.522	149.096	-0.426
30	226	161.698	163.811	2.113
35	227	149.213	154.648	5.435
35	228	157.197	155.937	-1.260
35	229	158.568	158.482	-0.086
35	230	155.371	157.074	1.703
35	231	155.980	155.814	-0.166
50	170	146.386	146.943	0.557
50	172	153.446	158.511	5.065
50	173	150.727	156.266	5.539
50	175	162.409	162.932	0.523
50	176	149.815	151.856	2.041
55	177	152.930	153.410	0.480
55	178	155.851	151.796	-4.055
55	179	161.842	162.461	0.619
55	180	162.605	163.896	1.291
55	181	143.902	144.045	0.143
100	154	147.337	153.690	6.353
100	155	154.902	159.532	4.630
100	156	165.717	167.027	1.310
100	157	158.665	166.245	7.580
100	158	146.935	145.431	-1.504
100	159	160.346	162.107	1.761
100	160	142.505	146.189	3.684
100	161	159.941	162.529	2.588
100	162	157.107	157.610	0.503
100	163	150.445	152.337	1.892
100	164	158.880	161.992	3.112
100	165	153.048	159.956	6.908
100	166	153.825	156.961	3.136
100	168	163.930	161.193	-2.737
100	169	155.903	156.407	0.504
100	182	158.772	157.379	-1.393
100	183	154.917	159.423	4.506
100	184	162.292	161.612	-0.680
100	185	152.265	153.263	0.998
100	186	157.868	156.294	-1.574
100	187	145.538	144.226	-1.312
100	188	142.347	149.629	7.282
100	189	144.335	148.459	4.124
100	190	148.025	151.150	3.125
105	191	151.637	154.185	2.548
105	192	157.801	159.464	1.663
105	193	153.929	159.144	5.215
105	194	159.243	158.931	-0.312
105	195	157.973	157.464	-0.509
105	196	165.128	169.742	4.614
Max		165.717	169.742	7.580
Average		154.383	155.559	1.177
Min		142.347	142.751	-4.055
Std Dev		5.978	5.942	2.478



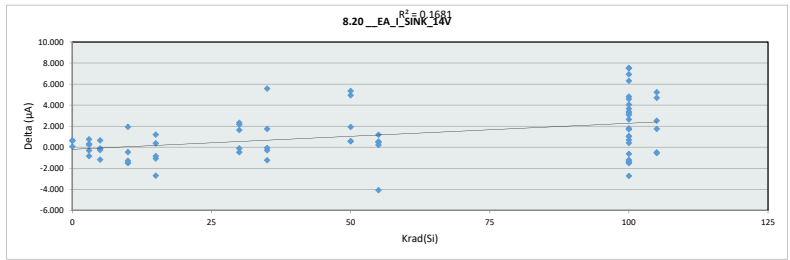
8.19_EA_I_SINK											
Test Site											
Tester											
Test Number											
Max Limit		190	µA								
Min Limit		100	µA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	151.036	142.751	149.033	149.495	147.020	146.723	154.648	146.943	144.045	144.226	154.185
Average	153.643	153.481	153.508	154.053	154.446	154.767	156.391	155.302	155.122	156.277	159.822
Max	156.936	161.797	162.686	160.480	162.479	163.811	158.482	162.932	163.896	167.027	169.742
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



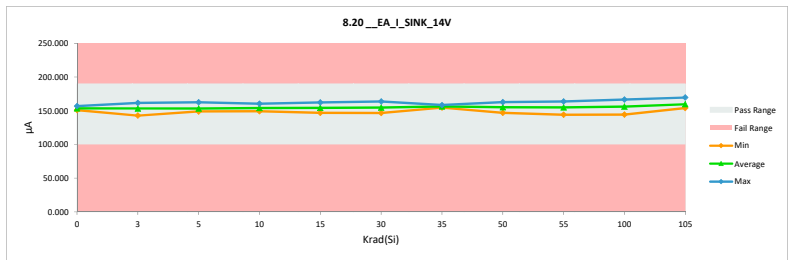


# HDR TID Report TPS7H5003-SP

8.20_EA_I_SINK_14V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	150.876	150.926	0.050
0	138	156.211	156.876	0.665
0	139	152.426	153.022	0.596
3	197	153.257	154.004	0.747
3	198	161.979	161.661	-0.318
3	199	142.560	142.799	0.239
3	202	149.367	149.676	0.309
3	203	159.817	158.970	-0.847
5	204	155.296	155.151	-0.145
5	205	162.670	162.543	-0.127
5	206	152.042	150.865	-1.177
5	208	148.313	148.959	0.646
5	209	149.983	149.715	-0.268
10	210	151.819	150.527	-1.292
10	211	160.917	160.451	-0.466
10	212	150.877	149.398	-1.479
10	213	157.875	156.383	-1.492
10	214	151.463	153.406	1.943
15	215	156.283	155.207	-1.076
15	217	158.974	156.278	-2.696
15	219	150.478	150.848	0.370
15	220	145.648	146.835	1.187
15	221	163.262	162.425	-0.837
30	222	159.883	162.203	2.320
30	223	145.149	146.777	1.628
30	224	151.914	151.805	-0.109
30	225	149.478	148.996	-0.482
30	226	161.560	163.709	2.149
35	227	149.105	154.681	5.576
35	228	157.158	155.923	-1.235
35	229	158.521	158.452	-0.069
35	230	155.239	156.976	1.737
35	231	155.944	155.643	-0.301
50	170	146.314	146.878	0.564
50	172	153.373	158.314	4.941
50	173	150.668	156.015	5.347
50	175	162.330	162.904	0.574
50	176	149.751	151.676	1.925
55	177	152.864	153.403	0.539
55	178	155.744	151.658	-4.086
55	179	161.767	162.224	0.457
55	180	162.567	163.747	1.180
55	181	143.881	144.084	0.203
100	154	147.361	153.661	6.300
100	155	154.824	159.631	4.807
100	156	165.631	166.652	1.021
100	157	158.662	166.196	7.534
100	158	146.884	145.365	-1.519
100	159	160.289	161.990	1.701
100	160	142.454	146.118	3.664
100	161	159.831	162.462	2.631
100	162	157.010	157.697	0.687
100	163	150.361	152.147	1.786
100	164	158.812	162.177	3.365
100	165	152.978	159.911	6.933
100	166	153.721	156.977	3.256
100	168	163.813	161.084	-2.729
100	169	155.888	156.295	0.407
100	182	158.769	157.459	-1.310
100	183	154.862	159.444	4.582
100	184	162.191	161.562	-0.629
100	185	152.251	153.294	1.043
100	186	157.747	156.293	-1.454
100	187	145.475	144.263	-1.212
100	188	142.281	149.780	7.499
100	189	144.316	148.374	4.058
100	190	147.920	150.994	3.074
105	191	151.547	154.061	2.514
105	192	157.740	159.475	1.735
105	193	153.846	159.073	5.227
105	194	159.126	158.630	-0.496
105	195	157.819	157.269	-0.550
105	196	164.989	169.674	4.685
Max		165.631	169.674	7.534
Average		154.315	155.493	1.178
Min		142.281	142.799	-4.086
Std Dev		5.963	5.919	2.485

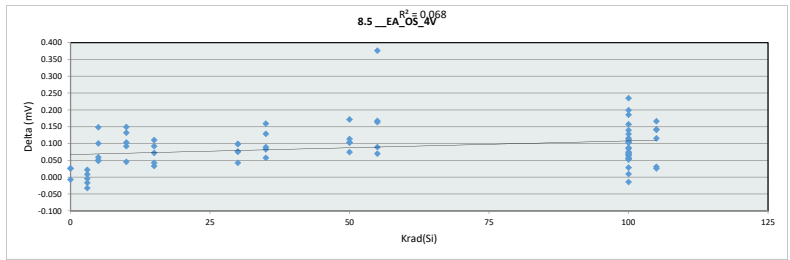


8.20_EA_I_SINK											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	150.926	142.799	148.959	149.398	146.835	146.777	154.681	146.878	144.084	144.263	154.061
Average	153.608	153.422	153.447	154.033	154.319	154.698	156.335	155.157	155.023	156.243	159.697
Max	156.876	161.661	162.543	160.451	162.425	163.709	158.452	162.904	163.747	166.652	169.674
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

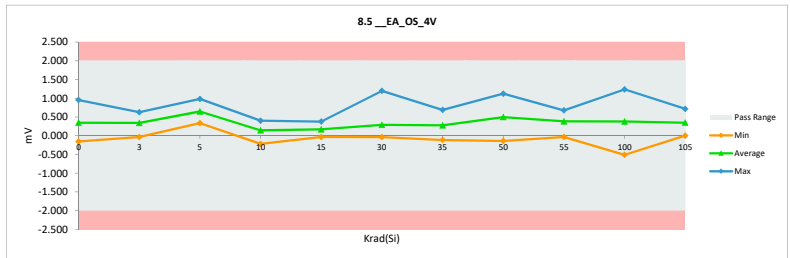


HDR TID Report  
TPS7H5003-SP

		8.5_EA_OS_4V		
Test Site	Tester			
Test Number	Unit	mV	mV	
Max Limit	Min Limit	2	-2	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	-0.149	-0.157	-0.007
0	138	0.220	0.245	0.026
0	139	0.927	0.953	0.027
3	197	0.596	0.579	-0.017
3	198	0.435	0.444	0.009
3	199	0.604	0.626	0.022
3	202	0.122	0.089	-0.032
3	203	-0.032	-0.036	-0.004
5	204	0.276	0.336	0.059
5	205	0.447	0.547	0.100
5	206	0.715	0.766	0.051
5	208	0.456	0.604	0.148
5	209	0.934	0.982	0.048
10	210	0.048	0.140	0.092
10	211	0.356	0.401	0.045
10	212	0.224	0.374	0.149
10	213	-0.086	0.017	0.103
10	214	-0.353	-0.221	0.132
15	215	0.015	-0.125	-0.110
15	217	0.343	0.376	0.033
15	219	0.260	0.332	0.072
15	220	-0.079	-0.036	0.042
15	221	-0.049	0.043	0.092
30	222	-0.109	-0.034	0.076
30	223	0.074	0.173	0.099
30	224	0.073	0.150	0.076
30	225	-0.082	-0.039	0.043
30	226	1.100	1.198	0.098
35	227	0.529	0.688	0.159
35	228	-0.173	-0.116	0.057
35	229	0.278	0.368	0.090
35	230	-0.125	0.004	0.129
35	231	0.357	0.440	0.083
50	170	0.494	0.597	0.103
50	172	0.455	0.569	0.114
50	173	1.047	1.121	0.075
50	175	-0.312	-0.140	0.172
50	176	0.228	0.331	0.103
55	177	0.607	0.676	0.070
55	178	-0.054	0.322	0.376
55	179	0.249	0.338	0.089
55	180	0.442	0.609	0.167
55	181	-0.198	-0.034	0.164
100	154	-0.109	0.126	0.235
100	155	-0.034	-0.005	0.028
100	156	0.189	0.247	0.058
100	157	-0.080	-0.013	0.067
100	158	0.128	0.230	0.102
100	159	0.942	1.127	0.186
100	160	0.182	0.246	0.064
100	161	-0.352	-0.246	0.105
100	162	0.590	0.599	0.009
100	163	-0.564	-0.512	0.052
100	164	0.016	0.091	0.075
100	165	0.252	0.367	0.116
100	166	0.770	0.840	0.070
100	168	0.051	0.156	0.104
100	169	0.338	0.496	0.157
100	182	0.682	0.795	0.113
100	183	0.275	0.331	0.056
100	184	1.066	1.051	-0.014
100	185	0.008	0.095	0.087
100	186	0.459	0.599	0.140
100	187	0.411	0.485	0.074
100	188	1.038	1.237	0.199
100	189	-0.140	-0.011	0.128
100	190	0.697	0.782	0.085
105	191	0.040	0.070	0.031
105	192	0.575	0.716	0.142
105	193	0.555	0.695	0.141
105	194	0.279	0.445	0.166
105	195	-0.112	0.003	0.115
105	196	0.125	0.152	0.026
	Max	1.100	1.237	0.376
	Average	0.266	0.355	0.090
	Min	-0.564	-0.512	-0.032
	Std Dev	0.375	0.376	0.064

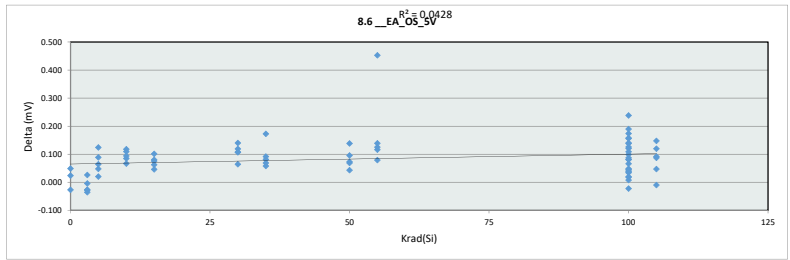


		8.5_EA_OS_4V										
Test Site	Tester											
Test Number	Unit	2	mV									
Max Limit	Min Limit	-2	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.157	-0.036	0.336	-0.221	-0.036	-0.039	-0.116	-0.140	-0.035	-0.512	0.003	
Average	0.347	0.340	0.647	0.142	0.168	0.290	0.277	0.495	0.382	0.380	0.347	
Max	0.954	0.626	0.982	0.401	0.376	1.198	0.688	1.121	0.676	1.238	0.717	
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	

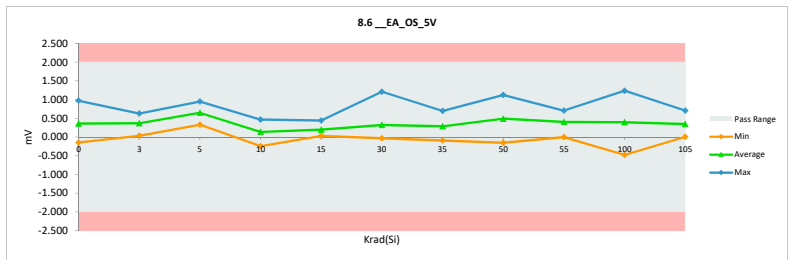


# HDR TID Report TPS7H5003-SP

8.6_EA_OS_5V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	2	2		
Min Limit	-2	-2		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	-0.170	-0.146	0.024
0	138	0.281	0.254	-0.027
0	139	0.921	0.970	0.049
3	197	0.594	0.567	-0.027
3	198	0.535	0.499	-0.036
3	199	0.659	0.630	-0.028
3	202	0.108	0.103	-0.004
3	203	0.010	0.036	0.026
5	204	0.266	0.330	0.064
5	205	0.477	0.524	0.048
5	206	0.683	0.808	0.124
5	208	0.535	0.624	0.089
5	209	0.931	0.951	0.020
10	210	0.029	0.123	0.094
10	211	0.362	0.467	0.085
10	212	0.250	0.368	0.118
10	213	-0.097	-0.031	0.066
10	214	-0.352	-0.242	0.109
15	215	0.061	0.108	0.046
15	217	0.340	0.441	0.102
15	219	0.249	0.324	0.075
15	220	-0.049	0.031	0.080
15	221	0.013	0.075	0.063
30	222	-0.098	-0.034	0.064
30	223	0.025	0.144	0.119
30	224	0.125	0.265	0.140
30	225	-0.079	0.029	0.107
30	226	1.102	1.210	0.108
35	227	0.606	0.698	0.092
35	228	-0.175	-0.093	0.082
35	229	0.239	0.411	0.172
35	230	-0.062	0.007	0.069
35	231	0.363	0.421	0.058
50	170	0.524	0.593	0.069
50	172	0.513	0.557	0.043
50	173	1.053	1.126	0.073
50	175	-0.249	-0.153	0.096
50	176	0.193	0.331	0.138
55	177	0.580	0.706	0.125
55	178	-0.097	0.355	0.452
55	179	0.249	0.388	0.139
55	180	0.496	0.575	0.079
55	181	-0.121	-0.005	0.116
100	154	-0.048	0.110	0.157
100	155	-0.023	0.011	0.034
100	156	0.229	0.328	0.099
100	157	-0.062	0.064	0.126
100	158	0.107	0.248	0.140
100	159	0.945	1.183	0.238
100	160	0.207	0.226	0.019
100	161	-0.389	-0.199	0.190
100	162	0.542	0.663	0.121
100	163	-0.514	-0.477	0.037
100	164	0.005	0.085	0.081
100	165	0.306	0.325	0.019
100	166	0.771	0.811	0.040
100	168	0.071	0.137	0.066
100	169	0.321	0.495	0.174
100	182	0.733	0.779	0.046
100	183	0.304	0.311	0.008
100	184	1.058	1.035	-0.023
100	185	0.036	0.085	0.048
100	186	0.488	0.569	0.081
100	187	0.474	0.583	0.109
100	188	1.082	1.238	0.156
100	189	-0.089	-0.002	0.087
100	190	0.713	0.851	0.138
105	191	0.022	0.069	0.047
105	192	0.618	0.705	0.087
105	193	0.529	0.677	0.147
105	194	0.362	0.482	0.120
105	195	-0.085	0.007	0.092
105	196	0.162	0.152	-0.010
	Max	1.102	1.238	0.452
	Average	0.284	0.368	0.085
	Min	-0.514	-0.477	-0.036
	Std Dev	0.374	0.373	0.070

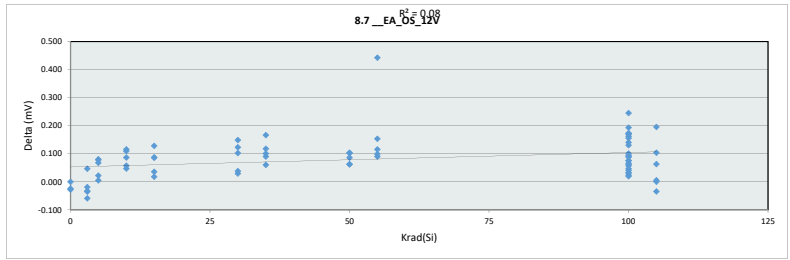


8.6_EA_OS_5V											
Test Site											
Tester											
Test Number											
Max Limit	2	mV									
Min Limit	-2	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.146	0.036	0.330	-0.242	0.031	-0.034	-0.093	-0.153	-0.005	-0.478	0.007
Average	0.359	0.367	0.647	0.137	0.196	0.323	0.289	0.491	0.404	0.394	0.349
Max	0.970	0.630	0.951	0.467	0.441	1.210	0.698	1.126	0.706	1.238	0.705
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

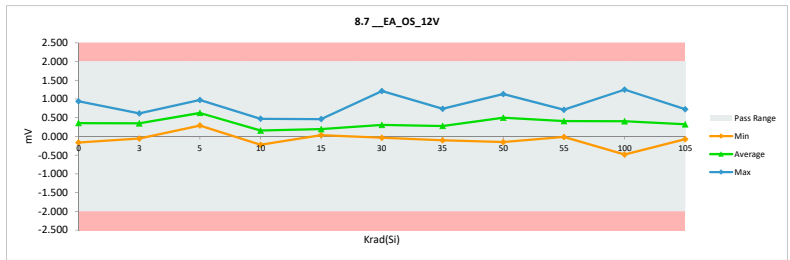


# HDR TID Report TPS7H5003-SP

8.7_EA_OS_12V				
Test Site	Tester	Test Number	Unit	
			mV	mV
Max Limit			2	2
Min Limit			-2	-2
Krad(Si)	Serial #	PRE	POST	Delta
0	137	-0.133	-0.162	-0.029
0	138	0.292	0.292	0.000
0	139	0.965	0.940	-0.024
3	197	0.603	0.584	-0.019
3	198	0.453	0.499	0.046
3	199	0.649	0.616	-0.033
3	202	0.142	0.107	-0.036
3	203	0.005	-0.054	-0.059
5	204	0.271	0.293	0.022
5	205	0.467	0.534	0.067
5	206	0.723	0.802	0.080
5	208	0.528	0.532	0.004
5	209	0.897	0.974	0.077
10	210	0.072	0.129	0.057
10	211	0.385	0.472	0.086
10	212	0.315	0.361	0.047
10	213	-0.063	0.052	0.115
10	214	-0.330	-0.221	0.109
15	215	0.071	0.089	0.018
15	217	0.336	0.464	0.128
15	219	0.259	0.345	0.086
15	220	-0.050	0.037	0.087
15	221	0.015	0.050	0.035
30	222	-0.062	-0.033	0.029
30	223	0.091	0.129	0.038
30	224	0.040	0.189	0.148
30	225	-0.085	0.038	0.122
30	226	1.112	1.214	0.102
35	227	0.575	0.740	0.166
35	228	-0.162	-0.102	0.060
35	229	0.229	0.347	0.117
35	230	-0.109	-0.009	0.100
35	231	0.335	0.424	0.090
50	170	0.532	0.595	0.062
50	172	0.536	0.622	0.086
50	173	1.069	1.131	0.062
50	175	-0.253	-0.150	0.103
50	176	0.193	0.294	0.101
55	177	0.559	0.712	0.153
55	178	-0.083	0.359	0.442
55	179	0.281	0.380	0.099
55	180	0.516	0.605	0.090
55	181	-0.129	-0.014	0.115
100	154	-0.029	-0.133	0.102
100	155	-0.024	0.016	0.040
100	156	0.254	0.328	0.075
100	157	-0.111	0.062	0.172
100	158	0.160	0.249	0.089
100	159	0.977	1.171	0.193
100	160	0.229	0.294	0.065
100	161	-0.390	-0.220	0.169
100	162	0.589	0.646	0.057
100	163	-0.506	-0.483	0.023
100	164	0.011	0.112	0.101
100	165	0.324	0.356	0.032
100	166	0.764	0.826	0.061
100	168	0.106	0.138	0.032
100	169	0.268	0.512	0.244
100	182	0.756	0.843	0.088
100	183	0.295	0.315	0.019
100	184	1.039	1.114	0.075
100	185	0.006	0.101	0.094
100	186	0.470	0.643	0.173
100	187	0.443	0.584	0.140
100	188	1.095	1.251	0.155
100	189	-0.090	-0.044	0.046
100	190	0.696	0.826	0.130
105	191	0.052	0.057	0.005
105	192	0.623	0.726	0.103
105	193	0.518	0.713	0.195
105	194	0.328	0.391	0.063
105	195	-0.073	-0.073	0.000
105	196	0.169	0.134	-0.035
Max		1.112	1.251	0.442
Average		0.288	0.369	0.081
Min		-0.506	-0.483	-0.059
Std Dev		0.373	0.381	0.075

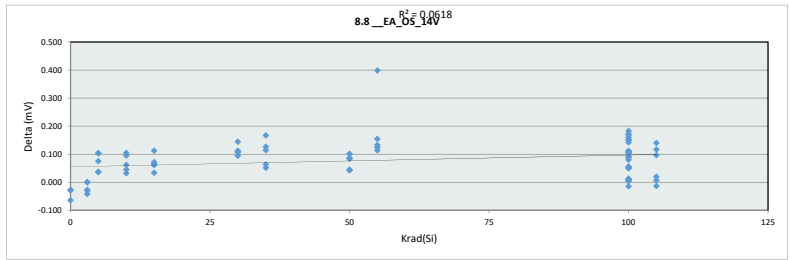


8.7_EA_OS_12V											
Test Site	Tester	Test Number	Unit								
			mV	mV							
Max Limit			2	2							
Min Limit			-2	-2							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.162	-0.055	0.293	-0.221	0.037	-0.033	-0.102	-0.150	-0.014	-0.483	-0.073
Average	0.357	0.350	0.627	0.159	0.197	0.307	0.280	0.498	0.409	0.407	0.325
Max	0.940	0.616	0.974	0.472	0.464	1.214	0.740	1.131	0.712	1.251	0.726
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

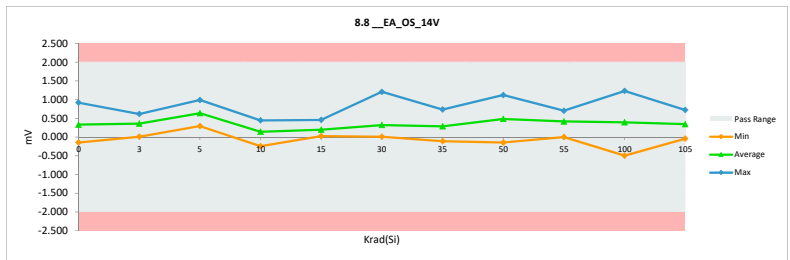


# HDR TID Report TPS7H5003-SP

8.8_EA_OS_14V				
Test Site	Tester	Test Number	Unit	
			mV	mV
Max Limit			2	2
Min Limit			-2	-2
Krad(Si)	Serial #	PRE	POST	Delta
0	137	-0.118	-0.146	-0.028
0	138	0.284	0.220	-0.064
0	139	0.948	0.921	-0.027
3	197	0.583	0.554	-0.029
3	198	0.484	0.483	0.000
3	199	0.658	0.617	-0.042
3	202	0.155	0.129	-0.027
3	203	0.010	0.010	0.001
5	204	0.256	0.293	0.037
5	205	0.446	0.549	0.103
5	206	0.693	0.797	0.104
5	208	0.522	0.560	0.038
5	209	0.915	0.990	0.075
10	210	0.052	0.114	0.062
10	211	0.400	0.445	0.045
10	212	0.293	0.388	0.095
10	213	-0.031	0.001	0.032
10	214	-0.348	-0.243	0.105
15	215	0.056	0.168	0.113
15	217	0.393	0.459	0.066
15	219	0.244	0.306	0.062
15	220	-0.045	0.027	0.071
15	221	-0.008	0.026	0.034
30	222	-0.086	0.009	0.095
30	223	0.042	0.148	0.106
30	224	0.077	0.221	0.145
30	225	-0.087	0.025	0.112
30	226	1.098	1.208	0.110
35	227	0.569	0.735	0.167
35	228	-0.173	-0.109	0.064
35	229	0.262	0.388	0.127
35	230	-0.091	0.024	0.115
35	231	0.346	0.398	0.052
50	170	0.525	0.568	0.042
50	172	0.523	0.568	0.045
50	173	1.039	1.123	0.084
50	175	-0.233	-0.146	0.087
50	176	0.208	0.309	0.101
55	177	0.571	0.703	0.132
55	178	-0.030	0.368	0.399
55	179	0.261	0.385	0.124
55	180	0.517	0.631	0.114
55	181	-0.154	0.000	0.154
100	154	-0.041	0.109	0.150
100	155	-0.023	0.032	0.055
100	156	0.233	0.321	0.088
100	157	-0.088	-0.008	0.080
100	158	0.148	0.254	0.105
100	159	0.993	1.176	0.183
100	160	0.198	0.303	0.105
100	161	-0.368	-0.208	0.159
100	162	0.584	0.634	0.050
100	163	-0.504	-0.496	0.008
100	164	0.069	0.073	0.004
100	165	0.304	0.395	0.090
100	166	0.753	0.809	0.056
100	168	0.072	0.183	0.110
100	169	0.307	0.476	0.169
100	182	0.743	0.729	-0.014
100	183	0.302	0.315	0.013
100	184	1.040	1.050	0.010
100	185	0.002	0.106	0.104
100	186	0.469	0.641	0.173
100	187	0.460	0.572	0.112
100	188	1.080	1.231	0.151
100	189	-0.091	-0.039	0.051
100	190	0.696	0.839	0.143
105	191	0.070	0.077	0.007
105	192	0.606	0.723	0.117
105	193	0.596	0.693	0.097
105	194	0.341	0.481	0.140
105	195	-0.061	-0.041	0.020
105	196	0.148	0.134	-0.013
	Max	1.098	1.231	0.399
	Average	0.289	0.367	0.078
	Min	-0.504	-0.496	-0.064
	Std Dev	0.370	0.374	0.070

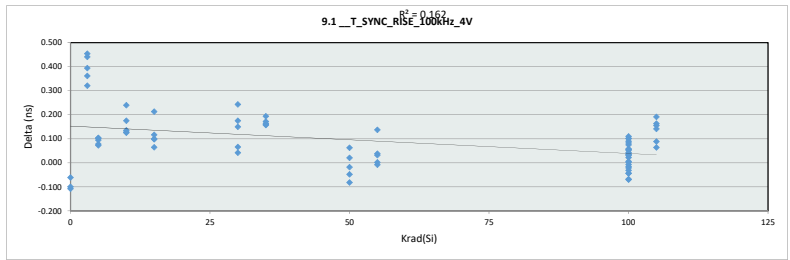


8.8 EA OS 14V											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			2	-2							
			mV	mV							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.146	0.011	0.293	-0.243	0.026	0.009	-0.109	-0.146	0.000	-0.497	-0.041
Average	0.332	0.359	0.638	0.141	0.197	0.322	0.287	0.484	0.418	0.396	0.345
Max	0.921	0.617	0.990	0.445	0.459	1.208	0.736	1.123	0.704	1.231	0.723
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

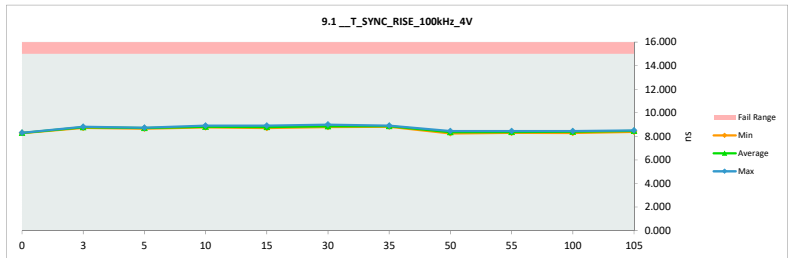


# HDR TID Report TPS7H5003-SP

9.1_T_SYNC_RISE_100kHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.352	8.290	-0.062
0	138	8.382	8.273	-0.109
0	139	8.404	8.303	-0.101
3	197	8.491	8.811	0.320
3	198	8.343	8.736	0.393
3	199	8.353	8.714	0.361
3	202	8.352	8.792	0.440
3	203	8.322	8.775	0.453
5	204	8.627	8.730	0.103
5	205	8.609	8.686	0.077
5	206	8.574	8.646	0.072
5	208	8.613	8.707	0.094
5	209	8.612	8.713	0.101
10	210	8.671	8.805	0.134
10	211	8.649	8.823	0.174
10	212	8.647	8.771	0.124
10	213	8.652	8.901	0.239
10	214	8.633	8.760	0.127
15	215	8.653	8.750	0.097
15	217	8.596	8.695	0.099
15	219	8.690	8.902	0.212
15	220	8.707	8.771	0.064
15	221	8.681	8.797	0.116
30	222	8.759	8.800	0.041
30	223	8.647	8.821	0.174
30	224	8.753	8.995	0.242
30	225	8.715	8.780	0.065
30	226	8.684	8.833	0.149
35	227	8.712	8.874	0.162
35	228	8.713	8.884	0.171
35	229	8.711	8.904	0.193
35	230	8.710	8.868	0.158
35	231	8.660	8.816	0.156
50	170	8.360	8.311	-0.049
50	172	8.270	8.251	-0.019
50	173	8.403	8.320	-0.083
50	175	8.356	8.418	0.062
50	176	8.418	8.438	0.020
55	177	8.375	8.366	-0.009
55	178	8.284	8.420	0.136
55	179	8.267	8.304	0.037
55	180	8.260	8.293	0.031
55	181	8.442	8.443	0.001
100	154	8.422	8.377	-0.045
100	155	8.422	8.353	-0.069
100	156	8.397	8.375	-0.022
100	157	8.316	8.308	-0.008
100	158	8.388	8.409	0.021
100	159	8.414	8.370	-0.044
100	160	8.353	8.442	0.089
100	161	8.433	8.435	0.002
100	162	8.403	8.445	0.042
100	163	8.323	8.423	0.100
100	164	8.383	8.312	-0.071
100	165	8.344	8.312	-0.032
100	166	8.408	8.391	-0.017
100	168	8.398	8.434	0.036
100	169	8.330	8.404	0.074
100	182	8.341	8.371	0.030
100	183	8.366	8.421	0.055
100	184	8.302	8.403	0.101
100	185	8.270	8.275	0.005
100	186	8.273	8.382	0.109
100	187	8.354	8.436	0.082
100	188	8.337	8.389	0.052
100	189	8.411	8.444	0.033
100	190	8.344	8.401	0.057
105	191	8.282	8.370	0.088
105	192	8.297	8.437	0.140
105	193	8.295	8.485	0.190
105	194	8.339	8.493	0.154
105	195	8.439	8.502	0.063
105	196	8.306	8.468	0.162
Max		8.759	8.995	0.453
Average		8.463	8.553	0.090
Min		8.260	8.251	-0.109
Std Dev		0.157	0.216	0.117



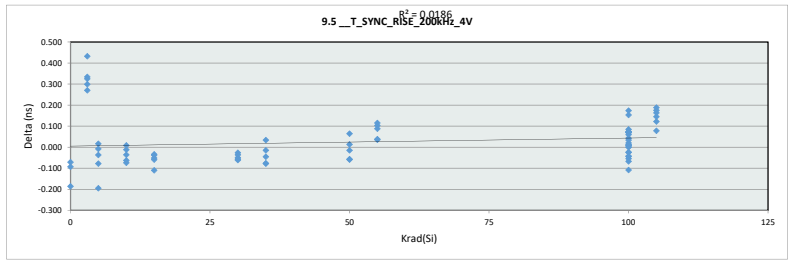
9.1_T_SYNC_RISE											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.273	8.714	8.646	8.760	8.695	8.780	8.816	8.251	8.291	8.275	8.370
Average	8.289	8.766	8.696	8.812	8.783	8.846	8.869	8.348	8.365	8.388	8.459
Max	8.303	8.811	8.730	8.901	8.902	8.995	8.904	8.438	8.443	8.445	8.502
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



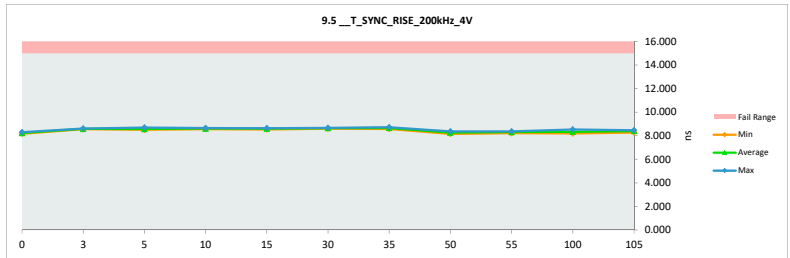
# HDR TID Report TPS7H5003-SP

9.5_T_SYNC_RISE_200kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.372	8.301	-0.071
0	138	8.352	8.166	-0.186
0	139	8.310	8.217	-0.093
3	197	8.182	8.614	0.432
3	198	8.264	8.563	0.299
3	199	8.246	8.580	0.334
3	202	8.321	8.591	0.270
3	203	8.228	8.553	0.325
5	204	8.678	8.694	0.016
5	205	8.757	8.562	-0.195
5	206	8.554	8.517	-0.037
5	208	8.566	8.558	-0.008
5	209	8.576	8.498	-0.078
10	210	8.648	8.657	0.009
10	211	8.626	8.563	-0.063
10	212	8.641	8.629	-0.012
10	213	8.649	8.613	-0.036
10	214	8.661	8.587	-0.074
15	215	8.618	8.580	-0.038
15	217	8.585	8.534	-0.051
15	219	8.677	8.643	-0.034
15	220	8.673	8.614	-0.059
15	221	8.707	8.597	-0.110
30	222	8.673	8.616	-0.057
30	223	8.638	8.602	-0.036
30	224	8.734	8.673	-0.061
30	225	8.714	8.664	-0.050
30	226	8.680	8.654	-0.026
35	227	8.686	8.720	0.034
35	228	8.680	8.665	-0.015
35	229	8.743	8.666	-0.077
35	230	8.701	8.656	-0.045
35	231	8.654	8.577	-0.077
50	170	8.254	8.197	-0.057
50	172	8.212	8.153	-0.059
50	173	8.269	8.282	0.013
50	175	8.321	8.306	-0.015
50	176	8.303	8.367	0.064
55	177	8.327	8.365	0.038
55	178	8.219	8.334	0.115
55	179	8.160	8.248	0.088
55	180	8.180	8.215	0.035
55	181	8.269	8.372	0.103
100	154	8.383	8.359	-0.024
100	155	8.253	8.287	0.034
100	156	8.356	8.332	-0.024
100	157	8.228	8.237	0.009
100	158	8.368	8.260	-0.108
100	159	8.276	8.296	0.020
100	160	8.363	8.423	0.060
100	161	8.385	8.395	0.010
100	162	8.441	8.442	0.001
100	163	8.296	8.308	0.012
100	164	8.334	8.292	-0.042
100	165	8.270	8.313	0.043
100	166	8.342	8.298	-0.044
100	168	8.358	8.532	0.174
100	169	8.325	8.271	-0.054
100	182	8.290	8.363	0.073
100	183	8.243	8.315	0.072
100	184	8.258	8.343	0.085
100	185	8.252	8.185	-0.067
100	186	8.213	8.283	0.070
100	187	8.285	8.438	0.153
100	188	8.314	8.383	0.069
100	189	8.268	8.341	0.073
100	190	8.290	8.290	0.000
105	191	8.201	8.279	0.078
105	192	8.257	8.379	0.122
105	193	8.278	8.466	0.188
105	194	8.261	8.406	0.145
105	195	8.298	8.461	0.163
105	196	8.218	8.393	0.175
Max		8.757	8.720	0.432
Average		8.414	8.440	0.026
Min		8.160	8.153	-0.195
Std Dev		0.189	0.159	0.116



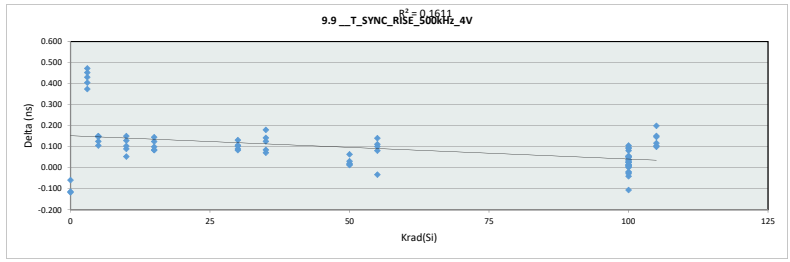
9.5_T_SYNC_RISE		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.166	8.553	8.498	8.563	8.534	8.602	8.577	8.153	8.215	8.185	8.279
Average		8.228	8.580	8.566	8.610	8.594	8.642	8.657	8.261	8.307	8.333	8.397
Max		8.301	8.614	8.694	8.657	8.643	8.673	8.720	8.367	8.372	8.532	8.466
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



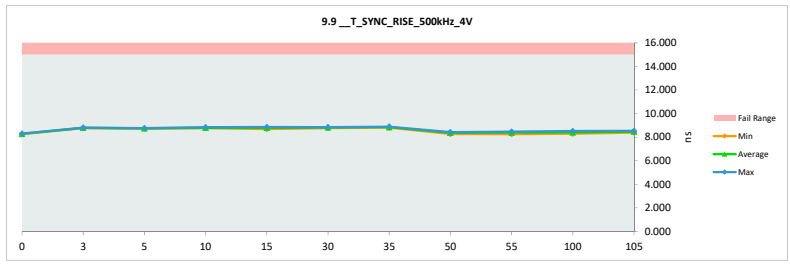
# HDR TID Report TPS7H5003-SP

9.9_T_SYNC_RISE_500kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.361	8.302	-0.059
0	138	8.388	8.270	-0.118
0	139	8.364	8.250	-0.114
3	197	8.359	8.789	0.430
3	198	8.403	8.777	0.374
3	199	8.327	8.799	0.472
3	202	8.369	8.773	0.404
3	203	8.330	8.782	0.452
5	204	8.659	8.764	0.105
5	205	8.613	8.738	0.125
5	206	8.583	8.732	0.149
5	208	8.616	8.766	0.150
5	209	8.616	8.762	0.146
10	210	8.733	8.836	0.103
10	211	8.646	8.796	0.150
10	212	8.687	8.776	0.089
10	213	8.714	8.767	0.053
10	214	8.665	8.794	0.129
15	215	8.672	8.796	0.124
15	217	8.607	8.690	0.083
15	219	8.716	8.861	0.145
15	220	8.711	8.796	0.085
15	221	8.681	8.780	0.099
30	222	8.719	8.826	0.107
30	223	8.685	8.774	0.089
30	224	8.752	8.835	0.083
30	225	8.743	8.845	0.102
30	226	8.714	8.845	0.131
35	227	8.720	8.862	0.142
35	228	8.712	8.892	0.180
35	229	8.736	8.807	0.071
35	230	8.760	8.845	0.085
35	231	8.689	8.814	0.125
50	170	8.316	8.336	0.020
50	172	8.262	8.274	0.012
50	173	8.365	8.380	0.015
50	175	8.362	8.425	0.063
50	176	8.392	8.423	0.031
55	177	8.362	8.443	0.081
55	178	8.289	8.429	0.140
55	179	8.254	8.355	0.101
55	180	8.289	8.256	-0.033
55	181	8.351	8.462	0.111
100	154	8.416	8.424	0.008
100	155	8.426	8.320	-0.106
100	156	8.469	8.448	-0.021
100	157	8.296	8.302	0.006
100	158	8.452	8.463	0.011
100	159	8.323	8.428	0.105
100	160	8.457	8.511	0.054
100	161	8.451	8.452	0.001
100	162	8.455	8.414	-0.041
100	163	8.373	8.386	0.013
100	164	8.335	8.371	0.036
100	165	8.323	8.372	0.049
100	166	8.400	8.371	-0.029
100	168	8.438	8.418	-0.020
100	169	8.408	8.462	0.054
100	182	8.349	8.404	0.055
100	183	8.359	8.450	0.091
100	184	8.364	8.402	0.038
100	185	8.304	8.332	0.028
100	186	8.348	8.362	0.014
100	187	8.385	8.480	0.095
100	188	8.364	8.445	0.081
100	189	8.377	8.401	0.024
100	190	8.393	8.418	0.025
105	191	8.299	8.398	0.099
105	192	8.290	8.436	0.146
105	193	8.367	8.518	0.151
105	194	8.387	8.491	0.104
105	195	8.410	8.527	0.117
105	196	8.320	8.519	0.199
Max		8.760	8.892	0.472
Average		8.474	8.565	0.091
Min		8.254	8.250	-0.118
Std Dev		0.162	0.205	0.114



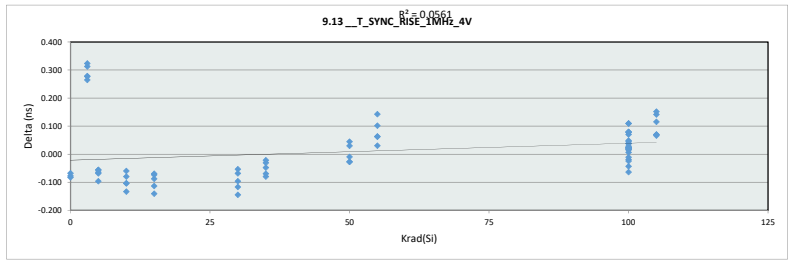
9.9_T_SYNC_RISE		0	3	5	10	15	30	35	50	55	100	105
LL		8.250	8.773	8.732	8.767	8.690	8.774	8.807	8.274	8.256	8.302	8.398
Min		8.274	8.784	8.752	8.794	8.785	8.825	8.844	8.368	8.389	8.410	8.482
Average		8.302	8.799	8.766	8.836	8.861	8.845	8.892	8.425	8.462	8.511	8.527
Max		8.302	8.799	8.766	8.836	8.861	8.845	8.892	8.425	8.462	8.511	8.527
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



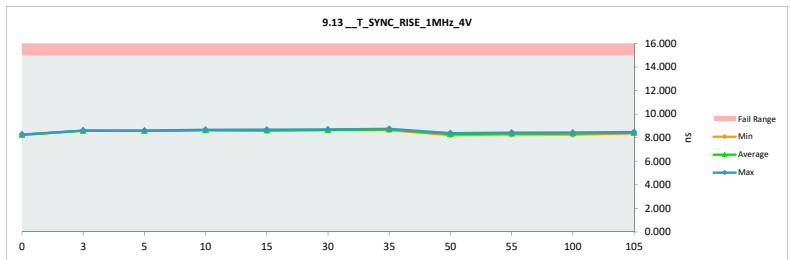


# HDR TID Report TPS7H5003-SP

9.13_T_SYNC_RISE_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.336	8.253	-0.083
0	138	8.351	8.273	-0.078
0	139	8.300	8.232	-0.068
3	197	8.328	8.606	0.278
3	198	8.305	8.617	0.312
3	199	8.320	8.584	0.264
3	202	8.322	8.599	0.277
3	203	8.264	8.587	0.323
5	204	8.691	8.594	-0.097
5	205	8.665	8.608	-0.057
5	206	8.643	8.578	-0.065
5	208	8.640	8.571	-0.069
5	209	8.653	8.597	-0.056
10	210	8.745	8.685	-0.060
10	211	8.730	8.650	-0.080
10	212	8.753	8.619	-0.134
10	213	8.750	8.646	-0.104
10	214	8.725	8.621	-0.104
15	215	8.709	8.621	-0.088
15	217	8.652	8.578	-0.074
15	219	8.759	8.689	-0.070
15	220	8.763	8.622	-0.141
15	221	8.744	8.630	-0.114
30	222	8.747	8.693	-0.054
30	223	8.723	8.654	-0.069
30	224	8.784	8.639	-0.145
30	225	8.796	8.699	-0.097
30	226	8.780	8.663	-0.117
35	227	8.769	8.721	-0.048
35	228	8.740	8.709	-0.031
35	229	8.779	8.757	-0.022
35	230	8.794	8.724	-0.070
35	231	8.722	8.642	-0.080
50	170	8.278	8.250	-0.028
50	172	8.251	8.223	-0.028
50	173	8.299	8.328	0.029
50	175	8.335	8.325	-0.010
50	176	8.356	8.400	0.044
55	177	8.340	8.370	0.030
55	178	8.254	8.396	0.142
55	179	8.238	8.300	0.062
55	180	8.211	8.274	0.063
55	181	8.338	8.439	0.101
100	154	8.439	8.375	-0.064
100	155	8.325	8.305	-0.020
100	156	8.421	8.377	-0.044
100	157	8.261	8.284	0.023
100	158	8.371	8.390	0.019
100	159	8.305	8.375	0.070
100	160	8.410	8.448	0.038
100	161	8.401	8.429	0.028
100	162	8.426	8.447	0.021
100	163	8.337	8.353	0.016
100	164	8.337	8.312	-0.025
100	165	8.306	8.330	0.024
100	166	8.334	8.371	0.037
100	168	8.396	8.385	-0.011
100	169	8.338	8.382	0.044
100	182	8.295	8.372	0.077
100	183	8.300	8.379	0.079
100	184	8.322	8.370	0.048
100	185	8.248	8.263	0.015
100	186	8.252	8.258	0.006
100	187	8.368	8.390	0.022
100	188	8.335	8.414	0.079
100	189	8.302	8.411	0.109
100	190	8.328	8.437	0.109
105	191	8.239	8.380	0.141
105	192	8.299	8.369	0.070
105	193	8.332	8.483	0.151
105	194	8.349	8.418	0.069
105	195	8.352	8.467	0.115
105	196	8.346	8.412	0.066
	Max	8.796	8.757	0.323
	Average	8.462	8.475	0.012
	Min	8.211	8.223	-0.145
	Std Dev	0.201	0.152	0.106



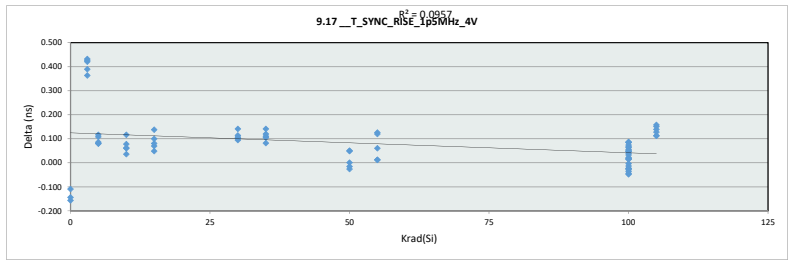
9.13_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.232	8.584	8.571	8.619	8.578	8.639	8.642	8.223	8.274	8.258	8.369
Average	8.253	8.599	8.590	8.644	8.628	8.670	8.711	8.305	8.356	8.369	8.422
Max	8.273	8.617	8.608	8.685	8.689	8.699	8.757	8.400	8.439	8.448	8.483
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



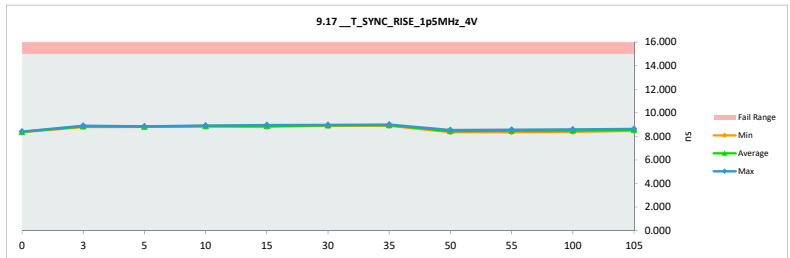
HDR TID Report  
TPS7H5003-SP

9.17_T_SYNC_RISE_1p5MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.507	8.362	-0.145
0	138	8.522	8.365	-0.157
0	139	8.511	8.401	-0.110
3	197	8.472	8.903	0.431
3	198	8.426	8.789	0.363
3	199	8.432	8.852	0.420
3	202	8.473	8.899	0.426
3	203	8.420	8.809	0.389
5	204	8.766	8.846	0.080
5	205	8.734	8.819	0.085
5	206	8.700	8.807	0.107
5	208	8.731	8.847	0.116
5	209	8.739	8.818	0.079
10	210	8.805	8.921	0.116
10	211	8.782	8.859	0.077
10	212	8.805	8.840	0.035
10	213	8.797	8.857	0.060
10	214	8.801	8.862	0.061
15	215	8.785	8.865	0.080
15	217	8.716	8.815	0.099
15	219	8.819	8.956	0.137
15	220	8.807	8.876	0.069
15	221	8.802	8.850	0.048
30	222	8.799	8.939	0.140
30	223	8.809	8.903	0.094
30	224	8.866	8.972	0.106
30	225	8.831	8.944	0.113
30	226	8.821	8.922	0.101
35	227	8.833	8.952	0.119
35	228	8.842	8.952	0.110
35	229	8.859	8.999	0.140
35	230	8.868	8.949	0.081
35	231	8.795	8.899	0.104
50	170	8.446	8.419	-0.027
50	172	8.356	8.356	0.000
50	173	8.443	8.427	-0.016
50	175	8.445	8.495	0.050
50	176	8.487	8.535	0.048
55	177	8.439	8.558	0.119
55	178	8.416	8.541	0.125
55	179	8.421	8.433	0.012
55	180	8.354	8.365	0.011
55	181	8.462	8.522	0.060
100	154	8.542	8.497	-0.045
100	155	8.461	8.425	-0.036
100	156	8.536	8.522	-0.014
100	157	8.406	8.422	0.016
100	158	8.532	8.503	-0.029
100	159	8.463	8.484	0.021
100	160	8.510	8.565	0.055
100	161	8.512	8.585	0.073
100	162	8.539	8.553	0.014
100	163	8.435	8.482	0.047
100	164	8.458	8.454	-0.004
100	165	8.453	8.404	-0.049
100	166	8.423	8.439	0.016
100	168	8.504	8.480	-0.024
100	169	8.552	8.528	-0.024
100	182	8.433	8.519	0.086
100	183	8.447	8.479	0.032
100	184	8.400	8.484	0.084
100	185	8.349	8.388	0.039
100	186	8.379	8.445	0.066
100	187	8.500	8.547	0.047
100	188	8.486	8.535	0.049
100	189	8.442	8.527	0.085
100	190	8.473	8.526	0.053
105	191	8.355	8.467	0.112
105	192	8.412	8.524	0.112
105	193	8.488	8.626	0.138
105	194	8.440	8.567	0.127
105	195	8.469	8.626	0.157
105	196	8.391	8.541	0.150
Max		8.868	8.999	0.431
Average		8.572	8.650	0.078
Min		8.349	8.356	-0.157
Std Dev		0.171	0.207	0.111

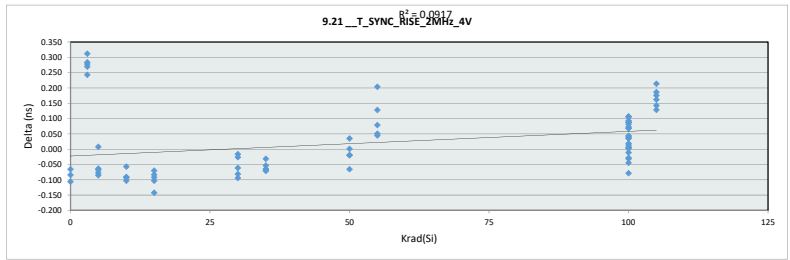


9.17_T_SYNC_R		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.362	8.789	8.807	8.840	8.815	8.903	8.899	8.356	8.365	8.388	8.467
Average		8.376	8.850	8.827	8.868	8.872	8.936	8.950	8.446	8.484	8.492	8.559
Max		8.401	8.903	8.847	8.921	8.956	8.972	8.999	8.535	8.558	8.585	8.626
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

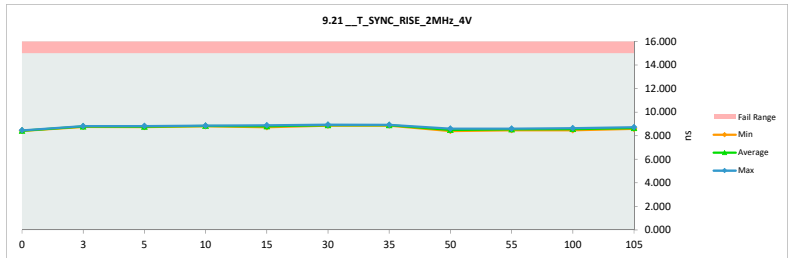


# HDR TID Report TPS7H5003-SP

9.21 __T_SYNC_RISE_2MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.499	8.414	-0.085
0	138	8.518	8.411	-0.107
0	139	8.515	8.449	-0.066
3	197	8.500	8.811	0.311
3	198	8.497	8.766	0.269
3	199	8.506	8.783	0.277
3	202	8.517	8.800	0.283
3	203	8.508	8.750	0.242
5	204	8.855	8.791	-0.064
5	205	8.842	8.756	-0.086
5	206	8.801	8.732	-0.069
5	208	8.809	8.816	0.007
5	209	8.834	8.756	-0.078
10	210	8.951	8.847	-0.104
10	211	8.892	8.800	-0.092
10	212	8.922	8.829	-0.093
10	213	8.913	8.820	-0.093
10	214	8.906	8.848	-0.058
15	215	8.894	8.751	-0.143
15	217	8.802	8.718	-0.084
15	219	8.947	8.876	-0.071
15	220	8.946	8.842	-0.104
15	221	8.903	8.810	-0.093
30	222	8.917	8.900	-0.017
30	223	8.895	8.868	-0.027
30	224	9.027	8.932	-0.095
30	225	8.971	8.909	-0.062
30	226	8.933	8.851	-0.082
35	227	8.970	8.916	-0.054
35	228	8.940	8.875	-0.065
35	229	8.983	8.915	-0.068
35	230	8.973	8.901	-0.072
35	231	8.890	8.858	-0.032
50	170	8.479	8.413	-0.066
50	172	8.421	8.401	-0.020
50	173	8.502	8.482	-0.020
50	175	8.496	8.530	0.034
50	176	8.592	8.592	0.000
55	177	8.517	8.561	0.044
55	178	8.386	8.589	0.203
55	179	8.377	8.504	0.127
55	180	8.386	8.464	0.078
55	181	8.525	8.575	0.050
100	154	8.617	8.538	-0.079
100	155	8.525	8.526	0.001
100	156	8.587	8.558	-0.029
100	157	8.435	8.453	0.018
100	158	8.600	8.568	-0.032
100	159	8.514	8.548	0.034
100	160	8.536	8.623	0.087
100	161	8.593	8.629	0.036
100	162	8.595	8.635	0.040
100	163	8.477	8.549	0.072
100	164	8.465	8.471	0.006
100	165	8.532	8.520	-0.012
100	166	8.566	8.521	-0.045
100	168	8.529	8.596	0.067
100	169	8.539	8.582	0.043
100	182	8.533	8.546	0.013
100	183	8.472	8.578	0.106
100	184	8.483	8.564	0.081
100	185	8.379	8.471	0.092
100	186	8.425	8.493	0.068
100	187	8.510	8.598	0.088
100	188	8.590	8.595	0.005
100	189	8.502	8.607	0.105
100	190	8.522	8.628	0.106
105	191	8.395	8.581	0.186
105	192	8.467	8.609	0.142
105	193	8.516	8.691	0.175
105	194	8.546	8.674	0.128
105	195	8.504	8.717	0.213
105	196	8.449	8.610	0.161
	Max	9.027	8.932	0.311
	Average	8.642	8.664	0.022
	Min	8.377	8.401	-0.143
	Std Dev	0.203	0.154	0.110



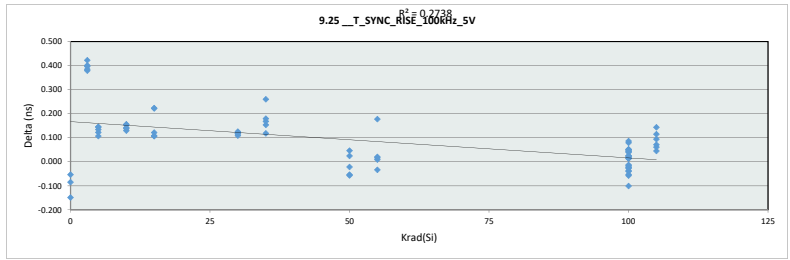
9.21 __T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
LL											
UL											
Min	8.411	8.750	8.732	8.800	8.718	8.851	8.858	8.401	8.464	8.453	8.581
Average	8.425	8.782	8.770	8.829	8.799	8.892	8.893	8.484	8.539	8.558	8.647
Max	8.449	8.811	8.816	8.848	8.876	8.932	8.916	8.592	8.589	8.635	8.717
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



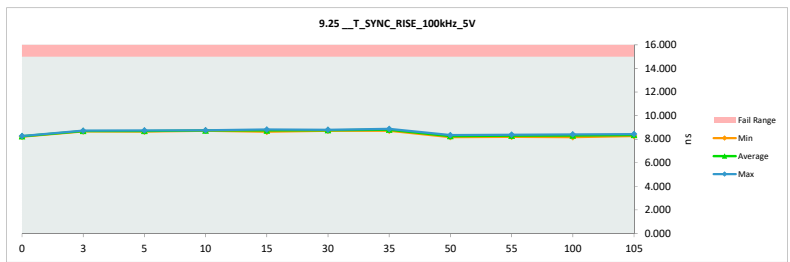
# HDR TID Report TPS7H5003-SP

9.25 _T_SYNC_RISE_100kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.368	8.218	-0.150
0	138	8.321	8.266	-0.055
0	139	8.330	8.244	-0.086
3	197	8.303	8.703	0.400
3	198	8.280	8.664	0.384
3	199	8.301	8.722	0.421
3	202	8.328	8.723	0.395
3	203	8.278	8.656	0.378
5	204	8.601	8.745	0.144
5	205	8.548	8.681	0.133
5	206	8.523	8.644	0.121
5	208	8.540	8.683	0.143
5	209	8.578	8.684	0.106
10	210	8.620	8.757	0.137
10	211	8.578	8.733	0.155
10	212	8.591	8.719	0.128
10	213	8.589	8.730	0.141
10	214	8.591	8.743	0.152
15	215	8.586	8.807	0.221
15	217	8.511	8.631	0.120
15	219	8.597	8.819	0.222
15	220	8.620	8.725	0.105
15	221	8.599	8.706	0.107
30	222	8.626	8.745	0.119
30	223	8.604	8.721	0.117
30	224	8.675	8.799	0.124
30	225	8.663	8.771	0.108
30	226	8.624	8.739	0.115
35	227	8.643	8.821	0.178
35	228	8.609	8.868	0.259
35	229	8.640	8.792	0.152
35	230	8.637	8.804	0.167
35	231	8.600	8.717	0.117
50	170	8.295	8.240	-0.055
50	172	8.151	8.174	0.023
50	173	8.313	8.255	-0.058
50	175	8.312	8.289	-0.023
50	176	8.298	8.343	0.045
55	177	8.300	8.319	0.019
55	178	8.204	8.380	0.176
55	179	8.235	8.250	0.015
55	180	8.232	8.197	-0.035
55	181	8.292	8.300	0.008
100	154	8.371	8.312	-0.059
100	155	8.277	8.223	-0.054
100	156	8.337	8.323	-0.014
100	157	8.225	8.237	0.012
100	158	8.324	8.347	0.023
100	159	8.301	8.323	0.022
100	160	8.410	8.308	-0.102
100	161	8.391	8.350	-0.041
100	162	8.338	8.381	0.043
100	163	8.306	8.283	-0.023
100	164	8.264	8.247	-0.017
100	165	8.282	8.295	0.013
100	166	8.319	8.295	-0.024
100	168	8.300	8.324	0.024
100	169	8.301	8.316	0.015
100	182	8.310	8.361	0.051
100	183	8.311	8.354	0.043
100	184	8.267	8.305	0.038
100	185	8.197	8.168	-0.029
100	186	8.244	8.205	-0.039
100	187	8.306	8.355	0.049
100	188	8.328	8.406	0.078
100	189	8.274	8.359	0.085
100	190	8.317	8.365	0.048
105	191	8.216	8.286	0.070
105	192	8.229	8.273	0.044
105	193	8.321	8.435	0.114
105	194	8.288	8.348	0.060
105	195	8.324	8.417	0.093
105	196	8.215	8.357	0.142
Max		8.675	8.868	0.421
Average		8.398	8.481	0.083
Min		8.151	8.168	-0.150
Std Dev		0.154	0.223	0.119



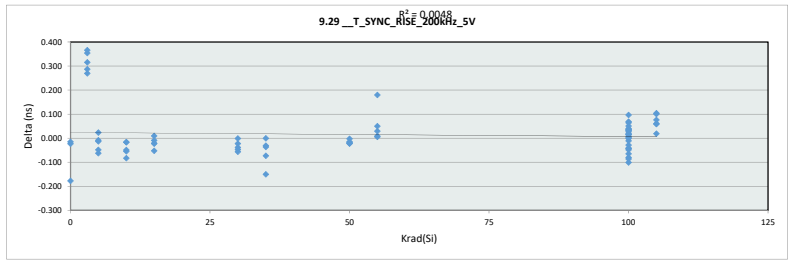
9.25 _T_SYNC_R		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.218	8.656	8.644	8.719	8.631	8.721	8.717	8.174	8.197	8.168	8.273
Average		8.243	8.694	8.687	8.736	8.738	8.755	8.800	8.260	8.289	8.310	8.353
Max		8.266	8.723	8.745	8.757	8.819	8.799	8.868	8.343	8.380	8.406	8.435
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



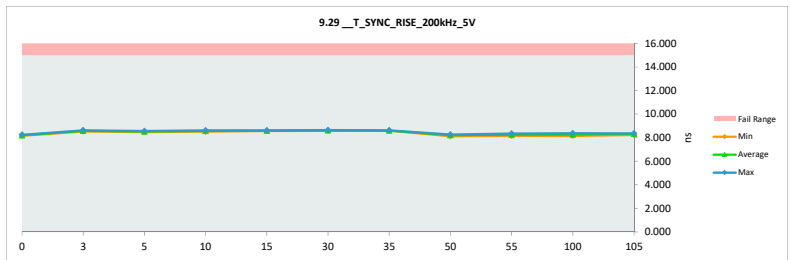
# HDR TID Report TPS7H5003-SP

9.29 _T_SYNC_RISE_200kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.262	8.246	-0.016
0	138	8.224	8.200	-0.024
0	139	8.340	8.162	-0.178
3	197	8.273	8.559	0.286
3	198	8.284	8.599	0.315
3	199	8.256	8.609	0.353
3	202	8.260	8.625	0.365
3	203	8.251	8.520	0.269
5	204	8.590	8.541	-0.049
5	205	8.532	8.523	-0.009
5	206	8.530	8.467	-0.063
5	208	8.541	8.527	-0.014
5	209	8.539	8.562	0.023
10	210	8.638	8.621	-0.017
10	211	8.589	8.572	-0.017
10	212	8.600	8.550	-0.050
10	213	8.630	8.575	-0.055
10	214	8.591	8.507	-0.084
15	215	8.577	8.567	-0.010
15	217	8.551	8.560	0.009
15	219	8.638	8.616	-0.022
15	220	8.624	8.601	-0.023
15	221	8.615	8.562	-0.053
30	222	8.637	8.598	-0.039
30	223	8.613	8.590	-0.023
30	224	8.702	8.645	-0.057
30	225	8.666	8.618	-0.048
30	226	8.620	8.618	-0.002
35	227	8.651	8.619	-0.032
35	228	8.626	8.589	-0.037
35	229	8.676	8.602	-0.074
35	230	8.741	8.590	-0.151
35	231	8.599	8.598	-0.001
50	170	8.226	8.203	-0.023
50	172	8.139	8.121	-0.018
50	173	8.252	8.249	-0.003
50	175	8.248	8.226	-0.022
50	176	8.287	8.271	-0.016
55	177	8.275	8.280	0.005
55	178	8.170	8.349	0.179
55	179	8.172	8.180	0.008
55	180	8.128	8.157	0.029
55	181	8.268	8.317	0.049
100	154	8.326	8.283	-0.043
100	155	8.256	8.207	-0.049
100	156	8.294	8.228	-0.066
100	157	8.173	8.181	0.008
100	158	8.326	8.297	-0.029
100	159	8.282	8.270	-0.012
100	160	8.293	8.326	0.033
100	161	8.360	8.258	-0.102
100	162	8.342	8.255	-0.087
100	163	8.206	8.274	0.068
100	164	8.277	8.196	-0.081
100	165	8.188	8.238	0.050
100	166	8.261	8.277	0.016
100	168	8.331	8.289	-0.042
100	169	8.290	8.306	0.016
100	182	8.265	8.304	0.039
100	183	8.247	8.273	0.026
100	184	8.223	8.240	0.017
100	185	8.153	8.151	-0.002
100	186	8.191	8.194	0.003
100	187	8.304	8.336	0.032
100	188	8.251	8.314	0.063
100	189	8.264	8.299	0.035
100	190	8.275	8.371	0.096
105	191	8.211	8.229	0.018
105	192	8.211	8.269	0.058
105	193	8.284	8.359	0.075
105	194	8.253	8.356	0.103
105	195	8.280	8.340	0.060
105	196	8.252	8.352	0.100
Max		8.741	8.645	0.365
Average		8.377	8.391	0.015
Min		8.128	8.121	-0.178
Std Dev		0.179	0.166	0.100



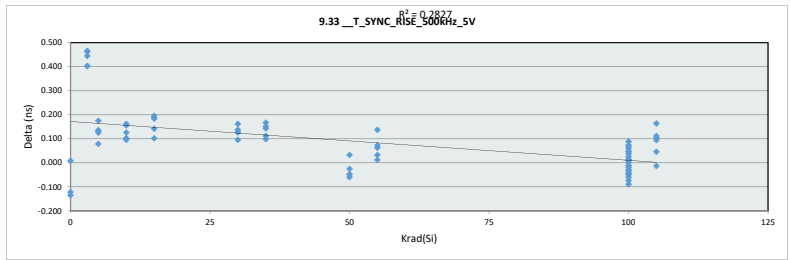
9.29 _T_SYNC_R		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.162	8.520	8.467	8.507	8.560	8.590	8.589	8.121	8.157	8.151	8.229
Average		8.203	8.582	8.524	8.565	8.581	8.614	8.600	8.214	8.257	8.265	8.318
Max		8.246	8.625	8.562	8.621	8.616	8.645	8.619	8.271	8.349	8.371	8.359
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



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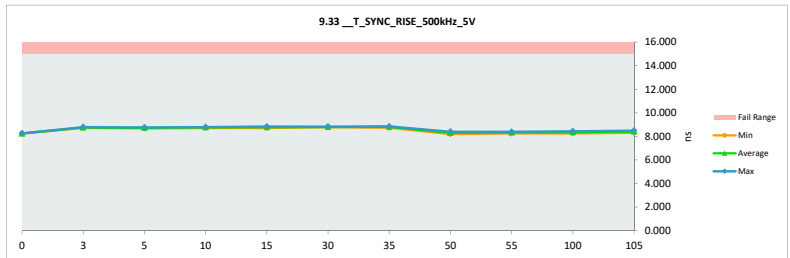
9.33 _T_SYNC_RISE_500kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.359	8.236	-0.123
0	138	8.369	8.233	-0.136
0	139	8.243	8.250	0.007
3	197	8.320	8.721	0.401
3	198	8.349	8.751	0.402
3	199	8.292	8.753	0.461
3	202	8.331	8.796	0.465
3	203	8.304	8.748	0.444
5	204	8.623	8.747	0.124
5	205	8.592	8.670	0.078
5	206	8.562	8.697	0.135
5	208	8.587	8.761	0.174
5	209	8.596	8.727	0.131
10	210	8.672	8.774	0.102
10	211	8.624	8.779	0.155
10	212	8.634	8.795	0.161
10	213	8.634	8.759	0.125
10	214	8.619	8.714	0.095
15	215	8.603	8.744	0.141
15	217	8.529	8.712	0.183
15	219	8.668	8.769	0.101
15	220	8.654	8.841	0.187
15	221	8.625	8.820	0.195
30	222	8.645	8.806	0.161
30	223	8.634	8.771	0.137
30	224	8.707	8.833	0.126
30	225	8.690	8.785	0.095
30	226	8.650	8.776	0.126
35	227	8.688	8.799	0.111
35	228	8.669	8.819	0.150
35	229	8.688	8.854	0.166
35	230	8.689	8.831	0.142
35	231	8.639	8.737	0.098
50	170	8.315	8.254	-0.061
50	172	8.234	8.185	-0.049
50	173	8.321	8.295	-0.026
50	175	8.342	8.374	0.032
50	176	8.444	8.395	-0.049
55	177	8.319	8.391	0.072
55	178	8.247	8.383	0.136
55	179	8.253	8.264	0.011
55	180	8.179	8.241	0.062
55	181	8.348	8.380	0.032
100	154	8.443	8.393	-0.050
100	155	8.372	8.282	-0.090
100	156	8.383	8.337	-0.046
100	157	8.245	8.259	0.014
100	158	8.350	8.289	-0.061
100	159	8.319	8.327	0.008
100	160	8.455	8.414	-0.041
100	161	8.378	8.389	0.011
100	162	8.418	8.432	0.014
100	163	8.288	8.276	-0.012
100	164	8.314	8.284	-0.030
100	165	8.305	8.293	-0.012
100	166	8.326	8.251	-0.075
100	168	8.388	8.368	-0.020
100	169	8.349	8.317	-0.032
100	182	8.344	8.381	0.037
100	183	8.285	8.373	0.088
100	184	8.336	8.409	0.073
100	185	8.237	8.296	0.059
100	186	8.246	8.246	0.000
100	187	8.338	8.383	0.045
100	188	8.371	8.418	0.047
100	189	8.333	8.359	0.026
100	190	8.371	8.438	0.067
105	191	8.317	8.303	-0.014
105	192	8.284	8.378	0.094
105	193	8.343	8.447	0.104
105	194	8.337	8.448	0.111
105	195	8.330	8.493	0.163
105	196	8.333	8.378	0.045
Max		8.707	8.854	0.465
Average		8.433	8.515	0.082
Min		8.179	8.185	-0.136
Std Dev		0.157	0.223	0.125



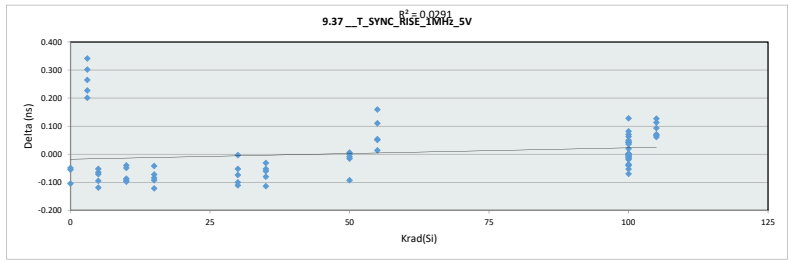
9.33 _T_SYNC_R	
Test Site	
Tester	
Test Number	
Max Limit	15 ns
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.233	8.721	8.670	8.714	8.712	8.771	8.737	8.185	8.241	8.246	8.303
Average	8.240	8.754	8.720	8.764	8.777	8.794	8.808	8.301	8.332	8.342	8.408
Max	8.250	8.796	8.761	8.795	8.841	8.833	8.854	8.395	8.391	8.438	8.493
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

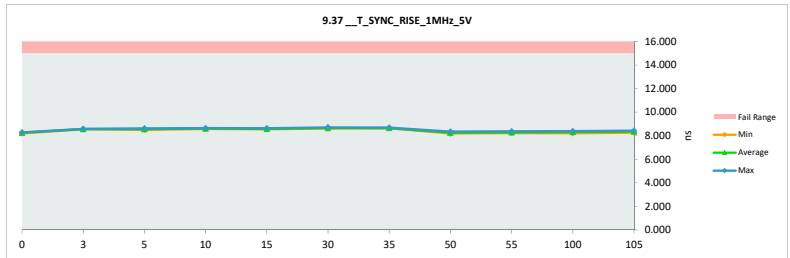


# HDR TID Report TPS7H5003-SP

9.37 __T_SYNC_RISE_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.317	8.268	-0.049
0	138	8.340	8.285	-0.055
0	139	8.303	8.198	-0.105
3	197	8.238	8.579	0.341
3	198	8.331	8.558	0.227
3	199	8.350	8.551	0.201
3	202	8.319	8.584	0.265
3	203	8.266	8.568	0.302
5	204	8.676	8.624	-0.052
5	205	8.628	8.564	-0.064
5	206	8.617	8.498	-0.119
5	208	8.650	8.579	-0.071
5	209	8.651	8.556	-0.095
10	210	8.685	8.645	-0.040
10	211	8.672	8.580	-0.092
10	212	8.692	8.605	-0.087
10	213	8.690	8.641	-0.049
10	214	8.688	8.590	-0.098
15	215	8.653	8.581	-0.072
15	217	8.588	8.546	-0.042
15	219	8.722	8.638	-0.084
15	220	8.676	8.583	-0.093
15	221	8.681	8.559	-0.122
30	222	8.691	8.639	-0.052
30	223	8.698	8.624	-0.074
30	224	8.772	8.661	-0.111
30	225	8.747	8.647	-0.100
30	226	8.711	8.708	-0.003
35	227	8.722	8.691	-0.031
35	228	8.707	8.627	-0.080
35	229	8.725	8.665	-0.060
35	230	8.756	8.642	-0.114
35	231	8.689	8.637	-0.052
50	170	8.299	8.206	-0.093
50	172	8.195	8.186	-0.009
50	173	8.258	8.257	-0.001
50	175	8.335	8.319	-0.016
50	176	8.345	8.351	0.006
55	177	8.265	8.316	0.051
55	178	8.215	8.374	0.159
55	179	8.209	8.223	0.014
55	180	8.179	8.233	0.054
55	181	8.227	8.337	0.110
100	154	8.386	8.316	-0.070
100	155	8.297	8.244	-0.053
100	156	8.345	8.326	-0.019
100	157	8.265	8.251	-0.014
100	158	8.313	8.354	0.041
100	159	8.313	8.310	-0.003
100	160	8.353	8.355	0.002
100	161	8.337	8.356	0.019
100	162	8.364	8.364	0.000
100	163	8.341	8.305	-0.036
100	164	8.282	8.242	-0.040
100	165	8.269	8.257	-0.012
100	166	8.304	8.354	0.050
100	168	8.385	8.345	-0.040
100	169	8.327	8.363	0.036
100	182	8.283	8.354	0.071
100	183	8.324	8.319	-0.005
100	184	8.215	8.343	0.128
100	185	8.224	8.225	0.001
100	186	8.222	8.214	-0.008
100	187	8.289	8.352	0.063
100	188	8.302	8.384	0.082
100	189	8.274	8.318	0.044
100	190	8.315	8.357	0.042
105	191	8.178	8.271	0.093
105	192	8.224	8.295	0.071
105	193	8.324	8.391	0.067
105	194	8.329	8.390	0.061
105	195	8.310	8.423	0.113
105	196	8.235	8.362	0.127
Max		8.772	8.708	0.341
Average		8.426	8.431	0.005
Min		8.178	8.186	-0.122
Std Dev		0.196	0.157	0.099



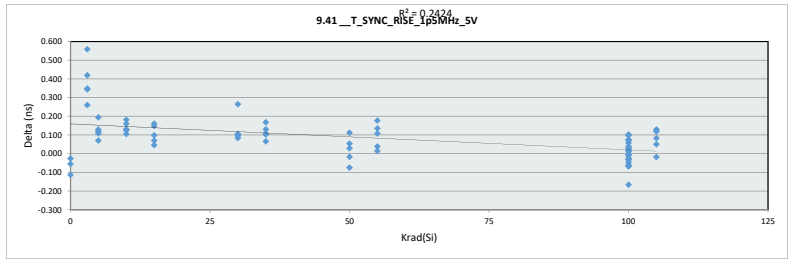
9.37 __T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.198	8.551	8.498	8.580	8.546	8.624	8.627	8.186	8.223	8.214	8.271
Average	8.250	8.568	8.564	8.612	8.581	8.656	8.652	8.264	8.297	8.317	8.355
Max	8.285	8.584	8.624	8.645	8.638	8.708	8.691	8.351	8.374	8.384	8.423
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



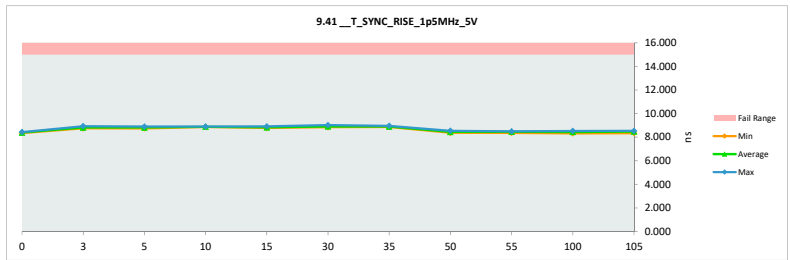
HDR TID Report  
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9.41_T_SYNC_RISE_1p5MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.465	8.350	-0.115
0	138	8.484	8.428	-0.056
0	139	8.403	8.376	-0.027
3	197	8.374	8.932	0.558
3	198	8.493	8.752	0.259
3	199	8.413	8.756	0.343
3	202	8.427	8.845	0.418
3	203	8.453	8.800	0.347
5	204	8.707	8.900	0.193
5	205	8.675	8.803	0.128
5	206	8.671	8.740	0.069
5	208	8.706	8.824	0.118
5	209	8.710	8.818	0.108
10	210	8.759	8.863	0.104
10	211	8.738	8.863	0.125
10	212	8.742	8.901	0.159
10	213	8.755	8.885	0.130
10	214	8.708	8.888	0.180
15	215	8.711	8.780	0.069
15	217	8.654	8.801	0.147
15	219	8.756	8.916	0.160
15	220	8.769	8.814	0.045
15	221	8.764	8.861	0.097
30	222	8.761	9.024	0.263
30	223	8.738	8.842	0.104
30	224	8.820	8.915	0.095
30	225	8.798	8.881	0.083
30	226	8.785	8.888	0.103
35	227	8.804	8.869	0.065
35	228	8.778	8.880	0.102
35	229	8.795	8.962	0.167
35	230	8.805	8.911	0.106
35	231	8.740	8.869	0.129
50	170	8.385	8.367	-0.018
50	172	8.339	8.367	0.028
50	173	8.485	8.538	0.053
50	175	8.397	8.508	0.111
50	176	8.471	8.395	-0.076
55	177	8.459	8.472	0.013
55	178	8.322	8.456	0.134
55	179	8.305	8.481	0.176
55	180	8.329	8.366	0.037
55	181	8.383	8.490	0.107
100	154	8.472	8.409	-0.063
100	155	8.455	8.386	-0.069
100	156	8.455	8.407	-0.048
100	157	8.393	8.367	-0.026
100	158	8.410	8.439	0.029
100	159	8.399	8.415	0.016
100	160	8.478	8.495	0.017
100	161	8.511	8.479	-0.032
100	162	8.462	8.457	-0.005
100	163	8.428	8.500	0.072
100	164	8.409	8.343	-0.066
100	165	8.414	8.380	-0.034
100	166	8.582	8.415	-0.167
100	168	8.428	8.525	0.097
100	169	8.458	8.447	-0.011
100	182	8.402	8.441	0.039
100	183	8.380	8.375	-0.005
100	184	8.396	8.497	0.101
100	185	8.306	8.379	0.073
100	186	8.317	8.327	0.010
100	187	8.409	8.483	0.074
100	188	8.424	8.453	0.029
100	189	8.384	8.441	0.057
100	190	8.409	8.509	0.100
105	191	8.287	8.336	0.049
105	192	8.418	8.399	-0.019
105	193	8.448	8.530	0.082
105	194	8.409	8.536	0.127
105	195	8.386	8.513	0.127
105	196	8.393	8.511	0.118
Max		8.820	9.024	0.558
Average		8.528	8.611	0.082
Min		8.287	8.327	-0.167
Std Dev		0.167	0.218	0.116



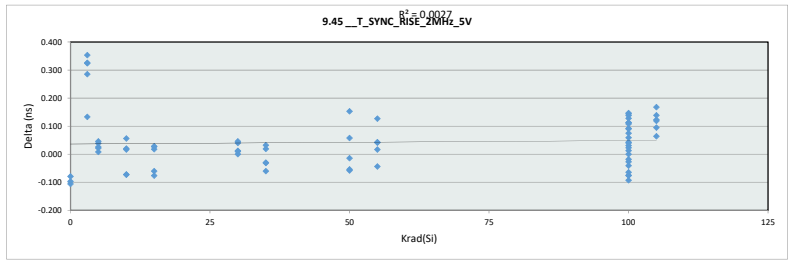
9.41_T_SYNC_R		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.350	8.752	8.740	8.863	8.780	8.842	8.869	8.367	8.366	8.327	8.336
Average		8.385	8.817	8.817	8.880	8.834	8.910	8.898	8.435	8.453	8.432	8.471
Max		8.428	8.932	8.900	8.901	8.916	9.024	8.962	8.538	8.490	8.525	8.536
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



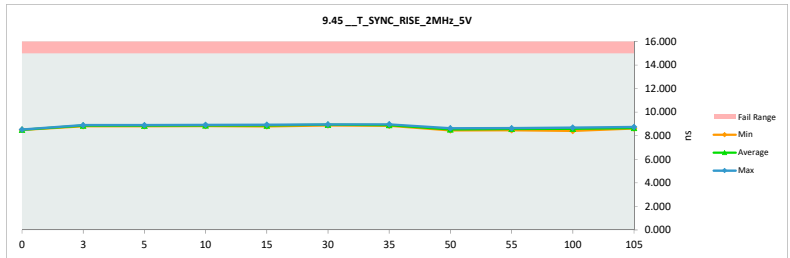


HDR TID Report  
TPS7H5003-SP

		9.45 _T_SYNC_RISE_2MHz_5V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	15	15	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.601	8.522	-0.079
0	138	8.597	8.491	-0.106
0	139	8.592	8.495	-0.097
3	197	8.570	8.896	0.326
3	198	8.533	8.857	0.324
3	199	8.522	8.875	0.353
3	202	8.670	8.803	0.133
3	203	8.592	8.877	0.285
5	204	8.850	8.896	0.046
5	205	8.842	8.868	0.026
5	206	8.818	8.857	0.039
5	208	8.824	8.846	0.022
5	209	8.800	8.808	0.008
10	210	8.890	8.818	-0.072
10	211	8.896	8.906	0.020
10	212	8.914	8.841	-0.073
10	213	8.858	8.914	0.056
10	214	8.878	8.895	0.017
15	215	8.856	8.796	-0.060
15	217	8.837	8.865	0.028
15	219	8.915	8.933	0.018
15	220	8.884	8.911	0.027
15	221	8.916	8.840	-0.076
30	222	8.922	8.962	0.040
30	223	8.863	8.875	0.012
30	224	8.945	8.955	0.010
30	225	8.904	8.950	0.046
30	226	8.924	8.924	0.000
35	227	8.903	8.843	-0.060
35	228	8.878	8.910	0.032
35	229	8.947	8.966	0.019
35	230	8.966	8.936	-0.030
35	231	8.909	8.878	-0.031
50	170	8.501	8.487	-0.014
50	172	8.511	8.457	-0.054
50	173	8.576	8.518	-0.058
50	175	8.529	8.587	0.058
50	176	8.474	8.627	0.153
55	177	8.621	8.638	0.017
55	178	8.497	8.624	0.127
55	179	8.467	8.508	0.041
55	180	8.514	8.470	-0.044
55	181	8.580	8.623	0.043
100	154	8.608	8.683	0.075
100	155	8.490	8.634	0.144
100	156	8.579	8.622	0.043
100	157	8.464	8.592	0.128
100	158	8.618	8.631	0.013
100	159	8.530	8.622	0.092
100	160	8.615	8.646	0.031
100	161	8.691	8.598	-0.093
100	162	8.638	8.574	-0.064
100	163	8.573	8.686	0.113
100	164	8.555	8.480	-0.075
100	165	8.479	8.403	-0.076
100	166	8.532	8.591	0.059
100	168	8.604	8.577	-0.027
100	169	8.554	8.577	0.023
100	182	8.518	8.630	0.112
100	183	8.568	8.658	0.090
100	184	8.569	8.528	-0.041
100	185	8.462	8.600	0.138
100	186	8.503	8.503	0.000
100	187	8.595	8.635	0.040
100	188	8.569	8.677	0.108
100	189	8.482	8.464	-0.018
100	190	8.507	8.654	0.147
105	191	8.514	8.633	0.119
105	192	8.490	8.613	0.123
105	193	8.576	8.744	0.168
105	194	8.593	8.732	0.139
105	195	8.519	8.614	0.095
105	196	8.582	8.646	0.064
Max		8.966	8.966	0.353
Average		8.666	8.710	0.043
Min		8.462	8.403	-0.106
Std Dev		0.167	0.161	0.098



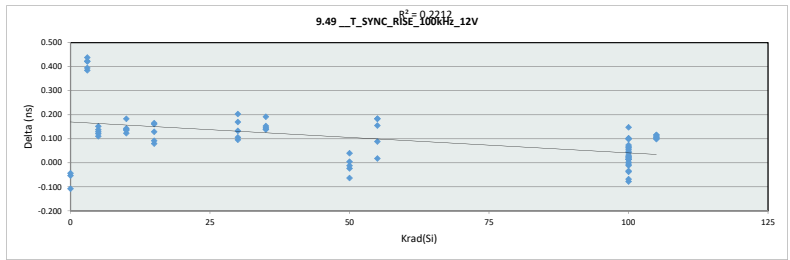
		9.45 _T_SYNC_RISE_2MHz_5V										
Test Site	Tester											
Test Number	Unit	ns	ns									
Max Limit	Min Limit	15	15									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.491	8.803	8.808	8.818	8.796	8.875	8.843	8.457	8.470	8.403	8.613
Average		8.503	8.862	8.855	8.875	8.869	8.933	8.907	8.535	8.573	8.594	8.664
Max		8.522	8.896	8.896	8.914	8.933	8.962	8.966	8.627	8.638	8.686	8.744
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



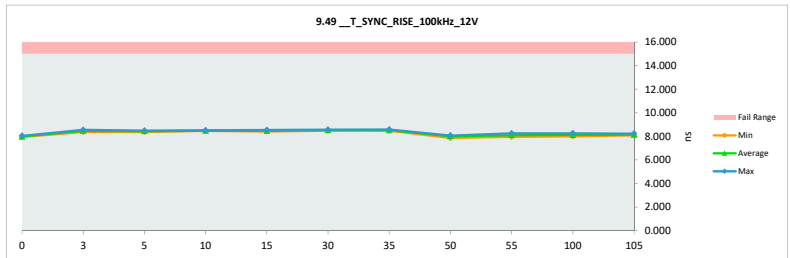
HDR TID Report  
TPS7H5003-SP

9.49 _T_SYNC_RISE_100kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.096	8.042	-0.054
0	138	8.059	7.950	-0.109
0	139	7.998	7.954	-0.044
3	197	8.135	8.556	0.421
3	198	8.105	8.527	0.422
3	199	8.027	8.421	0.394
3	202	8.079	8.516	0.437
3	203	7.986	8.370	0.384
5	204	8.344	8.481	0.137
5	205	8.242	8.370	0.128
5	206	8.320	8.440	0.120
5	208	8.339	8.449	0.110
5	209	8.301	8.452	0.151
10	210	8.345	8.487	0.142
10	211	8.336	8.473	0.137
10	212	8.279	8.461	0.182
10	213	8.332	8.454	0.122
10	214	8.373	8.508	0.135
15	215	8.412	8.540	0.128
15	217	8.273	8.433	0.160
15	219	8.322	8.486	0.164
15	220	8.383	8.474	0.091
15	221	8.331	8.410	0.079
30	222	8.366	8.568	0.202
30	223	8.430	8.525	0.095
30	224	8.414	8.546	0.132
30	225	8.424	8.529	0.105
30	226	8.302	8.471	0.169
35	227	8.391	8.536	0.145
35	228	8.415	8.553	0.138
35	229	8.329	8.468	0.139
35	230	8.393	8.584	0.191
35	231	8.352	8.504	0.152
50	170	8.038	8.025	-0.013
50	172	7.926	7.862	-0.064
50	173	8.093	8.069	-0.024
50	175	8.030	8.034	0.004
50	176	7.988	8.027	0.039
55	177	8.023	8.111	0.088
55	178	7.879	8.062	0.183
55	179	7.943	7.960	0.017
55	180	7.916	8.098	0.182
55	181	8.096	8.250	0.154
100	154	8.128	8.090	-0.038
100	155	8.053	8.072	0.019
100	156	8.121	8.134	0.013
100	157	8.029	8.060	0.031
100	158	8.111	8.125	0.014
100	159	8.006	8.072	0.066
100	160	8.157	8.171	0.014
100	161	8.124	8.225	0.101
100	162	8.089	8.113	0.024
100	163	8.078	8.134	0.056
100	164	8.177	8.108	-0.069
100	165	8.107	8.072	-0.035
100	166	8.026	8.067	0.041
100	168	8.052	8.054	0.002
100	169	8.179	8.173	-0.006
100	182	8.108	8.156	0.048
100	183	8.116	8.263	0.147
100	184	8.029	8.091	0.062
100	185	8.005	7.993	-0.012
100	186	8.037	8.135	0.098
100	187	8.064	8.137	0.073
100	188	8.082	8.106	0.024
100	189	8.202	8.123	-0.079
100	190	8.119	8.220	0.101
105	191	8.013	8.121	0.102
105	192	7.968	8.075	0.107
105	193	7.990	8.105	0.115
105	194	8.085	8.200	0.115
105	195	8.133	8.231	0.098
105	196	8.053	8.159	0.106
Max		8.430	8.584	0.437
Average		8.159	8.258	0.099
Min		7.879	7.862	-0.109
Std Dev		0.153	0.205	0.113



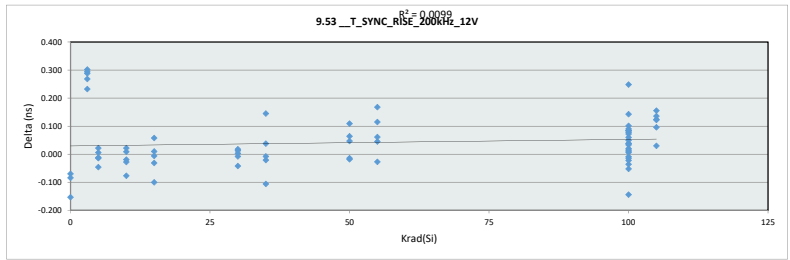
9.49 _T_SYNC_R		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		7.950	8.370	8.370	8.454	8.410	8.471	8.468	7.862	7.960	7.993	8.075
Average		7.982	8.478	8.438	8.477	8.469	8.528	8.529	8.003	8.096	8.121	8.149
Max		8.042	8.556	8.481	8.508	8.540	8.568	8.584	8.069	8.250	8.263	8.231
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



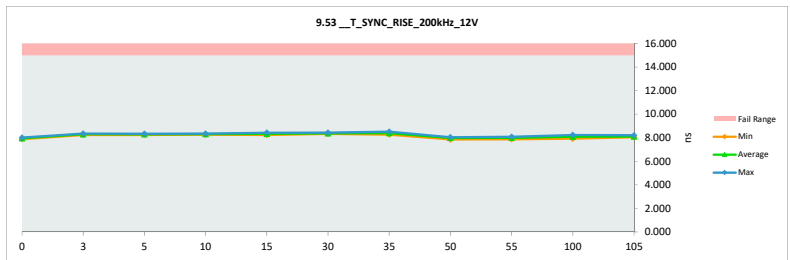
# HDR TID Report TPS7H5003-SP

9.53_T_SYNC_RISE_200kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.163	8.010	-0.153
0	138	7.964	7.880	-0.084
0	139	8.025	7.955	-0.070
3	197	8.100	8.332	0.232
3	198	8.008	8.302	0.294
3	199	7.975	8.263	0.288
3	202	8.050	8.352	0.302
3	203	7.968	8.236	0.268
5	204	8.352	8.306	-0.046
5	205	8.253	8.241	-0.012
5	206	8.292	8.278	-0.014
5	208	8.310	8.332	0.022
5	209	8.287	8.293	0.006
10	210	8.298	8.320	0.022
10	211	8.326	8.335	0.009
10	212	8.334	8.257	-0.077
10	213	8.313	8.294	-0.019
10	214	8.369	8.341	-0.028
15	215	8.366	8.424	0.058
15	217	8.337	8.237	-0.100
15	219	8.300	8.294	-0.006
15	220	8.333	8.343	0.010
15	221	8.356	8.325	-0.031
30	222	8.354	8.367	0.013
30	223	8.419	8.422	0.003
30	224	8.407	8.399	-0.008
30	225	8.400	8.358	-0.042
30	226	8.310	8.328	0.018
35	227	8.365	8.510	0.145
35	228	8.406	8.444	0.038
35	229	8.318	8.297	-0.021
35	230	8.420	8.412	-0.008
35	231	8.367	8.261	-0.106
50	170	7.995	7.981	-0.014
50	172	7.865	7.847	-0.018
50	173	7.971	8.035	0.064
50	175	7.954	8.001	0.047
50	176	7.941	8.050	0.109
55	177	7.958	8.073	0.115
55	178	7.851	8.019	0.168
55	179	7.871	7.916	0.045
55	180	7.892	7.865	-0.027
55	181	8.016	8.077	0.061
100	154	8.068	8.016	-0.052
100	155	8.043	8.007	-0.036
100	156	8.128	8.119	-0.009
100	157	7.956	7.962	0.006
100	158	8.068	8.079	0.011
100	159	7.987	8.235	0.248
100	160	8.127	8.177	0.050
100	161	8.129	8.149	0.020
100	162	8.111	7.967	-0.144
100	163	7.984	8.085	0.101
100	164	8.053	8.090	0.037
100	165	8.080	8.058	-0.022
100	166	8.035	8.050	0.015
100	168	7.997	7.983	-0.014
100	169	8.091	8.163	0.072
100	182	8.008	8.098	0.090
100	183	8.099	8.138	0.039
100	184	7.992	8.052	0.060
100	185	7.884	7.917	0.033
100	186	7.999	8.082	0.083
100	187	8.008	8.085	0.077
100	188	7.983	8.126	0.143
100	189	8.075	8.160	0.085
100	190	8.065	8.149	0.084
105	191	8.017	8.047	0.030
105	192	7.935	8.057	0.122
105	193	7.949	8.045	0.096
105	194	7.963	8.099	0.136
105	195	8.079	8.203	0.124
105	196	8.024	8.179	0.155
Max		8.420	8.510	0.302
Average		8.125	8.167	0.042
Min		7.851	7.847	-0.153
Std Dev		0.172	0.159	0.097



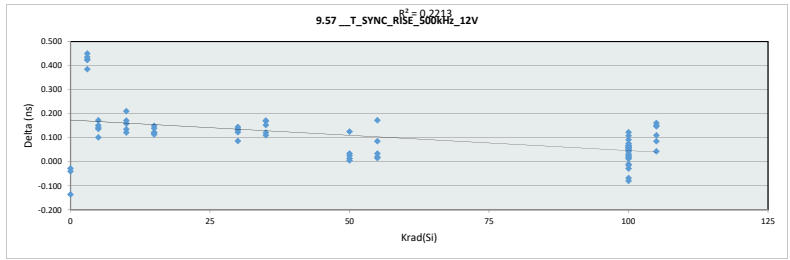
9.53_T_SYNC_R		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		7.880	8.236	8.241	8.257	8.237	8.328	8.261	7.847	7.865	7.917	8.045
Average		7.948	8.297	8.290	8.309	8.325	8.375	8.385	7.983	7.990	8.081	8.105
Max		8.010	8.352	8.332	8.341	8.424	8.422	8.510	8.050	8.077	8.235	8.203
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



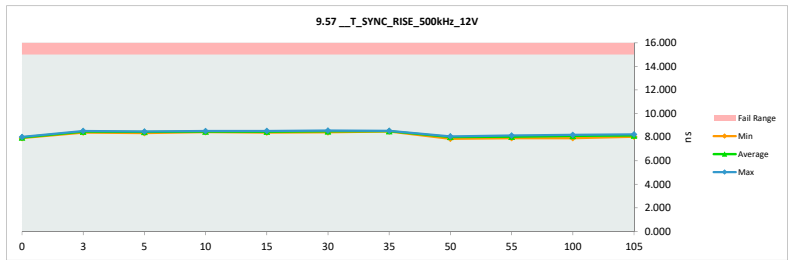
HDR TID Report  
TPS7H5003-SP

		9.57_T_SYNC_RISE_500kHz_12V	
Test Site	Tester		
Test Number	Unit	ns	ns
Max Limit	Min Limit	15	15

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.059	8.019	-0.040
0	138	8.001	7.973	-0.028
0	139	8.058	7.921	-0.137
3	197	8.091	8.516	0.425
3	198	8.043	8.466	0.423
3	199	8.004	8.389	0.385
3	202	8.088	8.523	0.435
3	203	7.978	8.428	0.450
5	204	8.323	8.474	0.151
5	205	8.245	8.346	0.101
5	206	8.294	8.435	0.141
5	208	8.325	8.461	0.136
5	209	8.282	8.454	0.172
10	210	8.303	8.513	0.210
10	211	8.308	8.479	0.171
10	212	8.266	8.424	0.158
10	213	8.318	8.453	0.135
10	214	8.354	8.475	0.121
15	215	8.375	8.524	0.149
15	217	8.270	8.389	0.119
15	219	8.325	8.438	0.113
15	220	8.350	8.472	0.122
15	221	8.315	8.454	0.139
30	222	8.361	8.505	0.144
30	223	8.420	8.555	0.135
30	224	8.390	8.522	0.132
30	225	8.386	8.472	0.086
30	226	8.285	8.407	0.122
35	227	8.370	8.539	0.169
35	228	8.399	8.509	0.110
35	229	8.316	8.487	0.171
35	230	8.379	8.500	0.121
35	231	8.334	8.486	0.152
50	170	8.022	8.036	0.014
50	172	7.858	7.863	0.005
50	173	8.026	8.060	0.034
50	175	7.998	8.023	0.025
50	176	7.939	8.064	0.125
55	177	8.060	8.075	0.015
55	178	7.883	8.055	0.172
55	179	7.914	7.948	0.034
55	180	7.894	7.914	0.020
55	181	8.057	8.142	0.085
100	154	8.118	8.038	-0.080
100	155	8.088	8.078	-0.010
100	156	8.049	8.117	0.068
100	157	8.007	8.056	0.049
100	158	8.086	8.147	0.061
100	159	7.948	7.974	0.026
100	160	8.122	8.177	0.055
100	161	8.098	8.140	0.042
100	162	8.074	8.124	0.050
100	163	8.070	8.082	0.012
100	164	8.076	8.061	-0.015
100	165	8.098	8.069	-0.029
100	166	7.961	8.028	0.067
100	168	8.019	8.039	0.020
100	169	8.101	8.133	0.032
100	182	8.007	8.129	0.122
100	183	8.099	8.190	0.091
100	184	8.026	8.102	0.076
100	185	7.983	7.915	-0.068
100	186	8.035	8.102	0.067
100	187	8.091	8.107	0.016
100	188	7.994	8.102	0.108
100	189	8.095	8.155	0.060
100	190	8.096	8.155	0.059
105	191	8.016	8.101	0.085
105	192	7.991	8.034	0.043
105	193	7.990	8.100	0.110
105	194	8.033	8.194	0.161
105	195	8.082	8.229	0.147
105	196	8.041	8.192	0.151
	Max	8.420	8.555	0.450
	Average	8.134	8.236	0.103
	Min	7.858	7.863	-0.137
	Std Dev	0.155	0.209	0.111

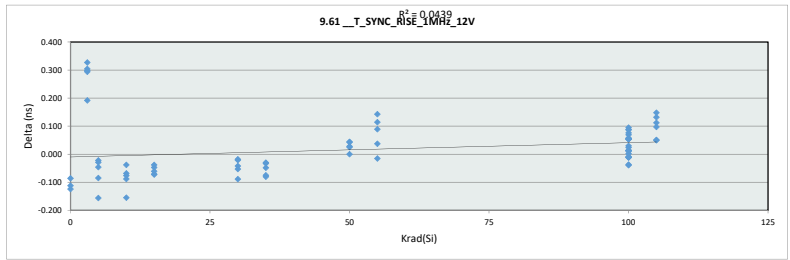


		9.57_T_SYNC_R										
Test Site	Tester											
Test Number	Unit	15	ns									
Max Limit	Min Limit	15	ns									
LL	Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
Min		7.921	8.389	8.346	8.424	8.389	8.407	8.486	7.863	7.914	7.915	8.034
Average		7.971	8.464	8.434	8.469	8.455	8.492	8.504	8.009	8.027	8.093	8.142
Max		8.019	8.523	8.474	8.513	8.524	8.555	8.539	8.064	8.142	8.190	8.229
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

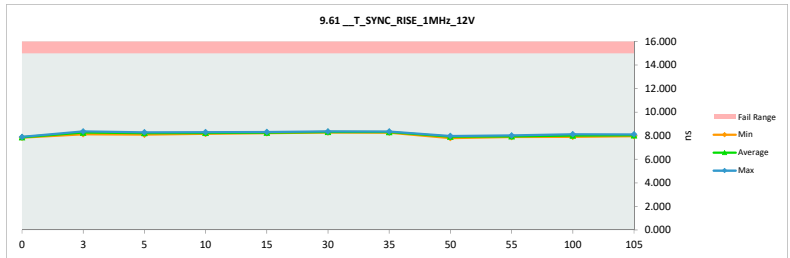


HDR TID Report  
TPS7H5003-SP

9.61 _T_SYNC_RISE_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.028	7.904	-0.124
0	138	7.954	7.842	-0.112
0	139	7.952	7.866	-0.086
3	197	8.069	8.363	0.294
3	198	7.976	8.303	0.327
3	199	7.949	8.247	0.298
3	202	7.998	8.302	0.304
3	203	7.933	8.125	0.192
5	204	8.324	8.278	-0.046
5	205	8.251	8.095	-0.156
5	206	8.298	8.213	-0.085
5	208	8.322	8.293	-0.029
5	209	8.248	8.226	-0.022
10	210	8.309	8.271	-0.038
10	211	8.314	8.159	-0.155
10	212	8.270	8.192	-0.078
10	213	8.332	8.256	-0.076
10	214	8.371	8.303	-0.068
15	215	8.388	8.316	-0.072
15	217	8.260	8.213	-0.047
15	219	8.293	8.255	-0.038
15	220	8.320	8.259	-0.061
15	221	8.321	8.250	-0.071
30	222	8.342	8.324	-0.018
30	223	8.408	8.366	-0.042
30	224	8.378	8.289	-0.089
30	225	8.384	8.331	-0.053
30	226	8.299	8.278	-0.021
35	227	8.387	8.307	-0.080
35	228	8.392	8.362	-0.030
35	229	8.334	8.259	-0.075
35	230	8.386	8.338	-0.048
35	231	8.329	8.296	-0.033
50	170	7.937	7.965	0.028
50	172	7.778	7.803	0.025
50	173	7.933	7.976	0.043
50	175	7.925	7.925	0.000
50	176	7.887	7.931	0.044
55	177	7.862	8.005	0.143
55	178	7.819	7.933	0.114
55	179	7.850	7.887	0.037
55	180	7.804	7.893	0.089
55	181	8.042	8.027	-0.015
100	154	8.017	8.008	-0.009
100	155	7.990	7.951	-0.039
100	156	8.042	8.005	-0.037
100	157	7.964	8.019	0.055
100	158	8.028	8.041	0.013
100	159	7.912	7.925	0.013
100	160	8.054	8.068	0.014
100	161	8.060	8.129	0.069
100	162	8.003	8.033	0.030
100	163	7.981	7.983	0.002
100	164	8.013	8.003	-0.010
100	165	8.009	7.998	-0.011
100	166	7.948	7.936	-0.012
100	168	7.934	7.948	0.014
100	169	8.063	8.074	0.011
100	182	7.957	8.012	0.055
100	183	8.034	8.110	0.076
100	184	7.978	8.002	0.024
100	185	7.810	7.905	0.095
100	186	7.938	8.025	0.087
100	187	7.996	8.049	0.053
100	188	7.928	8.016	0.088
100	189	8.041	8.095	0.054
100	190	8.023	8.081	0.058
105	191	7.917	7.967	0.050
105	192	7.914	7.965	0.051
105	193	7.907	8.039	0.132
105	194	7.957	8.069	0.112
105	195	8.017	8.114	0.097
105	196	7.930	8.078	0.148
Max		8.408	8.366	0.327
Average		8.087	8.105	0.018
Min		7.778	7.803	-0.156
Std Dev		0.188	0.155	0.100



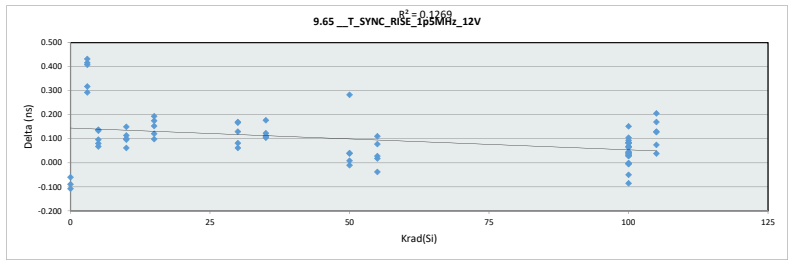
9.61 _T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.842	8.125	8.095	8.159	8.213	8.278	8.259	7.803	7.887	7.905	7.965
Average	7.871	8.268	8.221	8.234	8.259	8.318	8.312	7.920	7.949	8.017	8.039
Max	7.904	8.363	8.293	8.303	8.316	8.366	8.362	7.976	8.027	8.129	8.114
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



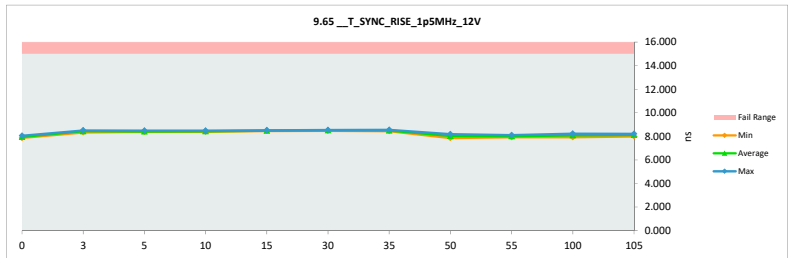
# HDR TID Report TPS7H5003-SP

9.65 _T_SYNC_RISE_1p5MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.109	8.048	-0.061
0	138	8.021	7.931	-0.090
0	139	7.973	7.864	-0.109
3	197	8.138	8.430	0.292
3	198	8.043	8.458	0.415
3	199	7.999	8.430	0.431
3	202	8.097	8.504	0.407
3	203	8.010	8.327	0.317
5	204	8.352	8.432	0.080
5	205	8.253	8.390	0.137
5	206	8.317	8.384	0.067
5	208	8.347	8.480	0.133
5	209	8.304	8.400	0.096
10	210	8.319	8.432	0.113
10	211	8.318	8.467	0.149
10	212	8.296	8.392	0.096
10	213	8.329	8.390	0.061
10	214	8.388	8.488	0.100
15	215	8.397	8.517	0.120
15	217	8.261	8.454	0.193
15	219	8.315	8.489	0.174
15	220	8.353	8.451	0.098
15	221	8.333	8.485	0.152
30	222	8.377	8.543	0.166
30	223	8.434	8.495	0.061
30	224	8.397	8.526	0.129
30	225	8.413	8.494	0.081
30	226	8.293	8.462	0.169
35	227	8.375	8.482	0.107
35	228	8.426	8.548	0.122
35	229	8.342	8.518	0.176
35	230	8.400	8.512	0.112
35	231	8.343	8.447	0.104
50	170	7.990	7.998	0.008
50	172	7.818	7.856	0.038
50	173	8.051	8.040	-0.011
50	175	7.908	8.190	0.282
50	176	7.962	8.002	0.040
55	177	7.991	8.101	0.110
55	178	7.897	7.974	0.077
55	179	7.907	7.924	0.017
55	180	8.003	7.964	-0.039
55	181	8.062	8.089	0.027
100	154	8.151	8.065	-0.086
100	155	7.990	7.984	-0.006
100	156	8.083	8.081	-0.002
100	157	7.994	8.057	0.063
100	158	8.050	8.078	0.028
100	159	7.953	8.046	0.093
100	160	8.166	8.162	-0.004
100	161	8.116	8.220	0.104
100	162	8.046	8.043	-0.003
100	163	8.088	8.128	0.040
100	164	8.038	8.080	0.042
100	165	8.019	8.050	0.031
100	166	8.028	8.056	0.028
100	168	7.966	8.033	0.067
100	169	8.093	8.132	0.039
100	182	8.048	8.129	0.081
100	183	8.087	8.154	0.067
100	184	7.959	8.043	0.084
100	185	7.977	7.926	-0.051
100	186	8.007	8.040	0.033
100	187	8.036	8.081	0.045
100	188	8.032	8.116	0.084
100	189	8.083	8.165	0.082
100	190	8.062	8.213	0.151
105	191	7.965	8.094	0.129
105	192	7.956	7.994	0.038
105	193	7.978	8.052	0.074
105	194	8.038	8.165	0.127
105	195	8.041	8.210	0.169
105	196	7.980	8.185	0.205
Max		8.434	8.548	0.431
Average		8.133	8.227	0.095
Min		7.818	7.856	-0.109
Std Dev		0.168	0.211	0.105



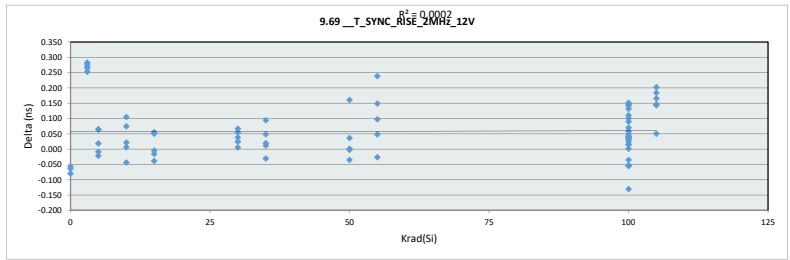
9.65 _T_SYNC_R		Krad(Si)										
		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		7.864	8.327	8.384	8.390	8.451	8.462	8.447	7.856	7.924	7.926	7.994
Average		7.948	8.430	8.417	8.434	8.479	8.504	8.501	8.017	8.010	8.087	8.117
Max		8.048	8.504	8.480	8.488	8.517	8.543	8.548	8.190	8.101	8.220	8.210
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



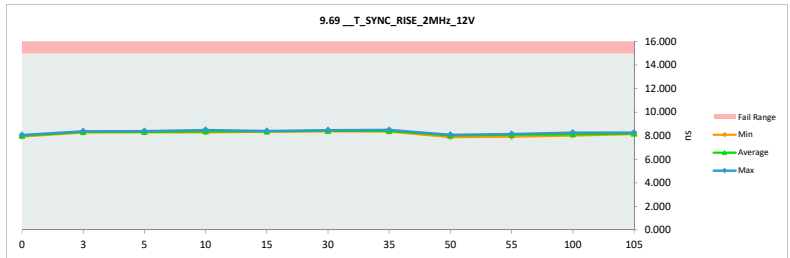
# HDR TID Report TPS7H5003-SP

9.69 __T_SYNC_RISE_2MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15			
Min Limit				

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.128	8.063	-0.065
0	138	7.997	7.939	-0.058
0	139	8.081	8.000	-0.081
3	197	8.103	8.385	0.282
3	198	8.109	8.361	0.252
3	199	8.039	8.308	0.269
3	202	8.037	8.301	0.264
3	203	7.990	8.267	0.277
5	204	8.331	8.322	-0.009
5	205	8.285	8.303	0.018
5	206	8.305	8.283	-0.022
5	208	8.334	8.398	0.064
5	209	8.278	8.342	0.064
10	210	8.333	8.339	0.006
10	211	8.333	8.407	0.074
10	212	8.306	8.327	0.021
10	213	8.332	8.288	-0.044
10	214	8.390	8.494	0.104
15	215	8.417	8.412	-0.005
15	217	8.302	8.352	0.050
15	219	8.319	8.374	0.055
15	220	8.350	8.334	-0.016
15	221	8.375	8.336	-0.039
30	222	8.365	8.419	0.054
30	223	8.414	8.480	0.066
30	224	8.391	8.396	0.005
30	225	8.405	8.429	0.024
30	226	8.335	8.373	0.038
35	227	8.369	8.417	0.048
35	228	8.404	8.498	0.094
35	229	8.334	8.353	0.019
35	230	8.417	8.386	-0.031
35	231	8.343	8.354	0.011
50	170	8.013	8.049	0.036
50	172	7.915	7.879	-0.036
50	173	8.058	8.055	-0.003
50	175	8.039	8.040	0.001
50	176	7.927	8.087	0.160
55	177	8.093	8.141	0.048
55	178	7.827	8.065	0.238
55	179	7.962	8.110	0.148
55	180	7.941	7.914	-0.027
55	181	8.056	8.153	0.097
100	154	8.241	8.110	-0.131
100	155	8.063	8.098	0.035
100	156	8.124	8.155	0.031
100	157	8.025	8.094	0.069
100	158	8.111	8.125	0.014
100	159	8.002	8.017	0.015
100	160	8.091	8.237	0.146
100	161	8.177	8.266	0.089
100	162	8.205	8.169	-0.036
100	163	8.086	8.126	0.040
100	164	8.122	8.069	-0.053
100	165	8.036	8.037	0.001
100	166	8.086	8.111	0.025
100	168	8.034	8.074	0.040
100	169	8.114	8.059	-0.055
100	182	8.106	8.247	0.141
100	183	8.138	8.248	0.110
100	184	8.079	8.125	0.046
100	185	7.979	8.017	0.038
100	186	8.011	8.111	0.100
100	187	8.077	8.134	0.057
100	188	8.115	8.060	-0.055
100	189	8.029	8.160	0.131
100	190	8.069	8.220	0.151
105	191	8.005	8.170	0.165
105	192	7.991	8.134	0.143
105	193	7.992	8.194	0.202
105	194	8.062	8.208	0.146
105	195	8.088	8.271	0.183
105	196	8.106	8.156	0.050
Max		8.417	8.498	0.282
Average		8.157	8.216	0.059
Min		7.827	7.879	-0.131
Std Dev		0.155	0.150	0.091



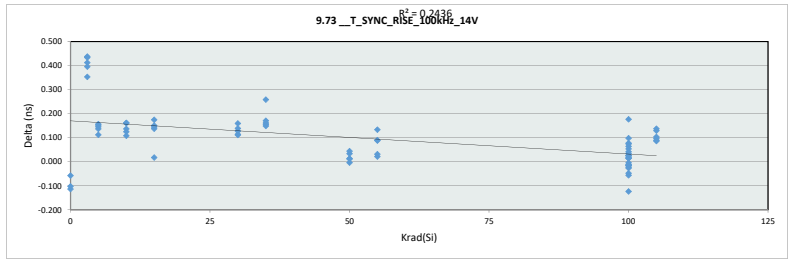
9.69 __T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.939	8.267	8.283	8.288	8.334	8.373	8.353	7.879	7.914	8.017	8.134
Average	8.001	8.324	8.330	8.371	8.362	8.419	8.402	8.022	8.077	8.128	8.189
Max	8.063	8.385	8.398	8.494	8.412	8.480	8.498	8.087	8.153	8.266	8.271
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



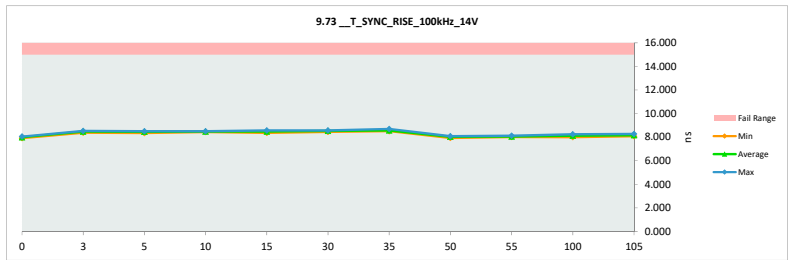
HDR TID Report  
TPS7H5003-SP

9.73_T_SYNC_RISE_100kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.166	8.052	-0.114
0	138	8.023	7.920	-0.103
0	139	8.052	7.994	-0.058
3	197	8.123	8.535	0.412
3	198	8.088	8.525	0.437
3	199	8.015	8.448	0.433
3	202	8.145	8.498	0.353
3	203	7.985	8.380	0.395
5	204	8.339	8.491	0.152
5	205	8.234	8.346	0.112
5	206	8.340	8.476	0.136
5	208	8.353	8.498	0.145
5	209	8.297	8.453	0.156
10	210	8.339	8.463	0.124
10	211	8.311	8.472	0.161
10	212	8.303	8.439	0.136
10	213	8.345	8.506	0.161
10	214	8.389	8.497	0.108
15	215	8.401	8.575	0.174
15	217	8.280	8.417	0.137
15	219	8.347	8.491	0.144
15	220	8.369	8.519	0.150
15	221	8.348	8.365	0.017
30	222	8.392	8.530	0.138
30	223	8.446	8.573	0.127
30	224	8.425	8.536	0.111
30	225	8.440	8.554	0.114
30	226	8.294	8.453	0.159
35	227	8.395	8.543	0.148
35	228	8.440	8.698	0.258
35	229	8.347	8.508	0.161
35	230	8.397	8.552	0.155
35	231	8.346	8.516	0.170
50	170	8.026	8.070	0.044
50	172	7.919	7.930	0.011
50	173	8.085	8.081	-0.004
50	175	7.991	8.004	0.013
50	176	7.993	8.026	0.033
55	177	8.078	8.099	0.021
55	178	7.923	8.013	0.090
55	179	7.921	8.009	0.088
55	180	7.919	8.052	0.133
55	181	8.097	8.128	0.031
100	154	8.125	8.111	-0.014
100	155	8.067	8.054	-0.013
100	156	8.184	8.127	-0.057
100	157	8.077	8.054	-0.023
100	158	8.116	8.141	0.025
100	159	8.070	8.043	-0.027
100	160	8.132	8.196	0.064
100	161	8.169	8.243	0.074
100	162	8.105	8.088	-0.017
100	163	8.076	8.092	0.016
100	164	8.213	8.089	-0.124
100	165	8.109	8.104	-0.005
100	166	8.008	8.106	0.098
100	168	8.059	8.044	-0.015
100	169	8.155	8.169	0.014
100	182	8.103	8.127	0.024
100	183	8.136	8.189	0.053
100	184	8.077	8.111	0.034
100	185	7.988	8.003	0.015
100	186	8.010	8.186	0.176
100	187	8.104	8.129	0.025
100	188	8.067	8.108	0.041
100	189	8.167	8.119	-0.048
100	190	8.131	8.208	0.077
105	191	8.026	8.112	0.086
105	192	7.996	8.085	0.089
105	193	7.998	8.101	0.103
105	194	8.029	8.158	0.129
105	195	8.176	8.273	0.097
105	196	8.087	8.224	0.137
Max		8.446	8.698	0.437
Average		8.167	8.261	0.093
Min		7.919	7.920	-0.124
Std Dev		0.153	0.210	0.116



9.73_T_SYNC_R		0	3	5	10	15	30	35	50	55	100	105
LL		7.920	8.380	8.346	8.439	8.365	8.453	8.508	7.930	8.009	8.003	8.085
Min		7.989	8.477	8.453	8.475	8.473	8.529	8.563	8.022	8.060	8.118	8.159
Average		8.052	8.535	8.498	8.506	8.575	8.573	8.698	8.081	8.128	8.243	8.273
Max		8.052	8.535	8.498	8.506	8.575	8.573	8.698	8.081	8.128	8.243	8.273
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

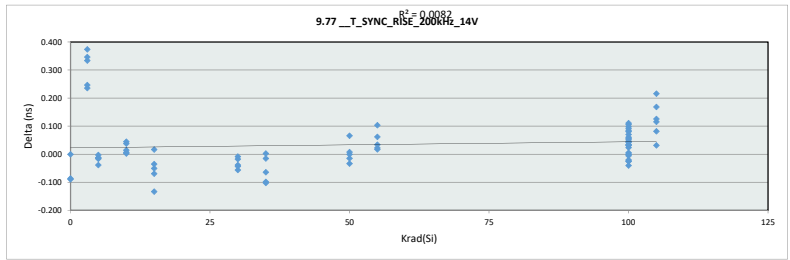




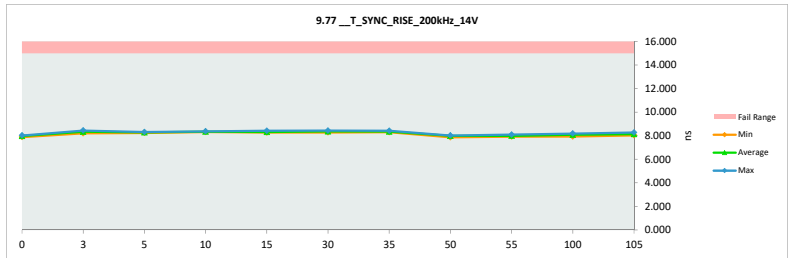
# HDR TID Report TPS7H5003-SP

9.77_T_SYNC_RISE_200kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.103	8.016	-0.087
0	138	8.014	8.013	-0.001
0	139	7.973	7.883	-0.090
3	197	8.062	8.435	0.373
3	198	8.061	8.307	0.246
3	199	7.968	8.314	0.346
3	202	8.072	8.405	0.333
3	203	7.968	8.203	0.235
5	204	8.345	8.306	-0.039
5	205	8.246	8.233	-0.013
5	206	8.286	8.283	-0.003
5	208	8.325	8.311	-0.014
5	209	8.290	8.273	-0.017
10	210	8.317	8.354	0.037
10	211	8.318	8.324	0.006
10	212	8.295	8.309	0.014
10	213	8.316	8.360	0.044
10	214	8.368	8.370	0.002
15	215	8.399	8.415	0.016
15	217	8.398	8.264	-0.134
15	219	8.313	8.277	-0.036
15	220	8.348	8.297	-0.051
15	221	8.345	8.275	-0.070
30	222	8.375	8.357	-0.018
30	223	8.440	8.431	-0.009
30	224	8.411	8.372	-0.039
30	225	8.436	8.379	-0.057
30	226	8.320	8.276	-0.044
35	227	8.387	8.322	-0.065
35	228	8.419	8.421	0.002
35	229	8.340	8.324	-0.016
35	230	8.496	8.397	-0.099
35	231	8.400	8.297	-0.103
50	170	8.020	7.986	-0.034
50	172	7.874	7.859	-0.015
50	173	8.015	8.014	-0.001
50	175	8.009	8.016	0.007
50	176	7.954	8.019	0.065
55	177	7.976	7.993	0.017
55	178	7.887	7.990	0.103
55	179	7.915	7.948	0.033
55	180	7.862	7.923	0.061
55	181	8.072	8.094	0.022
100	154	8.098	8.092	-0.006
100	155	7.989	7.989	0.000
100	156	8.075	8.180	0.105
100	157	8.001	8.084	0.083
100	158	8.045	8.050	0.005
100	159	7.976	8.086	0.110
100	160	8.104	8.138	0.034
100	161	8.182	8.161	-0.021
100	162	8.029	8.003	-0.026
100	163	7.997	8.042	0.045
100	164	8.069	8.100	0.031
100	165	8.072	8.047	-0.025
100	166	7.987	8.038	0.051
100	168	7.983	7.942	-0.041
100	169	8.102	8.125	0.023
100	182	8.010	8.066	0.056
100	183	8.051	8.131	0.080
100	184	8.007	8.039	0.032
100	185	7.928	7.926	-0.002
100	186	8.033	8.075	0.042
100	187	8.020	8.117	0.097
100	188	8.025	8.115	0.090
100	189	8.078	8.138	0.060
100	190	8.083	8.153	0.070
105	191	8.000	8.031	0.031
105	192	7.921	8.136	0.215
105	193	7.975	8.090	0.115
105	194	7.991	8.116	0.125
105	195	8.100	8.268	0.168
105	196	8.074	8.155	0.081
Max		8.496	8.435	0.373
Average		8.133	8.168	0.035
Min		7.862	7.859	-0.134
Std Dev		0.174	0.155	0.098



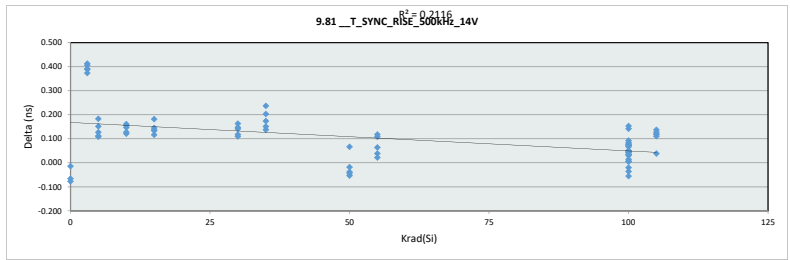
9.77_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.883	8.203	8.233	8.309	8.264	8.276	8.297	7.859	7.923	7.926	8.031
Average	7.971	8.333	8.281	8.343	8.306	8.363	8.352	7.979	7.990	8.077	8.133
Max	8.016	8.435	8.311	8.370	8.415	8.431	8.421	8.019	8.094	8.180	8.268
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



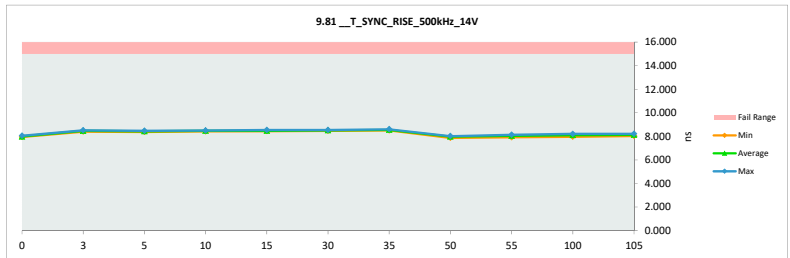
# HDR TID Report TPS7H5003-SP

9.81_T_SYNC_RISE_500kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.109	8.042	-0.067
0	138	7.985	7.970	-0.015
0	139	8.024	7.946	-0.078
3	197	8.113	8.503	0.390
3	198	8.066	8.471	0.405
3	199	8.026	8.414	0.388
3	202	8.140	8.513	0.373
3	203	7.989	8.401	0.412
5	204	8.316	8.427	0.111
5	205	8.244	8.370	0.126
5	206	8.282	8.464	0.182
5	208	8.319	8.470	0.151
5	209	8.271	8.379	0.108
10	210	8.326	8.481	0.155
10	211	8.315	8.475	0.160
10	212	8.294	8.421	0.127
10	213	8.325	8.445	0.120
10	214	8.345	8.491	0.146
15	215	8.387	8.521	0.144
15	217	8.267	8.448	0.181
15	219	8.302	8.435	0.133
15	220	8.333	8.469	0.136
15	221	8.329	8.445	0.116
30	222	8.361	8.507	0.146
30	223	8.409	8.518	0.109
30	224	8.390	8.530	0.140
30	225	8.387	8.504	0.117
30	226	8.301	8.463	0.162
35	227	8.363	8.599	0.236
35	228	8.395	8.597	0.202
35	229	8.333	8.506	0.173
35	230	8.378	8.515	0.137
35	231	8.350	8.500	0.150
50	170	8.043	8.004	-0.039
50	172	7.923	7.869	-0.054
50	173	8.059	8.015	-0.044
50	175	7.994	7.975	-0.019
50	176	7.950	8.016	0.066
55	177	8.018	8.126	0.108
55	178	7.904	8.021	0.117
55	179	7.952	8.015	0.063
55	180	7.896	7.917	0.021
55	181	8.087	8.125	0.038
100	154	8.107	8.070	-0.037
100	155	8.036	8.041	0.005
100	156	8.087	8.134	0.047
100	157	8.054	8.033	-0.021
100	158	8.112	8.116	0.004
100	159	7.937	7.985	0.048
100	160	8.124	8.166	0.042
100	161	8.093	8.185	0.092
100	162	8.037	8.115	0.078
100	163	8.031	8.114	0.083
100	164	8.088	8.102	0.014
100	165	8.012	8.041	0.029
100	166	7.966	8.107	0.141
100	168	7.983	8.055	0.072
100	169	8.121	8.154	0.033
100	182	8.040	8.115	0.075
100	183	8.050	8.202	0.152
100	184	8.019	8.085	0.066
100	185	8.019	7.963	-0.056
100	186	8.055	8.097	0.042
100	187	8.053	8.103	0.050
100	188	8.085	8.091	0.006
100	189	8.137	8.172	0.035
100	190	8.093	8.164	0.071
105	191	7.989	8.125	0.136
105	192	7.999	8.037	0.038
105	193	7.989	8.108	0.119
105	194	8.044	8.171	0.127
105	195	8.093	8.211	0.118
105	196	8.027	8.137	0.110
Max		8.409	8.599	0.412
Average		8.138	8.240	0.102
Min		7.896	7.869	-0.078
Std Dev		0.152	0.211	0.106



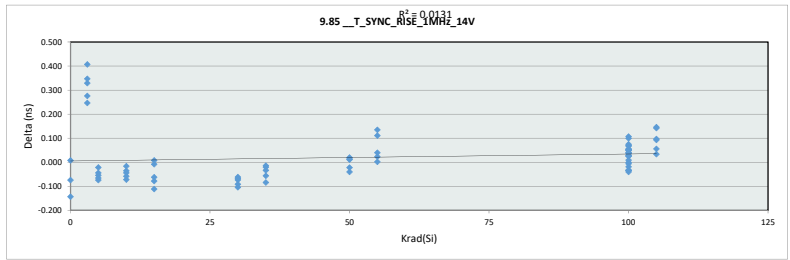
9.81_T_SYNC_R		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		7.946	8.401	8.370	8.421	8.435	8.463	8.500	7.869	7.917	7.963	8.037
Average		7.986	8.460	8.422	8.463	8.466	8.504	8.543	7.976	8.041	8.100	8.132
Max		8.042	8.513	8.470	8.491	8.531	8.530	8.599	8.016	8.126	8.202	8.211
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



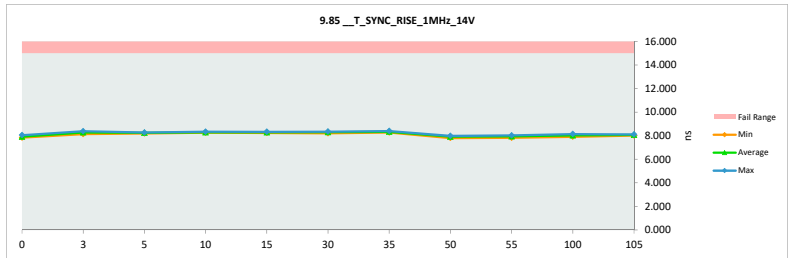
# HDR TID Report TPS7H5003-SP

9.85_T_SYNC_RISE_1MHZ_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.036	8.043	0.007
0	138	7.972	7.828	-0.144
0	139	7.949	7.874	-0.075
3	197	7.971	8.377	0.406
3	198	8.004	8.250	0.246
3	199	7.921	8.249	0.328
3	202	8.015	8.361	0.346
3	203	7.859	8.134	0.275
5	204	8.336	8.270	-0.066
5	205	8.234	8.189	-0.045
5	206	8.297	8.222	-0.075
5	208	8.325	8.270	-0.055
5	209	8.285	8.263	-0.022
10	210	8.312	8.295	-0.017
10	211	8.322	8.249	-0.073
10	212	8.295	8.250	-0.045
10	213	8.324	8.255	-0.069
10	214	8.370	8.334	-0.036
15	215	8.391	8.328	-0.063
15	217	8.251	8.258	0.007
15	219	8.316	8.307	-0.009
15	220	8.341	8.262	-0.079
15	221	8.343	8.231	-0.112
30	222	8.357	8.295	-0.062
30	223	8.415	8.341	-0.074
30	224	8.384	8.292	-0.092
30	225	8.409	8.341	-0.068
30	226	8.302	8.197	-0.105
35	227	8.367	8.310	-0.057
35	228	8.410	8.395	-0.015
35	229	8.323	8.302	-0.021
35	230	8.404	8.370	-0.034
35	231	8.356	8.271	-0.085
50	170	7.920	7.933	0.013
50	172	7.840	7.799	-0.041
50	173	8.002	7.979	-0.023
50	175	7.917	7.936	0.019
50	176	7.931	7.941	0.010
55	177	7.915	8.025	0.110
55	178	7.829	7.963	0.134
55	179	7.856	7.878	0.022
55	180	7.821	7.822	0.001
55	181	7.982	8.021	0.039
100	154	8.044	8.003	-0.041
100	155	7.972	7.965	-0.007
100	156	8.067	8.032	-0.035
100	157	7.950	7.948	-0.002
100	158	8.008	8.046	0.038
100	159	7.930	7.964	0.034
100	160	8.028	8.096	0.068
100	161	8.087	8.136	0.049
100	162	7.977	8.027	0.050
100	163	8.023	8.048	0.025
100	164	7.984	8.011	0.027
100	165	8.015	7.982	-0.033
100	166	7.958	7.938	-0.020
100	168	7.972	7.937	-0.035
100	169	8.048	8.101	0.053
100	182	7.950	7.990	0.040
100	183	8.025	8.131	0.106
100	184	7.934	8.033	0.099
100	185	7.883	7.908	0.025
100	186	7.955	8.029	0.074
100	187	7.957	8.024	0.067
100	188	7.968	7.976	0.008
100	189	8.045	8.095	0.050
100	190	8.018	8.073	0.055
105	191	7.986	8.019	0.033
105	192	7.908	8.004	0.096
105	193	7.945	8.090	0.145
105	194	7.989	8.081	0.092
105	195	8.046	8.101	0.055
105	196	7.931	8.072	0.141
Max		8.415	8.395	0.406
Average		8.093	8.115	0.021
Min		7.821	7.799	-0.144
Std Dev		0.187	0.159	0.104



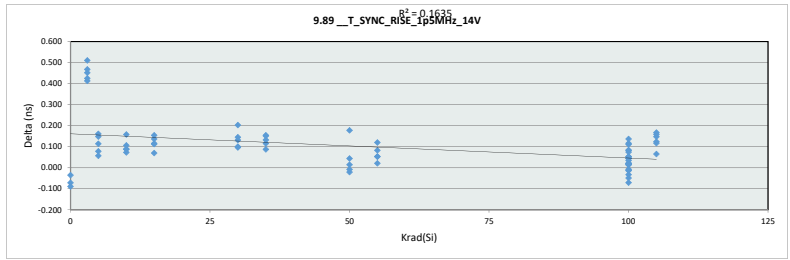
9.85_T_SYNC_R		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		7.828	8.134	8.189	8.249	8.231	8.197	8.271	7.799	7.822	7.908	8.004
Average		7.915	8.274	8.243	8.279	8.277	8.293	8.330	7.918	7.942	8.021	8.061
Max		8.043	8.377	8.270	8.334	8.328	8.341	8.395	7.979	8.025	8.136	8.101
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



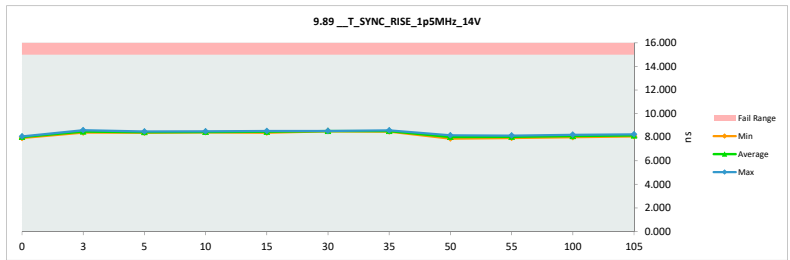
# HDR TID Report TPS7H5003-SP

9.89_T_SYNC_RISE_1p5MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.150	8.060	-0.090
0	138	7.998	7.925	-0.073
0	139	8.095	8.059	-0.036
3	197	8.075	8.584	0.509
3	198	8.044	8.456	0.412
3	199	7.966	8.417	0.451
3	202	8.093	8.517	0.424
3	203	7.916	8.383	0.467
5	204	8.341	8.418	0.077
5	205	8.247	8.407	0.160
5	206	8.317	8.373	0.056
5	208	8.333	8.480	0.147
5	209	8.310	8.423	0.113
10	210	8.324	8.411	0.087
10	211	8.323	8.411	0.088
10	212	8.297	8.402	0.105
10	213	8.337	8.494	0.157
10	214	8.374	8.446	0.072
15	215	8.406	8.520	0.114
15	217	8.246	8.382	0.136
15	219	8.322	8.475	0.153
15	220	8.368	8.479	0.111
15	221	8.333	8.402	0.069
30	222	8.382	8.526	0.144
30	223	8.417	8.516	0.099
30	224	8.398	8.528	0.130
30	225	8.404	8.499	0.095
30	226	8.302	8.504	0.202
35	227	8.387	8.473	0.086
35	228	8.442	8.573	0.131
35	229	8.332	8.482	0.150
35	230	8.407	8.521	0.114
35	231	8.355	8.508	0.153
50	170	7.968	8.011	0.043
50	172	7.864	7.877	0.013
50	173	8.072	8.063	-0.009
50	175	7.978	8.155	0.177
50	176	7.987	7.965	-0.022
55	177	8.050	8.132	0.082
55	178	7.895	8.014	0.119
55	179	7.915	7.935	0.020
55	180	7.923	7.975	0.052
55	181	8.035	8.087	0.052
100	154	8.137	8.065	-0.072
100	155	8.048	8.035	-0.013
100	156	8.086	8.099	0.013
100	157	7.962	8.045	0.083
100	158	8.080	8.098	0.018
100	159	7.966	8.010	0.044
100	160	8.067	8.140	0.073
100	161	8.194	8.186	-0.008
100	162	8.055	8.107	0.052
100	163	8.112	8.062	-0.050
100	164	8.044	8.029	-0.015
100	165	8.071	8.037	-0.034
100	166	8.003	8.024	0.021
100	168	7.985	8.019	0.034
100	169	8.156	8.173	0.017
100	182	8.046	8.098	0.052
100	183	8.061	8.197	0.136
100	184	7.995	8.076	0.081
100	185	7.978	7.991	0.013
100	186	8.005	8.049	0.044
100	187	8.069	8.089	0.020
100	188	8.035	8.078	0.043
100	189	8.065	8.180	0.115
100	190	8.089	8.199	0.110
105	191	8.001	8.065	0.064
105	192	7.959	8.082	0.123
105	193	7.962	8.108	0.146
105	194	8.013	8.171	0.158
105	195	8.121	8.237	0.116
105	196	8.026	8.192	0.166
Max		8.442	8.584	0.509
Average		8.139	8.236	0.097
Min		7.864	7.877	-0.090
Std Dev		0.165	0.207	0.117



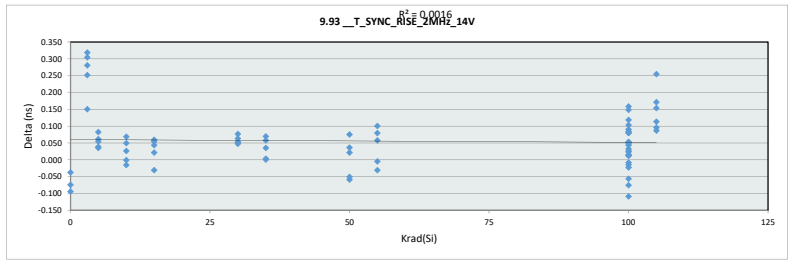
9.89_T_SYNC_R		Krad(Si)										
		0	3	5	10	15	30	35	50	55	100	105
LL		7.925	8.383	8.373	8.402	8.382	8.499	8.473	7.877	7.935	7.991	8.065
Min		8.015	8.471	8.420	8.433	8.452	8.515	8.511	8.014	8.029	8.087	8.143
Average		8.060	8.584	8.480	8.494	8.520	8.528	8.573	8.155	8.132	8.199	8.237
Max		8.060	8.584	8.480	8.494	8.520	8.528	8.573	8.155	8.132	8.199	8.237
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



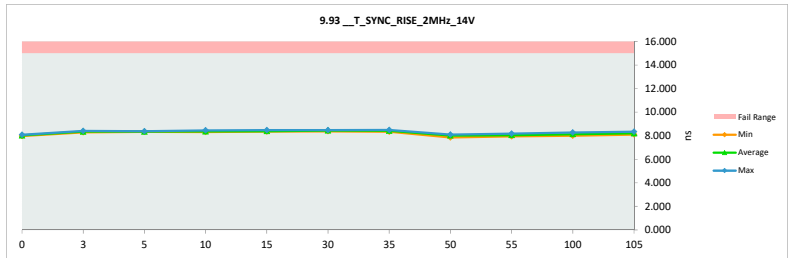
HDR TID Report  
TPS7H5003-SP

9.93 _T_SYNC_RISE_2MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.170	8.075	-0.095
0	138	8.037	7.962	-0.075
0	139	8.065	8.027	-0.038
3	197	8.119	8.399	0.280
3	198	8.071	8.389	0.318
3	199	8.027	8.278	0.251
3	202	8.219	8.369	0.150
3	203	7.994	8.298	0.304
5	204	8.320	8.374	0.054
5	205	8.268	8.329	0.061
5	206	8.283	8.321	0.038
5	208	8.331	8.366	0.035
5	209	8.277	8.359	0.082
10	210	8.323	8.307	-0.016
10	211	8.337	8.386	0.049
10	212	8.318	8.317	-0.001
10	213	8.330	8.356	0.026
10	214	8.381	8.449	0.068
15	215	8.408	8.467	0.059
15	217	8.302	8.356	0.054
15	219	8.317	8.360	0.043
15	220	8.349	8.370	0.021
15	221	8.383	8.352	-0.031
30	222	8.373	8.449	0.076
30	223	8.407	8.470	0.063
30	224	8.400	8.453	0.053
30	225	8.389	8.436	0.047
30	226	8.322	8.372	0.050
35	227	8.373	8.376	0.003
35	228	8.417	8.486	0.069
35	229	8.329	8.329	0.000
35	230	8.432	8.467	0.035
35	231	8.346	8.403	0.057
50	170	8.051	8.000	-0.051
50	172	7.909	7.850	-0.059
50	173	8.048	8.069	0.021
50	175	8.058	8.094	0.036
50	176	7.984	8.059	0.075
55	177	8.035	8.135	0.100
55	178	7.962	8.019	0.057
55	179	7.949	7.944	-0.005
55	180	7.976	7.945	-0.031
55	181	8.088	8.167	0.079
100	154	8.246	8.137	-0.109
100	155	8.088	8.073	-0.015
100	156	8.183	8.174	-0.009
100	157	8.019	8.070	0.051
100	158	8.090	8.116	0.026
100	159	7.965	7.997	0.032
100	160	8.131	8.249	0.118
100	161	8.176	8.266	0.090
100	162	8.073	8.176	0.103
100	163	8.082	8.129	0.047
100	164	8.131	8.074	-0.057
100	165	8.023	8.046	0.023
100	166	8.043	8.056	0.013
100	168	8.077	8.088	0.011
100	169	8.153	8.077	-0.076
100	182	8.020	8.178	0.158
100	183	8.172	8.251	0.079
100	184	8.078	8.131	0.053
100	185	8.001	8.016	0.015
100	186	8.057	8.141	0.084
100	187	8.118	8.095	-0.023
100	188	7.978	8.021	0.043
100	189	8.036	8.184	0.148
100	190	8.120	8.200	0.080
105	191	8.048	8.201	0.153
105	192	8.002	8.097	0.095
105	193	7.954	8.125	0.171
105	194	8.093	8.206	0.113
105	195	8.076	8.330	0.254
105	196	8.112	8.198	0.086
Max		8.432	8.486	0.318
Average		8.162	8.218	0.056
Min		7.909	7.850	-0.109
Std Dev		0.150	0.160	0.085



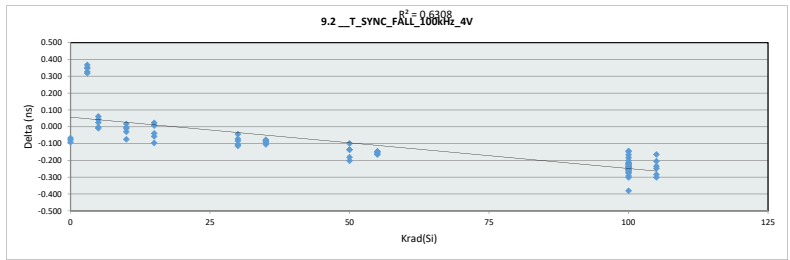
9.93 _T_SYNC_R		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		7.962	8.278	8.321	8.307	8.352	8.372	8.329	7.850	7.944	7.997	8.097
Average		8.021	8.347	8.350	8.363	8.381	8.436	8.412	8.014	8.042	8.123	8.193
Max		8.075	8.399	8.374	8.449	8.467	8.470	8.486	8.094	8.167	8.266	8.330
UL		15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



# HDR TID Report TPS7H5003-SP

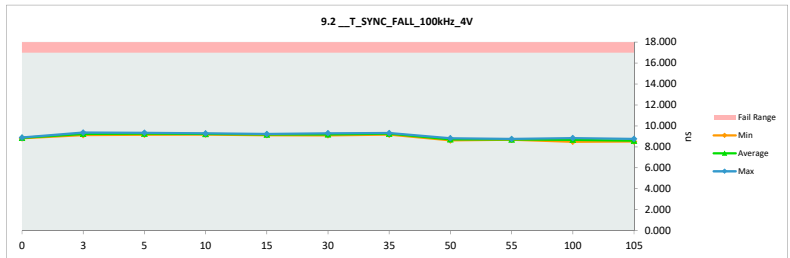
9.2_T_SYNC_FALL_100kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.957	8.879	-0.078
0	138	8.878	8.811	-0.067
0	139	8.983	8.891	-0.092
3	197	9.002	9.353	0.351
3	198	8.836	9.184	0.348
3	199	8.830	9.157	0.327
3	202	8.989	9.357	0.368
3	203	8.803	9.121	0.318
5	204	9.215	9.240	0.025
5	205	9.229	9.227	-0.002
5	206	9.272	9.333	0.061
5	208	9.297	9.340	0.043
5	209	9.165	9.156	-0.009
10	210	9.153	9.170	0.017
10	211	9.254	9.244	-0.010
10	212	9.305	9.230	-0.075
10	213	9.293	9.289	-0.004
10	214	9.220	9.190	-0.030
15	215	9.203	9.226	0.023
15	217	9.101	9.108	0.007
15	219	9.193	9.135	-0.058
15	220	9.301	9.204	-0.097
15	221	9.269	9.229	-0.040
30	222	9.206	9.100	-0.106
30	223	9.371	9.299	-0.072
30	224	9.232	9.148	-0.084
30	225	9.258	9.214	-0.044
30	226	9.279	9.165	-0.114
35	227	9.248	9.165	-0.083
35	228	9.387	9.302	-0.085
35	229	9.313	9.235	-0.078
35	230	9.423	9.318	-0.105
35	231	9.344	9.250	-0.094
50	170	8.753	8.616	-0.137
50	172	8.809	8.628	-0.181
50	173	8.954	8.817	-0.137
50	175	9.021	8.819	-0.202
50	176	8.793	8.694	-0.099
55	177	8.870	8.716	-0.154
55	178	8.916	8.751	-0.165
55	179	8.857	8.710	-0.147
55	180	8.841	8.679	-0.162
55	181	8.834	8.687	-0.147
100	154	8.913	8.532	-0.381
100	155	8.811	8.510	-0.301
100	156	8.865	8.601	-0.264
100	157	8.887	8.672	-0.215
100	158	8.928	8.689	-0.239
100	159	8.850	8.610	-0.240
100	160	9.035	8.781	-0.254
100	161	8.927	8.700	-0.227
100	162	8.941	8.687	-0.254
100	163	8.918	8.702	-0.216
100	164	8.922	8.649	-0.273
100	165	8.839	8.564	-0.275
100	166	8.857	8.615	-0.242
100	168	8.834	8.594	-0.240
100	169	9.034	8.806	-0.228
100	182	9.019	8.835	-0.184
100	183	8.959	8.811	-0.148
100	184	8.860	8.640	-0.220
100	185	8.776	8.483	-0.293
100	186	8.884	8.717	-0.167
100	187	9.028	8.770	-0.258
100	188	8.848	8.647	-0.201
100	189	8.985	8.750	-0.235
100	190	8.827	8.683	-0.144
105	191	8.714	8.509	-0.205
105	192	8.881	8.580	-0.301
105	193	8.921	8.756	-0.165
105	194	8.851	8.567	-0.284
105	195	8.816	8.581	-0.235
105	196	8.774	8.526	-0.248
Max		9.423	9.357	0.368
Average		9.016	8.903	-0.112
Min		8.714	8.483	-0.381
Std Dev		0.194	0.284	0.158



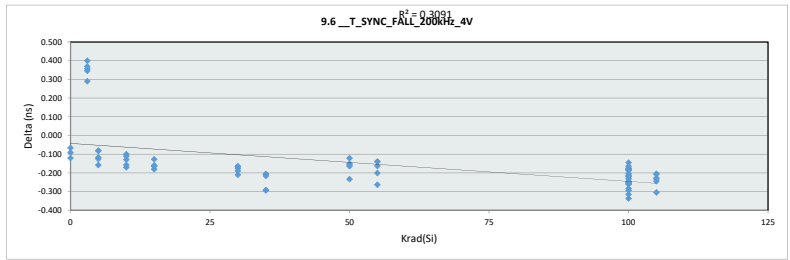
9.2_T_SYNC_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.811	9.121	9.156	9.170	9.108	9.100	9.165	8.616	8.679	8.483	8.509
Average	8.860	9.234	9.259	9.225	9.180	9.185	9.254	8.715	8.709	8.669	8.587
Max	8.891	9.357	9.340	9.289	9.229	9.299	9.318	8.819	8.751	8.835	8.756
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

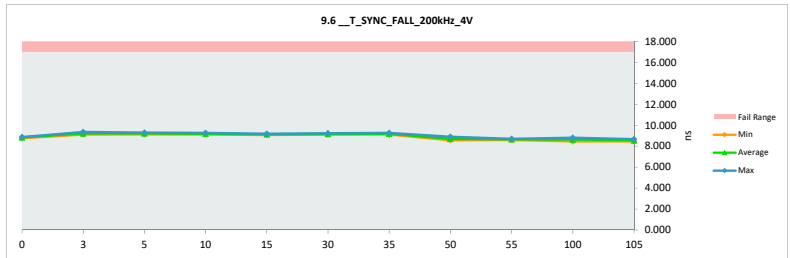


# HDR TID Report TPS7H5003-SP

		9.6_T_SYNC_FALL_200kHz_4V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	17	17	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.941	8.875	-0.066
0	138	8.859	8.768	-0.091
0	139	8.995	8.875	-0.120
3	197	8.964	9.364	0.400
3	198	8.856	9.146	0.290
3	199	8.776	9.145	0.369
3	202	8.945	9.301	0.356
3	203	8.776	9.122	0.346
5	204	9.315	9.233	-0.082
5	205	9.333	9.175	-0.158
5	206	9.369	9.252	-0.117
5	208	9.376	9.295	-0.081
5	209	9.249	9.124	-0.125
10	210	9.250	9.121	-0.129
10	211	9.331	9.231	-0.100
10	212	9.373	9.262	-0.111
10	213	9.385	9.228	-0.157
10	214	9.303	9.133	-0.170
15	215	9.285	9.125	-0.160
15	217	9.192	9.065	-0.127
15	219	9.283	9.120	-0.163
15	220	9.368	9.188	-0.180
15	221	9.337	9.172	-0.165
30	222	9.281	9.115	-0.166
30	223	9.430	9.241	-0.189
30	224	9.327	9.117	-0.210
30	225	9.341	9.176	-0.165
30	226	9.366	9.191	-0.175
35	227	9.337	9.121	-0.216
35	228	9.481	9.273	-0.208
35	229	9.410	9.205	-0.205
35	230	9.527	9.236	-0.291
35	231	9.449	9.156	-0.293
50	170	8.719	8.568	-0.151
50	172	8.805	8.641	-0.164
50	173	9.005	8.772	-0.233
50	175	9.018	8.897	-0.121
50	176	8.813	8.657	-0.156
55	177	8.839	8.700	-0.139
55	178	8.910	8.647	-0.263
55	179	8.840	8.700	-0.140
55	180	8.800	8.599	-0.201
55	181	8.824	8.661	-0.163
100	154	8.858	8.521	-0.337
100	155	8.815	8.501	-0.314
100	156	8.892	8.599	-0.293
100	157	8.819	8.632	-0.187
100	158	8.909	8.689	-0.220
100	159	8.821	8.563	-0.258
100	160	8.978	8.747	-0.231
100	161	8.912	8.664	-0.248
100	162	8.820	8.646	-0.174
100	163	8.832	8.627	-0.205
100	164	8.881	8.622	-0.259
100	165	8.860	8.576	-0.284
100	166	8.755	8.577	-0.178
100	168	8.833	8.573	-0.260
100	169	8.984	8.805	-0.179
100	182	8.954	8.790	-0.164
100	183	8.879	8.697	-0.182
100	184	8.817	8.564	-0.253
100	185	8.696	8.452	-0.244
100	186	8.911	8.662	-0.249
100	187	8.951	8.769	-0.182
100	188	8.792	8.647	-0.145
100	189	8.996	8.783	-0.213
100	190	8.823	8.605	-0.218
105	191	8.665	8.459	-0.206
105	192	8.786	8.581	-0.205
105	193	8.906	8.679	-0.227
105	194	8.847	8.543	-0.304
105	195	8.777	8.533	-0.244
105	196	8.764	8.532	-0.232
Max		9.527	9.364	0.400
Average		9.026	8.872	-0.155
Min		8.665	8.452	-0.337
Std Dev		0.247	0.282	0.151

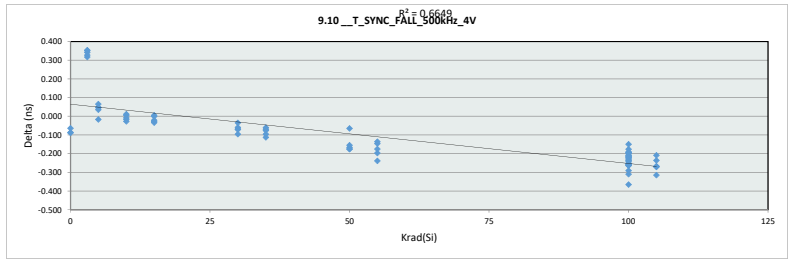


		9.6_T_SYNC_FALL										
Test Site	Tester											
Test Number	Unit	17	ns									
Max Limit	Min Limit	17	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.768	9.122	9.124	9.121	9.065	9.115	9.121	8.568	8.599	8.452	8.459
Average		8.839	9.216	9.216	9.195	9.134	9.168	9.198	8.707	8.661	8.638	8.555
Max		8.875	9.364	9.295	9.262	9.188	9.241	9.273	8.897	8.700	8.805	8.679
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

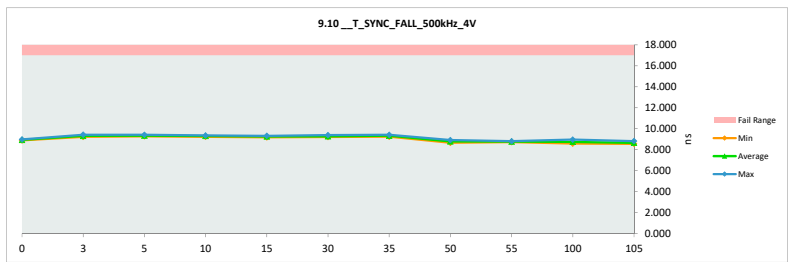


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		9.10 _T_SYNC_FALL_500kHz_4V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	17	17	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	9.018	8.931	-0.087
0	138	8.948	8.883	-0.065
0	139	9.067	8.977	-0.090
3	197	9.046	9.396	0.350
3	198	8.946	9.262	0.316
3	199	8.890	9.216	0.326
3	202	9.082	9.423	0.341
3	203	8.888	9.241	0.353
5	204	9.284	9.331	0.047
5	205	9.289	9.271	-0.018
5	206	9.330	9.364	0.034
5	208	9.350	9.414	0.064
5	209	9.234	9.279	0.045
10	210	9.218	9.222	0.004
10	211	9.308	9.318	0.010
10	212	9.376	9.348	-0.028
10	213	9.364	9.355	-0.009
10	214	9.286	9.269	-0.017
15	215	9.258	9.262	0.004
15	217	9.170	9.168	-0.002
15	219	9.253	9.231	-0.022
15	220	9.355	9.320	-0.035
15	221	9.322	9.293	-0.029
30	222	9.267	9.198	-0.069
30	223	9.420	9.384	-0.036
30	224	9.309	9.212	-0.097
30	225	9.329	9.268	-0.061
30	226	9.353	9.281	-0.072
35	227	9.313	9.236	-0.077
35	228	9.463	9.366	-0.097
35	229	9.377	9.315	-0.062
35	230	9.486	9.416	-0.070
35	231	9.417	9.303	-0.114
50	170	8.830	8.654	-0.176
50	172	8.863	8.695	-0.168
50	173	9.029	8.853	-0.176
50	175	9.070	8.914	-0.156
50	176	8.875	8.809	-0.066
55	177	8.944	8.797	-0.147
55	178	8.984	8.786	-0.198
55	179	8.944	8.807	-0.137
55	180	8.890	8.714	-0.176
55	181	8.859	8.719	-0.140
100	154	8.986	8.620	-0.366
100	155	8.892	8.600	-0.292
100	156	8.981	8.670	-0.311
100	157	8.964	8.723	-0.241
100	158	9.017	8.755	-0.262
100	159	8.862	8.653	-0.209
100	160	9.100	8.844	-0.256
100	161	8.993	8.774	-0.219
100	162	8.979	8.802	-0.177
100	163	8.965	8.704	-0.261
100	164	8.948	8.710	-0.238
100	165	8.905	8.671	-0.234
100	166	8.911	8.659	-0.252
100	168	8.889	8.669	-0.220
100	169	9.076	8.884	-0.192
100	182	9.105	8.954	-0.151
100	183	9.001	8.775	-0.226
100	184	8.874	8.677	-0.197
100	185	8.793	8.580	-0.213
100	186	9.039	8.774	-0.265
100	187	9.074	8.813	-0.261
100	188	8.936	8.742	-0.194
100	189	9.074	8.860	-0.214
100	190	8.942	8.741	-0.201
105	191	8.824	8.554	-0.270
105	192	8.941	8.673	-0.268
105	193	9.024	8.814	-0.210
105	194	8.924	8.608	-0.316
105	195	8.894	8.622	-0.272
105	196	8.860	8.623	-0.237
Max		9.486	9.423	0.353
Average		9.085	8.973	-0.111
Min		8.793	8.554	-0.366
Std Dev		0.191	0.289	0.160



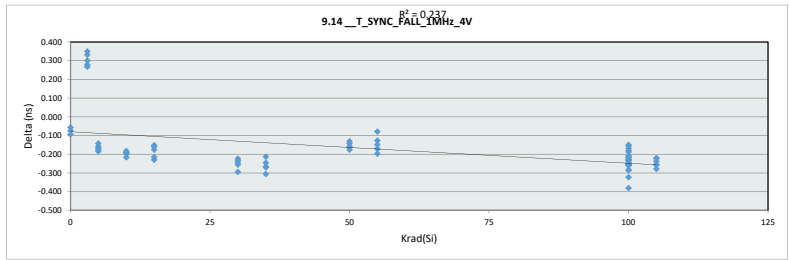
		9.10 _T_SYNC_FALL_500kHz_4V										
Test Site	Tester											
Test Number	Unit	17	ns									
Max Limit	Min Limit	17	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.883	9.216	9.271	9.222	9.168	9.198	9.236	8.654	8.714	8.580	8.554
Average		8.930	9.308	9.332	9.302	9.255	9.269	9.327	8.785	8.765	8.736	8.649
Max		8.977	9.423	9.414	9.355	9.320	9.384	9.416	8.914	8.807	8.954	8.814
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



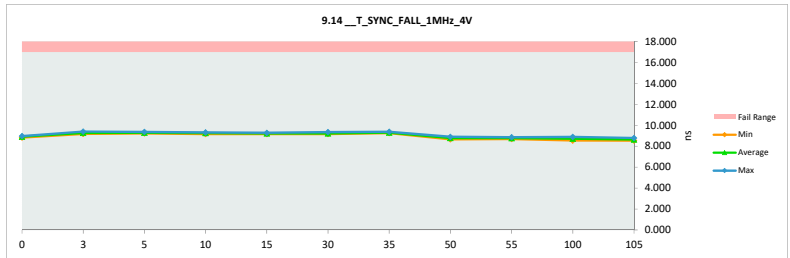


HDR TID Report  
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9.14_T_SYNC_FALL_1MHz_4V				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.990	8.933	-0.057
0	138	8.923	8.829	-0.094
0	139	9.034	8.960	-0.074
3	197	9.048	9.397	0.349
3	198	8.925	9.192	0.267
3	199	8.893	9.194	0.301
3	202	9.037	9.368	0.331
3	203	8.894	9.172	0.278
5	204	9.433	9.249	-0.184
5	205	9.439	9.271	-0.168
5	206	9.470	9.293	-0.177
5	208	9.500	9.358	-0.142
5	209	9.374	9.215	-0.159
10	210	9.363	9.164	-0.199
10	211	9.441	9.258	-0.183
10	212	9.506	9.289	-0.217
10	213	9.504	9.311	-0.193
10	214	9.418	9.226	-0.192
15	215	9.401	9.241	-0.160
15	217	9.310	9.158	-0.152
15	219	9.387	9.211	-0.176
15	220	9.505	9.275	-0.230
15	221	9.466	9.251	-0.215
30	222	9.400	9.168	-0.232
30	223	9.566	9.342	-0.224
30	224	9.453	9.158	-0.295
30	225	9.463	9.219	-0.244
30	226	9.501	9.246	-0.255
35	227	9.459	9.246	-0.213
35	228	9.607	9.301	-0.306
35	229	9.533	9.287	-0.246
35	230	9.639	9.372	-0.267
35	231	9.536	9.266	-0.270
50	170	8.813	8.653	-0.160
50	172	8.875	8.713	-0.162
50	173	9.043	8.867	-0.176
50	175	9.023	8.892	-0.131
50	176	8.886	8.743	-0.143
55	177	8.938	8.859	-0.079
55	178	8.933	8.806	-0.127
55	179	8.946	8.798	-0.148
55	180	8.870	8.699	-0.171
55	181	8.885	8.688	-0.197
100	154	8.983	8.602	-0.381
100	155	8.878	8.555	-0.323
100	156	8.951	8.695	-0.256
100	157	8.939	8.678	-0.261
100	158	9.021	8.737	-0.284
100	159	8.914	8.670	-0.244
100	160	9.056	8.830	-0.226
100	161	9.013	8.782	-0.231
100	162	8.959	8.743	-0.216
100	163	8.924	8.716	-0.208
100	164	8.991	8.706	-0.285
100	165	8.909	8.658	-0.251
100	166	8.871	8.640	-0.231
100	168	8.925	8.672	-0.253
100	169	9.106	8.851	-0.255
100	182	9.035	8.885	-0.150
100	183	9.013	8.796	-0.217
100	184	8.826	8.670	-0.156
100	185	8.772	8.541	-0.231
100	186	8.967	8.711	-0.256
100	187	9.014	8.834	-0.180
100	188	8.906	8.718	-0.188
100	189	9.066	8.850	-0.216
100	190	8.927	8.758	-0.169
105	191	8.788	8.532	-0.256
105	192	8.917	8.679	-0.238
105	193	9.008	8.785	-0.223
105	194	8.892	8.614	-0.278
105	195	8.864	8.607	-0.257
105	196	8.850	8.630	-0.220
Max		9.639	9.397	0.349
Average		9.122	8.949	-0.173
Min		8.772	8.532	-0.381
Std Dev		0.262	0.274	0.143



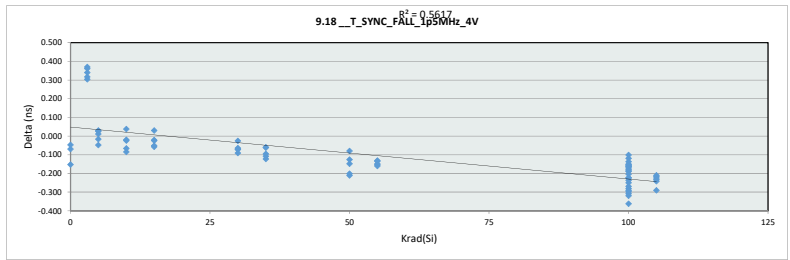
9.14_T_SYNC_FALL_1MHz_4V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.829	9.172	9.215	9.164	9.158	9.158	9.246	8.653	8.688	8.541	8.532
Average	8.907	9.265	9.277	9.250	9.227	9.227	9.294	8.774	8.770	8.721	8.641
Max	8.960	9.397	9.358	9.311	9.275	9.342	9.372	8.892	8.859	8.885	8.785
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



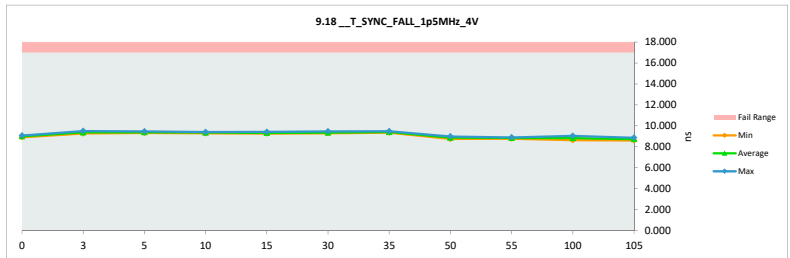
# HDR TID Report TPS7H5003-SP

9.18_T_SYNC_FALL_1p5MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	9.088	9.018	-0.070
0	138	9.063	8.910	-0.153
0	139	9.124	9.076	-0.048
3	197	9.140	9.509	0.369
3	198	9.008	9.324	0.316
3	199	8.932	9.292	0.360
3	202	9.151	9.490	0.339
3	203	8.966	9.269	0.303
5	204	9.370	9.353	-0.017
5	205	9.363	9.314	-0.049
5	206	9.410	9.420	0.010
5	208	9.444	9.469	0.025
5	209	9.326	9.355	0.029
10	210	9.301	9.338	0.037
10	211	9.402	9.377	-0.025
10	212	9.442	9.375	-0.067
10	213	9.430	9.409	-0.021
10	214	9.375	9.289	-0.086
15	215	9.351	9.329	-0.022
15	217	9.235	9.264	0.029
15	219	9.326	9.274	-0.052
15	220	9.446	9.421	-0.025
15	221	9.393	9.335	-0.058
30	222	9.360	9.290	-0.070
30	223	9.499	9.472	-0.027
30	224	9.404	9.331	-0.073
30	225	9.409	9.344	-0.065
30	226	9.416	9.324	-0.092
35	227	9.422	9.360	-0.062
35	228	9.547	9.485	-0.062
35	229	9.463	9.354	-0.109
35	230	9.579	9.483	-0.096
35	231	9.502	9.379	-0.123
50	170	8.965	8.753	-0.212
50	172	8.974	8.772	-0.202
50	173	9.135	8.986	-0.149
50	175	9.104	8.977	-0.127
50	176	8.970	8.889	-0.081
55	177	9.006	8.871	-0.135
55	178	9.050	8.895	-0.155
55	179	8.996	8.835	-0.161
55	180	8.923	8.773	-0.150
55	181	8.981	8.849	-0.132
100	154	9.075	8.712	-0.363
100	155	8.942	8.633	-0.309
100	156	9.070	8.788	-0.282
100	157	9.036	8.812	-0.224
100	158	9.136	8.899	-0.237
100	159	8.925	8.762	-0.163
100	160	9.138	8.959	-0.179
100	161	9.078	8.888	-0.190
100	162	9.024	8.868	-0.156
100	163	9.059	8.760	-0.299
100	164	9.117	8.797	-0.320
100	165	8.963	8.758	-0.205
100	166	9.045	8.754	-0.291
100	168	8.994	8.743	-0.251
100	169	9.168	9.003	-0.165
100	182	9.174	9.036	-0.138
100	183	9.107	8.882	-0.225
100	184	8.926	8.770	-0.156
100	185	8.906	8.636	-0.270
100	186	9.036	8.865	-0.171
100	187	9.126	8.939	-0.187
100	188	8.912	8.810	-0.102
100	189	9.106	8.986	-0.120
100	190	9.023	8.872	-0.151
105	191	8.837	8.612	-0.225
105	192	9.034	8.743	-0.291
105	193	9.107	8.865	-0.242
105	194	8.977	8.759	-0.218
105	195	8.958	8.724	-0.234
105	196	8.884	8.673	-0.211
Max		9.579	9.509	0.369
Average		9.160	9.054	-0.106
Min		8.837	8.612	-0.363
Std Dev		0.198	0.281	0.153

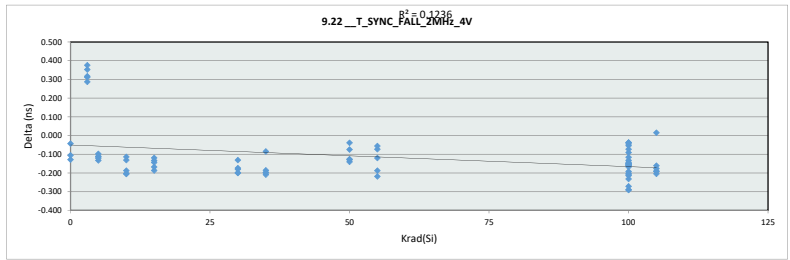


9.18_T_SYNC_FALL_1p5MHz_4V		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.910	9.269	9.314	9.289	9.264	9.290	9.354	8.753	8.773	8.633	8.612
Average		9.001	9.377	9.382	9.358	9.325	9.352	9.412	8.875	8.845	8.831	8.729
Max		9.076	9.509	9.469	9.409	9.421	9.472	9.485	8.986	8.895	9.036	8.865
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

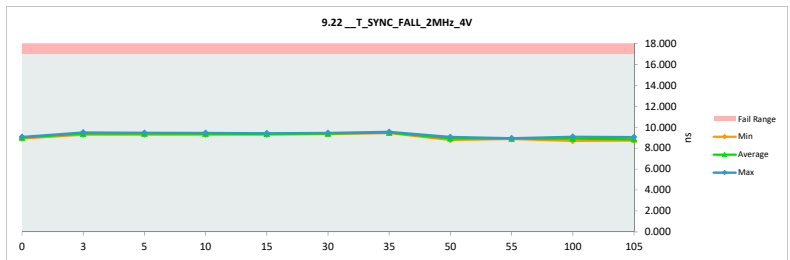


HDR TID Report  
TPS7H5003-SP

		9.22 _T_SYNC_FALL_2MHz_4V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	17	17	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	9.121	9.078	-0.043
0	138	9.059	8.930	-0.129
0	139	9.170	9.064	-0.106
3	197	9.127	9.503	0.376
3	198	9.072	9.359	0.287
3	199	8.943	9.296	0.353
3	202	9.195	9.512	0.317
3	203	9.000	9.312	0.312
5	204	9.528	9.417	-0.111
5	205	9.531	9.398	-0.133
5	206	9.575	9.454	-0.121
5	208	9.569	9.471	-0.098
5	209	9.410	9.296	-0.114
10	210	9.422	9.291	-0.131
10	211	9.562	9.468	-0.114
10	212	9.619	9.413	-0.206
10	213	9.617	9.429	-0.188
10	214	9.548	9.345	-0.203
15	215	9.520	9.386	-0.134
15	217	9.419	9.300	-0.119
15	219	9.487	9.343	-0.144
15	220	9.574	9.387	-0.187
15	221	9.603	9.435	-0.168
30	222	9.513	9.382	-0.131
30	223	9.650	9.470	-0.180
30	224	9.549	9.350	-0.199
30	225	9.578	9.377	-0.201
30	226	9.600	9.426	-0.174
35	227	9.531	9.446	-0.085
35	228	9.693	9.507	-0.186
35	229	9.654	9.456	-0.198
35	230	9.760	9.562	-0.198
35	231	9.679	9.470	-0.209
50	170	8.934	8.793	-0.141
50	172	9.025	8.895	-0.130
50	173	9.176	9.050	-0.126
50	175	9.149	9.074	-0.075
50	176	8.945	8.906	-0.039
55	177	9.028	8.955	-0.073
55	178	9.141	8.922	-0.219
55	179	9.126	8.938	-0.188
55	180	8.933	8.877	-0.056
55	181	9.032	8.912	-0.120
100	154	9.071	8.780	-0.291
100	155	9.015	8.725	-0.290
100	156	9.109	8.895	-0.214
100	157	9.071	8.913	-0.158
100	158	9.071	8.920	-0.151
100	159	9.019	8.851	-0.168
100	160	9.245	9.053	-0.192
100	161	9.104	8.944	-0.160
100	162	9.120	8.918	-0.202
100	163	9.051	8.918	-0.133
100	164	9.171	8.899	-0.272
100	165	8.950	8.834	-0.116
100	166	8.958	8.809	-0.149
100	168	9.060	8.828	-0.232
100	169	9.185	9.042	-0.143
100	182	9.102	9.048	-0.054
100	183	9.087	9.042	-0.045
100	184	8.998	8.845	-0.153
100	185	8.903	8.693	-0.210
100	186	9.120	8.962	-0.158
100	187	9.161	9.070	-0.091
100	188	8.986	8.913	-0.073
100	189	9.133	9.096	-0.037
100	190	8.960	8.922	-0.038
105	191	8.922	8.731	-0.191
105	192	9.013	8.852	-0.161
105	193	9.052	9.067	0.015
105	194	9.018	8.841	-0.177
105	195	8.988	8.798	-0.190
105	196	9.008	8.803	-0.205
Max		9.760	9.562	0.376
Average		9.233	9.119	-0.115
Min		8.903	8.693	-0.291
Std Dev		0.257	0.261	0.135



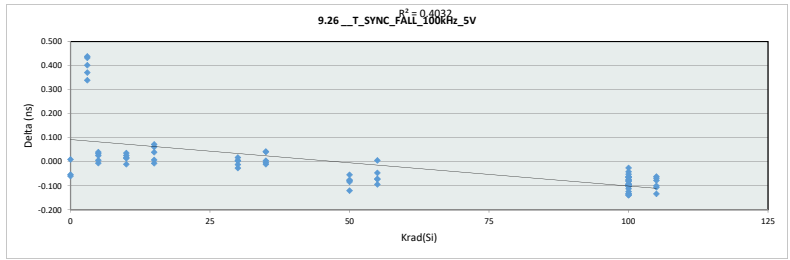
		9.22 _T_SYNC_FALL_2MHz_4V										
Test Site	Tester											
Test Number	Unit	17	ns									
Max Limit	Min Limit	17	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.930	9.296	9.296	9.291	9.300	9.350	9.446	8.793	8.877	8.693	8.731
Average		9.024	9.396	9.407	9.389	9.370	9.401	9.488	8.944	8.921	8.913	8.849
Max		9.078	9.512	9.471	9.468	9.435	9.470	9.562	9.074	8.955	9.096	9.067
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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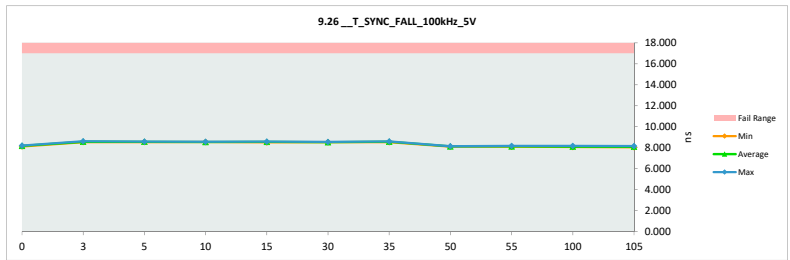
9.26 __T_SYNC_FALL_100kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.185	8.194	0.009
0	138	8.168	8.108	-0.060
0	139	8.225	8.171	-0.054
3	197	8.177	8.609	0.432
3	198	8.135	8.536	0.401
3	199	8.206	8.577	0.371
3	202	8.187	8.526	0.339
3	203	8.104	8.542	0.438
5	204	8.544	8.550	0.006
5	205	8.526	8.520	-0.006
5	206	8.539	8.564	0.025
5	208	8.535	8.575	0.040
5	209	8.510	8.543	0.033
10	210	8.489	8.525	0.036
10	211	8.519	8.536	0.017
10	212	8.535	8.524	-0.011
10	213	8.534	8.560	0.026
10	214	8.512	8.526	0.014
15	215	8.503	8.496	-0.007
15	217	8.474	8.513	0.039
15	219	8.500	8.572	0.072
15	220	8.537	8.544	0.007
15	221	8.514	8.575	0.061
30	222	8.506	8.509	0.003
30	223	8.543	8.548	0.005
30	224	8.521	8.509	-0.012
30	225	8.536	8.509	-0.027
30	226	8.501	8.518	0.017
35	227	8.530	8.519	-0.011
35	228	8.554	8.595	0.041
35	229	8.511	8.553	0.042
35	230	8.562	8.558	-0.004
35	231	8.548	8.552	0.004
50	170	8.164	8.085	-0.079
50	172	8.217	8.096	-0.121
50	173	8.211	8.127	-0.084
50	175	8.185	8.130	-0.055
50	176	8.191	8.115	-0.076
55	177	8.168	8.121	-0.047
55	178	8.214	8.142	-0.072
55	179	8.160	8.165	0.005
55	180	8.153	8.090	-0.073
55	181	8.180	8.085	-0.095
100	154	8.219	8.079	-0.140
100	155	8.184	8.087	-0.097
100	156	8.208	8.069	-0.139
100	157	8.193	8.058	-0.135
100	158	8.190	8.110	-0.080
100	159	8.169	8.076	-0.093
100	160	8.244	8.162	-0.082
100	161	8.190	8.116	-0.074
100	162	8.156	8.130	-0.026
100	163	8.190	8.057	-0.133
100	164	8.188	8.111	-0.077
100	165	8.173	8.060	-0.113
100	166	8.143	8.081	-0.062
100	168	8.154	8.051	-0.103
100	169	8.204	8.102	-0.102
100	182	8.189	8.125	-0.064
100	183	8.189	8.064	-0.125
100	184	8.169	8.067	-0.102
100	185	8.129	8.063	-0.066
100	186	8.162	8.098	-0.064
100	187	8.191	8.149	-0.042
100	188	8.192	8.097	-0.095
100	189	8.196	8.144	-0.052
100	190	8.177	8.083	-0.094
105	191	8.144	8.037	-0.107
105	192	8.151	8.082	-0.069
105	193	8.226	8.148	-0.078
105	194	8.187	8.086	-0.101
105	195	8.163	8.101	-0.062
105	196	8.193	8.059	-0.134
Max		8.562	8.609	0.438
Average		8.298	8.283	-0.015
Min		8.104	8.037	-0.140
Std Dev		0.165	0.221	0.125



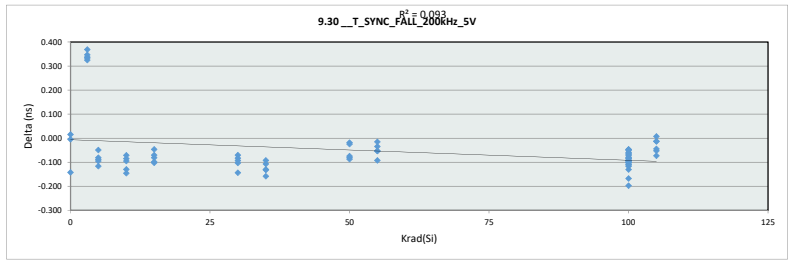
9.26 __T_SYNC_FALL_100kHz_5V	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.108	8.526	8.520	8.524	8.496	8.509	8.519	8.085	8.080	8.051	8.037
Average	8.158	8.558	8.550	8.534	8.540	8.519	8.555	8.111	8.119	8.093	8.086
Max	8.194	8.609	8.575	8.560	8.575	8.548	8.595	8.130	8.165	8.162	8.148
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

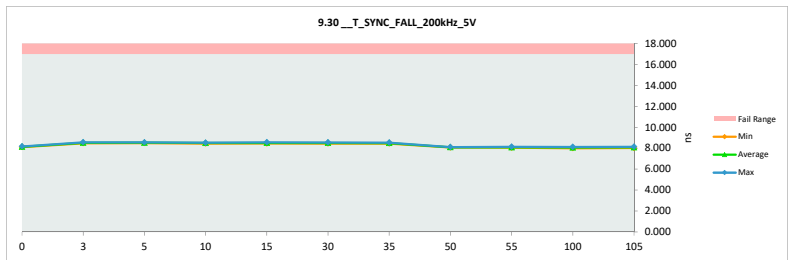


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9.30 _T_SYNC_FALL_200kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.130	8.125	-0.005
0	138	8.149	8.164	0.015
0	139	8.243	8.100	-0.143
3	197	8.203	8.540	0.337
3	198	8.142	8.475	0.333
3	199	8.146	8.470	0.324
3	202	8.190	8.558	0.368
3	203	8.179	8.525	0.346
5	204	8.604	8.554	-0.050
5	205	8.612	8.516	-0.096
5	206	8.623	8.534	-0.089
5	208	8.625	8.543	-0.082
5	209	8.601	8.484	-0.117
10	210	8.585	8.513	-0.072
10	211	8.592	8.496	-0.096
10	212	8.609	8.523	-0.086
10	213	8.621	8.491	-0.130
10	214	8.584	8.438	-0.146
15	215	8.586	8.483	-0.103
15	217	8.548	8.449	-0.099
15	219	8.578	8.507	-0.071
15	220	8.606	8.559	-0.047
15	221	8.591	8.509	-0.082
30	222	8.575	8.471	-0.104
30	223	8.619	8.548	-0.071
30	224	8.587	8.443	-0.144
30	225	8.598	8.514	-0.084
30	226	8.605	8.511	-0.094
35	227	8.598	8.439	-0.159
35	228	8.624	8.531	-0.093
35	229	8.596	8.488	-0.108
35	230	8.636	8.505	-0.131
35	231	8.613	8.481	-0.132
50	170	8.114	8.089	-0.025
50	172	8.139	8.064	-0.075
50	173	8.185	8.103	-0.082
50	175	8.175	8.087	-0.088
50	176	8.104	8.085	-0.019
55	177	8.136	8.101	-0.035
55	178	8.111	8.057	-0.054
55	179	8.146	8.130	-0.016
55	180	8.118	8.064	-0.054
55	181	8.145	8.052	-0.093
100	154	8.184	8.066	-0.118
100	155	8.127	7.996	-0.131
100	156	8.158	8.056	-0.102
100	157	8.155	8.044	-0.111
100	158	8.158	8.065	-0.093
100	159	8.114	8.028	-0.086
100	160	8.177	8.117	-0.060
100	161	8.145	8.097	-0.048
100	162	8.147	8.055	-0.092
100	163	8.138	8.055	-0.083
100	164	8.156	8.044	-0.112
100	165	8.148	8.067	-0.081
100	166	8.122	8.075	-0.047
100	168	8.149	8.080	-0.069
100	169	8.172	8.108	-0.064
100	182	8.192	8.089	-0.103
100	183	8.153	8.058	-0.095
100	184	8.143	8.092	-0.051
100	185	8.089	7.993	-0.096
100	186	8.146	8.043	-0.103
100	187	8.185	8.091	-0.094
100	188	8.156	8.082	-0.074
100	189	8.243	8.075	-0.168
100	190	8.233	8.035	-0.198
105	191	8.091	8.017	-0.074
105	192	8.110	8.096	-0.014
105	193	8.129	8.136	0.007
105	194	8.124	8.071	-0.053
105	195	8.125	8.080	-0.045
105	196	8.106	8.093	-0.013
Max		8.636	8.559	0.368
Average		8.305	8.251	-0.054
Min		8.089	7.993	-0.198
Std Dev		0.217	0.215	0.115

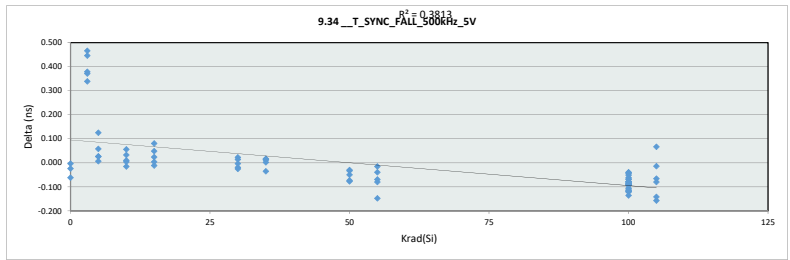


9.30 _T_SYNC_FALL_200kHz_5V											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.100	8.470	8.484	8.438	8.449	8.443	8.439	8.064	8.052	7.993	8.017
Average	8.130	8.514	8.526	8.492	8.501	8.497	8.489	8.086	8.081	8.063	8.082
Max	8.164	8.558	8.554	8.523	8.559	8.548	8.531	8.103	8.130	8.117	8.136
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

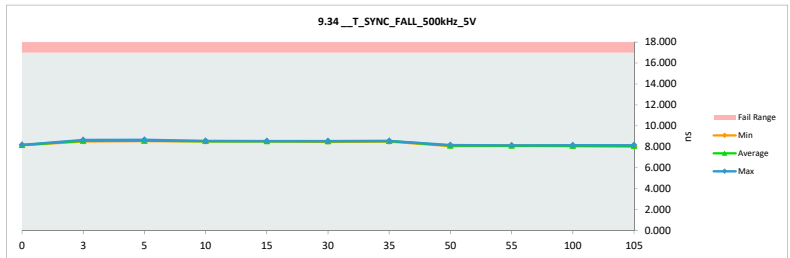


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9.34 _T_SYNC_FALL_500kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.204	8.179	-0.025
0	138	8.160	8.156	-0.004
0	139	8.228	8.165	-0.063
3	197	8.202	8.540	0.338
3	198	8.198	8.576	0.378
3	199	8.168	8.613	0.445
3	202	8.190	8.655	0.465
3	203	8.151	8.522	0.371
5	204	8.562	8.588	0.026
5	205	8.530	8.536	0.006
5	206	8.545	8.602	0.057
5	208	8.545	8.669	0.124
5	209	8.517	8.542	0.025
10	210	8.499	8.554	0.055
10	211	8.525	8.557	0.032
10	212	8.531	8.515	-0.016
10	213	8.553	8.562	0.009
10	214	8.518	8.521	0.003
15	215	8.500	8.548	0.048
15	217	8.459	8.539	0.080
15	219	8.522	8.545	0.023
15	220	8.539	8.542	0.003
15	221	8.527	8.515	-0.012
30	222	8.507	8.481	-0.026
30	223	8.566	8.546	-0.020
30	224	8.530	8.526	-0.004
30	225	8.534	8.556	0.022
30	226	8.517	8.530	0.013
35	227	8.531	8.545	0.014
35	228	8.550	8.561	0.011
35	229	8.526	8.542	0.016
35	230	8.563	8.564	0.001
35	231	8.542	8.506	-0.036
50	170	8.121	8.070	-0.051
50	172	8.173	8.139	-0.034
50	173	8.239	8.165	-0.074
50	175	8.168	8.137	-0.031
50	176	8.167	8.089	-0.078
55	177	8.159	8.143	-0.016
55	178	8.167	8.086	-0.081
55	179	8.160	8.120	-0.040
55	180	8.243	8.094	-0.149
55	181	8.196	8.126	-0.070
100	154	8.165	8.079	-0.086
100	155	8.160	8.069	-0.091
100	156	8.208	8.071	-0.137
100	157	8.197	8.118	-0.079
100	158	8.229	8.137	-0.092
100	159	8.175	8.065	-0.110
100	160	8.235	8.129	-0.106
100	161	8.229	8.111	-0.118
100	162	8.165	8.123	-0.042
100	163	8.158	8.090	-0.068
100	164	8.207	8.091	-0.116
100	165	8.193	8.071	-0.122
100	166	8.170	8.130	-0.040
100	168	8.174	8.081	-0.093
100	169	8.249	8.128	-0.121
100	182	8.243	8.159	-0.084
100	183	8.200	8.119	-0.081
100	184	8.167	8.104	-0.063
100	185	8.113	8.061	-0.052
100	186	8.160	8.090	-0.070
100	187	8.205	8.117	-0.088
100	188	8.163	8.084	-0.079
100	189	8.202	8.156	-0.046
100	190	8.173	8.079	-0.094
105	191	8.068	8.134	0.066
105	192	8.165	8.084	-0.081
105	193	8.175	8.160	-0.015
105	194	8.168	8.101	-0.067
105	195	8.165	8.007	-0.158
105	196	8.213	8.070	-0.143
Max		8.566	8.669	0.465
Average		8.302	8.292	-0.010
Min		8.068	8.007	-0.158
Std Dev		0.169	0.223	0.127



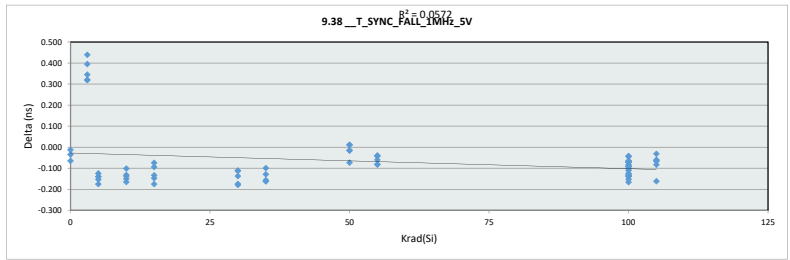
9.34 _T_SYNC_FALL_500kHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.156	8.522	8.536	8.515	8.515	8.481	8.506	8.070	8.086	8.061	8.007
Average	8.167	8.581	8.587	8.542	8.538	8.528	8.544	8.120	8.114	8.103	8.093
Max	8.179	8.655	8.669	8.562	8.548	8.556	8.564	8.165	8.143	8.159	8.160
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



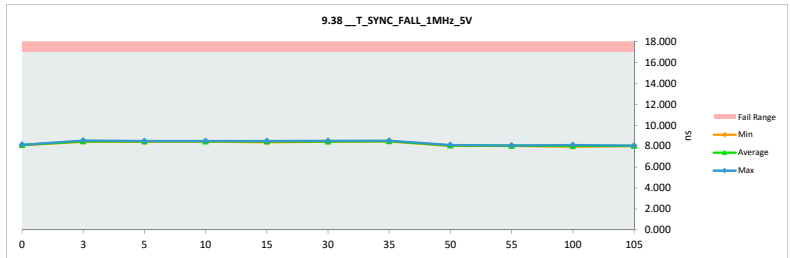
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9.38_T_SYNC_FALL_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.149	8.136	-0.013
0	138	8.121	8.086	-0.035
0	139	8.170	8.104	-0.066
3	197	8.102	8.540	0.438
3	198	8.123	8.443	0.320
3	199	8.102	8.420	0.318
3	202	8.146	8.490	0.344
3	203	8.041	8.435	0.394
5	204	8.614	8.489	-0.125
5	205	8.592	8.416	-0.176
5	206	8.617	8.476	-0.141
5	208	8.618	8.478	-0.140
5	209	8.585	8.431	-0.154
10	210	8.572	8.470	-0.102
10	211	8.598	8.465	-0.133
10	212	8.620	8.454	-0.166
10	213	8.630	8.492	-0.138
10	214	8.592	8.441	-0.151
15	215	8.589	8.494	-0.095
15	217	8.543	8.367	-0.176
15	219	8.577	8.443	-0.134
15	220	8.617	8.469	-0.148
15	221	8.570	8.495	-0.075
30	222	8.581	8.443	-0.138
30	223	8.622	8.510	-0.112
30	224	8.598	8.418	-0.180
30	225	8.601	8.426	-0.175
30	226	8.590	8.477	-0.113
35	227	8.596	8.466	-0.130
35	228	8.635	8.473	-0.162
35	229	8.612	8.454	-0.158
35	230	8.642	8.480	-0.162
35	231	8.622	8.522	-0.100
50	170	8.039	8.049	0.010
50	172	8.033	8.018	-0.015
50	173	8.150	8.076	-0.074
50	175	8.125	8.108	-0.017
50	176	8.092	8.102	0.010
55	177	8.146	8.063	-0.083
55	178	8.089	8.049	-0.040
55	179	8.128	8.067	-0.061
55	180	8.103	8.021	-0.082
55	181	8.091	8.047	-0.044
100	154	8.179	8.012	-0.167
100	155	8.104	7.951	-0.153
100	156	8.156	8.031	-0.125
100	157	8.119	8.027	-0.092
100	158	8.201	8.062	-0.139
100	159	8.125	7.987	-0.138
100	160	8.162	8.069	-0.093
100	161	8.124	8.031	-0.093
100	162	8.157	8.025	-0.132
100	163	8.179	8.040	-0.139
100	164	8.140	8.029	-0.111
100	165	8.105	8.015	-0.090
100	166	8.100	8.034	-0.066
100	168	8.109	8.041	-0.068
100	169	8.154	8.085	-0.069
100	182	8.144	8.041	-0.103
100	183	8.175	8.108	-0.067
100	184	8.093	8.047	-0.046
100	185	8.044	7.975	-0.069
100	186	8.118	8.047	-0.071
100	187	8.157	8.084	-0.073
100	188	8.131	8.047	-0.084
100	189	8.183	8.046	-0.137
100	190	8.087	8.045	-0.042
105	191	8.107	8.023	-0.084
105	192	8.066	8.034	-0.032
105	193	8.212	8.050	-0.162
105	194	8.106	8.044	-0.062
105	195	8.074	8.012	-0.062
105	196	8.084	8.017	-0.067
Max		8.642	8.540	0.438
Average		8.286	8.217	-0.069
Min		8.033	7.951	-0.180
Std Dev		0.232	0.209	0.127



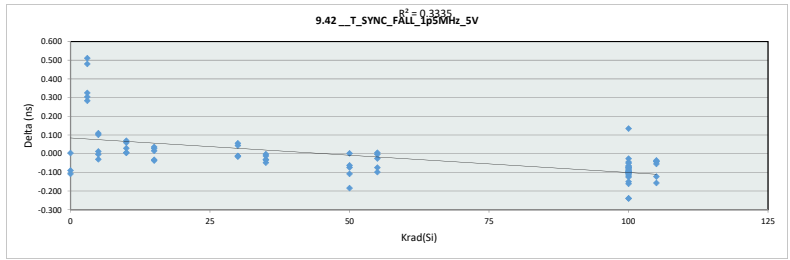
9.38_T_SYNC_FALL_1MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.086	8.420	8.416	8.441	8.367	8.418	8.454	8.018	8.021	7.951	8.012
Average	8.109	8.466	8.458	8.464	8.454	8.455	8.479	8.071	8.049	8.037	8.030
Max	8.136	8.540	8.489	8.492	8.495	8.510	8.522	8.108	8.067	8.108	8.050
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



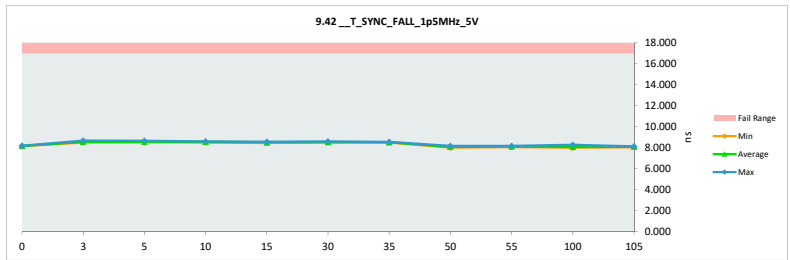
HDR TID Report  
TPS7H5003-SP

9.42_T_SYNC_FALL_1p5MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.250	8.160	-0.090
0	138	8.167	8.170	0.003
0	139	8.259	8.151	-0.108
3	197	8.211	8.494	0.283
3	198	8.160	8.671	0.511
3	199	8.171	8.496	0.325
3	202	8.240	8.544	0.304
3	203	8.096	8.576	0.480
5	204	8.549	8.519	-0.030
5	205	8.522	8.518	-0.004
5	206	8.486	8.587	0.101
5	208	8.536	8.645	0.109
5	209	8.513	8.525	0.012
10	210	8.478	8.482	0.004
10	211	8.525	8.584	0.059
10	212	8.524	8.593	0.069
10	213	8.551	8.557	0.006
10	214	8.510	8.539	0.029
15	215	8.490	8.453	-0.037
15	217	8.467	8.503	0.036
15	219	8.503	8.470	-0.033
15	220	8.535	8.550	0.015
15	221	8.514	8.542	0.028
30	222	8.495	8.550	0.055
30	223	8.546	8.589	0.043
30	224	8.524	8.512	-0.012
30	225	8.518	8.505	-0.013
30	226	8.511	8.495	-0.016
35	227	8.517	8.505	-0.012
35	228	8.555	8.522	-0.033
35	229	8.525	8.493	-0.032
35	230	8.568	8.520	-0.048
35	231	8.546	8.543	-0.003
50	170	8.067	8.004	-0.063
50	172	8.131	8.057	-0.074
50	173	8.248	8.064	-0.184
50	175	8.161	8.162	0.001
50	176	8.169	8.061	-0.108
55	177	8.257	8.158	-0.099
55	178	8.131	8.056	-0.075
55	179	8.159	8.164	0.005
55	180	8.129	8.103	-0.026
55	181	8.098	8.094	-0.004
100	154	8.180	8.030	-0.150
100	155	8.135	8.016	-0.119
100	156	8.138	8.087	-0.051
100	157	8.144	8.040	-0.104
100	158	8.150	8.123	-0.027
100	159	8.157	8.049	-0.108
100	160	8.287	8.126	-0.161
100	161	8.212	8.119	-0.093
100	162	8.127	8.051	-0.076
100	163	8.236	7.996	-0.240
100	164	8.163	8.079	-0.084
100	165	8.114	8.014	-0.100
100	166	8.153	8.088	-0.065
100	168	8.142	8.044	-0.098
100	169	8.198	8.073	-0.125
100	182	8.202	8.106	-0.096
100	183	8.127	8.261	0.134
100	184	8.129	8.054	-0.075
100	185	8.257	8.018	-0.239
100	186	8.165	8.088	-0.077
100	187	8.177	8.085	-0.092
100	188	8.111	8.066	-0.045
100	189	8.173	8.105	-0.068
100	190	8.175	8.066	-0.109
105	191	8.098	8.057	-0.041
105	192	8.167	8.044	-0.123
105	193	8.162	8.119	-0.043
105	194	8.238	8.081	-0.157
105	195	8.108	8.072	-0.036
105	196	8.154	8.099	-0.055
Max		8.568	8.671	0.511
Average		8.289	8.270	-0.018
Min		8.067	7.996	-0.240
Std Dev		0.174	0.229	0.132



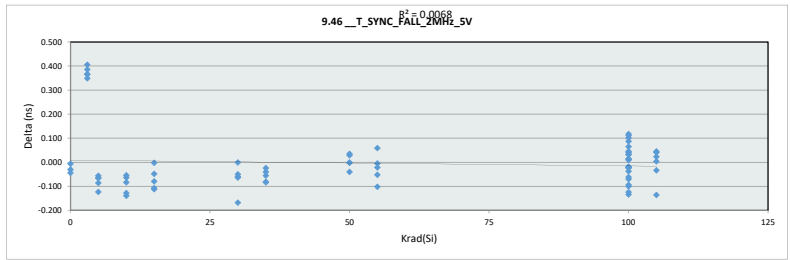
9.42_T_SYNC_FALL_1p5MHz_5V		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.151	8.494	8.518	8.482	8.453	8.495	8.493	8.004	8.056	7.996	8.044
Average		8.160	8.556	8.559	8.551	8.504	8.530	8.517	8.070	8.115	8.074	8.079
Max		8.170	8.671	8.645	8.593	8.550	8.589	8.543	8.162	8.164	8.261	8.119
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



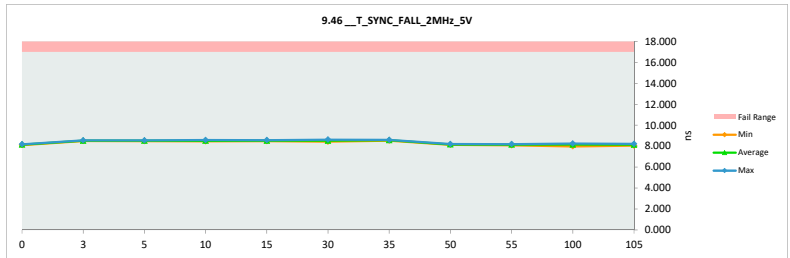


# HDR TID Report TPS7H5003-SP

		9.46_T_SYNC_FALL_2MHz_5V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	17	17	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.185	8.178	-0.007
0	138	8.148	8.103	-0.045
0	139	8.186	8.155	-0.031
3	197	8.158	8.543	0.385
3	198	8.137	8.503	0.366
3	199	8.146	8.551	0.405
3	202	8.188	8.536	0.348
3	203	8.128	8.492	0.364
5	204	8.605	8.481	-0.124
5	205	8.616	8.549	-0.067
5	206	8.606	8.549	-0.057
5	208	8.605	8.540	-0.065
5	209	8.558	8.471	-0.087
10	210	8.561	8.497	-0.064
10	211	8.620	8.536	-0.084
10	212	8.620	8.491	-0.129
10	213	8.602	8.462	-0.140
10	214	8.640	8.585	-0.055
15	215	8.592	8.512	-0.080
15	217	8.539	8.536	-0.003
15	219	8.572	8.523	-0.049
15	220	8.587	8.474	-0.113
15	221	8.682	8.574	-0.108
30	222	8.559	8.498	-0.061
30	223	8.620	8.618	-0.002
30	224	8.584	8.533	-0.051
30	225	8.595	8.426	-0.169
30	226	8.617	8.553	-0.064
35	227	8.590	8.534	-0.056
35	228	8.607	8.566	-0.041
35	229	8.644	8.562	-0.082
35	230	8.665	8.581	-0.084
35	231	8.626	8.601	-0.025
50	170	8.108	8.136	0.028
50	172	8.175	8.134	-0.041
50	173	8.171	8.169	-0.002
50	175	8.205	8.202	-0.003
50	176	8.132	8.166	0.034
55	177	8.135	8.193	0.058
55	178	8.247	8.144	-0.103
55	179	8.186	8.180	-0.006
55	180	8.128	8.075	-0.053
55	181	8.157	8.135	-0.022
100	154	8.167	8.129	-0.038
100	155	8.180	8.109	-0.071
100	156	8.100	8.186	0.086
100	157	8.173	8.152	-0.021
100	158	8.182	8.213	0.031
100	159	8.115	8.125	0.010
100	160	8.231	8.208	-0.023
100	161	8.231	8.207	-0.024
100	162	8.210	8.109	-0.101
100	163	8.139	8.153	0.014
100	164	8.216	8.154	-0.062
100	165	8.149	8.014	-0.135
100	166	8.127	8.108	-0.019
100	168	8.246	8.151	-0.095
100	169	8.193	8.232	0.039
100	182	8.102	8.134	0.032
100	183	8.155	8.200	0.045
100	184	8.110	7.985	-0.125
100	185	8.104	8.113	0.009
100	186	8.124	8.234	0.110
100	187	8.150	8.250	0.100
100	188	8.055	8.172	0.117
100	189	8.151	8.129	-0.022
100	190	8.114	8.178	0.064
105	191	8.124	8.090	-0.034
105	192	8.164	8.167	0.003
105	193	8.169	8.213	0.044
105	194	8.145	8.185	0.040
105	195	8.187	8.050	-0.137
105	196	8.113	8.135	0.022
Max		8.682	8.618	0.405
Average		8.310	8.306	-0.004
Min		8.055	7.985	-0.169
Std Dev		0.217	0.194	0.120



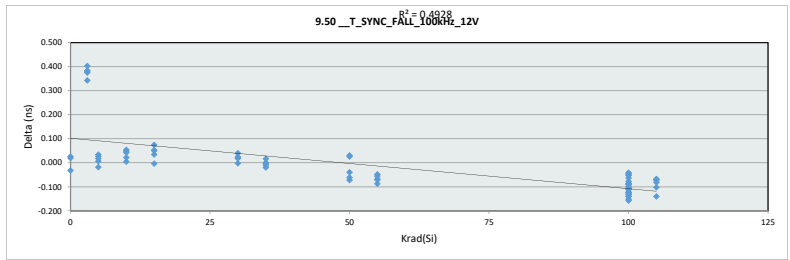
		9.46_T_SYNC_FALL_2MHz_5V										
Test Site	Tester											
Test Number	Unit	17	ns									
Max Limit	Min Limit	17	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.103	8.492	8.471	8.462	8.474	8.426	8.534	8.134	8.075	7.985	8.050
Average		8.145	8.525	8.518	8.514	8.524	8.526	8.569	8.161	8.145	8.152	8.140
Max		8.178	8.551	8.549	8.585	8.574	8.618	8.601	8.202	8.193	8.250	8.213
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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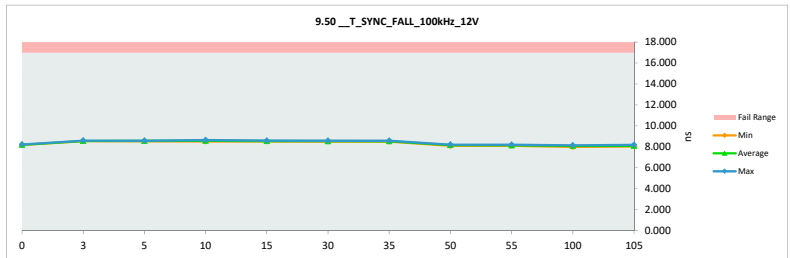
9.50_T_SYNC_FALL_100kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.154	8.173	0.019
0	138	8.236	8.203	-0.033
0	139	8.193	8.218	0.025
3	197	8.178	8.561	0.383
3	198	8.169	8.571	0.402
3	199	8.216	8.591	0.375
3	202	8.199	8.541	0.342
3	203	8.195	8.576	0.381
5	204	8.552	8.533	-0.019
5	205	8.574	8.580	0.006
5	206	8.541	8.558	0.017
5	208	8.533	8.559	0.026
5	209	8.524	8.557	0.033
10	210	8.534	8.582	0.048
10	211	8.534	8.576	0.042
10	212	8.598	8.641	0.053
10	213	8.571	8.575	0.004
10	214	8.499	8.520	0.021
15	215	8.468	8.518	0.050
15	217	8.475	8.549	0.074
15	219	8.538	8.572	0.034
15	220	8.546	8.542	-0.004
15	221	8.534	8.586	0.052
30	222	8.489	8.528	0.039
30	223	8.511	8.508	-0.003
30	224	8.514	8.533	0.019
30	225	8.521	8.545	0.024
30	226	8.566	8.584	0.018
35	227	8.526	8.526	0.000
35	228	8.519	8.508	-0.011
35	229	8.568	8.584	0.016
35	230	8.556	8.536	-0.020
35	231	8.528	8.524	-0.004
50	170	8.159	8.097	-0.062
50	172	8.238	8.198	-0.040
50	173	8.142	8.167	0.025
50	175	8.238	8.165	-0.073
50	176	8.182	8.212	0.030
55	177	8.188	8.120	-0.068
55	178	8.282	8.194	-0.088
55	179	8.196	8.147	-0.049
55	180	8.195	8.139	-0.056
55	181	8.180	8.108	-0.072
100	154	8.237	8.130	-0.107
100	155	8.210	8.070	-0.140
100	156	8.165	8.032	-0.133
100	157	8.199	8.041	-0.158
100	158	8.189	8.096	-0.093
100	159	8.181	8.130	-0.051
100	160	8.228	8.087	-0.141
100	161	8.150	8.086	-0.064
100	162	8.208	8.084	-0.124
100	163	8.185	8.132	-0.053
100	164	8.179	8.090	-0.089
100	165	8.148	8.060	-0.088
100	166	8.160	8.118	-0.042
100	168	8.244	8.112	-0.132
100	169	8.158	8.072	-0.086
100	182	8.210	8.083	-0.127
100	183	8.153	8.001	-0.152
100	184	8.175	8.052	-0.123
100	185	8.162	8.051	-0.111
100	186	8.149	8.039	-0.110
100	187	8.212	8.134	-0.078
100	188	8.241	8.140	-0.101
100	189	8.156	8.111	-0.045
100	190	8.197	8.086	-0.111
105	191	8.127	8.045	-0.082
105	192	8.222	8.081	-0.141
105	193	8.247	8.177	-0.070
105	194	8.162	8.088	-0.074
105	195	8.150	8.047	-0.103
105	196	8.156	8.088	-0.068
Max		8.588	8.641	0.402
Average		8.307	8.293	-0.014
Min		8.127	8.001	-0.158
Std Dev		0.167	0.226	0.123



9.50_T_SYNC_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

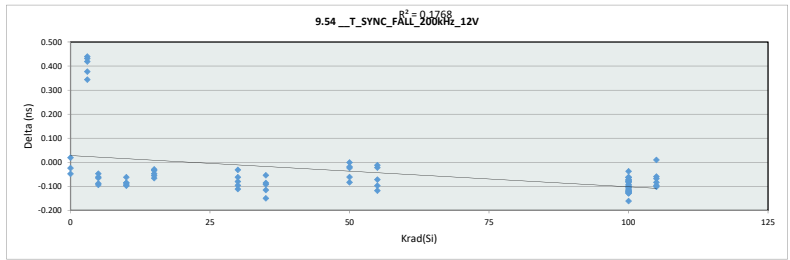
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.173	8.541	8.533	8.520	8.518	8.508	8.508	8.097	8.108	8.001	8.045
Average	8.198	8.568	8.557	8.579	8.553	8.540	8.536	8.168	8.142	8.085	8.088
Max	8.218	8.591	8.580	8.641	8.586	8.584	8.584	8.212	8.194	8.140	8.177
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



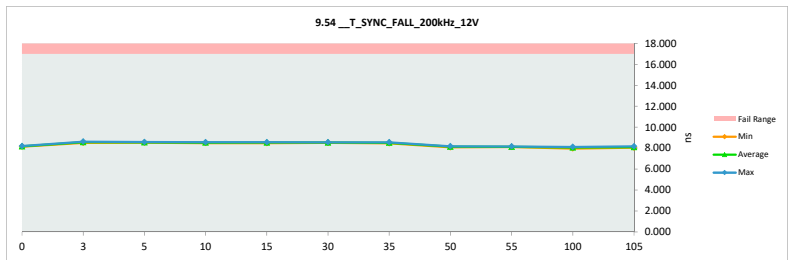
# HDR TID Report TPS7H5003-SP

9.54_T_SYNC_FALL_200kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.194	8.145	-0.049
0	138	8.187	8.162	-0.025
0	139	8.185	8.203	0.018
3	197	8.175	8.593	0.418
3	198	8.144	8.520	0.376
3	199	8.154	8.593	0.439
3	202	8.165	8.508	0.343
3	203	8.190	8.621	0.431
5	204	8.643	8.581	-0.062
5	205	8.658	8.563	-0.095
5	206	8.613	8.524	-0.089
5	208	8.608	8.560	-0.048
5	209	8.602	8.536	-0.066
10	210	8.613	8.550	-0.063
10	211	8.610	8.523	-0.087
10	212	8.661	8.563	-0.098
10	213	8.613	8.527	-0.086
10	214	8.576	8.481	-0.095
15	215	8.546	8.479	-0.067
15	217	8.542	8.512	-0.030
15	219	8.620	8.564	-0.056
15	220	8.612	8.564	-0.048
15	221	8.601	8.566	-0.035
30	222	8.581	8.549	-0.032
30	223	8.592	8.529	-0.063
30	224	8.620	8.508	-0.112
30	225	8.610	8.513	-0.097
30	226	8.640	8.559	-0.081
35	227	8.620	8.504	-0.116
35	228	8.606	8.455	-0.151
35	229	8.621	8.534	-0.087
35	230	8.613	8.558	-0.055
35	231	8.616	8.523	-0.093
50	170	8.095	8.075	-0.020
50	172	8.164	8.162	-0.002
50	173	8.176	8.092	-0.084
50	175	8.190	8.166	-0.024
50	176	8.239	8.177	-0.062
55	177	8.181	8.108	-0.073
55	178	8.251	8.153	-0.098
55	179	8.175	8.162	-0.013
55	180	8.231	8.112	-0.119
55	181	8.132	8.109	-0.023
100	154	8.207	8.087	-0.120
100	155	8.191	8.061	-0.130
100	156	8.167	8.068	-0.099
100	157	8.168	8.006	-0.162
100	158	8.173	8.094	-0.079
100	159	8.216	8.090	-0.126
100	160	8.186	8.102	-0.084
100	161	8.174	8.066	-0.108
100	162	8.158	8.095	-0.063
100	163	8.126	8.052	-0.074
100	164	8.137	8.023	-0.114
100	165	8.115	8.022	-0.093
100	166	8.150	8.070	-0.080
100	168	8.225	8.103	-0.122
100	169	8.124	8.051	-0.073
100	182	8.181	8.118	-0.063
100	183	8.083	7.965	-0.118
100	184	8.146	8.067	-0.079
100	185	8.168	8.044	-0.124
100	186	8.154	8.050	-0.104
100	187	8.212	8.083	-0.129
100	188	8.152	8.114	-0.038
100	189	8.137	8.034	-0.103
100	190	8.122	8.057	-0.065
105	191	8.105	8.114	0.009
105	192	8.149	8.052	-0.097
105	193	8.234	8.174	-0.060
105	194	8.175	8.106	-0.069
105	195	8.142	8.058	-0.084
105	196	8.154	8.051	-0.103
Max		8.661	8.621	0.439
Average		8.319	8.275	-0.044
Min		8.083	7.965	-0.162
Std Dev		0.214	0.226	0.127



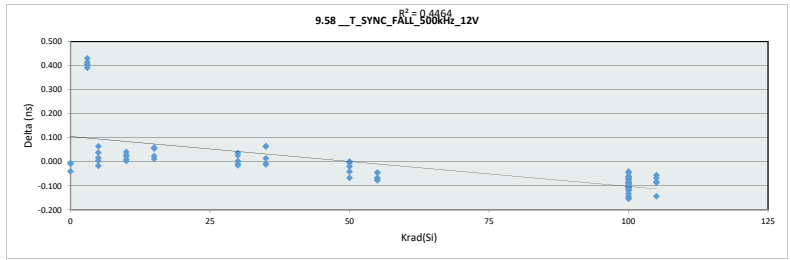
9.54_T_SYNC_FALL_200kHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.145	8.508	8.524	8.481	8.479	8.508	8.455	8.075	8.108	7.965	8.051
Average	8.170	8.567	8.553	8.529	8.537	8.532	8.515	8.134	8.129	8.063	8.093
Max	8.203	8.621	8.581	8.563	8.566	8.559	8.558	8.177	8.162	8.118	8.174
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



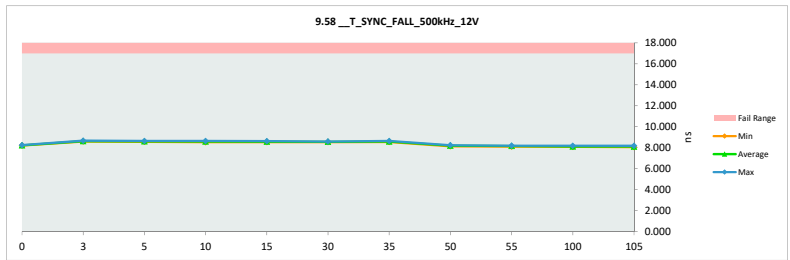
# HDR TID Report TPS7H5003-SP

		9.58_T_SYNC_FALL_500kHz_12V	
Test Site	Tester		
Test Number	Unit	ns	ns
Max Limit	Min Limit	17	17

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.224	8.218	-0.006
0	138	8.246	8.205	-0.041
0	139	8.250	8.240	-0.010
3	197	8.234	8.624	0.390
3	198	8.194	8.596	0.402
3	199	8.226	8.631	0.405
3	202	8.172	8.585	0.413
3	203	8.235	8.664	0.429
5	204	8.598	8.580	-0.018
5	205	8.601	8.638	0.037
5	206	8.553	8.556	0.003
5	208	8.564	8.579	0.015
5	209	8.563	8.626	0.063
10	210	8.555	8.577	0.022
10	211	8.559	8.568	0.009
10	212	8.599	8.639	0.040
10	213	8.578	8.605	0.027
10	214	8.516	8.518	0.002
15	215	8.475	8.529	0.054
15	217	8.507	8.565	0.058
15	219	8.566	8.623	0.057
15	220	8.569	8.592	0.023
15	221	8.546	8.557	0.011
30	222	8.542	8.526	-0.016
30	223	8.532	8.568	0.036
30	224	8.556	8.581	0.025
30	225	8.544	8.535	-0.009
30	226	8.563	8.565	0.002
35	227	8.560	8.573	0.013
35	228	8.551	8.542	-0.009
35	229	8.575	8.638	0.063
35	230	8.569	8.632	0.063
35	231	8.566	8.554	-0.012
50	170	8.147	8.126	-0.021
50	172	8.230	8.230	0.000
50	173	8.209	8.141	-0.068
50	175	8.213	8.207	-0.006
50	176	8.280	8.238	-0.042
55	177	8.210	8.137	-0.073
55	178	8.237	8.190	-0.047
55	179	8.227	8.181	-0.046
55	180	8.245	8.177	-0.068
55	181	8.173	8.094	-0.079
100	154	8.236	8.102	-0.134
100	155	8.240	8.095	-0.145
100	156	8.172	8.098	-0.074
100	157	8.190	8.102	-0.088
100	158	8.268	8.148	-0.120
100	159	8.237	8.129	-0.108
100	160	8.230	8.123	-0.107
100	161	8.203	8.115	-0.088
100	162	8.232	8.162	-0.070
100	163	8.224	8.072	-0.152
100	164	8.212	8.107	-0.105
100	165	8.152	8.110	-0.042
100	166	8.223	8.104	-0.119
100	168	8.227	8.179	-0.048
100	169	8.186	8.089	-0.097
100	182	8.226	8.145	-0.081
100	183	8.206	8.094	-0.112
100	184	8.193	8.102	-0.091
100	185	8.239	8.084	-0.155
100	186	8.151	8.089	-0.062
100	187	8.225	8.162	-0.063
100	188	8.208	8.143	-0.065
100	189	8.212	8.111	-0.101
100	190	8.176	8.086	-0.090
105	191	8.150	8.093	-0.057
105	192	8.189	8.120	-0.069
105	193	8.260	8.175	-0.085
105	194	8.216	8.129	-0.087
105	195	8.198	8.053	-0.145
105	196	8.152	8.064	-0.088
Max		8.601	8.664	0.429
Average		8.330	8.320	-0.010
Min		8.147	8.053	-0.155
Std Dev		0.167	0.228	0.128



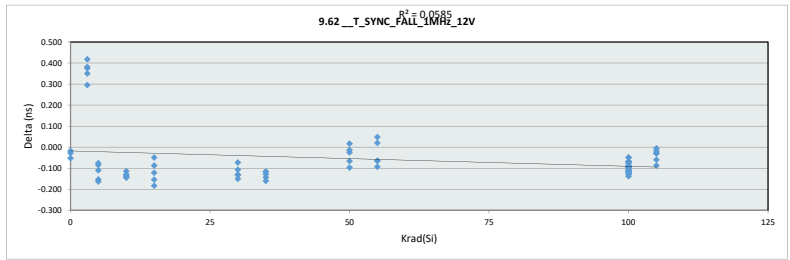
		9.58_T_SYNC_FALL_500kHz_12V										
Test Site	Tester											
Test Number	Unit	17	ns									
Max Limit	Min Limit	17	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		8.205	8.585	8.556	8.518	8.529	8.526	8.542	8.126	8.094	8.072	8.053
Average		8.221	8.620	8.596	8.581	8.573	8.555	8.588	8.188	8.156	8.115	8.106
Max		8.240	8.664	8.638	8.639	8.623	8.581	8.638	8.238	8.190	8.179	8.175
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



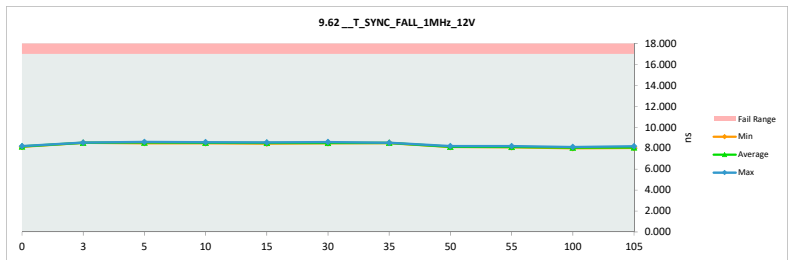
# HDR TID Report TPS7H5003-SP

9.62 _T_SYNC_FALL_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.169	8.117	-0.052
0	138	8.217	8.199	-0.018
0	139	8.215	8.187	-0.028
3	197	8.150	8.531	0.381
3	198	8.092	8.510	0.418
3	199	8.184	8.534	0.350
3	202	8.147	8.522	0.375
3	203	8.212	8.507	0.295
5	204	8.676	8.523	-0.153
5	205	8.678	8.593	-0.085
5	206	8.642	8.566	-0.076
5	208	8.639	8.529	-0.110
5	209	8.644	8.481	-0.163
10	210	8.630	8.500	-0.130
10	211	8.653	8.513	-0.140
10	212	8.687	8.573	-0.114
10	213	8.677	8.532	-0.145
10	214	8.604	8.471	-0.133
15	215	8.562	8.441	-0.121
15	217	8.569	8.520	-0.049
15	219	8.679	8.496	-0.183
15	220	8.660	8.506	-0.154
15	221	8.635	8.548	-0.087
30	222	8.619	8.487	-0.132
30	223	8.607	8.477	-0.130
30	224	8.634	8.484	-0.150
30	225	8.625	8.518	-0.107
30	226	8.661	8.588	-0.073
35	227	8.635	8.506	-0.129
35	228	8.628	8.512	-0.116
35	229	8.664	8.521	-0.143
35	230	8.657	8.497	-0.160
35	231	8.637	8.520	-0.117
50	170	8.142	8.117	-0.025
50	172	8.203	8.137	-0.066
50	173	8.147	8.134	-0.013
50	175	8.175	8.192	0.017
50	176	8.209	8.112	-0.097
55	177	8.163	8.099	-0.064
55	178	8.227	8.134	-0.093
55	179	8.067	8.087	0.020
55	180	8.152	8.200	0.048
55	181	8.161	8.097	-0.064
100	154	8.182	8.072	-0.110
100	155	8.174	8.050	-0.124
100	156	8.166	8.049	-0.117
100	157	8.140	8.050	-0.090
100	158	8.184	8.073	-0.111
100	159	8.169	8.082	-0.087
100	160	8.191	8.099	-0.092
100	161	8.173	8.044	-0.129
100	162	8.205	8.091	-0.114
100	163	8.161	8.064	-0.097
100	164	8.131	7.993	-0.138
100	165	8.105	8.009	-0.096
100	166	8.126	8.056	-0.070
100	168	8.193	8.117	-0.076
100	169	8.132	8.064	-0.068
100	182	8.149	8.058	-0.091
100	183	8.152	8.048	-0.104
100	184	8.162	8.054	-0.108
100	185	8.140	8.019	-0.121
100	186	8.087	8.020	-0.067
100	187	8.157	8.107	-0.050
100	188	8.189	8.093	-0.096
100	189	8.148	8.078	-0.070
100	190	8.121	8.073	-0.048
105	191	8.038	8.033	-0.005
105	192	8.149	8.089	-0.060
105	193	8.194	8.177	-0.017
105	194	8.151	8.064	-0.087
105	195	8.077	8.047	-0.030
105	196	8.053	8.025	-0.028
Max		8.687	8.593	0.418
Average		8.321	8.263	-0.058
Min		8.038	7.993	-0.183
Std Dev		0.235	0.218	0.124



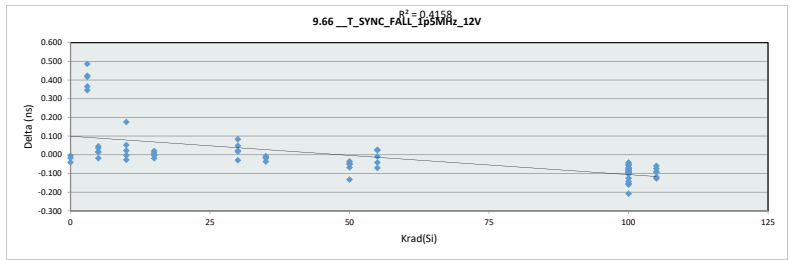
9.62 _T_SYNC_FALL_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
LL											
UL											
Min	8.117	8.507	8.481	8.471	8.441	8.477	8.497	8.112	8.087	7.993	8.025
Average	8.168	8.521	8.538	8.518	8.502	8.511	8.511	8.138	8.123	8.061	8.073
Max	8.199	8.534	8.593	8.573	8.548	8.588	8.521	8.192	8.200	8.117	8.177
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



# HDR TID Report TPS7H5003-SP

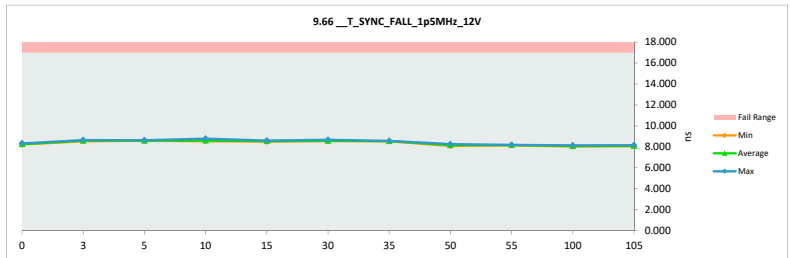
9.66 __T_SYNC_FALL_1p5MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.224	8.218	-0.006
0	138	8.242	8.224	-0.018
0	139	8.377	8.336	-0.041
3	197	8.165	8.650	0.485
3	198	8.109	8.531	0.422
3	199	8.279	8.624	0.345
3	202	8.189	8.553	0.364
3	203	8.185	8.601	0.416
5	204	8.590	8.634	0.044
5	205	8.604	8.585	-0.019
5	206	8.564	8.577	0.013
5	208	8.561	8.576	0.015
5	209	8.554	8.589	0.035
10	210	8.552	8.524	-0.028
10	211	8.572	8.593	0.021
10	212	8.606	8.780	0.174
10	213	8.575	8.571	-0.004
10	214	8.510	8.561	0.051
15	215	8.495	8.491	-0.004
15	217	8.509	8.489	-0.020
15	219	8.582	8.579	-0.003
15	220	8.569	8.577	0.008
15	221	8.578	8.597	0.019
30	222	8.521	8.569	0.048
30	223	8.534	8.550	0.016
30	224	8.565	8.535	-0.030
30	225	8.527	8.548	0.021
30	226	8.595	8.677	0.082
35	227	8.559	8.550	-0.009
35	228	8.541	8.526	-0.015
35	229	8.589	8.570	-0.019
35	230	8.584	8.547	-0.037
35	231	8.550	8.536	-0.014
50	170	8.154	8.085	-0.069
50	172	8.190	8.138	-0.052
50	173	8.201	8.155	-0.046
50	175	8.296	8.260	-0.036
50	176	8.280	8.146	-0.134
55	177	8.163	8.187	0.024
55	178	8.239	8.168	-0.071
55	179	8.178	8.168	-0.010
55	180	8.111	8.136	0.025
55	181	8.203	8.161	-0.042
100	154	8.233	8.072	-0.161
100	155	8.235	8.026	-0.209
100	156	8.195	8.093	-0.102
100	157	8.158	8.063	-0.095
100	158	8.191	8.100	-0.091
100	159	8.167	8.099	-0.068
100	160	8.230	8.074	-0.156
100	161	8.190	8.047	-0.143
100	162	8.190	8.114	-0.076
100	163	8.203	8.107	-0.096
100	164	8.199	8.045	-0.154
100	165	8.174	8.079	-0.095
100	166	8.167	8.125	-0.042
100	168	8.230	8.139	-0.091
100	169	8.188	8.085	-0.103
100	182	8.213	8.112	-0.101
100	183	8.142	8.091	-0.051
100	184	8.166	8.079	-0.087
100	185	8.098	8.046	-0.052
100	186	8.097	8.018	-0.079
100	187	8.189	8.133	-0.056
100	188	8.185	8.106	-0.079
100	189	8.156	8.078	-0.078
100	190	8.183	8.057	-0.126
105	191	8.136	8.062	-0.074
105	192	8.180	8.052	-0.128
105	193	8.256	8.162	-0.094
105	194	8.191	8.131	-0.060
105	195	8.150	8.059	-0.091
105	196	8.184	8.065	-0.119
Max		8.606	8.780	0.485
Average		8.318	8.304	-0.014
Min		8.097	8.018	-0.209
Std Dev		0.181	0.237	0.132



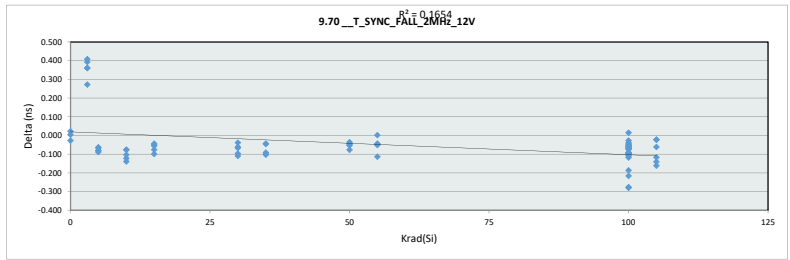
9.66 __T_SYNC_FALL_1p5MHz_12V	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.218	8.531	8.576	8.524	8.489	8.535	8.526	8.085	8.136	8.018	8.052
Average	8.259	8.592	8.592	8.606	8.547	8.576	8.546	8.157	8.164	8.083	8.089
Max	8.336	8.650	8.634	8.780	8.597	8.677	8.570	8.260	8.187	8.139	8.162
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

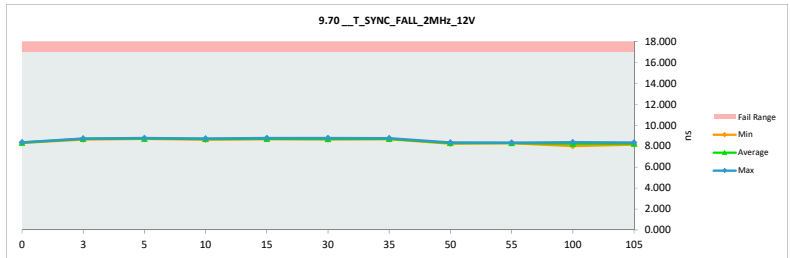


HDR TID Report  
TPS7H5003-SP

9.70 __T_SYNC_FALL_2MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.308	8.313	0.005
0	138	8.316	8.339	0.023
0	139	8.386	8.359	-0.027
3	197	8.328	8.689	0.361
3	198	8.311	8.719	0.408
3	199	8.339	8.732	0.393
3	202	8.375	8.647	0.272
3	203	8.374	8.734	0.360
5	204	8.797	8.715	-0.082
5	205	8.842	8.776	-0.066
5	206	8.783	8.704	-0.079
5	208	8.778	8.689	-0.089
5	209	8.778	8.715	-0.063
10	210	8.769	8.693	-0.076
10	211	8.796	8.719	-0.077
10	212	8.835	8.732	-0.103
10	213	8.806	8.684	-0.122
10	214	8.767	8.628	-0.139
15	215	8.714	8.666	-0.048
15	217	8.735	8.680	-0.055
15	219	8.777	8.678	-0.099
15	220	8.769	8.693	-0.076
15	221	8.814	8.771	-0.043
30	222	8.748	8.687	-0.061
30	223	8.728	8.662	-0.066
30	224	8.746	8.650	-0.096
30	225	8.761	8.652	-0.109
30	226	8.810	8.772	-0.038
35	227	8.772	8.672	-0.100
35	228	8.731	8.686	-0.045
35	229	8.804	8.761	-0.043
35	230	8.799	8.708	-0.091
35	231	8.782	8.680	-0.102
50	170	8.287	8.251	-0.036
50	172	8.386	8.309	-0.077
50	173	8.361	8.317	-0.044
50	175	8.404	8.352	-0.052
50	176	8.353	8.302	-0.051
55	177	8.377	8.334	-0.043
55	178	8.363	8.312	-0.051
55	179	8.410	8.297	-0.113
55	180	8.341	8.294	-0.047
55	181	8.302	8.304	0.002
100	154	8.365	8.299	-0.066
100	155	8.338	8.264	-0.074
100	156	8.371	8.276	-0.095
100	157	8.307	8.241	-0.066
100	158	8.342	8.297	-0.045
100	159	8.325	8.222	-0.103
100	160	8.368	8.309	-0.059
100	161	8.346	8.291	-0.055
100	162	8.454	8.345	-0.109
100	163	8.351	8.324	-0.027
100	164	8.301	8.022	-0.279
100	165	8.291	8.015	-0.276
100	166	8.334	8.236	-0.098
100	168	8.445	8.380	-0.065
100	169	8.307	8.268	-0.039
100	182	8.310	8.192	-0.118
100	183	8.282	8.234	-0.048
100	184	8.323	8.338	0.015
100	185	8.311	8.256	-0.055
100	186	8.317	8.225	-0.092
100	187	8.360	8.289	-0.071
100	188	8.347	8.161	-0.186
100	189	8.235	8.019	-0.216
100	190	8.361	8.271	-0.090
105	191	8.263	8.240	-0.023
105	192	8.380	8.263	-0.117
105	193	8.377	8.355	-0.022
105	194	8.373	8.312	-0.061
105	195	8.399	8.258	-0.141
105	196	8.307	8.146	-0.161
Max		8.842	8.776	0.408
Average		8.492	8.444	-0.048
Min		8.235	8.015	-0.279
Std Dev		0.211	0.226	0.124



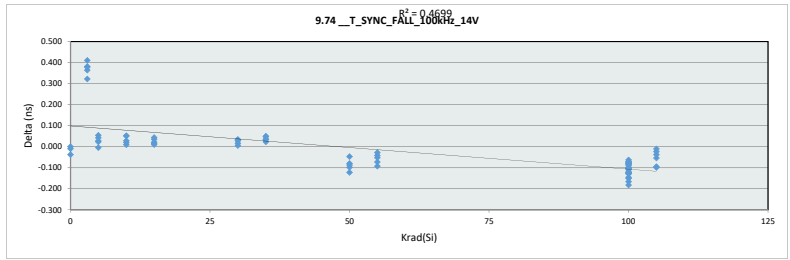
9.70 __T_SYNC_FALL_2MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL											
UL											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
Min	8.313	8.647	8.689	8.628	8.666	8.650	8.672	8.251	8.294	8.015	8.146
Average	8.337	8.704	8.720	8.691	8.698	8.685	8.701	8.306	8.308	8.241	8.262
Max	8.359	8.734	8.776	8.732	8.771	8.772	8.761	8.352	8.334	8.380	8.355
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



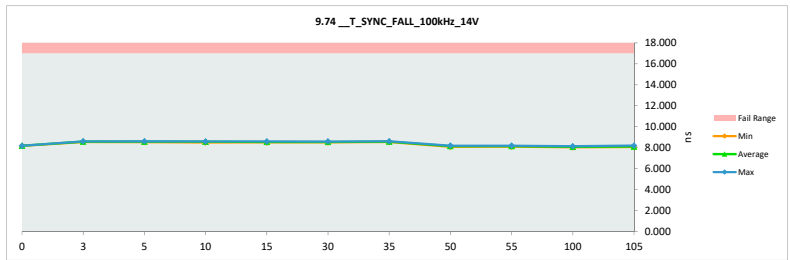
HDR TID Report  
TPS7H5003-SP

9.74_T_SYNC_FALL_100kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.179	8.179	0.000
0	138	8.192	8.182	-0.010
0	139	8.205	8.167	-0.038
3	197	8.177	8.587	0.410
3	198	8.178	8.556	0.378
3	199	8.215	8.596	0.381
3	202	8.213	8.535	0.322
3	203	8.211	8.575	0.364
5	204	8.554	8.577	0.023
5	205	8.563	8.605	0.042
5	206	8.513	8.567	0.054
5	208	8.529	8.524	-0.005
5	209	8.519	8.546	0.027
10	210	8.529	8.581	0.052
10	211	8.533	8.584	0.051
10	212	8.567	8.587	0.020
10	213	8.543	8.571	0.028
10	214	8.485	8.494	0.009
15	215	8.469	8.512	0.043
15	217	8.483	8.498	0.015
15	219	8.553	8.588	0.035
15	220	8.535	8.556	0.021
15	221	8.524	8.534	0.010
30	222	8.495	8.500	0.005
30	223	8.497	8.529	0.032
30	224	8.524	8.540	0.016
30	225	8.524	8.552	0.028
30	226	8.547	8.581	0.034
35	227	8.519	8.555	0.036
35	228	8.504	8.527	0.023
35	229	8.554	8.602	0.048
35	230	8.544	8.577	0.033
35	231	8.517	8.566	0.049
50	170	8.160	8.080	-0.080
50	172	8.261	8.163	-0.098
50	173	8.227	8.104	-0.123
50	175	8.265	8.177	-0.088
50	176	8.235	8.188	-0.047
55	177	8.134	8.092	-0.042
55	178	8.242	8.189	-0.053
55	179	8.229	8.136	-0.093
55	180	8.187	8.159	-0.028
55	181	8.194	8.121	-0.073
100	154	8.214	8.135	-0.079
100	155	8.200	8.033	-0.167
100	156	8.186	8.058	-0.128
100	157	8.123	8.060	-0.063
100	158	8.213	8.092	-0.121
100	159	8.195	8.085	-0.110
100	160	8.200	8.079	-0.121
100	161	8.153	8.051	-0.102
100	162	8.248	8.097	-0.151
100	163	8.197	8.051	-0.146
100	164	8.129	8.053	-0.076
100	165	8.162	8.061	-0.101
100	166	8.222	8.039	-0.183
100	168	8.221	8.137	-0.084
100	169	8.140	8.067	-0.073
100	182	8.226	8.097	-0.129
100	183	8.139	8.048	-0.091
100	184	8.135	8.063	-0.072
100	185	8.178	8.054	-0.124
100	186	8.144	8.040	-0.104
100	187	8.212	8.130	-0.082
100	188	8.217	8.089	-0.128
100	189	8.157	8.077	-0.080
100	190	8.140	8.041	-0.099
105	191	8.111	8.057	-0.054
105	192	8.198	8.099	-0.099
105	193	8.217	8.193	-0.024
105	194	8.181	8.086	-0.095
105	195	8.126	8.088	-0.038
105	196	8.113	8.102	-0.011
Max		8.567	8.605	0.410
Average		8.303	8.288	-0.015
Min		8.111	8.033	-0.183
Std Dev		0.165	0.230	0.123



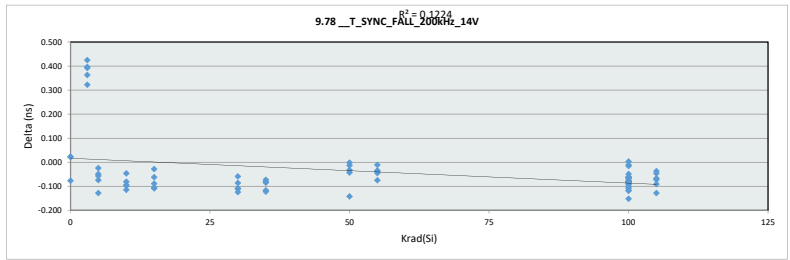
9.74_T_SYNC_FALL_100kHz_14V		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.167	8.535	8.524	8.494	8.498	8.500	8.527	8.080	8.092	8.033	8.057
Average		8.176	8.570	8.564	8.563	8.538	8.540	8.565	8.142	8.139	8.072	8.104
Max		8.182	8.596	8.605	8.587	8.588	8.581	8.602	8.188	8.189	8.137	8.193
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



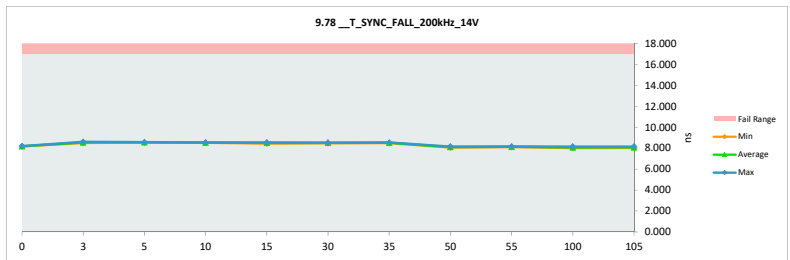


# HDR TID Report TPS7H5003-SP

9.78_T_SYNC_FALL_200kHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.151	8.173	0.022
0	138	8.180	8.203	0.023
0	139	8.247	8.170	-0.077
3	197	8.176	8.498	0.322
3	198	8.142	8.505	0.363
3	199	8.192	8.617	0.425
3	202	8.145	8.541	0.396
3	203	8.190	8.582	0.392
5	204	8.649	8.521	-0.128
5	205	8.641	8.567	-0.074
5	206	8.611	8.554	-0.057
5	208	8.600	8.576	-0.024
5	209	8.621	8.572	-0.049
10	210	8.609	8.528	-0.081
10	211	8.610	8.516	-0.094
10	212	8.666	8.551	-0.115
10	213	8.620	8.522	-0.098
10	214	8.581	8.535	-0.046
15	215	8.534	8.506	-0.028
15	217	8.555	8.446	-0.109
15	219	8.613	8.551	-0.062
15	220	8.626	8.521	-0.105
15	221	8.601	8.512	-0.089
30	222	8.579	8.520	-0.059
30	223	8.582	8.471	-0.111
30	224	8.605	8.481	-0.124
30	225	8.594	8.486	-0.108
30	226	8.620	8.534	-0.086
35	227	8.612	8.527	-0.085
35	228	8.597	8.480	-0.117
35	229	8.622	8.541	-0.081
35	230	8.610	8.537	-0.073
35	231	8.601	8.479	-0.122
50	170	8.056	8.054	-0.002
50	172	8.193	8.161	-0.032
50	173	8.131	8.088	-0.043
50	175	8.258	8.116	-0.142
50	176	8.149	8.136	-0.013
55	177	8.179	8.104	-0.075
55	178	8.208	8.172	-0.036
55	179	8.151	8.140	-0.011
55	180	8.156	8.121	-0.035
55	181	8.146	8.101	-0.045
100	154	8.191	8.072	-0.119
100	155	8.196	8.044	-0.152
100	156	8.121	8.057	-0.064
100	157	8.151	8.067	-0.084
100	158	8.116	8.105	-0.011
100	159	8.172	8.094	-0.078
100	160	8.207	8.099	-0.108
100	161	8.165	8.079	-0.086
100	162	8.179	8.097	-0.082
100	163	8.142	8.079	-0.063
100	164	8.162	8.070	-0.092
100	165	8.123	8.043	-0.080
100	166	8.160	8.078	-0.082
100	168	8.158	8.161	0.003
100	169	8.141	8.061	-0.080
100	182	8.158	8.041	-0.117
100	183	8.083	8.014	-0.069
100	184	8.114	8.050	-0.064
100	185	8.121	8.019	-0.102
100	186	8.074	8.014	-0.060
100	187	8.206	8.106	-0.100
100	188	8.190	8.113	-0.077
100	189	8.125	8.076	-0.049
100	190	8.111	8.095	-0.016
105	191	8.097	8.030	-0.067
105	192	8.155	8.118	-0.037
105	193	8.207	8.161	-0.046
105	194	8.164	8.091	-0.073
105	195	8.130	8.039	-0.091
105	196	8.180	8.052	-0.128
Max		8.666	8.617	0.0425
Average		8.311	8.270	-0.041
Min		8.056	8.014	-0.152
Std Dev		0.218	0.219	0.121

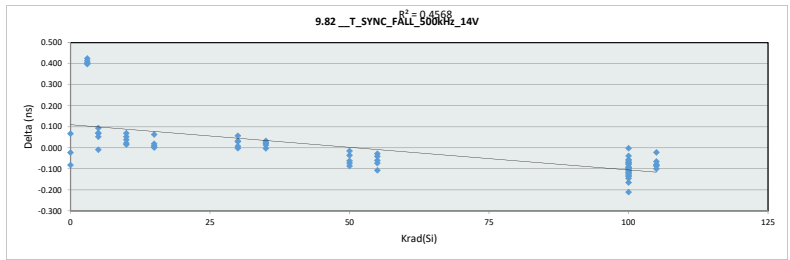


9.78_T_SYNC_FALL_200kHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.170	8.498	8.521	8.516	8.446	8.471	8.479	8.054	8.101	8.014	8.030
Average	8.182	8.549	8.558	8.530	8.507	8.498	8.513	8.111	8.128	8.072	8.082
Max	8.203	8.617	8.576	8.551	8.551	8.534	8.541	8.161	8.172	8.161	8.161
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

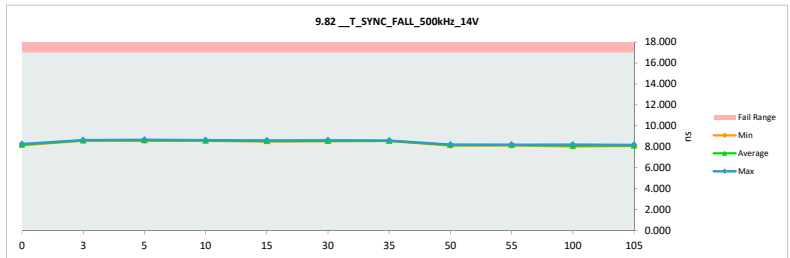


HDR TID Report  
TPS7H5003-SP

9.82_T_SYNC_FALL_500kHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.231	8.148	-0.083
0	138	8.214	8.280	0.066
0	139	8.222	8.198	-0.024
3	197	8.215	8.627	0.412
3	198	8.196	8.598	0.402
3	199	8.245	8.647	0.402
3	202	8.190	8.587	0.397
3	203	8.203	8.627	0.424
5	204	8.585	8.575	-0.010
5	205	8.590	8.683	0.093
5	206	8.552	8.604	0.052
5	208	8.550	8.619	0.069
5	209	8.552	8.620	0.068
10	210	8.558	8.572	0.014
10	211	8.563	8.600	0.037
10	212	8.598	8.640	0.052
10	213	8.572	8.593	0.021
10	214	8.525	8.593	0.068
15	215	8.497	8.499	0.002
15	217	8.506	8.507	0.001
15	219	8.549	8.611	0.062
15	220	8.567	8.576	0.009
15	221	8.544	8.562	0.018
30	222	8.513	8.542	0.029
30	223	8.533	8.530	-0.003
30	224	8.555	8.562	0.007
30	225	8.545	8.576	0.031
30	226	8.579	8.635	0.056
35	227	8.543	8.555	0.012
35	228	8.548	8.569	0.021
35	229	8.563	8.595	0.032
35	230	8.569	8.565	-0.004
35	231	8.555	8.578	0.023
50	170	8.200	8.112	-0.088
50	172	8.213	8.197	-0.016
50	173	8.212	8.138	-0.074
50	175	8.253	8.216	-0.037
50	176	8.281	8.218	-0.063
55	177	8.233	8.125	-0.108
55	178	8.267	8.206	-0.061
55	179	8.232	8.203	-0.029
55	180	8.241	8.167	-0.074
55	181	8.200	8.158	-0.042
100	154	8.239	8.093	-0.146
100	155	8.238	8.103	-0.135
100	156	8.208	8.109	-0.099
100	157	8.198	8.032	-0.166
100	158	8.183	8.114	-0.069
100	159	8.205	8.127	-0.078
100	160	8.247	8.111	-0.136
100	161	8.265	8.053	-0.212
100	162	8.262	8.146	-0.116
100	163	8.163	8.104	-0.059
100	164	8.167	8.108	-0.059
100	165	8.183	8.086	-0.097
100	166	8.207	8.077	-0.130
100	168	8.252	8.212	-0.040
100	169	8.187	8.093	-0.094
100	182	8.223	8.152	-0.071
100	183	8.158	8.088	-0.070
100	184	8.181	8.102	-0.079
100	185	8.221	8.113	-0.108
100	186	8.196	8.073	-0.123
100	187	8.240	8.134	-0.106
100	188	8.176	8.173	-0.003
100	189	8.195	8.103	-0.092
100	190	8.209	8.090	-0.119
105	191	8.144	8.078	-0.066
105	192	8.198	8.118	-0.080
105	193	8.279	8.193	-0.086
105	194	8.212	8.125	-0.087
105	195	8.174	8.073	-0.101
105	196	8.165	8.142	-0.023
	Max	8.590	8.683	0.424
	Average	8.329	8.320	-0.009
	Min	8.144	8.032	-0.212
	Std Dev	0.165	0.231	0.131

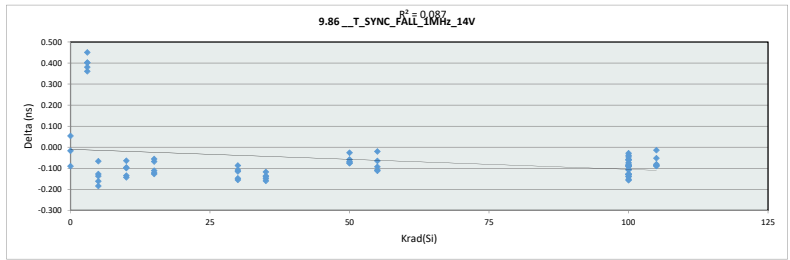


9.82_T_SYNC_FALL_500kHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL											
UL											
Min	8.148	8.587	8.575	8.572	8.499	8.530	8.555	8.112	8.125	8.032	8.073
Average	8.209	8.617	8.620	8.600	8.551	8.569	8.572	8.176	8.172	8.108	8.122
Max	8.280	8.647	8.683	8.640	8.611	8.635	8.595	8.218	8.206	8.212	8.193
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

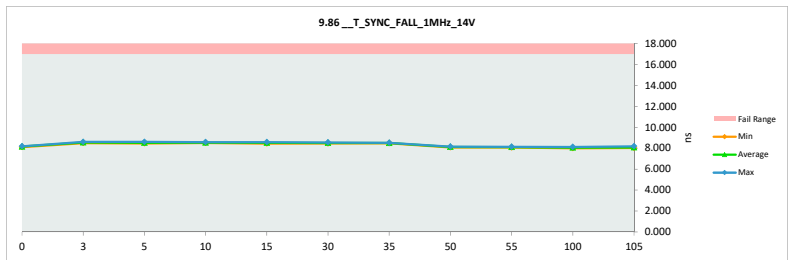


HDR TID Report  
TPS7H5003-SP

9.86_T_SYNC_FALL_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.193	8.102	-0.091
0	138	8.131	8.184	0.053
0	139	8.200	8.182	-0.018
3	197	8.088	8.538	0.450
3	198	8.109	8.489	0.380
3	199	8.199	8.559	0.360
3	202	8.126	8.527	0.401
3	203	8.200	8.601	0.401
5	204	8.668	8.540	-0.128
5	205	8.669	8.602	-0.067
5	206	8.639	8.454	-0.185
5	208	8.644	8.482	-0.162
5	209	8.641	8.503	-0.138
10	210	8.635	8.499	-0.136
10	211	8.648	8.583	-0.065
10	212	8.677	8.590	-0.097
10	213	8.660	8.516	-0.144
10	214	8.606	8.506	-0.100
15	215	8.572	8.444	-0.128
15	217	8.575	8.518	-0.057
15	219	8.646	8.577	-0.069
15	220	8.645	8.522	-0.123
15	221	8.632	8.520	-0.112
30	222	8.598	8.449	-0.149
30	223	8.594	8.506	-0.088
30	224	8.635	8.479	-0.156
30	225	8.622	8.506	-0.116
30	226	8.656	8.547	-0.109
35	227	8.637	8.519	-0.118
35	228	8.624	8.474	-0.150
35	229	8.655	8.514	-0.141
35	230	8.647	8.486	-0.161
35	231	8.620	8.483	-0.137
50	170	8.101	8.074	-0.027
50	172	8.194	8.135	-0.059
50	173	8.193	8.116	-0.077
50	175	8.194	8.126	-0.068
50	176	8.221	8.146	-0.075
55	177	8.188	8.075	-0.113
55	178	8.197	8.131	-0.066
55	179	8.220	8.111	-0.109
55	180	8.155	8.134	-0.021
55	181	8.160	8.066	-0.094
100	154	8.124	8.077	-0.047
100	155	8.167	8.036	-0.131
100	156	8.165	8.056	-0.109
100	157	8.133	7.992	-0.141
100	158	8.150	8.060	-0.090
100	159	8.178	8.050	-0.128
100	160	8.209	8.080	-0.129
100	161	8.167	8.009	-0.158
100	162	8.211	8.082	-0.129
100	163	8.145	8.039	-0.106
100	164	8.129	8.040	-0.089
100	165	8.119	8.056	-0.063
100	166	8.173	8.034	-0.139
100	168	8.157	8.118	-0.039
100	169	8.160	8.007	-0.153
100	182	8.157	8.082	-0.075
100	183	8.159	8.028	-0.131
100	184	8.118	8.089	-0.029
100	185	8.141	8.083	-0.058
100	186	8.110	8.051	-0.059
100	187	8.162	8.080	-0.082
100	188	8.176	8.090	-0.086
100	189	8.135	8.045	-0.090
100	190	8.130	8.037	-0.093
105	191	8.126	8.044	-0.082
105	192	8.143	8.057	-0.086
105	193	8.231	8.178	-0.053
105	194	8.152	8.063	-0.089
105	195	8.055	8.040	-0.015
105	196	8.116	8.026	-0.090
Max		8.677	8.602	0.450
Average		8.321	8.258	-0.063
Min		8.055	7.992	-0.185
Std Dev		0.230	0.223	0.133



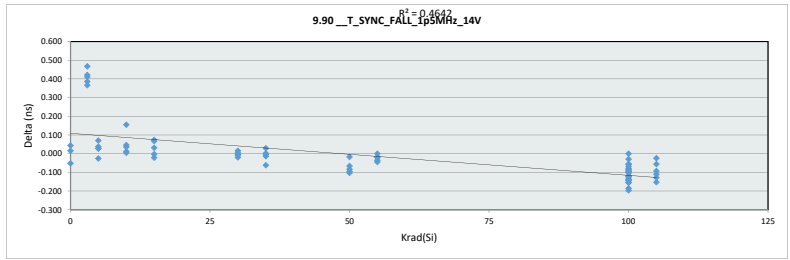
9.86_T_SYNC_FALL_1MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.102	8.489	8.454	8.499	8.444	8.444	8.474	8.074	8.066	7.992	8.026
Average	8.156	8.543	8.516	8.537	8.516	8.497	8.495	8.119	8.103	8.055	8.068
Max	8.184	8.601	8.602	8.583	8.577	8.547	8.519	8.146	8.134	8.118	8.178
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



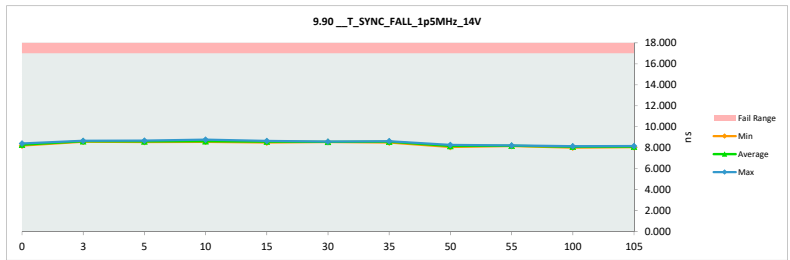
# HDR TID Report TPS7H5003-SP

9.90_T_SYNC_FALL_1p5MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.217	8.232	0.015
0	138	8.242	8.190	-0.052
0	139	8.345	8.388	0.043
3	197	8.195	8.581	0.386
3	198	8.158	8.625	0.467
3	199	8.226	8.647	0.421
3	202	8.196	8.562	0.366
3	203	8.168	8.579	0.411
5	204	8.585	8.613	0.028
5	205	8.592	8.662	0.070
5	206	8.548	8.522	-0.026
5	208	8.558	8.595	0.037
5	209	8.541	8.568	0.027
10	210	8.546	8.551	0.005
10	211	8.541	8.577	0.036
10	212	8.608	8.763	0.155
10	213	8.569	8.591	0.022
10	214	8.515	8.561	0.046
15	215	8.486	8.483	-0.003
15	217	8.493	8.524	0.031
15	219	8.554	8.627	0.073
15	220	8.575	8.553	-0.022
15	221	8.572	8.639	0.067
30	222	8.516	8.531	0.015
30	223	8.531	8.523	-0.008
30	224	8.554	8.534	-0.020
30	225	8.537	8.541	0.004
30	226	8.573	8.569	-0.004
35	227	8.545	8.541	-0.004
35	228	8.546	8.484	-0.062
35	229	8.591	8.592	0.001
35	230	8.583	8.612	0.029
35	231	8.552	8.538	-0.014
50	170	8.116	8.050	-0.066
50	172	8.257	8.154	-0.103
50	173	8.206	8.123	-0.083
50	175	8.265	8.248	-0.017
50	176	8.253	8.158	-0.095
55	177	8.225	8.180	-0.045
55	178	8.207	8.185	-0.022
55	179	8.236	8.201	-0.035
55	180	8.148	8.148	0.000
55	181	8.160	8.143	-0.017
100	154	8.211	8.075	-0.136
100	155	8.241	8.045	-0.196
100	156	8.218	8.033	-0.185
100	157	8.137	8.038	-0.099
100	158	8.245	8.091	-0.154
100	159	8.174	8.105	-0.069
100	160	8.213	8.129	-0.084
100	161	8.189	8.051	-0.138
100	162	8.212	8.091	-0.121
100	163	8.216	8.125	-0.091
100	164	8.148	8.023	-0.125
100	165	8.171	8.075	-0.096
100	166	8.228	8.085	-0.143
100	168	8.235	8.116	-0.119
100	169	8.209	8.055	-0.154
100	182	8.205	8.126	-0.079
100	183	8.107	8.078	-0.029
100	184	8.096	8.096	0.000
100	185	8.144	8.089	-0.055
100	186	8.144	8.000	-0.144
100	187	8.207	8.107	-0.100
100	188	8.210	8.107	-0.103
100	189	8.122	8.064	-0.058
100	190	8.229	8.075	-0.154
105	191	8.141	8.048	-0.093
105	192	8.191	8.038	-0.153
105	193	8.279	8.150	-0.129
105	194	8.145	8.089	-0.056
105	195	8.158	8.047	-0.111
105	196	8.163	8.139	-0.024
Max		8.608	8.763	0.467
Average		8.318	8.302	-0.016
Min		8.096	8.000	-0.196
Std Dev		0.176	0.239	0.136

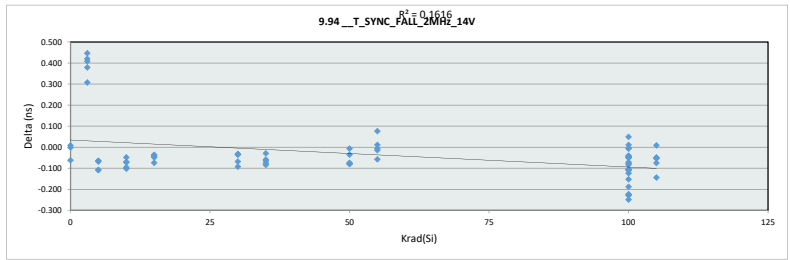


9.90_T_SYNC_FALL_1p5MHz_14V		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.190	8.562	8.522	8.551	8.483	8.523	8.484	8.050	8.143	8.000	8.038
Average		8.270	8.599	8.592	8.607	8.565	8.540	8.553	8.147	8.171	8.078	8.085
Max		8.388	8.647	8.662	8.763	8.639	8.569	8.612	8.248	8.201	8.129	8.150
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

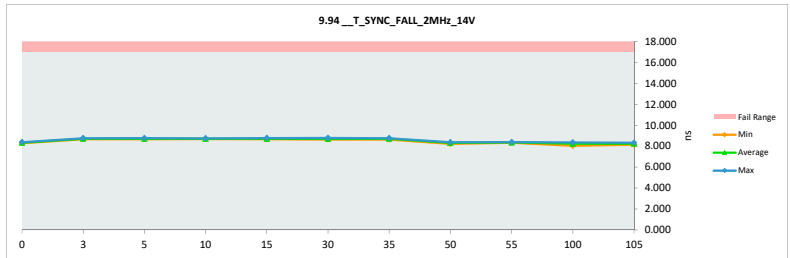


HDR TID Report  
TPS7H5003-SP

9.94 __T_SYNC_FALL_2MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.349	8.286	-0.063
0	138	8.331	8.329	-0.002
0	139	8.367	8.375	0.008
3	197	8.291	8.737	0.446
3	198	8.327	8.705	0.378
3	199	8.362	8.669	0.307
3	202	8.279	8.698	0.419
3	203	8.353	8.760	0.407
5	204	8.807	8.698	-0.109
5	205	8.834	8.768	-0.066
5	206	8.791	8.725	-0.066
5	208	8.791	8.722	-0.069
5	209	8.777	8.668	-0.109
10	210	8.747	8.698	-0.049
10	211	8.799	8.703	-0.096
10	212	8.819	8.749	-0.070
10	213	8.803	8.700	-0.103
10	214	8.759	8.686	-0.073
15	215	8.705	8.659	-0.046
15	217	8.722	8.673	-0.049
15	219	8.771	8.733	-0.038
15	220	8.767	8.692	-0.075
15	221	8.811	8.773	-0.038
30	222	8.734	8.700	-0.034
30	223	8.728	8.635	-0.093
30	224	8.743	8.674	-0.069
30	225	8.753	8.717	-0.036
30	226	8.810	8.777	-0.033
35	227	8.771	8.711	-0.060
35	228	8.722	8.637	-0.085
35	229	8.799	8.733	-0.066
35	230	8.790	8.760	-0.030
35	231	8.787	8.710	-0.077
50	170	8.296	8.215	-0.081
50	172	8.378	8.371	-0.007
50	173	8.331	8.295	-0.036
50	175	8.408	8.329	-0.079
50	176	8.417	8.342	-0.075
55	177	8.287	8.363	0.076
55	178	8.362	8.355	-0.007
55	179	8.374	8.384	0.010
55	180	8.393	8.335	-0.058
55	181	8.342	8.327	-0.015
100	154	8.334	8.283	-0.051
100	155	8.284	8.243	-0.041
100	156	8.345	8.240	-0.105
100	157	8.332	8.260	-0.072
100	158	8.314	8.265	-0.049
100	159	8.363	8.239	-0.124
100	160	8.337	8.333	-0.004
100	161	8.299	8.250	-0.049
100	162	8.378	8.272	-0.106
100	163	8.350	8.304	-0.046
100	164	8.309	8.059	-0.250
100	165	8.264	8.034	-0.230
100	166	8.318	8.210	-0.108
100	168	8.346	8.356	0.010
100	169	8.325	8.215	-0.110
100	182	8.389	8.163	-0.226
100	183	8.291	8.246	-0.045
100	184	8.309	8.357	0.048
100	185	8.316	8.238	-0.078
100	186	8.272	8.264	-0.008
100	187	8.362	8.278	-0.084
100	188	8.373	8.149	-0.224
100	189	8.260	8.071	-0.189
100	190	8.363	8.210	-0.153
105	191	8.270	8.219	-0.051
105	192	8.336	8.260	-0.076
105	193	8.381	8.327	-0.054
105	194	8.308	8.258	-0.050
105	195	8.242	8.250	0.008
105	196	8.286	8.141	-0.145
Max		8.834	8.777	0.446
Average		8.483	8.446	-0.037
Min		8.242	8.034	-0.250
Std Dev		0.215	0.231	0.131

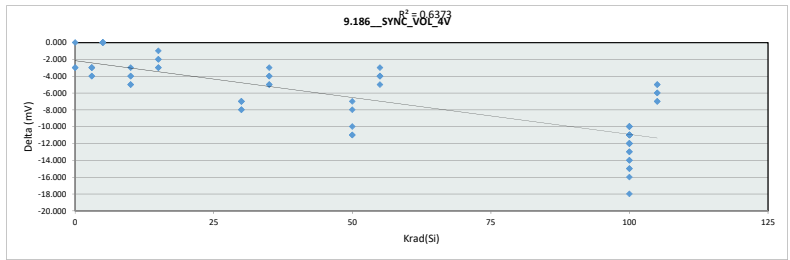


9.94 __T_SYNC_FALL_2MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL											
UL											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
Min	8.286	8.669	8.668	8.686	8.659	8.635	8.637	8.215	8.327	8.034	8.141
Average	8.330	8.714	8.716	8.707	8.706	8.701	8.710	8.310	8.353	8.231	8.243
Max	8.375	8.760	8.768	8.749	8.773	8.777	8.760	8.371	8.384	8.357	8.327
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

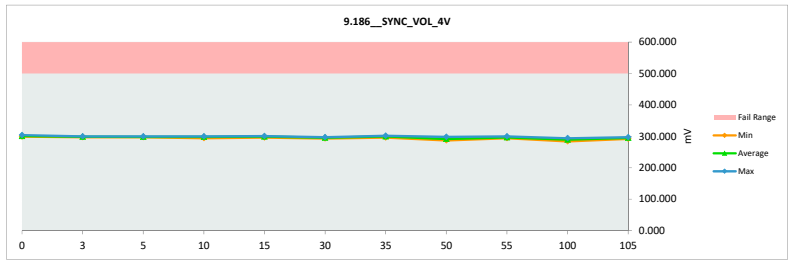


HDR TID Report  
TPS7H5003-SP

9.186_SYNC_VOL_4V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	500	500		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	304.000	304.000	0.000
0	138	303.000	300.000	-3.000
0	139	302.000	299.000	-3.000
3	197	302.000	299.000	-3.000
3	198	304.000	300.000	-4.000
3	199	300.000	297.000	-3.000
3	202	303.000	299.000	-4.000
3	203	301.000	298.000	-3.000
5	204	300.000	300.000	0.000
5	205	299.000	299.000	0.000
5	206	297.000	297.000	0.000
5	208	298.000	298.000	0.000
5	209	298.000	298.000	0.000
10	210	303.000	300.000	-3.000
10	211	298.000	294.000	-4.000
10	212	304.000	299.000	-5.000
10	213	303.000	299.000	-4.000
10	214	304.000	299.000	-5.000
15	215	299.000	298.000	-1.000
15	217	298.000	296.000	-2.000
15	219	303.000	301.000	-2.000
15	220	304.000	301.000	-3.000
15	221	302.000	299.000	-3.000
30	222	303.000	296.000	-7.000
30	223	300.000	293.000	-7.000
30	224	305.000	297.000	-8.000
30	225	305.000	297.000	-8.000
30	226	303.000	296.000	-7.000
35	227	303.000	300.000	-3.000
35	228	306.000	302.000	-4.000
35	229	304.000	300.000	-4.000
35	230	305.000	300.000	-5.000
35	231	301.000	296.000	-5.000
50	170	298.000	287.000	-11.000
50	172	299.000	288.000	-11.000
50	173	299.000	289.000	-10.000
50	175	305.000	298.000	-7.000
50	176	303.000	295.000	-8.000
55	177	303.000	300.000	-3.000
55	178	299.000	295.000	-4.000
55	179	300.000	296.000	-4.000
55	180	298.000	294.000	-4.000
55	181	304.000	299.000	-5.000
100	154	304.000	286.000	-18.000
100	155	301.000	286.000	-15.000
100	156	304.000	288.000	-16.000
100	157	300.000	287.000	-13.000
100	158	304.000	292.000	-12.000
100	159	298.000	287.000	-11.000
100	160	305.000	294.000	-11.000
100	161	304.000	293.000	-11.000
100	162	301.000	289.000	-12.000
100	163	304.000	292.000	-12.000
100	164	301.000	287.000	-14.000
100	165	300.000	285.000	-15.000
100	166	301.000	288.000	-13.000
100	168	304.000	292.000	-12.000
100	169	303.000	292.000	-11.000
100	182	300.000	290.000	-10.000
100	183	303.000	293.000	-10.000
100	184	301.000	291.000	-10.000
100	185	298.000	284.000	-14.000
100	186	299.000	287.000	-12.000
100	187	304.000	293.000	-11.000
100	188	302.000	291.000	-11.000
100	189	302.000	291.000	-11.000
100	190	300.000	290.000	-10.000
105	191	298.000	292.000	-6.000
105	192	299.000	292.000	-7.000
105	193	302.000	297.000	-5.000
105	194	304.000	297.000	-7.000
105	195	303.000	297.000	-6.000
105	196	300.000	295.000	-5.000
Max		306.000	304.000	0.000
Average		301.658	294.658	-7.000
Min		297.000	284.000	-18.000
Std Dev		2.358	4.871	4.515

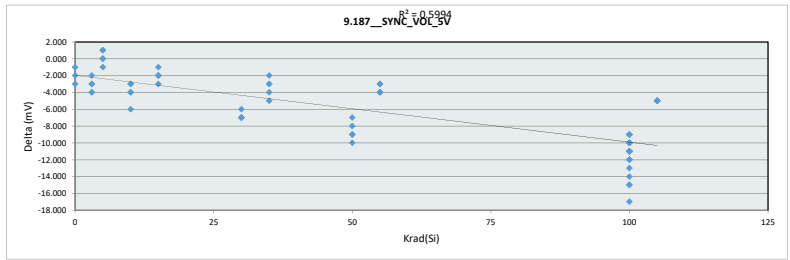


9.186_SYNC_VOL											
Test Site											
Tester											
Test Number											
Max Limit	500	mV									
Min Limit		mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	299.000	297.000	297.000	294.000	296.000	293.000	296.000	287.000	294.000	284.000	292.000
Min	301.000	298.600	298.400	298.200	299.000	295.800	299.600	291.400	296.800	289.500	295.000
Average	304.000	300.000	300.000	300.000	301.000	297.000	302.000	298.000	300.000	294.000	297.000
Max	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

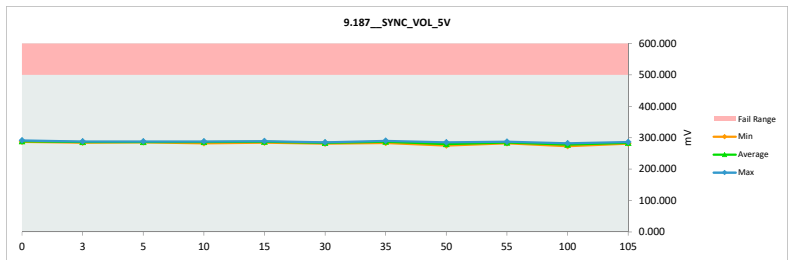


HDR TID Report  
TPS7H5003-SP

9.187_SYNC_VOL_5V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	500	500		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	292.000	291.000	-1.000
0	138	291.000	288.000	-3.000
0	139	289.000	287.000	-2.000
3	197	289.000	287.000	-2.000
3	198	291.000	288.000	-3.000
3	199	288.000	284.000	-4.000
3	202	290.000	287.000	-3.000
3	203	289.000	285.000	-4.000
5	204	287.000	288.000	1.000
5	205	287.000	287.000	0.000
5	206	284.000	285.000	1.000
5	208	286.000	285.000	-1.000
5	209	286.000	286.000	0.000
10	210	291.000	288.000	-3.000
10	211	286.000	282.000	-4.000
10	212	291.000	287.000	-4.000
10	213	290.000	287.000	-3.000
10	214	292.000	286.000	-6.000
15	215	287.000	286.000	-1.000
15	217	286.000	284.000	-2.000
15	219	291.000	289.000	-2.000
15	220	291.000	289.000	-2.000
15	221	290.000	287.000	-3.000
30	222	290.000	284.000	-6.000
30	223	288.000	281.000	-7.000
30	224	292.000	285.000	-7.000
30	225	292.000	285.000	-7.000
30	226	290.000	283.000	-7.000
35	227	290.000	288.000	-2.000
35	228	293.000	290.000	-3.000
35	229	291.000	288.000	-3.000
35	230	292.000	288.000	-4.000
35	231	288.000	283.000	-5.000
50	170	285.000	275.000	-10.000
50	172	286.000	277.000	-9.000
50	173	286.000	277.000	-9.000
50	175	293.000	285.000	-8.000
50	176	290.000	283.000	-7.000
55	177	290.000	287.000	-3.000
55	178	286.000	283.000	-3.000
55	179	288.000	284.000	-4.000
55	180	286.000	282.000	-4.000
55	181	281.000	287.000	-4.000
100	154	292.000	275.000	-17.000
100	155	289.000	274.000	-15.000
100	156	292.000	277.000	-15.000
100	157	287.000	276.000	-11.000
100	158	292.000	281.000	-11.000
100	159	285.000	276.000	-9.000
100	160	293.000	282.000	-11.000
100	161	292.000	281.000	-11.000
100	162	288.000	278.000	-10.000
100	163	291.000	281.000	-10.000
100	164	289.000	276.000	-13.000
100	165	288.000	274.000	-14.000
100	166	289.000	277.000	-12.000
100	168	292.000	280.000	-12.000
100	169	290.000	280.000	-10.000
100	182	287.000	278.000	-9.000
100	183	290.000	281.000	-9.000
100	184	288.000	279.000	-9.000
100	185	285.000	273.000	-12.000
100	186	286.000	275.000	-11.000
100	187	291.000	281.000	-10.000
100	188	290.000	279.000	-11.000
100	189	289.000	279.000	-10.000
100	190	288.000	279.000	-9.000
105	191	286.000	281.000	-5.000
105	192	286.000	281.000	-5.000
105	193	290.000	285.000	-5.000
105	194	291.000	286.000	-5.000
105	195	291.000	286.000	-5.000
105	196	288.000	283.000	-5.000
Max		293.000	291.000	1.000
Average		289.123	282.767	-6.356
Min		284.000	273.000	-17.000
Std Dev		2.351	4.548	4.221

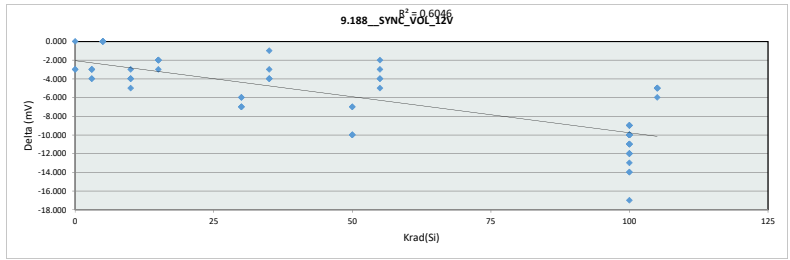


9.187_SYNC_VOL											
Test Site											
Tester											
Test Number											
Max Limit	500	mV									
Min Limit		mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	287.000	284.000	285.000	282.000	284.000	281.000	283.000	275.000	282.000	273.000	281.000
Min	288.667	286.200	286.200	286.000	287.000	283.600	287.400	279.400	284.600	278.000	283.667
Average	291.000	288.000	288.000	288.000	289.000	285.000	290.000	285.000	287.000	282.000	286.000
Max	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

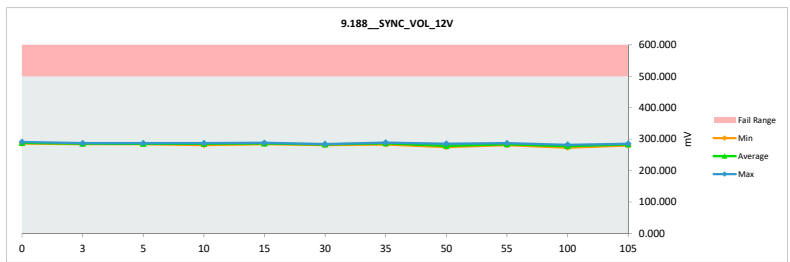


# HDR TID Report TPS7H5003-SP

9.188_SYNC_VOL_12V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	500	500		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	291.000	291.000	0.000
0	138	290.000	287.000	-3.000
0	139	289.000	286.000	-3.000
3	197	289.000	286.000	-3.000
3	198	291.000	287.000	-4.000
3	199	287.000	284.000	-3.000
3	202	290.000	286.000	-4.000
3	203	288.000	285.000	-3.000
5	204	287.000	287.000	0.000
5	205	286.000	286.000	0.000
5	206	284.000	284.000	0.000
5	208	285.000	285.000	0.000
5	209	285.000	285.000	0.000
10	210	290.000	287.000	-3.000
10	211	285.000	281.000	-4.000
10	212	290.000	286.000	-4.000
10	213	290.000	286.000	-4.000
10	214	291.000	286.000	-5.000
15	215	287.000	285.000	-2.000
15	217	286.000	284.000	-2.000
15	219	290.000	288.000	-2.000
15	220	290.000	288.000	-2.000
15	221	289.000	286.000	-3.000
30	222	289.000	283.000	-6.000
30	223	287.000	281.000	-6.000
30	224	291.000	284.000	-7.000
30	225	291.000	284.000	-7.000
30	226	290.000	283.000	-7.000
35	227	289.000	288.000	-1.000
35	228	293.000	289.000	-4.000
35	229	290.000	287.000	-3.000
35	230	291.000	287.000	-4.000
35	231	287.000	283.000	-4.000
50	170	285.000	275.000	-10.000
50	172	286.000	276.000	-10.000
50	173	286.000	276.000	-10.000
50	175	292.000	285.000	-7.000
50	176	289.000	282.000	-7.000
55	177	289.000	287.000	-2.000
55	178	285.000	282.000	-3.000
55	179	287.000	283.000	-4.000
55	180	285.000	281.000	-4.000
55	181	291.000	286.000	-5.000
100	154	291.000	274.000	-17.000
100	155	288.000	274.000	-14.000
100	156	291.000	277.000	-14.000
100	157	287.000	275.000	-12.000
100	158	291.000	280.000	-11.000
100	159	285.000	275.000	-10.000
100	160	292.000	282.000	-10.000
100	161	291.000	281.000	-10.000
100	162	287.000	277.000	-10.000
100	163	290.000	280.000	-10.000
100	164	288.000	276.000	-12.000
100	165	287.000	274.000	-13.000
100	166	288.000	276.000	-12.000
100	168	291.000	280.000	-11.000
100	169	290.000	280.000	-10.000
100	182	287.000	278.000	-9.000
100	183	290.000	281.000	-9.000
100	184	288.000	279.000	-9.000
100	185	285.000	273.000	-12.000
100	186	286.000	275.000	-11.000
100	187	291.000	280.000	-11.000
100	188	289.000	279.000	-10.000
100	189	289.000	279.000	-10.000
100	190	287.000	278.000	-9.000
105	191	286.000	281.000	-5.000
105	192	285.000	280.000	-5.000
105	193	289.000	284.000	-5.000
105	194	291.000	285.000	-6.000
105	195	290.000	285.000	-5.000
105	196	288.000	283.000	-5.000
Max		293.000	291.000	0.000
Average		288.507	282.178	-6.329
Min		284.000	273.000	-17.000
Std Dev		2.231	4.408	4.083



9.188_SYNC_VOL											
Test Site											
Tester											
Test Number											
Max Limit	500	mV									
Min Limit		mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	286.000	284.000	284.000	281.000	284.000	281.000	283.000	275.000	281.000	273.000	280.000
Min	288.000	285.600	285.400	285.200	286.200	283.000	286.800	278.800	283.800	277.625	283.000
Average	291.000	287.000	287.000	287.000	288.000	284.000	289.000	285.000	287.000	282.000	285.000
Max	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

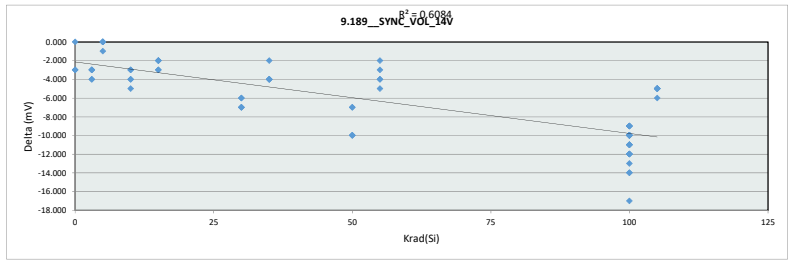




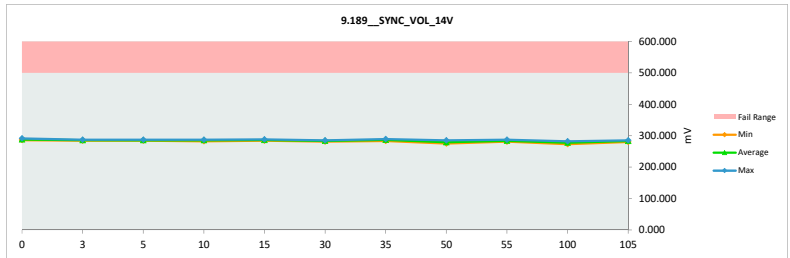
# HDR TID Report

## TPS7H5003-SP

9.189_SYNC_VOL_14V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	500	500		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	291.000	291.000	0.000
0	138	290.000	287.000	-3.000
0	139	289.000	286.000	-3.000
3	197	289.000	286.000	-3.000
3	198	291.000	287.000	-4.000
3	199	287.000	284.000	-3.000
3	202	290.000	286.000	-4.000
3	203	288.000	285.000	-3.000
5	204	287.000	287.000	0.000
5	205	286.000	286.000	0.000
5	206	284.000	284.000	0.000
5	208	285.000	285.000	0.000
5	209	286.000	285.000	-1.000
10	210	290.000	287.000	-3.000
10	211	285.000	282.000	-3.000
10	212	290.000	286.000	-4.000
10	213	290.000	286.000	-4.000
10	214	291.000	286.000	-5.000
15	215	287.000	285.000	-2.000
15	217	286.000	284.000	-2.000
15	219	290.000	288.000	-2.000
15	220	291.000	288.000	-3.000
15	221	289.000	286.000	-3.000
30	222	290.000	283.000	-7.000
30	223	287.000	281.000	-6.000
30	224	291.000	284.000	-7.000
30	225	291.000	285.000	-6.000
30	226	290.000	283.000	-7.000
35	227	289.000	287.000	-2.000
35	228	293.000	289.000	-4.000
35	229	291.000	287.000	-4.000
35	230	291.000	287.000	-4.000
35	231	287.000	283.000	-4.000
50	170	285.000	275.000	-10.000
50	172	286.000	276.000	-10.000
50	173	286.000	276.000	-10.000
50	175	292.000	285.000	-7.000
50	176	289.000	282.000	-7.000
55	177	289.000	287.000	-2.000
55	178	285.000	282.000	-3.000
55	179	287.000	283.000	-4.000
55	180	285.000	281.000	-4.000
55	181	291.000	286.000	-5.000
100	154	291.000	274.000	-17.000
100	155	288.000	274.000	-14.000
100	156	291.000	277.000	-14.000
100	157	287.000	275.000	-12.000
100	158	291.000	280.000	-11.000
100	159	285.000	275.000	-10.000
100	160	292.000	282.000	-10.000
100	161	291.000	281.000	-10.000
100	162	287.000	277.000	-10.000
100	163	290.000	280.000	-10.000
100	164	288.000	276.000	-12.000
100	165	287.000	274.000	-13.000
100	166	288.000	276.000	-12.000
100	168	291.000	280.000	-11.000
100	169	290.000	280.000	-10.000
100	182	287.000	278.000	-9.000
100	183	290.000	281.000	-9.000
100	184	288.000	279.000	-9.000
100	185	285.000	273.000	-12.000
100	186	286.000	275.000	-11.000
100	187	291.000	280.000	-11.000
100	188	289.000	279.000	-10.000
100	189	289.000	279.000	-10.000
100	190	287.000	278.000	-9.000
105	191	286.000	281.000	-5.000
105	192	285.000	280.000	-5.000
105	193	289.000	284.000	-5.000
105	194	291.000	285.000	-6.000
105	195	290.000	285.000	-5.000
105	196	288.000	283.000	-5.000
Max		293.000	291.000	0.000
Average		288.562	282.192	-6.370
Min		284.000	273.000	-17.000
Std Dev		2.242	4.396	4.033

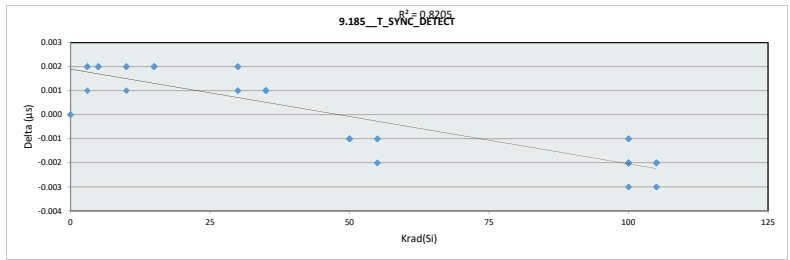


9.189_SYNC_VOL											
Test Site											
Tester											
Test Number											
Max Limit	500	mV									
Min Limit		mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	286.000	284.000	284.000	282.000	284.000	281.000	283.000	275.000	281.000	273.000	280.000
Min	288.000	285.600	285.400	285.400	286.200	283.200	286.600	278.800	283.800	277.625	283.000
Average	291.000	287.000	287.000	287.000	288.000	285.000	289.000	285.000	287.000	282.000	285.000
Max	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

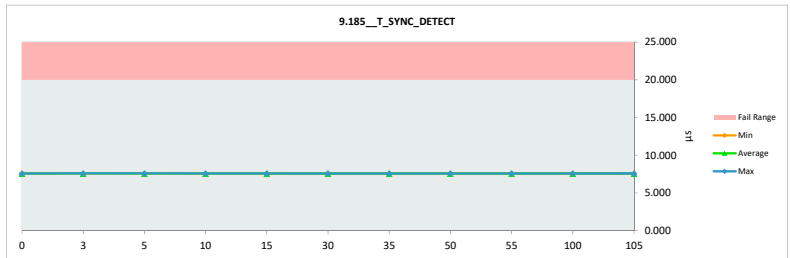


HDR TID Report  
TPS7H5003-SP

9.185_T_SYNC_DETECT				
Test Site				
Tester				
Test Number				
Unit	µs	µs		
Max Limit	20	20		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.590	7.590	0.000
0	138	7.589	7.589	0.000
0	139	7.589	7.589	0.000
3	197	7.589	7.591	0.002
3	198	7.589	7.591	0.002
3	199	7.589	7.590	0.001
3	202	7.589	7.591	0.002
3	203	7.589	7.591	0.002
5	204	7.588	7.590	0.002
5	205	7.588	7.590	0.002
5	206	7.588	7.590	0.002
5	208	7.588	7.590	0.002
5	209	7.588	7.590	0.002
10	210	7.588	7.590	0.002
10	211	7.588	7.589	0.001
10	212	7.588	7.590	0.002
10	213	7.588	7.590	0.002
10	214	7.588	7.590	0.002
15	215	7.588	7.590	0.002
15	217	7.587	7.589	0.002
15	219	7.588	7.590	0.002
15	220	7.589	7.591	0.002
15	221	7.588	7.590	0.002
30	222	7.588	7.590	0.002
30	223	7.588	7.589	0.001
30	224	7.589	7.590	0.001
30	225	7.588	7.590	0.002
30	226	7.588	7.590	0.002
35	227	7.588	7.589	0.001
35	228	7.589	7.590	0.001
35	229	7.589	7.590	0.001
35	230	7.589	7.590	0.001
35	231	7.588	7.589	0.001
50	170	7.588	7.587	-0.001
50	172	7.588	7.587	-0.001
50	173	7.589	7.588	-0.001
50	175	7.589	7.588	-0.001
50	176	7.588	7.587	-0.001
55	177	7.589	7.588	-0.001
55	178	7.589	7.587	-0.002
55	179	7.588	7.586	-0.002
55	180	7.587	7.586	-0.001
55	181	7.589	7.588	-0.001
100	154	7.589	7.587	-0.002
100	155	7.589	7.587	-0.002
100	156	7.589	7.587	-0.002
100	157	7.588	7.586	-0.002
100	158	7.589	7.587	-0.002
100	159	7.587	7.586	-0.001
100	160	7.589	7.587	-0.002
100	161	7.589	7.587	-0.002
100	162	7.588	7.586	-0.002
100	163	7.589	7.586	-0.003
100	164	7.589	7.586	-0.003
100	165	7.588	7.586	-0.002
100	166	7.588	7.586	-0.002
100	168	7.588	7.587	-0.001
100	169	7.590	7.588	-0.002
100	182	7.589	7.587	-0.002
100	183	7.589	7.588	-0.001
100	184	7.589	7.587	-0.002
100	185	7.588	7.586	-0.002
100	186	7.588	7.586	-0.002
100	187	7.589	7.587	-0.002
100	188	7.588	7.587	-0.001
100	189	7.589	7.587	-0.002
100	190	7.589	7.587	-0.002
105	191	7.588	7.586	-0.002
105	192	7.588	7.586	-0.002
105	193	7.589	7.586	-0.003
105	194	7.588	7.586	-0.002
105	195	7.589	7.587	-0.002
105	196	7.589	7.586	-0.003
Max		7.590	7.591	0.002
Average		7.588	7.588	0.000
Min		7.587	7.586	-0.003
Std Dev		0.001	0.002	0.002



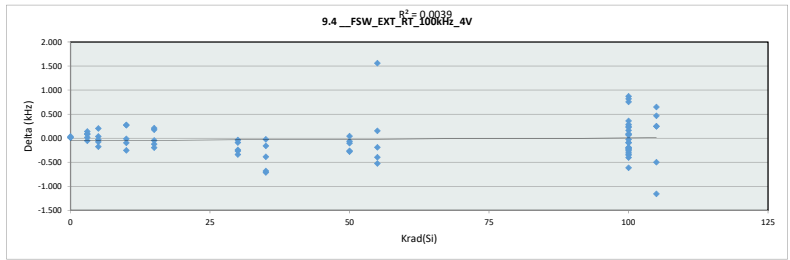
9.185_T_SYNC_DETECT											
Test Site											
Tester											
Test Number											
Max Limit	20	µs									
Min Limit		µs									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.589	7.590	7.590	7.589	7.589	7.589	7.589	7.587	7.586	7.586	7.586
Average	7.589	7.591	7.590	7.590	7.590	7.590	7.590	7.587	7.587	7.587	7.586
Max	7.590	7.591	7.590	7.590	7.591	7.590	7.590	7.588	7.588	7.588	7.587
UL	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000



# HDR TID Report TPS7H5003-SP

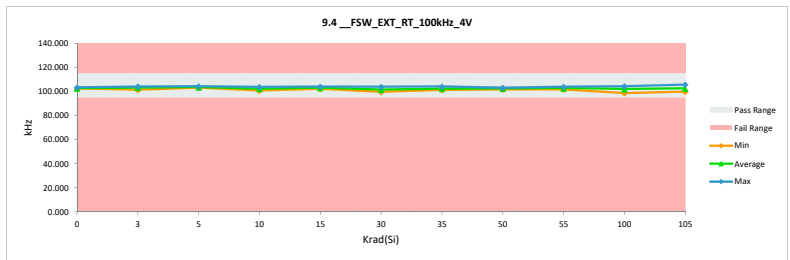
9.4_FSW_EXT_RT_100kHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	115
Min Limit	95

Krad(Si)	Serial #	PRE	POST	Delta
0	137	102.576	102.591	0.015
0	138	103.422	103.444	0.022
0	139	102.453	102.495	0.042
3	197	104.047	104.137	0.090
3	198	103.645	103.668	0.023
3	199	102.743	102.889	0.146
3	202	101.339	101.435	0.096
3	203	103.945	103.897	-0.048
5	204	103.780	103.610	-0.170
5	205	103.279	103.210	-0.069
5	206	104.039	104.082	0.043
5	208	103.738	103.716	-0.022
5	209	104.120	104.328	0.208
10	210	104.088	103.840	-0.248
10	211	101.699	101.983	0.284
10	212	103.770	103.763	-0.007
10	213	100.909	101.186	0.277
10	214	100.835	100.746	-0.089
15	215	102.356	102.573	0.217
15	217	104.103	104.064	-0.039
15	219	102.361	102.243	-0.118
15	220	103.758	103.568	-0.190
15	221	103.330	103.513	0.183
30	222	102.477	102.242	-0.235
30	223	102.028	101.944	-0.084
30	224	99.778	99.747	-0.031
30	225	100.648	100.315	-0.333
30	226	104.240	103.980	-0.260
35	227	102.813	102.661	-0.152
35	228	104.264	104.247	-0.017
35	229	102.336	101.956	-0.380
35	230	101.995	101.289	-0.706
35	231	102.527	101.851	-0.676
50	170	103.029	102.933	-0.096
50	172	103.092	103.143	0.051
50	173	102.364	102.090	-0.274
50	175	102.901	102.844	-0.057
50	176	101.986	101.731	-0.255
55	177	102.131	102.290	0.159
55	178	100.208	101.773	1.565
55	179	103.422	103.032	-0.390
55	180	104.437	103.918	-0.519
55	181	103.512	103.329	-0.183
100	154	102.113	101.908	-0.205
100	155	101.746	101.725	-0.021
100	156	101.527	101.437	-0.090
100	157	101.650	101.723	0.073
100	158	102.796	102.401	-0.395
100	159	103.459	103.716	0.257
100	160	101.783	101.877	0.094
100	161	101.646	101.410	-0.236
100	162	97.937	98.699	0.762
100	163	103.573	103.491	-0.082
100	164	100.416	101.295	0.879
100	165	100.812	100.571	-0.241
100	166	105.039	104.430	-0.609
100	168	100.741	100.548	-0.193
100	169	102.124	102.489	0.365
100	182	101.696	101.352	-0.344
100	183	102.763	102.579	-0.184
100	184	103.224	103.517	0.293
100	185	102.314	102.546	0.232
100	186	100.891	101.717	0.826
100	187	101.976	102.146	0.170
100	188	103.452	103.169	-0.283
100	189	103.648	103.325	-0.323
100	190	101.832	101.924	0.092
105	191	105.524	105.776	0.252
105	192	99.608	100.262	0.654
105	193	103.847	104.103	0.256
105	194	101.047	99.897	-1.150
105	195	100.653	100.159	-0.494
105	196	105.014	105.487	0.473
Max		105.524	105.776	1.565
Average		102.539	102.520	-0.019
Min		97.937	98.699	-1.150
Std Dev		1.408	1.362	0.386



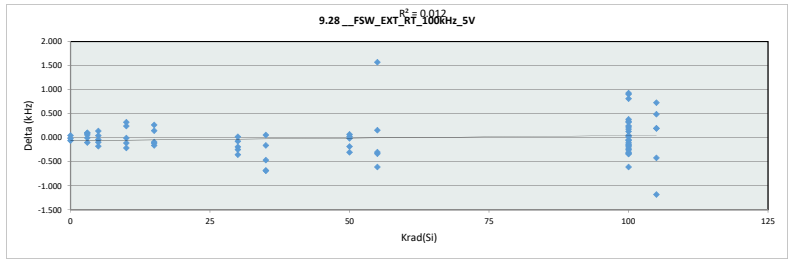
9.4_FSW_EXT_R	
Test Site	
Tester	
Test Number	
Max Limit	115 kHz
Min Limit	95 kHz

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.495	101.435	103.210	100.746	102.243	99.747	101.289	101.731	101.773	98.699	99.897
Average	102.843	103.205	103.789	102.304	103.192	101.646	102.401	102.548	102.868	102.083	102.614
Max	103.444	104.137	104.328	103.840	104.064	103.980	104.247	103.143	103.918	104.430	105.776
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

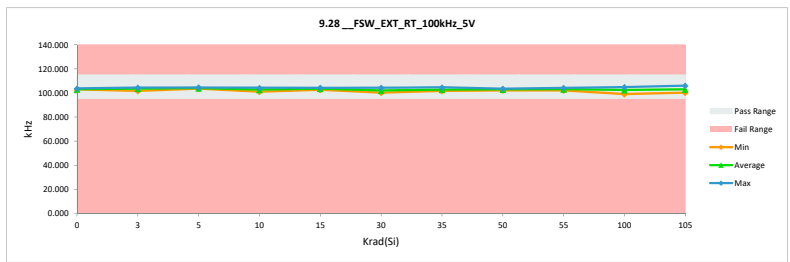


# HDR TID Report TPS7H5003-SP

9.28_FSW_EXT_RT_100kHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	115	115		
Min Limit	95	95		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	102.892	102.838	-0.054
0	138	103.761	103.753	-0.008
0	139	102.780	102.830	0.050
3	197	104.406	104.507	0.101
3	198	104.008	104.010	0.002
3	199	103.129	103.238	0.109
3	202	101.649	101.714	0.065
3	203	104.335	104.241	-0.094
5	204	104.126	103.954	-0.172
5	205	103.605	103.515	-0.090
5	206	104.378	104.427	0.049
5	208	104.095	104.062	-0.033
5	209	104.491	104.636	0.145
10	210	104.462	104.254	-0.208
10	211	102.106	102.353	0.247
10	212	104.110	104.117	-0.002
10	213	101.256	101.584	0.328
10	214	101.182	101.077	-0.105
15	215	102.665	102.937	0.272
15	217	104.483	104.399	-0.084
15	219	102.690	102.580	-0.110
15	220	104.111	103.954	-0.157
15	221	103.707	103.858	0.151
30	222	102.798	102.614	-0.184
30	223	102.396	102.328	-0.068
30	224	100.155	100.180	0.025
30	225	100.988	100.641	-0.347
30	226	104.601	104.358	-0.243
35	227	103.181	103.026	-0.155
35	228	104.619	104.679	0.060
35	229	102.665	102.203	-0.462
35	230	102.341	101.664	-0.677
35	231	102.885	102.202	-0.683
50	170	103.350	103.342	-0.008
50	172	103.449	103.522	0.073
50	173	102.728	102.428	-0.300
50	175	103.254	103.286	0.032
50	176	102.325	102.145	-0.180
55	177	102.461	102.623	0.162
55	178	100.546	102.121	1.575
55	179	103.723	103.395	-0.328
55	180	104.821	104.217	-0.604
55	181	103.833	103.839	-0.294
100	154	102.471	102.315	-0.156
100	155	102.089	102.113	0.024
100	156	101.827	101.773	-0.054
100	157	102.047	102.100	0.053
100	158	103.118	102.783	-0.335
100	159	103.793	104.140	0.347
100	160	102.150	102.334	0.184
100	161	102.029	101.731	-0.298
100	162	98.240	99.056	0.816
100	163	103.886	103.846	-0.040
100	164	100.728	101.661	0.933
100	165	101.151	100.924	-0.227
100	166	105.496	104.891	-0.605
100	168	101.051	100.920	-0.131
100	169	102.423	102.810	0.387
100	182	102.022	101.773	-0.249
100	183	103.087	102.986	-0.101
100	184	103.563	103.890	0.327
100	185	102.620	102.871	0.251
100	186	101.160	102.069	0.909
100	187	102.362	102.588	0.226
100	188	103.796	103.609	-0.187
100	189	103.998	103.673	-0.325
100	190	102.198	102.332	0.134
105	191	105.874	106.076	0.202
105	192	99.916	100.648	0.732
105	193	104.221	104.417	0.196
105	194	101.357	100.178	-1.179
105	195	100.925	100.509	-0.416
105	196	105.333	105.826	0.493
Max		105.874	106.076	1.575
Average		102.885	102.881	-0.004
Min		98.240	99.056	-1.179
Std Dev		1.421	1.360	0.395

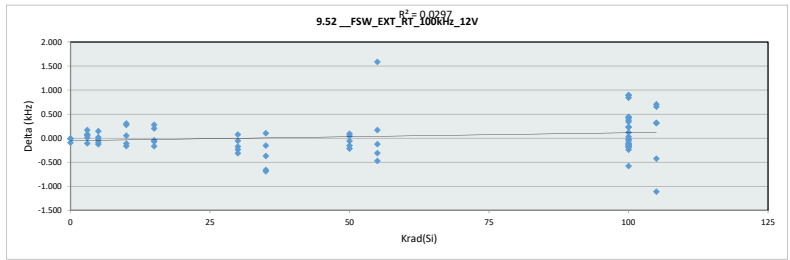


9.28_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	115	kHz									
Min Limit	95	kHz									
Krad(Si)	0	5	10	15	30	35	50	55	100	105	
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	
Min	102.830	101.714	103.515	101.077	102.580	100.180	101.664	102.145	102.121	99.056	
Average	103.140	103.542	104.119	102.677	103.546	102.024	102.755	102.945	103.199	102.466	
Max	103.753	104.507	104.636	104.254	104.399	104.358	104.679	103.522	104.217	104.891	
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	

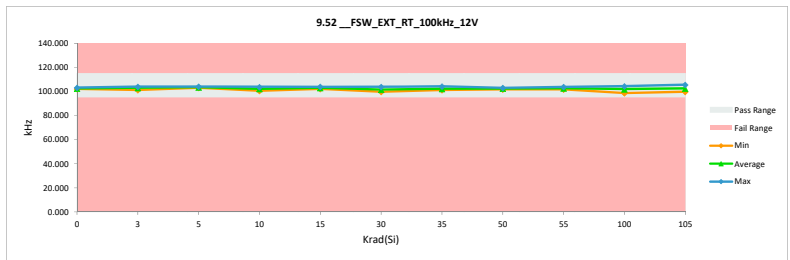


# HDR TID Report TPS7H5003-SP

9.52_FSW_EXT_RT_100kHz_12V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	115	115		
Min Limit	95	95		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	102.429	102.422	-0.007
0	138	103.292	103.293	0.001
0	139	102.336	102.250	-0.086
3	197	104.000	104.064	0.064
3	198	103.555	103.570	0.015
3	199	102.667	102.844	0.177
3	202	101.217	101.307	0.090
3	203	103.874	103.773	-0.101
5	204	103.626	103.506	-0.120
5	205	103.155	103.070	-0.085
5	206	103.904	103.931	0.027
5	208	103.643	103.621	-0.022
5	209	104.030	104.184	0.154
10	210	104.007	103.851	-0.156
10	211	101.661	101.943	0.282
10	212	103.654	103.715	0.061
10	213	100.843	101.156	0.313
10	214	100.768	100.665	-0.103
15	215	102.250	102.535	0.285
15	217	104.029	103.993	-0.036
15	219	102.249	102.185	-0.064
15	220	103.702	103.543	-0.159
15	221	103.254	103.463	0.209
30	222	102.399	102.234	-0.165
30	223	101.993	101.946	-0.047
30	224	99.732	99.814	0.082
30	225	100.593	100.286	-0.307
30	226	104.205	103.976	-0.229
35	227	102.764	102.616	-0.148
35	228	104.211	104.319	0.108
35	229	102.223	101.860	-0.363
35	230	101.946	101.263	-0.683
35	231	102.441	101.788	-0.653
50	170	102.972	102.923	-0.049
50	172	102.972	103.070	0.098
50	173	102.264	102.055	-0.209
50	175	102.805	102.859	0.054
50	176	101.919	101.722	-0.147
55	177	102.030	102.207	0.177
55	178	100.181	101.773	1.592
55	179	103.258	102.955	-0.303
55	180	104.329	103.863	-0.466
55	181	103.464	103.347	-0.117
100	154	102.080	101.921	-0.159
100	155	101.667	101.707	0.040
100	156	101.396	101.386	-0.010
100	157	101.590	101.717	0.127
100	158	102.669	102.436	-0.233
100	159	103.340	103.723	0.383
100	160	101.684	101.922	0.238
100	161	101.576	101.407	-0.169
100	162	97.847	98.695	0.848
100	163	103.491	103.472	-0.019
100	164	100.342	101.247	0.905
100	165	100.700	100.553	-0.147
100	166	105.035	104.461	-0.574
100	168	100.650	100.552	-0.098
100	169	102.029	102.474	0.445
100	182	101.554	101.437	-0.117
100	183	102.684	102.635	-0.049
100	184	103.101	103.550	0.449
100	185	102.074	102.500	0.426
100	186	100.756	101.654	0.898
100	187	101.936	102.281	0.345
100	188	103.373	103.252	-0.121
100	189	103.507	103.321	-0.186
100	190	101.781	102.017	0.236
105	191	105.357	105.677	0.320
105	192	99.556	100.268	0.712
105	193	103.735	104.061	0.326
105	194	100.970	99.867	-1.103
105	195	100.537	100.117	-0.420
105	196	104.908	105.570	0.662
	Max	105.357	105.677	1.592
	Average	102.449	102.489	0.040
	Min	97.847	98.695	-1.103
	Std Dev	1.403	1.352	0.390

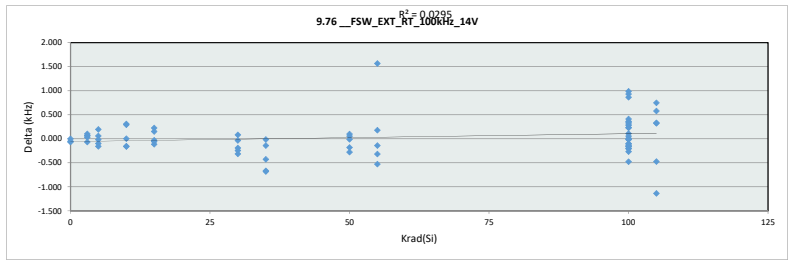


9.52_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	115	kHz									
Min Limit	95	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.250	101.307	103.070	100.665	102.185	99.814	101.263	101.772	101.773	98.695	99.867
Average	102.655	103.112	103.662	102.266	103.144	101.651	102.369	102.536	102.829	102.097	102.593
Max	103.293	104.064	104.184	103.851	103.993	103.976	104.319	103.070	103.863	104.461	105.677
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

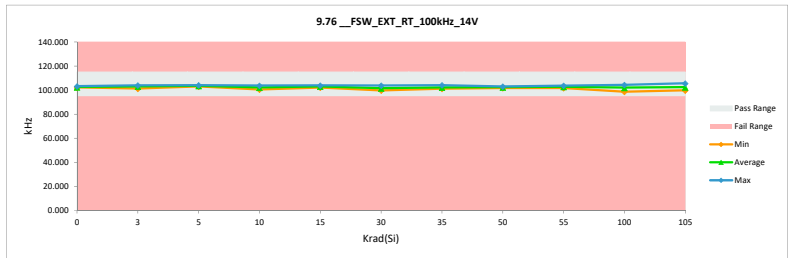


HDR TID Report  
TPS7H5003-SP

9.76_FSW_EXT_RT_100kHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	115	115		
Min Limit	95	95		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	102.450	102.405	-0.045
0	138	103.294	103.300	0.006
0	139	102.316	102.253	-0.063
3	197	103.968	104.074	0.106
3	198	103.572	103.606	0.034
3	199	102.714	102.785	0.071
3	202	101.252	101.321	0.069
3	203	103.806	103.744	-0.062
5	204	103.612	103.461	-0.151
5	205	103.141	103.048	-0.093
5	206	103.898	103.964	0.066
5	208	103.617	103.604	-0.013
5	209	104.007	104.209	0.202
10	210	103.996	103.842	-0.154
10	211	101.663	101.960	0.297
10	212	103.666	103.674	0.008
10	213	100.842	101.157	0.315
10	214	100.761	100.612	-0.149
15	215	102.256	102.486	0.230
15	217	104.019	103.967	-0.052
15	219	102.249	102.224	-0.025
15	220	103.691	103.582	-0.109
15	221	103.251	103.410	0.159
30	222	102.399	102.215	-0.184
30	223	101.973	101.944	-0.029
30	224	99.738	99.827	0.089
30	225	100.587	100.277	-0.310
30	226	104.185	103.945	-0.240
35	227	102.745	102.609	-0.136
35	228	104.215	104.206	-0.009
35	229	102.237	101.814	-0.423
35	230	101.936	101.262	-0.674
35	231	102.455	101.794	-0.661
50	170	102.927	102.921	-0.006
50	172	102.971	103.074	0.103
50	173	102.307	102.035	-0.272
50	175	102.800	102.862	0.062
50	176	101.921	101.746	-0.175
55	177	102.032	102.215	0.183
55	178	100.191	101.764	1.573
55	179	103.254	102.943	-0.311
55	180	104.305	103.785	-0.520
55	181	103.448	103.314	-0.134
100	154	102.045	102.037	-0.012
100	155	101.647	101.707	0.060
100	156	101.408	101.402	-0.006
100	157	101.618	101.732	0.114
100	158	102.697	102.436	-0.261
100	159	103.386	103.738	0.352
100	160	101.678	101.914	0.236
100	161	101.557	101.401	-0.156
100	162	97.840	98.711	0.871
100	163	103.472	103.486	0.014
100	164	100.332	101.324	0.992
100	165	100.723	100.528	-0.195
100	166	104.929	104.458	-0.471
100	168	100.665	100.571	-0.094
100	169	102.011	102.428	0.417
100	182	101.644	101.486	-0.158
100	183	102.680	102.577	-0.103
100	184	103.107	103.475	0.368
100	185	102.156	102.471	0.315
100	186	100.738	101.675	0.937
100	187	101.956	102.239	0.283
100	188	103.365	103.230	-0.135
100	189	103.494	103.292	-0.202
100	190	101.757	101.991	0.234
105	191	105.373	105.700	0.327
105	192	99.541	100.293	0.752
105	193	103.730	104.066	0.336
105	194	100.983	99.853	-1.130
105	195	100.556	100.088	-0.468
105	196	104.874	105.458	0.584
Max		105.373	105.700	1.573
Average		102.447	102.479	0.033
Min		97.840	98.711	-1.130
Std Dev		1.395	1.342	0.393

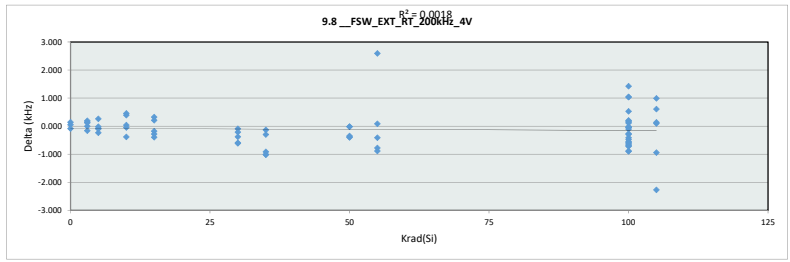


9.76_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	115	kHz									
Min Limit	95	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.253	101.321	103.048	100.612	102.224	99.827	101.262	101.746	101.764	98.711	99.853
Average	102.653	103.106	103.657	102.249	103.134	101.642	102.337	102.528	102.804	102.096	102.576
Max	103.300	104.074	104.209	103.842	103.967	103.945	104.206	103.074	103.785	104.458	105.700
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

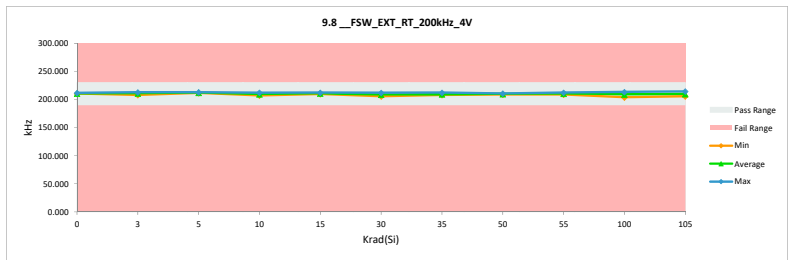


HDR TID Report  
TPS7H5003-SP

9.8_FSW_EXT_RT_200kHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	210.766	210.692	-0.074
0	138	211.742	211.897	0.155
0	139	210.699	210.763	0.064
3	197	212.770	212.927	0.157
3	198	211.603	211.615	0.012
3	199	210.863	211.067	0.204
3	202	207.977	208.106	0.129
3	203	212.578	212.430	-0.148
5	204	212.188	211.963	-0.225
5	205	211.786	211.697	-0.089
5	206	212.428	212.419	-0.009
5	208	212.597	212.530	-0.067
5	209	212.892	213.167	0.275
10	210	212.705	212.331	-0.374
10	211	209.233	209.638	0.405
10	212	212.229	212.280	0.051
10	213	207.803	208.269	0.466
10	214	207.392	207.357	-0.035
15	215	209.486	209.819	0.333
15	217	212.672	212.505	-0.167
15	219	210.344	210.076	-0.268
15	220	212.152	211.770	-0.382
15	221	211.633	211.854	0.221
30	222	210.224	209.856	-0.368
30	223	210.273	210.071	-0.202
30	224	206.057	205.978	-0.079
30	225	207.444	206.860	-0.584
30	226	212.826	212.225	-0.601
35	227	210.260	209.975	-0.285
35	228	212.484	212.363	-0.121
35	229	209.845	208.940	-0.905
35	230	209.163	208.164	-0.999
35	231	210.326	209.322	-1.004
50	170	211.079	210.739	-0.340
50	172	210.999	210.990	-0.009
50	173	210.242	209.871	-0.371
50	175	210.547	210.537	-0.010
50	176	209.513	209.121	-0.392
55	177	210.030	210.126	0.096
55	178	206.513	209.120	2.607
55	179	211.562	210.688	-0.874
55	180	213.147	212.383	-0.764
55	181	211.573	211.173	-0.400
100	154	209.237	209.146	-0.091
100	155	208.887	208.485	-0.402
100	156	208.585	208.128	-0.457
100	157	208.880	208.852	-0.028
100	158	210.750	209.862	-0.888
100	159	211.380	211.593	0.213
100	160	208.963	209.120	0.157
100	161	208.780	208.249	-0.531
100	162	202.990	204.035	1.045
100	163	212.166	211.910	-0.256
100	164	206.641	208.079	1.438
100	165	207.549	206.866	-0.683
100	166	214.433	213.559	-0.874
100	168	207.491	206.899	-0.592
100	169	209.520	210.061	0.541
100	182	209.170	208.519	-0.651
100	183	211.133	210.852	-0.281
100	184	211.598	211.790	0.192
100	185	210.244	210.367	0.123
100	186	208.075	209.130	1.055
100	187	209.266	209.227	-0.039
100	188	211.920	211.342	-0.578
100	189	211.857	211.148	-0.709
100	190	209.476	209.470	-0.006
105	191	214.536	214.685	0.149
105	192	205.808	206.811	1.003
105	193	212.307	212.415	0.108
105	194	208.336	206.077	-2.259
105	195	206.919	205.988	-0.931
105	196	214.018	214.638	0.620
	Max	214.536	214.685	2.607
	Average	210.295	210.178	-0.118
	Min	202.990	204.035	-2.259
	Std Dev	2.206	2.143	0.637

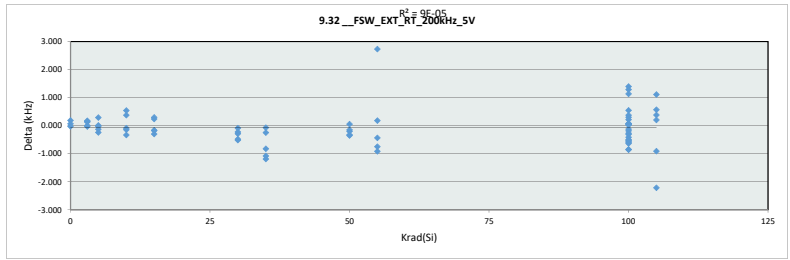


9.8_FSW_EXT_RT_200kHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	210.692	208.106	211.697	207.357	209.819	205.978	208.164	209.121	209.120	204.035	205.988
Average	211.117	211.229	212.355	209.975	211.205	208.998	209.753	210.252	210.698	209.445	210.102
Max	211.897	212.927	213.167	212.331	212.505	212.225	212.363	210.990	212.383	213.559	214.685
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

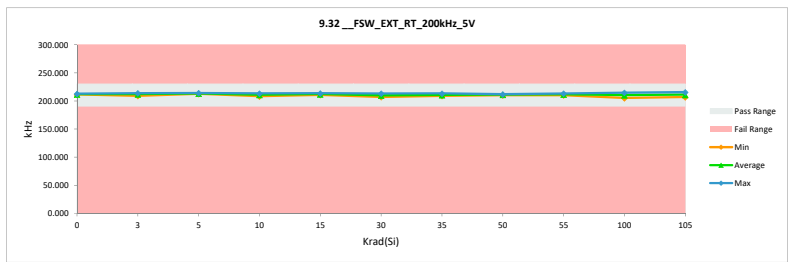


HDR TID Report  
TPS7H5003-SP

9.32_FSW_EXT_RT_200kHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	211.530	211.515	-0.015
0	138	212.719	212.917	0.198
0	139	211.608	211.683	0.075
3	197	213.655	213.837	0.182
3	198	212.607	212.621	0.014
3	199	211.974	212.122	0.148
3	202	208.910	209.053	0.143
3	203	213.433	213.409	-0.024
5	204	213.149	212.916	-0.233
5	205	212.747	212.626	-0.121
5	206	213.372	213.393	0.021
5	208	213.498	213.480	-0.018
5	209	213.926	214.225	0.299
10	210	213.746	213.421	-0.325
10	211	210.328	210.711	0.383
10	212	213.363	213.284	-0.079
10	213	208.737	209.288	0.551
10	214	208.469	208.334	-0.135
15	215	210.457	210.762	0.305
15	217	213.851	213.690	-0.161
15	219	211.319	211.157	-0.162
15	220	213.135	212.843	-0.292
15	221	212.692	212.939	0.247
30	222	211.239	210.958	-0.281
30	223	211.287	211.073	-0.214
30	224	207.075	206.995	-0.080
30	225	208.368	207.898	-0.470
30	226	213.837	213.332	-0.505
35	227	211.330	211.089	-0.241
35	228	213.488	213.424	-0.064
35	229	210.704	209.888	-0.816
35	230	210.310	209.130	-1.180
35	231	211.462	210.392	-1.070
50	170	212.001	211.857	-0.144
50	172	211.977	212.042	0.065
50	173	211.170	210.854	-0.316
50	175	211.781	211.587	-0.194
50	176	210.500	210.158	-0.342
55	177	210.999	211.187	0.188
55	178	207.424	210.166	2.742
55	179	212.445	211.704	-0.741
55	180	214.247	213.342	-0.905
55	181	212.629	212.203	-0.426
100	154	210.185	210.237	0.052
100	155	209.826	209.525	-0.301
100	156	209.582	209.320	-0.262
100	157	209.912	209.982	0.070
100	158	211.690	210.840	-0.850
100	159	212.338	212.723	0.385
100	160	210.001	210.223	0.222
100	161	209.767	209.218	-0.549
100	162	203.939	205.237	1.298
100	163	213.117	213.005	-0.112
100	164	207.622	209.025	1.403
100	165	208.463	207.834	-0.629
100	166	215.518	214.683	-0.835
100	168	208.484	208.078	-0.406
100	169	210.378	210.933	0.555
100	182	210.165	209.655	-0.510
100	183	212.057	211.880	-0.177
100	184	212.549	212.861	0.312
100	185	211.205	211.283	0.078
100	186	208.980	210.123	1.143
100	187	210.242	210.329	0.087
100	188	212.957	212.470	-0.487
100	189	212.740	212.156	-0.584
100	190	210.513	210.543	0.030
105	191	215.490	215.703	0.213
105	192	206.752	207.873	1.121
105	193	213.307	213.695	0.388
105	194	209.279	207.071	-2.208
105	195	207.863	206.960	-0.903
105	196	215.000	215.580	0.580
Max		215.518	215.703	2.742
Average		211.280	211.213	-0.067
Min		203.939	205.237	-2.208
Std Dev		2.218	2.140	0.651



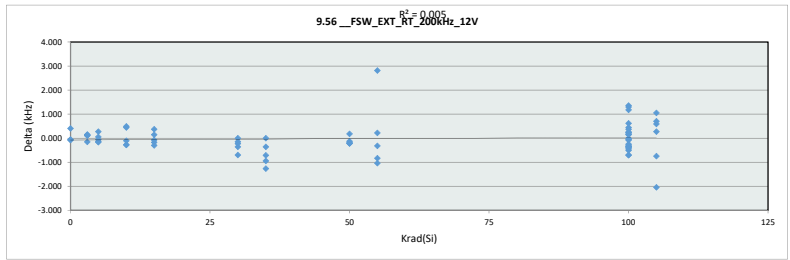
9.32_FSW_EXT										
Test Site										
Tester										
Test Number										
Max Limit	230	kHz								
Min Limit	190	kHz								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	211.515	209.053	212.626	208.334	210.762	206.995	209.130	210.158	210.166	205.237
Average	212.038	212.208	213.328	211.008	212.278	210.051	210.785	211.300	211.720	210.507
Max	212.917	213.837	214.225	213.421	213.690	213.332	213.424	212.042	213.342	214.683
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000



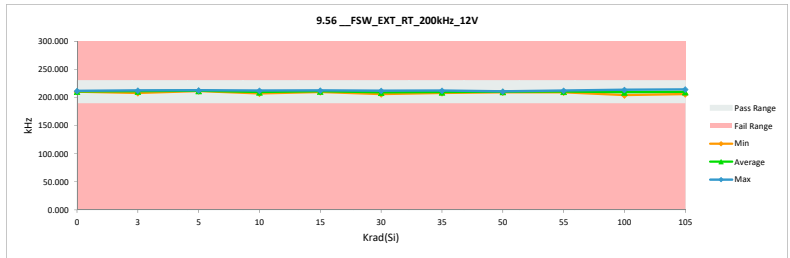


# HDR TID Report TPS7H5003-SP

9.56_FSW_EXT_RT_200kHz_12V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	210.495	210.457	-0.038
0	138	211.566	211.983	0.417
0	139	210.647	210.580	-0.067
3	197	212.658	212.788	0.130
3	198	211.570	211.688	0.118
3	199	210.977	211.122	0.145
3	202	207.922	208.089	0.167
3	203	212.471	212.331	-0.140
5	204	211.986	211.835	-0.151
5	205	211.705	211.579	-0.126
5	206	212.293	212.361	0.068
5	208	212.355	212.305	-0.050
5	209	212.846	213.134	0.288
10	210	212.679	212.411	-0.268
10	211	209.350	209.809	0.459
10	212	212.315	212.052	-0.263
10	213	207.785	208.292	0.507
10	214	207.469	207.372	-0.097
15	215	209.432	209.819	0.387
15	217	212.719	212.661	-0.058
15	219	210.302	210.139	-0.163
15	220	212.158	211.868	-0.290
15	221	211.637	211.798	0.161
30	222	210.264	210.053	-0.211
30	223	210.324	210.191	-0.133
30	224	206.134	206.148	0.014
30	225	207.442	207.095	-0.347
30	226	212.842	212.155	-0.687
35	227	210.313	209.968	-0.345
35	228	212.483	212.498	0.015
35	229	209.716	209.020	-0.696
35	230	209.331	208.083	-1.248
35	231	210.464	209.533	-0.931
50	170	210.930	210.725	-0.205
50	172	210.869	211.063	0.194
50	173	210.140	209.938	-0.202
50	175	210.709	210.598	-0.111
50	176	209.447	209.273	-0.174
55	177	209.951	210.180	0.229
55	178	206.419	209.248	2.829
55	179	211.673	210.648	-1.025
55	180	213.162	212.342	-0.820
55	181	211.612	211.306	-0.306
100	154	209.278	209.283	0.005
100	155	208.887	208.550	-0.337
100	156	208.551	208.318	-0.233
100	157	208.852	209.087	0.235
100	158	210.749	210.057	-0.692
100	159	211.413	211.796	0.383
100	160	209.085	209.355	0.270
100	161	208.835	208.400	-0.435
100	162	202.972	204.282	1.310
100	163	212.171	212.134	-0.037
100	164	206.653	208.021	1.368
100	165	207.424	206.934	-0.490
100	166	214.430	213.747	-0.683
100	168	207.486	207.211	-0.275
100	169	209.459	210.092	0.633
100	182	209.133	208.811	-0.322
100	183	211.084	211.017	-0.067
100	184	211.513	211.972	0.459
100	185	210.175	210.344	0.169
100	186	208.039	209.227	1.188
100	187	209.262	209.420	0.158
100	188	211.860	211.508	-0.352
100	189	211.766	211.377	-0.389
100	190	209.508	209.750	0.242
105	191	214.430	214.717	0.287
105	192	205.736	206.802	1.066
105	193	212.259	212.864	0.605
105	194	208.288	206.261	-2.027
105	195	206.909	206.179	-0.730
105	196	213.918	214.633	0.715
	Max	214.430	214.717	2.829
	Average	210.270	210.256	-0.014
	Min	202.972	204.282	-2.027
	Std Dev	2.191	2.102	0.643

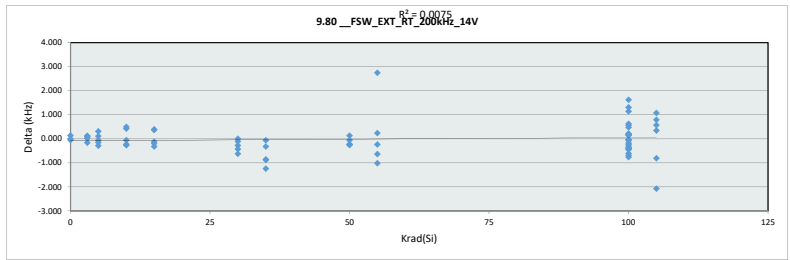


9.56_FSW_EXT										
Test Site										
Tester										
Test Number										
Max Limit	230	kHz								
Min Limit	190	kHz								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	210.457	208.089	211.579	207.372	209.819	206.148	208.083	209.273	209.248	204.282
Average	211.007	211.204	212.243	209.987	211.257	209.128	209.820	210.319	210.745	209.612
Max	211.983	212.788	213.134	212.411	212.661	212.155	212.498	211.063	212.342	213.747
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

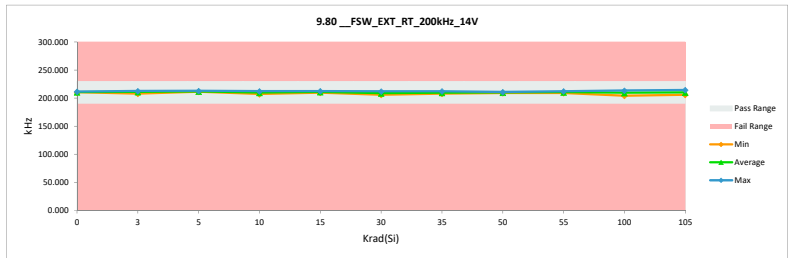


HDR TID Report  
TPS7H5003-SP

9.80_FSW_EXT_RT_200kHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	210.516	210.477	-0.039
0	138	211.600	211.746	0.146
0	139	210.592	210.598	0.006
3	197	212.623	212.755	0.132
3	198	211.615	211.641	0.026
3	199	210.971	211.081	0.110
3	202	207.984	208.078	0.094
3	203	212.447	212.297	-0.150
5	204	211.925	211.648	-0.277
5	205	211.698	211.560	-0.138
5	206	212.258	212.377	0.119
5	208	212.349	212.307	-0.042
5	209	212.829	213.151	0.322
10	210	212.653	212.433	-0.220
10	211	209.326	209.760	0.434
10	212	212.352	212.095	-0.257
10	213	207.980	208.285	0.505
10	214	207.438	207.391	-0.047
15	215	209.380	209.779	0.399
15	217	212.766	212.657	-0.109
15	219	210.297	210.115	-0.182
15	220	212.158	211.844	-0.314
15	221	211.617	211.999	0.382
30	222	210.271	210.005	-0.266
30	223	210.298	210.197	-0.101
30	224	206.129	206.147	0.018
30	225	207.444	207.028	-0.416
30	226	212.806	212.197	-0.609
35	227	210.301	209.988	-0.313
35	228	212.475	212.425	-0.050
35	229	209.731	208.883	-0.848
35	230	209.298	208.066	-1.232
35	231	210.460	209.594	-0.866
50	170	210.967	210.754	-0.213
50	172	210.893	211.037	0.144
50	173	210.151	209.913	-0.238
50	175	210.602	210.556	-0.046
50	176	209.450	209.209	-0.241
55	177	209.953	210.199	0.246
55	178	206.434	209.195	2.761
55	179	211.619	210.613	-1.006
55	180	213.044	212.418	-0.626
55	181	211.534	211.312	-0.222
100	154	209.236	209.219	-0.017
100	155	208.814	208.596	-0.218
100	156	208.604	208.441	-0.163
100	157	208.942	209.133	0.191
100	158	210.707	210.032	-0.675
100	159	211.392	211.964	0.572
100	160	209.133	209.337	0.204
100	161	208.749	208.343	-0.406
100	162	202.743	204.374	1.631
100	163	212.141	212.116	-0.025
100	164	206.647	207.970	1.323
100	165	207.514	206.916	-0.598
100	166	214.457	213.710	-0.747
100	168	207.521	207.227	-0.294
100	169	209.465	210.101	0.636
100	182	209.168	208.820	-0.348
100	183	211.087	211.031	-0.056
100	184	211.461	211.950	0.489
100	185	210.137	210.292	0.155
100	186	207.998	209.154	1.156
100	187	209.275	209.455	0.180
100	188	211.950	211.556	-0.394
100	189	211.792	211.356	-0.436
100	190	209.470	209.701	0.231
105	191	214.361	214.716	0.355
105	192	205.772	206.862	1.090
105	193	212.209	212.793	0.584
105	194	208.309	206.247	-2.062
105	195	206.934	206.137	-0.797
105	196	213.914	214.715	0.801
	Max	214.457	214.716	2.761
	Average	210.259	210.248	-0.012
	Min	202.743	204.374	-2.062
	Std Dev	2.191	2.101	0.652

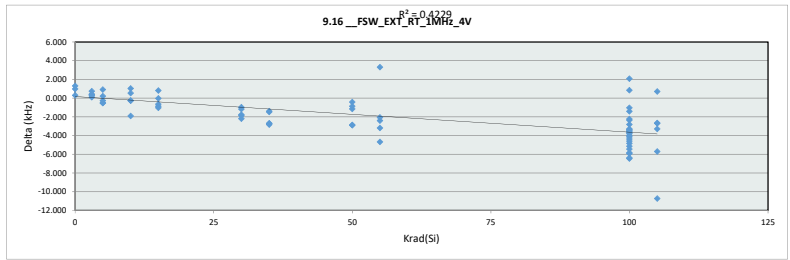


9.80_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	210.477	208.078	211.560	207.391	209.779	206.147	208.066	209.209	209.195	204.374	206.137
Average	210.940	211.170	212.209	209.993	211.279	209.115	209.791	210.294	210.747	209.616	210.245
Max	211.746	212.755	213.151	212.433	212.657	212.197	212.425	211.037	212.418	213.710	214.716
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

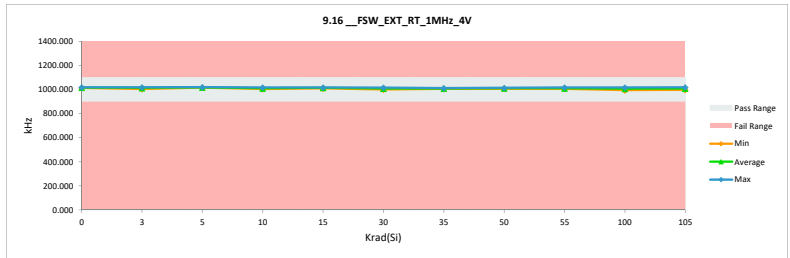


# HDR TID Report TPS7H5003-SP

9.16_FSW_EXT_RT_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	1100	1100		
Min Limit	900	900		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1014.067	1014.403	0.336
0	138	1014.888	1016.225	1.337
0	139	1018.431	1019.443	1.012
3	197	1019.144	1019.587	0.443
3	198	1014.844	1014.963	0.119
3	199	1015.106	1015.515	0.409
3	202	1004.316	1005.086	0.770
3	203	1017.227	1017.532	0.305
5	204	1017.993	1017.505	-0.488
5	205	1019.283	1019.047	-0.236
5	206	1017.161	1016.677	-0.484
5	208	1020.071	1020.312	0.241
5	209	1015.105	1016.058	0.953
10	210	1014.739	1012.866	-1.873
10	211	1011.096	1012.161	1.065
10	212	1017.744	1017.510	-0.234
10	213	1006.708	1007.287	0.579
10	214	1004.729	1004.501	-0.228
15	215	1009.235	1009.243	0.008
15	217	1018.696	1018.081	-0.615
15	219	1013.609	1012.785	-0.824
15	220	1016.363	1015.363	-1.000
15	221	1017.431	1018.272	0.841
30	222	1012.503	1011.573	-0.930
30	223	1015.199	1013.464	-1.735
30	224	1001.114	999.971	-1.143
30	225	1005.635	1003.786	-1.849
30	226	1018.185	1016.024	-2.161
35	227	1007.895	1006.454	-1.441
35	228	1013.502	1012.135	-1.367
35	229	1010.598	1007.797	-2.801
35	230	1009.442	1006.756	-2.686
35	231	1012.670	1010.011	-2.659
50	170	1012.442	1009.600	-2.842
50	172	1015.748	1014.633	-1.115
50	173	1013.390	1013.009	-0.381
50	175	1014.381	1013.548	-0.833
50	176	1008.876	1006.005	-2.871
55	177	1015.060	1013.027	-2.033
55	178	1002.809	1006.146	3.337
55	179	1015.426	1010.784	-4.642
55	180	1021.069	1017.904	-3.165
55	181	1013.516	1011.145	-2.371
100	154	1005.634	1006.517	0.883
100	155	1006.796	1003.122	-3.674
100	156	1009.042	1004.234	-4.808
100	157	1009.774	1007.567	-2.207
100	158	1012.167	1006.365	-5.802
100	159	1010.825	1008.043	-2.782
100	160	1008.845	1006.523	-2.322
100	161	1008.993	1004.992	-4.001
100	162	995.655	994.656	-0.999
100	163	1018.617	1015.093	-3.524
100	164	1004.139	1006.255	2.116
100	165	1002.141	996.733	-5.408
100	166	1022.058	1017.715	-4.343
100	168	1008.216	1003.109	-5.107
100	169	1010.605	1007.273	-3.332
100	182	1008.578	1002.219	-6.359
100	183	1018.971	1015.190	-3.781
100	184	1018.287	1014.174	-4.113
100	185	1015.456	1012.199	-3.257
100	186	1009.855	1008.476	-1.379
100	187	1007.364	1002.796	-4.568
100	188	1015.032	1008.602	-6.430
100	189	1013.643	1007.753	-5.890
100	190	1010.526	1007.077	-3.449
105	191	1020.163	1016.903	-3.260
105	192	1004.388	1005.137	0.749
105	193	1015.626	1012.955	-2.671
105	194	1011.476	1000.775	-10.701
105	195	1003.090	997.414	-5.676
105	196	1021.874	1019.241	-2.633
Max		1022.058	1020.312	3.337
Average		1012.401	1010.456	-1.945
Min		995.655	994.656	-10.701
Std Dev		5.561	6.056	2.417



9.16_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	1100	kHz									
Min Limit	900	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1014.403	1005.086	1016.058	1004.501	1009.243	999.971	1006.454	1006.005	1006.146	994.656	997.414
Average	1016.690	1014.537	1017.920	1010.865	1014.749	1008.964	1008.631	1011.359	1011.801	1006.945	1008.738
Max	1019.443	1019.587	1020.312	1017.510	1018.272	1016.024	1012.135	1014.633	1017.904	1017.715	1019.241
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

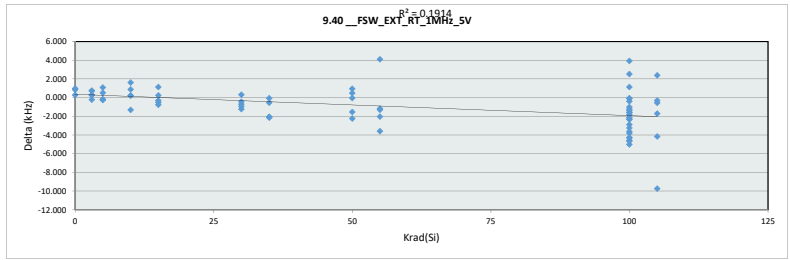


# HDR TID Report

## TPS7H5003-SP

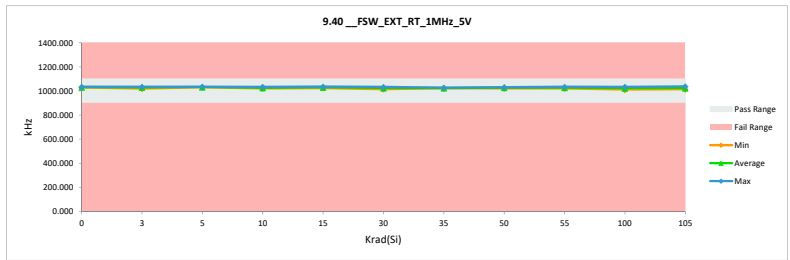
9.40_FSW_EXT_RT_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	1100
Min Limit	900

Krad(Si)	Serial #	PRE	POST	Delta
0	137	1029.099	1029.408	0.309
0	138	1029.777	1030.790	1.013
0	139	1033.253	1034.121	0.868
3	197	1033.639	1034.400	0.761
3	198	1030.501	1030.305	-0.196
3	199	1030.311	1030.632	0.321
3	202	1019.375	1020.150	0.775
3	203	1032.694	1032.993	0.299
5	204	1032.290	1032.134	-0.156
5	205	1035.128	1034.962	-0.166
5	206	1031.768	1031.539	-0.229
5	208	1034.156	1034.704	0.548
5	209	1029.211	1030.327	1.116
10	210	1028.375	1027.085	-1.290
10	211	1027.421	1029.070	1.649
10	212	1033.410	1033.655	0.245
10	213	1020.982	1021.887	0.905
10	214	1020.524	1020.773	0.249
15	215	1024.284	1024.558	0.274
15	217	1034.770	1034.299	-0.471
15	219	1028.994	1028.719	-0.275
15	220	1030.745	1030.015	-0.730
15	221	1034.355	1035.526	1.171
30	222	1028.103	1028.451	0.348
30	223	1029.916	1029.249	-0.667
30	224	1015.495	1015.083	-0.412
30	225	1021.308	1020.108	-1.200
30	226	1034.340	1033.435	-0.905
35	227	1022.626	1022.590	-0.036
35	228	1027.722	1027.212	-0.510
35	229	1026.028	1023.915	-2.113
35	230	1024.771	1022.747	-2.024
35	231	1028.271	1026.209	-2.062
50	170	1026.868	1024.662	-2.206
50	172	1031.315	1031.305	-0.010
50	173	1028.695	1029.679	0.984
50	175	1030.293	1030.807	0.514
50	176	1022.694	1021.194	-1.500
55	177	1030.397	1029.245	-1.152
55	178	1017.092	1021.217	4.125
55	179	1031.224	1027.663	-3.561
55	180	1036.504	1034.506	-1.998
55	181	1028.723	1027.454	-1.269
100	154	1020.465	1023.021	2.556
100	155	1021.639	1019.835	-1.804
100	156	1024.623	1020.845	-3.778
100	157	1024.680	1024.614	-0.066
100	158	1026.721	1022.451	-4.270
100	159	1025.384	1024.179	-1.205
100	160	1024.789	1024.429	-0.360
100	161	1025.186	1022.882	-2.304
100	162	1011.280	1012.449	1.169
100	163	1033.608	1032.251	-1.357
100	164	1019.583	1023.537	3.954
100	165	1015.129	1010.863	-4.266
100	166	1036.869	1034.017	-2.852
100	168	1024.710	1021.106	-3.604
100	169	1024.878	1022.774	-2.104
100	182	1021.249	1016.267	-4.982
100	183	1033.793	1032.225	-1.568
100	184	1033.869	1031.607	-2.262
100	185	1030.604	1029.068	-1.536
100	186	1024.333	1024.299	-0.034
100	187	1022.349	1019.130	-3.219
100	188	1028.722	1024.107	-4.615
100	189	1026.591	1022.043	-4.548
100	190	1024.784	1023.807	-0.977
105	191	1035.234	1033.556	-1.678
105	192	1018.203	1020.610	2.407
105	193	1029.744	1029.211	-0.533
105	194	1027.179	1017.469	-9.710
105	195	1019.132	1015.012	-4.120
105	196	1037.963	1037.682	-0.281
	Max	1037.963	1037.682	4.125
	Average	1027.407	1026.495	-0.912
	Min	1011.280	1010.863	-9.710
	Std Dev	5.721	6.017	2.150



9.40_FSW_EXT	
Test Site	
Tester	
Test Number	
Max Limit	1100 kHz
Min Limit	900 kHz

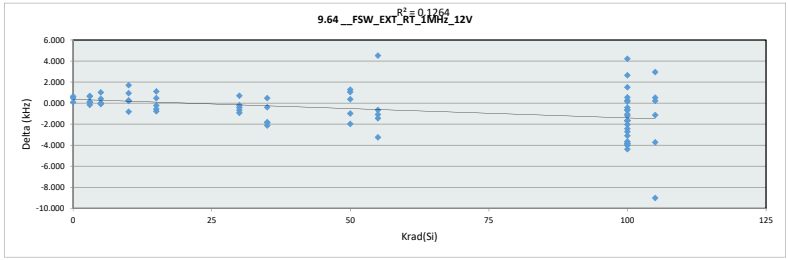
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1029.408	1020.150	1030.327	1020.773	1024.558	1015.083	1022.590	1021.194	1021.217	1010.863	1015.012
Average	1031.440	1029.696	1032.733	1026.494	1030.623	1025.265	1024.535	1027.529	1028.017	1023.409	1025.590
Max	1034.121	1034.400	1034.962	1033.655	1035.526	1033.435	1027.212	1031.305	1034.506	1034.017	1037.682
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000



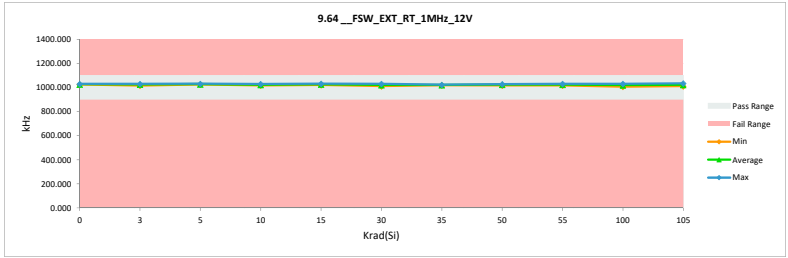
HDR TID Report  
TPS7H5003-SP

		9.64_FSW_EXT_RT_1MHz_12V	
Test Site	Tester		
Test Number	Unit	kHz	kHz
Max Limit	1100	1100	
Min Limit	900	900	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	1025.670	1025.796	0.126
0	138	1026.507	1027.173	0.666
0	139	1029.878	1030.393	0.515
3	197	1029.885	1030.593	0.708
3	198	1026.840	1026.704	-0.136
3	199	1026.845	1027.009	0.164
3	202	1015.866	1016.533	0.667
3	203	1029.340	1029.420	0.080
5	204	1028.327	1028.360	0.033
5	205	1031.662	1031.651	-0.011
5	206	1027.939	1027.880	-0.059
5	208	1030.209	1030.654	0.445
5	209	1025.293	1026.352	1.059
10	210	1024.545	1023.761	-0.784
10	211	1024.094	1025.835	1.741
10	212	1029.974	1030.267	0.293
10	213	1017.501	1018.473	0.972
10	214	1017.148	1017.376	0.228
15	215	1020.623	1021.127	0.504
15	217	1031.307	1030.782	-0.525
15	219	1025.619	1025.430	-0.189
15	220	1027.344	1026.597	-0.747
15	221	1031.328	1032.475	1.147
30	222	1024.657	1025.393	0.736
30	223	1026.394	1026.011	-0.383
30	224	1012.158	1011.999	-0.159
30	225	1017.971	1017.083	-0.888
30	226	1031.289	1030.652	-0.637
35	227	1019.054	1019.558	0.504
35	228	1024.176	1023.813	-0.363
35	229	1022.844	1021.058	-1.786
35	230	1021.585	1019.759	-1.826
35	231	1024.934	1022.842	-2.092
50	170	1022.994	1021.046	-1.948
50	172	1027.831	1028.222	0.391
50	173	1025.027	1026.337	1.310
50	175	1026.845	1027.933	1.088
50	176	1019.089	1018.139	-0.950
55	177	1026.673	1026.060	-0.613
55	178	1013.642	1018.189	4.547
55	179	1027.652	1024.439	-3.213
55	180	1032.956	1031.548	-1.408
55	181	1025.243	1024.200	-1.043
100	154	1017.091	1019.763	2.672
100	155	1018.241	1016.605	-1.636
100	156	1021.262	1017.641	-3.621
100	157	1020.914	1021.495	0.581
100	158	1023.130	1019.335	-3.795
100	159	1021.950	1021.324	-0.626
100	160	1021.476	1021.649	0.173
100	161	1021.903	1019.903	-2.000
100	162	1008.162	1009.703	1.541
100	163	1030.036	1029.398	-0.638
100	164	1016.206	1020.455	4.249
100	165	1011.459	1007.485	-3.974
100	166	1033.180	1030.784	-2.396
100	168	1021.675	1018.602	-3.073
100	169	1021.300	1019.670	-1.630
100	182	1017.444	1013.104	-4.340
100	183	1030.180	1029.166	-1.014
100	184	1030.428	1028.785	-1.643
100	185	1027.125	1025.961	-1.164
100	186	1020.782	1021.069	0.287
100	187	1018.902	1016.219	-2.683
100	188	1024.873	1021.083	-3.790
100	189	1022.819	1018.885	-3.934
100	190	1021.215	1020.821	-0.394
105	191	1031.350	1030.247	-1.103
105	192	1014.664	1017.651	2.987
105	193	1026.110	1026.360	0.250
105	194	1023.633	1014.657	-8.976
105	195	1015.683	1011.986	-3.697
105	196	1034.397	1034.951	0.554
	Max	1034.397	1034.951	4.547
	Average	1023.895	1023.283	-0.612
	Min	1008.162	1007.485	-8.976
	Std Dev	5.681	5.926	2.044

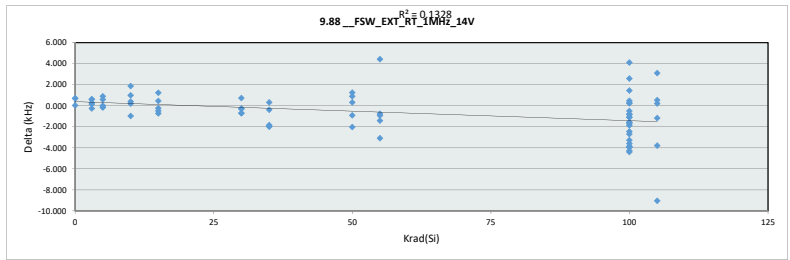


		9.64_FSW_EXT												
Test Site	Tester													
Test Number	Unit	1100	kHz											
Max Limit	900	kHz												
Min Limit	900	kHz												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1025.796	1016.533	1026.352	1017.376	1021.127	1011.999	1019.558	1018.139	1018.189	1007.485	1011.986			
Average	1027.787	1026.052	1028.979	1023.142	1027.282	1022.228	1021.406	1024.335	1024.887	1020.371	1022.642			
Max	1030.393	1030.593	1031.651	1030.267	1032.475	1030.652	1023.813	1028.222	1031.548	1030.784	1034.951			
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

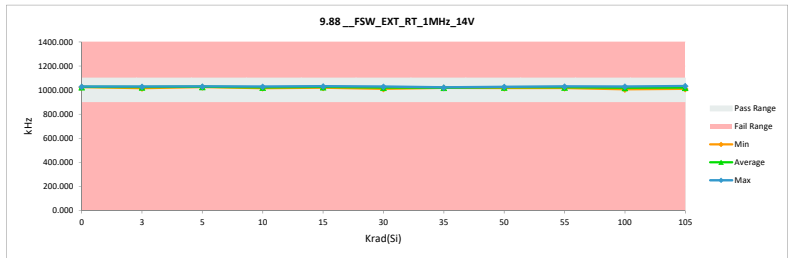


# HDR TID Report TPS7H5003-SP

9.88_FSW_EXT_RT_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	1100	1100		
Min Limit	900	900		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1025.664	1025.715	0.051
0	138	1026.302	1027.028	0.726
0	139	1029.751	1030.477	0.726
3	197	1029.888	1030.511	0.623
3	198	1026.816	1026.586	-0.230
3	199	1026.650	1026.951	0.301
3	202	1015.834	1016.492	0.658
3	203	1029.208	1029.389	0.181
5	204	1028.298	1028.345	0.047
5	205	1031.649	1031.522	-0.127
5	206	1027.808	1027.680	-0.128
5	208	1030.054	1030.676	0.622
5	209	1025.307	1026.238	0.931
10	210	1024.618	1023.678	-0.940
10	211	1023.814	1025.721	1.907
10	212	1029.824	1030.255	0.431
10	213	1017.360	1018.380	1.020
10	214	1017.178	1017.404	0.226
15	215	1020.524	1021.008	0.484
15	217	1031.260	1030.800	-0.460
15	219	1025.528	1025.331	-0.197
15	220	1027.222	1026.512	-0.710
15	221	1031.262	1032.528	1.266
30	222	1024.522	1025.277	0.755
30	223	1026.220	1025.989	-0.231
30	224	1012.093	1011.820	-0.273
30	225	1017.874	1017.183	-0.691
30	226	1031.158	1030.478	-0.680
35	227	1019.089	1019.430	0.341
35	228	1024.108	1023.751	-0.357
35	229	1022.736	1020.922	-1.814
35	230	1021.487	1019.572	-1.915
35	231	1024.847	1022.882	-1.965
50	170	1023.019	1021.024	-1.995
50	172	1027.627	1027.973	0.346
50	173	1024.969	1026.246	1.277
50	175	1026.865	1027.789	0.924
50	176	1019.021	1018.129	-0.892
55	177	1026.710	1025.974	-0.736
55	178	1013.634	1018.085	4.451
55	179	1027.512	1024.454	-3.058
55	180	1032.898	1031.511	-1.387
55	181	1025.104	1024.206	-0.898
100	154	1016.846	1019.465	2.619
100	155	1018.143	1016.601	-1.542
100	156	1021.135	1017.564	-3.571
100	157	1020.971	1021.464	0.493
100	158	1022.989	1019.114	-3.875
100	159	1021.850	1021.082	-0.768
100	160	1021.295	1021.601	0.306
100	161	1021.844	1019.994	-1.850
100	162	1008.177	1009.643	1.466
100	163	1029.902	1029.100	-0.802
100	164	1016.177	1020.317	4.140
100	165	1011.414	1007.481	-3.933
100	166	1033.102	1030.686	-2.416
100	168	1021.667	1018.423	-3.244
100	169	1021.307	1019.751	-1.556
100	182	1017.337	1012.968	-4.369
100	183	1030.190	1029.106	-1.084
100	184	1030.361	1028.673	-1.688
100	185	1026.938	1025.884	-1.054
100	186	1020.693	1020.943	0.250
100	187	1018.851	1016.190	-2.661
100	188	1024.881	1021.036	-3.845
100	189	1022.805	1018.558	-4.247
100	190	1021.083	1020.606	-0.477
105	191	1031.338	1030.200	-1.138
105	192	1014.497	1017.630	3.133
105	193	1026.072	1026.313	0.241
105	194	1023.610	1014.599	-9.011
105	195	1015.635	1011.876	-3.759
105	196	1034.311	1034.869	0.558
Max		1034.311	1034.869	4.451
Average		1023.818	1023.201	-0.617
Min		1008.177	1007.481	-9.011
Std Dev		5.673	5.929	2.052

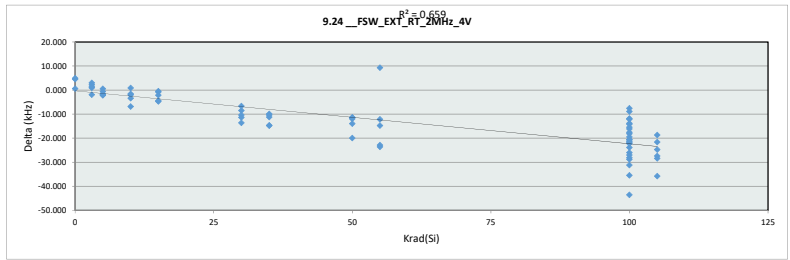


9.88_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	1100	kHz									
Min Limit	900	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1025.715	1016.492	1026.238	1017.404	1021.008	1011.820	1019.430	1018.129	1018.085	1007.481	1011.876
Average	1027.740	1025.986	1028.892	1023.088	1027.236	1022.149	1021.311	1024.232	1024.846	1020.260	1022.581
Max	1030.477	1030.511	1031.522	1030.255	1032.528	1030.478	1023.751	1027.973	1031.511	1030.686	1034.869
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

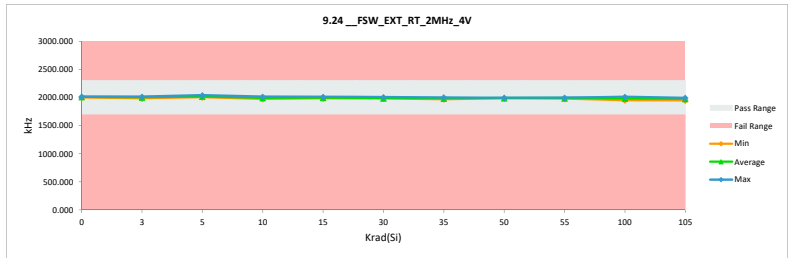


# HDR TID Report TPS7H5003-SP

		9.24_FSW_EXT_RT_2MHz_4V		
Test Site	Tester			
Test Number	Unit	kHz	kHz	
Max Limit	2300	2300		
Min Limit	1700	1700		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	2000.619	2001.381	0.762
0	138	2013.899	2018.966	5.067
0	139	2012.978	2017.626	4.648
3	197	2017.984	2016.200	-1.784
3	198	1997.468	1998.944	1.476
3	199	2014.240	2016.613	2.373
3	202	1984.299	1985.351	1.052
3	203	2006.519	2009.637	3.118
5	204	2012.954	2010.895	-2.059
5	205	2007.174	2006.692	-0.482
5	206	2014.265	2012.745	-1.520
5	208	2028.349	2026.663	-1.686
5	209	2042.353	2043.054	0.701
10	210	2023.418	2016.739	-6.679
10	211	1989.726	1988.306	-1.420
10	212	1999.295	1996.042	-3.253
10	213	2000.856	2001.893	1.037
10	214	1978.159	1976.312	-1.847
15	215	1990.524	1989.887	-0.637
15	217	2009.130	2007.137	-1.993
15	219	2004.486	2000.211	-4.275
15	220	2019.012	2014.456	-4.556
15	221	1985.650	1985.361	-0.289
30	222	1999.451	1988.248	-11.203
30	223	2021.049	2007.548	-13.501
30	224	1994.974	1986.582	-8.392
30	225	1991.210	1984.710	-6.500
30	226	1998.057	1987.803	-10.254
35	227	2001.574	1986.930	-14.644
35	228	2014.490	2003.399	-11.091
35	229	1984.681	1970.104	-14.577
35	230	1992.138	1982.347	-9.791
35	231	1996.587	1986.331	-10.256
50	170	2010.378	1999.105	-11.273
50	172	2007.730	1993.901	-13.829
50	173	2003.451	1991.626	-11.825
50	175	1997.276	1986.075	-11.201
50	176	2017.790	1998.003	-19.787
55	177	2008.450	1993.829	-14.621
55	178	1989.255	1998.749	9.494
55	179	2003.036	1980.287	-22.749
55	180	2022.177	1998.691	-23.486
55	181	1996.463	1984.467	-11.996
100	154	1998.484	1991.029	-7.455
100	155	1994.821	1974.447	-20.374
100	156	1988.697	1974.665	-14.032
100	157	2013.535	1984.910	-28.625
100	158	2017.355	1990.469	-26.886
100	159	2015.824	2004.111	-11.713
100	160	1979.432	1961.452	-17.980
100	161	1993.390	1971.246	-22.144
100	162	1970.101	1952.709	-17.392
100	163	2016.762	1990.872	-25.890
100	164	1978.210	1969.419	-8.791
100	165	2017.966	1986.932	-31.034
100	166	2033.231	2013.887	-19.344
100	168	1981.051	1965.013	-16.038
100	169	2002.826	1990.840	-11.986
100	182	2023.576	1980.183	-43.393
100	183	2020.130	1998.992	-21.138
100	184	2007.869	1986.258	-21.611
100	185	2016.841	1996.289	-20.552
100	186	2007.628	1992.328	-15.300
100	187	1994.511	1980.854	-13.657
100	188	2027.073	1991.741	-35.332
100	189	2027.339	1999.362	-27.977
100	190	2010.309	1986.592	-23.717
105	191	2014.195	1992.730	-21.465
105	192	2014.782	1996.262	-18.520
105	193	2018.101	1990.850	-27.251
105	194	1997.690	1962.058	-35.632
105	195	1974.267	1949.695	-24.572
105	196	2006.270	1978.004	-28.266
	Max	2042.353	2043.054	9.494
	Average	2005.012	1992.521	-12.490
	Min	1970.101	1949.695	-43.393
	Std Dev	14.802	16.681	11.159



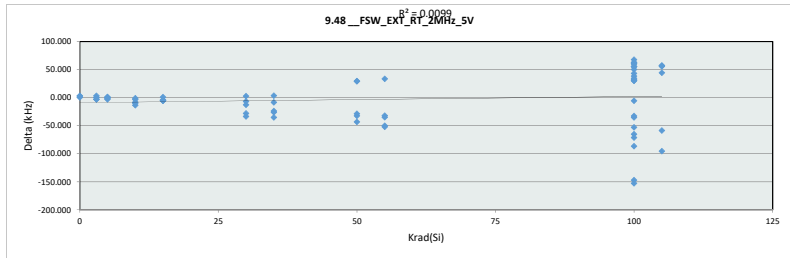
		9.24_FSW_EXT													
Test Site	Tester														
Test Number	Unit	kHz	kHz												
Max Limit	2300	2300													
Min Limit	1700	1700													
Krad(Si)	LL	Min	Average	Max	UL	LL	Min	Average	Max	UL	LL	Min	Average	Max	UL
0	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
3	2001.381	1985.351	2006.692	1976.312	1985.361	1984.710	1970.104	1986.075	1980.287	1952.709	1949.695				
5	2012.658	2005.349	2020.010	1995.858	1999.410	1990.978	1985.822	1993.742	1991.205	1984.775	1978.267				
10	2018.966	2016.613	2043.054	2016.739	2014.456	2007.548	2003.399	1999.105	1998.749	2013.887	1996.262				
15	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



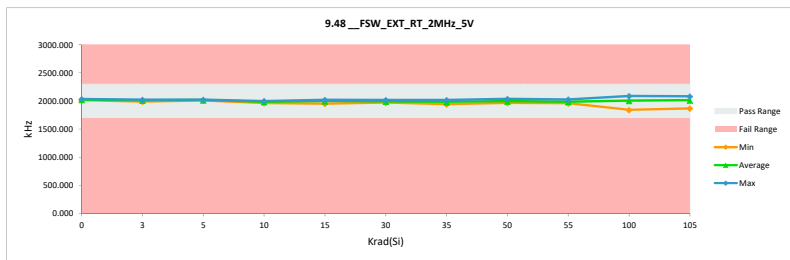
# HDR TID Report

## TPS7H5003-SP

		9.48_FSW_EXT_RT_2MHz_5V		
Test Site	Tester			
Test Number	Unit	kHz	kHz	
Max Limit	2300	2300		
Min Limit	1700	1700		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	2011.680	2012.487	0.807
0	138	2017.410	2020.924	3.514
0	139	2029.870	2033.435	3.565
3	197	2018.222	2019.188	0.966
3	198	1998.911	2002.616	3.705
3	199	2021.676	2022.158	0.482
3	202	1991.539	1989.048	-2.491
3	203	2020.574	2018.152	-2.422
5	204	2012.899	2013.780	0.881
5	205	2010.274	2009.426	-0.848
5	206	2021.428	2018.745	-2.683
5	208	2020.748	2022.387	1.639
5	209	2015.644	2016.099	0.455
10	210	1993.176	1990.407	-2.769
10	211	1999.255	1989.704	-9.551
10	212	2003.615	1990.400	-13.215
10	213	1999.438	1998.897	-0.541
10	214	1971.986	1964.155	-7.831
15	215	1996.226	1992.415	-3.811
15	217	2022.902	2018.151	-4.751
15	219	2018.736	2013.465	-5.271
15	220	2019.974	2021.345	1.371
15	221	1954.835	1950.464	-4.371
30	222	2016.673	1983.610	-33.063
30	223	2024.134	2017.957	-6.177
30	224	1994.176	1997.313	3.137
30	225	2002.815	1990.553	-12.262
30	226	1999.484	1971.929	-27.555
35	227	2002.775	1994.849	-7.926
35	228	2012.887	2017.002	4.115
35	229	1977.462	1942.667	-34.795
35	230	2004.054	1978.426	-25.628
35	231	2011.830	1988.424	-23.406
50	170	2007.484	2037.077	29.593
50	172	2019.149	1976.393	-42.756
50	173	2018.820	1986.894	-31.926
50	175	1994.080	1965.855	-28.225
50	176	1996.506	2026.363	29.857
55	177	2020.622	1986.029	-34.593
55	178	1993.059	2027.094	34.035
55	179	2008.970	1959.258	-49.712
55	180	2041.486	1989.727	-51.759
55	181	2004.546	1972.785	-31.761
100	154	1995.289	2058.632	63.343
100	155	1998.265	2059.537	61.272
100	156	1994.720	2062.795	68.075
100	157	2013.681	2074.221	60.540
100	158	2012.913	2043.549	30.636
100	159	2005.741	2040.502	34.761
100	160	1970.218	1917.649	-52.569
100	161	2000.391	1935.495	-64.896
100	162	1976.079	1905.209	-70.870
100	163	2025.406	2081.212	55.806
100	164	1970.789	1935.943	-34.846
100	165	1984.366	1898.073	-86.293
100	166	2033.003	2071.898	38.895
100	168	1960.864	1928.497	-32.367
100	169	2009.166	2041.933	32.767
100	182	1988.443	1841.685	-146.758
100	183	2026.469	2089.061	62.592
100	184	2017.130	1864.827	-152.303
100	185	2026.587	2082.511	55.924
100	186	2012.817	2063.633	50.816
100	187	1998.949	2042.673	43.724
100	188	1994.194	2025.039	30.845
100	189	1998.603	1993.592	-5.011
100	190	2001.078	2056.654	55.576
105	191	2024.364	2081.890	57.526
105	192	2004.747	2060.992	56.245
105	193	2008.570	2066.052	57.482
105	194	2012.363	2057.286	44.923
105	195	1962.769	1867.836	-94.933
105	196	2004.676	1946.370	-58.306
	Max	2041.486	2089.061	68.075
	Average	2004.859	2001.826	-3.033
	Min	1954.835	1841.685	-152.303
	Std Dev	17.473	52.911	45.407



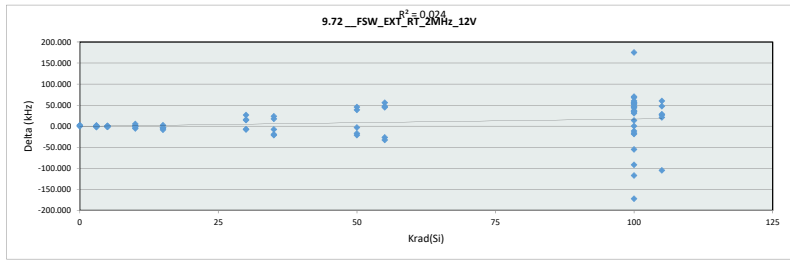
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Test Site	Tester												
Test Number	Unit	2300	kHz										
Max Limit	2300	kHz											
Min Limit	1700	kHz											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2012.487	1989.048	2009.426	1964.155	1950.464	1971.929	1942.667	1965.855	1959.258	1841.685	1867.836		
Average	2022.282	2010.232	2016.087	1986.713	1999.168	1992.272	1984.274	1998.516	1986.979	2004.784	2013.404		
Max	2033.435	2022.158	2022.387	1998.897	2021.345	2017.957	2017.002	2037.077	2027.094	2089.061	2081.890		
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



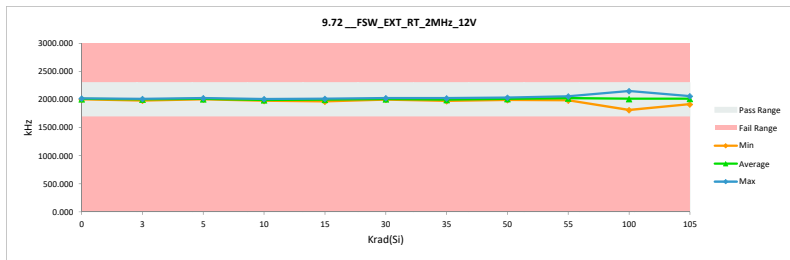


# HDR TID Report TPS7H5003-SP

		9.72_FSW_EXT_RT_2MHz_12V		
Test Site	Tester			
Test Number	Unit	kHz	kHz	
Max Limit	2300	2300		
Min Limit	1700	1700		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	2002.838	2003.096	0.258
0	138	2007.636	2010.718	3.082
0	139	2018.885	2021.648	2.763
3	197	2007.088	2009.557	2.469
3	198	1999.703	1998.258	-1.445
3	199	2012.121	2012.470	0.349
3	202	1979.989	1981.500	1.511
3	203	2009.532	2009.590	0.058
5	204	2002.879	2004.144	1.265
5	205	2027.772	2026.920	-0.852
5	206	2010.414	2009.425	-0.989
5	208	2009.110	2010.413	1.303
5	209	2004.128	2003.680	-0.448
10	210	1981.491	1976.968	-4.523
10	211	2000.518	2006.476	5.958
10	212	2008.025	2009.339	1.314
10	213	1989.808	1989.712	-0.096
10	214	1986.597	1985.754	-0.843
15	215	1992.976	1989.736	-3.240
15	217	2018.610	2015.645	-2.965
15	219	2010.221	2013.319	3.098
15	220	2009.894	2006.566	-3.328
15	221	1972.526	1964.969	-7.557
30	222	2004.846	1998.328	-6.518
30	223	2011.398	2026.603	15.205
30	224	1984.283	2011.471	27.188
30	225	1995.100	2011.170	16.070
30	226	2011.661	2004.619	-7.042
35	227	1992.235	2010.360	18.125
35	228	2001.451	2025.666	24.215
35	229	1994.646	1973.899	-20.747
35	230	2001.707	1983.108	-18.599
35	231	2000.847	1993.732	-7.115
50	170	1996.065	2035.650	39.585
50	172	2013.316	1992.799	-20.517
50	173	2006.593	2004.340	-2.253
50	175	2011.608	1995.342	-16.266
50	176	1985.356	2031.663	46.307
55	177	2010.061	2058.248	48.187
55	178	1987.276	2032.745	45.469
55	179	2010.987	1985.161	-25.826
55	180	2028.390	1996.476	-31.914
55	181	1995.300	2061.897	56.597
100	154	1984.335	2019.532	35.197
100	155	1989.043	2035.719	46.676
100	156	2002.157	2040.103	37.946
100	157	2001.515	2057.518	56.003
100	158	2001.892	1991.213	-10.679
100	159	1994.019	1975.906	-18.113
100	160	1987.917	2056.579	68.662
100	161	2007.702	2060.728	53.026
100	162	1982.598	2043.233	60.635
100	163	2013.213	2061.273	48.060
100	164	1990.281	1873.914	-116.367
100	165	1970.915	1916.416	-54.499
100	166	2022.122	2036.309	14.187
100	168	1990.409	2061.314	70.905
100	169	1999.187	1983.507	-15.680
100	182	1976.204	2151.938	175.734
100	183	2013.879	2070.870	56.991
100	184	2020.315	2074.428	54.113
100	185	2015.825	2060.246	44.421
100	186	2001.405	2032.689	31.284
100	187	1992.094	1993.042	0.948
100	188	1982.082	1891.162	-90.920
100	189	1985.785	1814.429	-171.356
100	190	1989.318	2024.725	35.407
105	191	2012.531	2060.800	48.269
105	192	1993.385	2022.492	29.107
105	193	1998.401	2025.638	27.237
105	194	2004.979	2026.410	21.431
105	195	1983.837	2044.494	60.657
105	196	2020.662	1916.563	-104.099
	Max	2028.390	2151.938	175.734
	Average	2000.464	2009.676	9.212
	Min	1970.915	1814.429	-171.356
	Std Dev	13.037	46.910	46.016

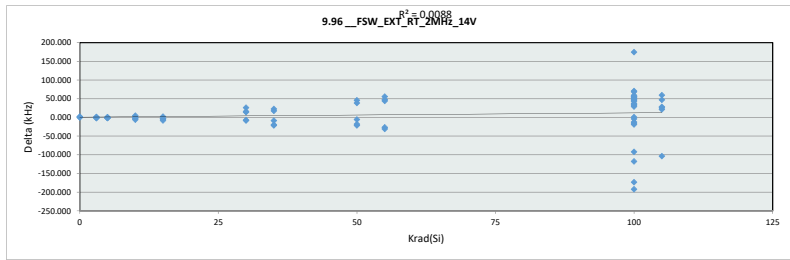


		9.72_FSW_EXT											
Test Site	Tester												
Test Number	Unit	2300	kHz										
Max Limit	2300	kHz											
Min Limit	1700	kHz											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2003.096	1981.500	2003.680	1976.968	1964.969	1998.328	1973.899	1992.799	1985.161	1814.429	1916.563		
Average	2011.821	2002.275	2010.916	1993.650	1998.047	2010.438	1997.353	2011.959	2024.905	2013.616	2016.066		
Max	2021.648	2012.470	2026.920	2009.339	2015.645	2026.603	2025.666	2035.650	2058.248	2151.938	2060.800		
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000

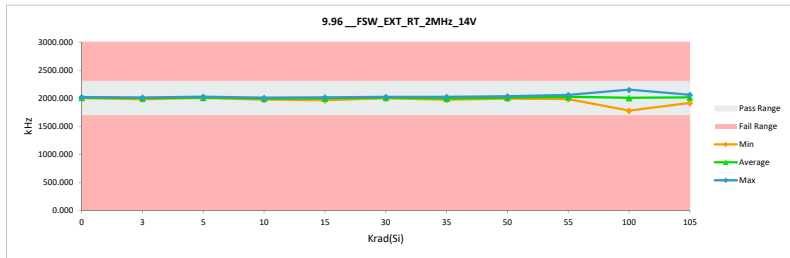


# HDR TID Report TPS7H5003-SP

9.96_FSW_EXT_RT_2MHz_14V				
Test Site	Tester	Test Number	Unit	
			kHz	kHz
Max Limit		2300		2300
Min Limit		1700		1700
Krad(Si)	Serial #	PRE	POST	Delta
0	137	2002.618	2002.788	0.170
0	138	2007.558	2010.470	2.912
0	139	2018.675	2020.835	2.160
3	197	2007.049	2009.251	2.202
3	198	1999.172	1997.968	-1.204
3	199	2011.906	2012.295	0.389
3	202	1979.919	1980.894	0.975
3	203	2009.227	2009.484	0.257
5	204	2002.493	2003.762	1.269
5	205	2027.591	2026.459	-1.132
5	206	2010.049	2009.151	-0.898
5	208	2009.002	2010.015	1.013
5	209	2003.925	2003.954	0.029
10	210	1981.633	1976.587	-5.046
10	211	2000.016	2005.482	5.466
10	212	2007.615	2008.814	1.199
10	213	1989.842	1989.881	0.039
10	214	1986.557	1985.349	-1.208
15	215	1992.978	1990.034	-2.944
15	217	2018.131	2015.263	-2.868
15	219	2009.961	2012.697	2.736
15	220	2009.816	2006.805	-3.011
15	221	1971.850	1964.482	-7.368
30	222	2004.634	1998.019	-6.615
30	223	2011.156	2025.858	14.702
30	224	1984.228	2011.198	26.970
30	225	1994.605	2011.079	16.474
30	226	2011.204	2004.447	-7.157
35	227	1992.018	2010.423	18.405
35	228	2001.091	2024.680	23.589
35	229	1993.791	1973.371	-20.420
35	230	2001.643	1982.914	-18.729
35	231	2000.781	1993.130	-7.651
50	170	1996.076	2035.220	39.144
50	172	2012.903	1992.352	-20.551
50	173	2005.992	2001.093	-4.899
50	175	2011.335	1994.262	-17.073
50	176	1985.180	2031.842	46.662
55	177	2010.337	2058.641	48.304
55	178	1987.049	2032.075	45.026
55	179	2010.389	1984.763	-25.626
55	180	2027.991	1998.023	-29.968
55	181	1995.312	2051.558	56.246
100	154	1984.279	2018.163	33.884
100	155	1988.469	2035.651	47.182
100	156	2001.630	2039.810	38.180
100	157	2001.071	2056.862	55.791
100	158	2001.541	1989.842	-11.699
100	159	1993.714	1975.507	-18.207
100	160	1987.318	2056.458	69.140
100	161	2007.570	2059.912	52.342
100	162	1982.186	2042.231	60.045
100	163	2012.839	2061.275	48.436
100	164	1990.220	1873.318	-116.902
100	165	1970.809	1779.584	-191.225
100	166	2021.877	2018.984	-2.893
100	168	1989.367	2060.713	71.346
100	169	1998.780	1984.225	-14.555
100	182	1975.931	2151.439	175.508
100	183	2013.696	2070.139	56.443
100	184	2020.033	2074.315	54.282
100	185	2015.829	2059.765	43.936
100	186	2001.357	2031.292	29.935
100	187	1991.450	1993.191	1.741
100	188	1982.573	1891.358	-91.215
100	189	1985.801	1813.197	-172.604
100	190	1988.914	2023.538	34.624
105	191	2012.715	2060.394	47.679
105	192	1993.274	2022.280	29.006
105	193	1998.177	2024.512	26.335
105	194	2004.673	2026.902	22.229
105	195	1983.468	2043.963	60.495
105	196	2020.300	1917.555	-102.745
Max		2027.991	2151.439	175.508
Average		2000.208	2007.173	6.966
Min		1970.809	1779.584	-191.225
Std Dev		13.026	52.886	51.128

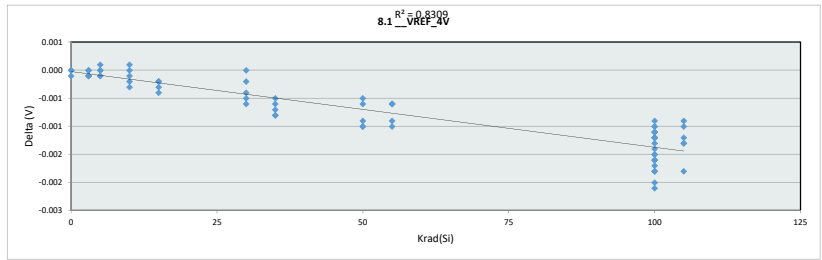


9.96_FSW_EXT														
Test Site	Tester	Test Number	Max Limit	Min Limit	0	5	10	15	30	35	50	55	100	105
LL			1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min			2002.788	1980.894	2003.762	1976.587	1964.482	1998.019	1973.371	1992.352	1984.763	1779.584	1917.555	
Average			2011.364	2001.978	2010.668	1993.223	1997.856	2010.040	1996.904	2010.954	2025.012	2006.699	2015.934	
Max			2020.835	2012.295	2026.459	2008.814	2015.263	2025.858	2024.680	2035.220	2058.641	2151.439	2060.394	
UL			2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	

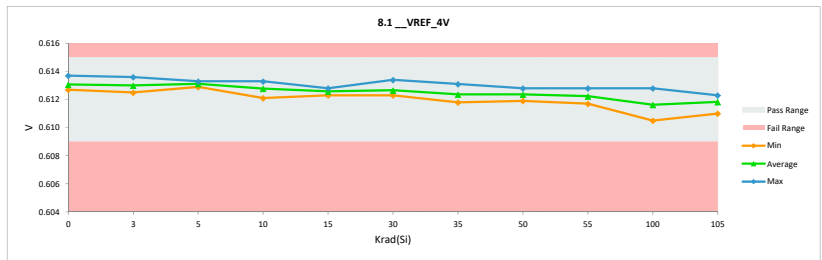


HDR TID Report  
TPS7H5003-SP

		8.1_VREF_4V		
Test Site				
Tester				
Test Number				
Unit		V	V	
Max Limit		0.615	0.615	
Min Limit		0.609	0.609	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.613	0.613	0.000
0	138	0.613	0.613	0.000
0	139	0.614	0.614	0.000
3	197	0.614	0.614	0.000
3	198	0.613	0.613	0.000
3	199	0.613	0.613	0.000
3	202	0.613	0.613	0.000
3	203	0.613	0.613	0.000
5	204	0.613	0.613	0.000
5	205	0.613	0.613	0.000
5	206	0.613	0.613	0.000
5	208	0.613	0.613	0.000
5	209	0.613	0.613	0.000
10	210	0.612	0.612	0.000
10	211	0.613	0.613	0.000
10	212	0.613	0.613	0.000
10	213	0.613	0.613	0.000
10	214	0.613	0.612	0.000
10	215	0.613	0.613	0.000
15	217	0.613	0.613	0.000
15	219	0.613	0.613	0.000
15	220	0.613	0.612	0.000
15	221	0.613	0.613	0.000
30	222	0.613	0.612	-0.001
30	223	0.613	0.613	0.000
30	224	0.613	0.613	0.000
30	225	0.613	0.613	0.000
30	226	0.614	0.613	-0.001
35	227	0.614	0.613	-0.001
35	228	0.613	0.612	-0.001
35	229	0.613	0.612	-0.001
35	230	0.613	0.612	-0.001
35	231	0.613	0.613	-0.001
50	170	0.613	0.613	-0.001
50	172	0.613	0.613	-0.001
50	173	0.614	0.613	-0.001
50	175	0.612	0.612	-0.001
50	176	0.613	0.612	-0.001
55	177	0.613	0.613	-0.001
55	178	0.613	0.612	-0.001
55	179	0.613	0.612	-0.001
55	180	0.613	0.613	-0.001
55	181	0.613	0.612	-0.001
100	154	0.613	0.611	-0.001
100	155	0.613	0.611	-0.002
100	156	0.613	0.612	-0.001
100	157	0.613	0.611	-0.002
100	158	0.613	0.612	-0.001
100	159	0.614	0.612	-0.002
100	160	0.612	0.611	-0.002
100	161	0.612	0.611	-0.002
100	162	0.613	0.611	-0.002
100	163	0.612	0.611	-0.002
100	164	0.613	0.612	-0.001
100	165	0.613	0.612	-0.001
100	166	0.614	0.612	-0.002
100	168	0.613	0.612	-0.001
100	169	0.613	0.612	-0.001
100	182	0.613	0.612	-0.002
100	183	0.613	0.612	-0.001
100	184	0.614	0.613	-0.001
100	185	0.613	0.612	-0.001
100	186	0.613	0.612	-0.001
100	187	0.613	0.612	-0.002
100	188	0.614	0.613	-0.001
100	189	0.612	0.610	-0.002
100	190	0.614	0.612	-0.001
105	191	0.613	0.612	-0.001
105	192	0.613	0.612	-0.001
105	193	0.613	0.612	-0.001
105	194	0.613	0.612	-0.001
105	195	0.613	0.611	-0.002
105	196	0.613	0.612	-0.001
	Max	0.614	0.614	0.000
	Average	0.613	0.612	-0.001
	Min	0.612	0.610	-0.002
	Std Dev	0.000	0.001	0.001

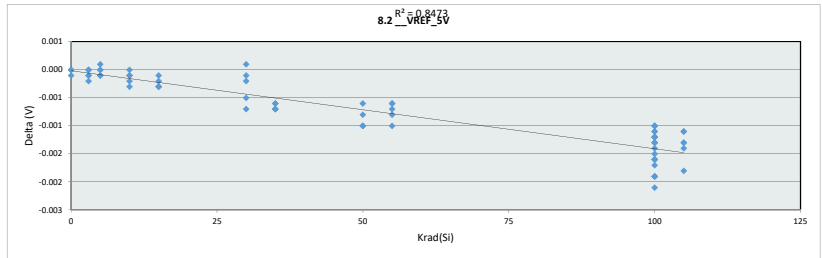


		8.1_VREF_4V											
Test Site													
Tester													
Test Number													
Max Limit		0.615	V										
Min Limit		0.609	V										
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105	
LL		0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	
Min		0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612	0.612	0.611	0.611	
Average		0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612	0.612	
Max		0.614	0.614	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	
UL		0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	

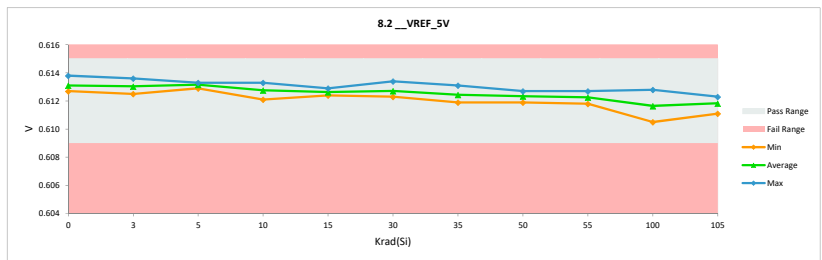


# HDR TID Report TPS7H5003-SP

		8.2_VREF_5V		
Test Site				
Tester				
Test Number				
Unit		V	V	
Max Limit		0.615	0.615	
Min Limit		0.609	0.609	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.613	0.613	0.000
0	138	0.613	0.613	0.000
0	139	0.614	0.614	0.000
3	197	0.614	0.614	0.000
3	198	0.613	0.613	0.000
3	199	0.613	0.613	0.000
3	202	0.613	0.613	0.000
3	203	0.613	0.613	0.000
5	204	0.613	0.613	0.000
5	205	0.613	0.613	0.000
5	206	0.613	0.613	0.000
5	208	0.613	0.613	0.000
5	209	0.613	0.613	0.000
10	210	0.612	0.612	0.000
10	211	0.613	0.613	0.000
10	212	0.613	0.613	0.000
10	213	0.613	0.613	0.000
10	214	0.613	0.612	0.000
10	215	0.613	0.613	0.000
15	217	0.613	0.613	0.000
15	219	0.613	0.613	0.000
15	220	0.613	0.612	0.000
15	221	0.613	0.613	0.000
30	222	0.613	0.612	-0.001
30	223	0.613	0.613	0.000
30	224	0.613	0.613	0.000
30	225	0.613	0.613	0.000
30	226	0.614	0.613	-0.001
35	227	0.614	0.613	-0.001
35	228	0.613	0.612	-0.001
35	229	0.613	0.612	-0.001
35	230	0.613	0.612	-0.001
35	231	0.613	0.613	-0.001
50	170	0.613	0.613	-0.001
50	172	0.613	0.613	-0.001
50	173	0.614	0.613	-0.001
50	175	0.613	0.612	-0.001
50	176	0.613	0.612	-0.001
55	177	0.613	0.613	-0.001
55	178	0.613	0.612	-0.001
55	179	0.613	0.612	-0.001
55	180	0.613	0.613	-0.001
55	181	0.613	0.612	-0.001
100	154	0.613	0.611	-0.001
100	155	0.613	0.611	-0.002
100	156	0.613	0.612	-0.001
100	157	0.613	0.611	-0.002
100	158	0.613	0.612	-0.001
100	159	0.614	0.612	-0.002
100	160	0.613	0.611	-0.002
100	161	0.612	0.611	-0.002
100	162	0.613	0.611	-0.002
100	163	0.612	0.611	-0.002
100	164	0.613	0.612	-0.001
100	165	0.613	0.612	-0.001
100	166	0.614	0.612	-0.002
100	168	0.613	0.612	-0.001
100	169	0.613	0.612	-0.001
100	182	0.613	0.612	-0.002
100	183	0.613	0.612	-0.001
100	184	0.614	0.613	-0.001
100	185	0.613	0.612	-0.001
100	186	0.613	0.612	-0.001
100	187	0.613	0.612	-0.002
100	188	0.614	0.613	-0.001
100	189	0.612	0.610	-0.002
100	190	0.614	0.613	-0.001
105	191	0.613	0.612	-0.001
105	192	0.613	0.612	-0.001
105	193	0.613	0.612	-0.001
105	194	0.613	0.612	-0.001
105	195	0.613	0.611	-0.002
105	196	0.613	0.612	-0.001
Max		0.614	0.614	0.000
Average		0.613	0.612	-0.001
Min		0.612	0.610	-0.002
Std Dev		0.000	0.001	0.001

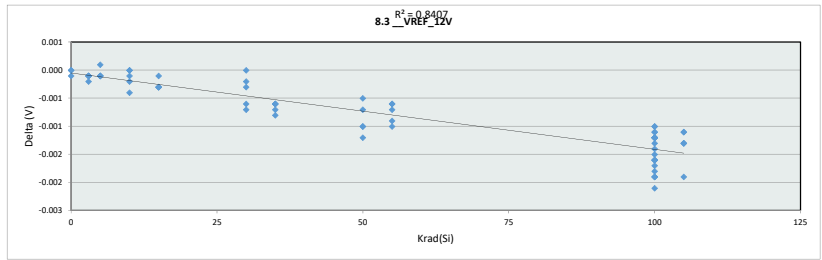


		8.2_VREF_5V										
Test Site												
Tester												
Test Number		0.615	V									
Max Limit		0.615	V									
Min Limit		0.609	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	
Min	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612	0.612	0.612	0.611	
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.613	0.612	
Max	0.614	0.614	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	

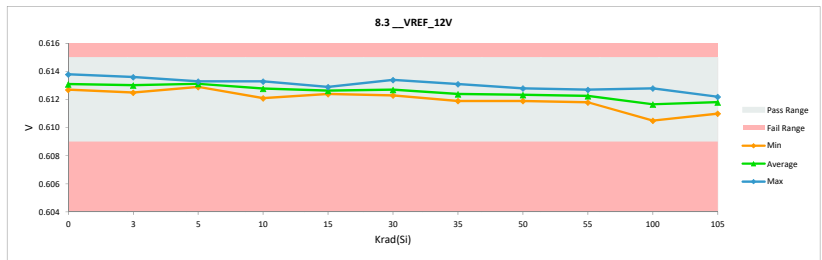


# HDR TID Report TPS7H5003-SP

		8.3_VREF_12V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	Min Limit	0.615	0.615	
		0.609	0.609	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.613	0.613	0.000
0	138	0.613	0.613	0.000
0	139	0.614	0.614	0.000
3	197	0.614	0.614	0.000
3	198	0.613	0.613	0.000
3	199	0.613	0.613	0.000
3	202	0.613	0.613	0.000
3	203	0.613	0.613	0.000
5	204	0.613	0.613	0.000
5	205	0.613	0.613	0.000
5	206	0.613	0.613	0.000
5	208	0.613	0.613	0.000
5	209	0.613	0.613	0.000
10	210	0.613	0.612	0.000
10	211	0.613	0.613	0.000
10	212	0.613	0.613	0.000
10	213	0.613	0.613	0.000
10	214	0.613	0.612	0.000
10	215	0.613	0.613	0.000
15	217	0.613	0.613	0.000
15	219	0.613	0.613	0.000
15	220	0.613	0.612	0.000
15	221	0.613	0.613	0.000
30	222	0.613	0.612	-0.001
30	223	0.613	0.613	0.000
30	224	0.613	0.613	0.000
30	225	0.613	0.613	0.000
30	226	0.614	0.613	-0.001
35	227	0.614	0.613	-0.001
35	228	0.613	0.612	-0.001
35	229	0.613	0.612	-0.001
35	230	0.613	0.612	-0.001
35	231	0.613	0.613	-0.001
50	170	0.613	0.613	-0.001
50	172	0.613	0.613	-0.001
50	173	0.614	0.613	-0.001
50	175	0.612	0.612	-0.001
50	176	0.613	0.612	-0.001
55	177	0.613	0.613	-0.001
55	178	0.613	0.612	-0.001
55	179	0.613	0.612	-0.001
55	180	0.613	0.613	-0.001
55	181	0.613	0.612	-0.001
100	154	0.613	0.611	-0.001
100	155	0.613	0.611	-0.002
100	156	0.613	0.612	-0.001
100	157	0.613	0.611	-0.002
100	158	0.613	0.612	-0.001
100	159	0.614	0.612	-0.002
100	160	0.613	0.611	-0.002
100	161	0.612	0.611	-0.002
100	162	0.613	0.611	-0.002
100	163	0.612	0.611	-0.002
100	164	0.613	0.612	-0.001
100	165	0.613	0.612	-0.001
100	166	0.614	0.612	-0.002
100	168	0.613	0.612	-0.001
100	169	0.613	0.612	-0.001
100	182	0.613	0.612	-0.002
100	183	0.613	0.612	-0.001
100	184	0.614	0.613	-0.001
100	185	0.613	0.612	-0.001
100	186	0.613	0.612	-0.001
100	187	0.613	0.612	-0.002
100	188	0.614	0.613	-0.001
100	189	0.612	0.610	-0.002
100	190	0.614	0.612	-0.001
105	191	0.613	0.612	-0.001
105	192	0.613	0.612	-0.001
105	193	0.613	0.612	-0.001
105	194	0.613	0.612	-0.001
105	195	0.613	0.611	-0.002
105	196	0.613	0.612	-0.001
	Max	0.614	0.614	0.000
	Average	0.613	0.612	-0.001
	Min	0.612	0.610	-0.002
	Std Dev	0.000	0.001	0.001

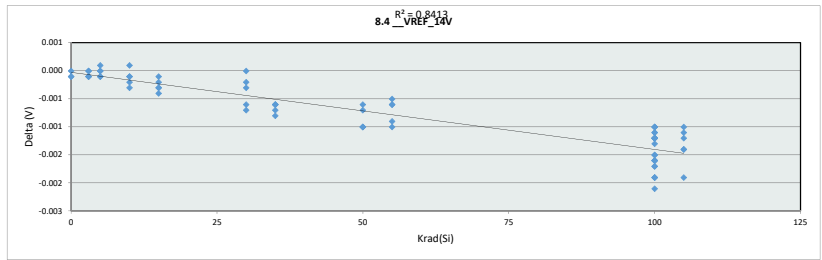


		8.3_VREF_12V										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit	Min Limit	0.615	0.615									
		0.609	0.609									
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min		0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612	0.612	0.611	0.611
Average		0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.613	0.612
Max		0.614	0.614	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612
UL		0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615

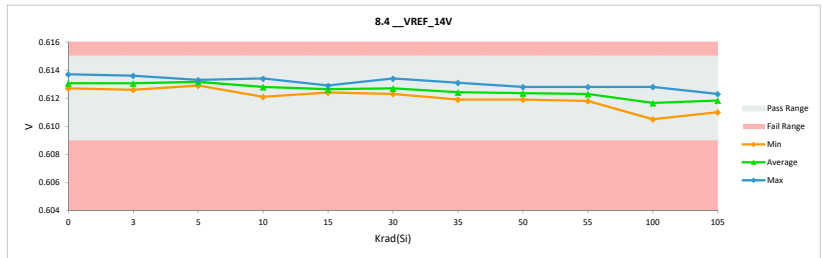


HDR TID Report  
TPS7H5003-SP

		8.4_VREF_14V		
Test Site				
Tester				
Test Number				
Unit		V	V	
Max Limit		0.615	0.615	
Min Limit		0.609	0.609	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.613	0.613	0.000
0	138	0.613	0.613	0.000
0	139	0.614	0.614	0.000
3	197	0.614	0.614	0.000
3	198	0.613	0.613	0.000
3	199	0.613	0.613	0.000
3	202	0.613	0.613	0.000
3	203	0.613	0.613	0.000
5	204	0.613	0.613	0.000
5	205	0.613	0.613	0.000
5	206	0.613	0.613	0.000
5	208	0.613	0.613	0.000
5	209	0.613	0.613	0.000
10	210	0.612	0.612	0.000
10	211	0.613	0.613	0.000
10	212	0.613	0.613	0.000
10	213	0.613	0.613	0.000
10	214	0.613	0.612	0.000
10	215	0.613	0.613	0.000
15	217	0.613	0.613	0.000
15	219	0.613	0.613	0.000
15	220	0.613	0.612	0.000
15	221	0.613	0.613	0.000
30	222	0.613	0.612	-0.001
30	223	0.613	0.613	0.000
30	224	0.613	0.613	0.000
30	225	0.613	0.613	0.000
30	226	0.614	0.613	-0.001
35	227	0.614	0.613	-0.001
35	228	0.613	0.612	-0.001
35	229	0.613	0.612	-0.001
35	230	0.613	0.612	-0.001
35	231	0.613	0.613	-0.001
50	170	0.613	0.613	-0.001
50	172	0.613	0.613	-0.001
50	173	0.614	0.613	-0.001
50	175	0.613	0.612	-0.001
50	176	0.613	0.612	-0.001
55	177	0.613	0.613	-0.001
55	178	0.613	0.612	-0.001
55	179	0.613	0.612	-0.001
55	180	0.613	0.613	-0.001
55	181	0.613	0.612	-0.001
100	154	0.613	0.611	-0.001
100	155	0.613	0.611	-0.002
100	156	0.613	0.612	-0.001
100	157	0.613	0.611	-0.002
100	158	0.613	0.612	-0.001
100	159	0.614	0.612	-0.002
100	160	0.613	0.611	-0.002
100	161	0.612	0.611	-0.002
100	162	0.613	0.611	-0.002
100	163	0.612	0.611	-0.002
100	164	0.613	0.612	-0.001
100	165	0.613	0.612	-0.001
100	166	0.614	0.612	-0.002
100	168	0.613	0.612	-0.001
100	169	0.613	0.612	-0.001
100	182	0.613	0.612	-0.002
100	183	0.613	0.612	-0.001
100	184	0.614	0.613	-0.001
100	185	0.613	0.612	-0.001
100	186	0.613	0.612	-0.001
100	187	0.613	0.612	-0.002
100	188	0.614	0.613	-0.001
100	189	0.612	0.610	-0.002
100	190	0.614	0.613	-0.001
105	191	0.613	0.612	-0.001
105	192	0.613	0.612	-0.001
105	193	0.613	0.612	-0.001
105	194	0.613	0.612	-0.001
105	195	0.613	0.611	-0.002
105	196	0.613	0.612	-0.001
Max		0.614	0.614	0.000
Average		0.613	0.612	-0.001
Min		0.612	0.610	-0.002
Std Dev		0.000	0.001	0.001

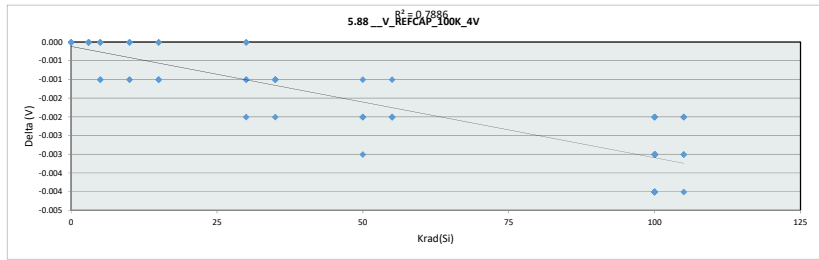


		8.4_VREF_14V										
Test Site												
Tester												
Test Number		0.615	V									
Max Limit		0.615	V									
Min Limit		0.609	V									
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min		0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612	0.612	0.611	0.611
Average		0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612	0.612
Max		0.614	0.614	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612
UL		0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615

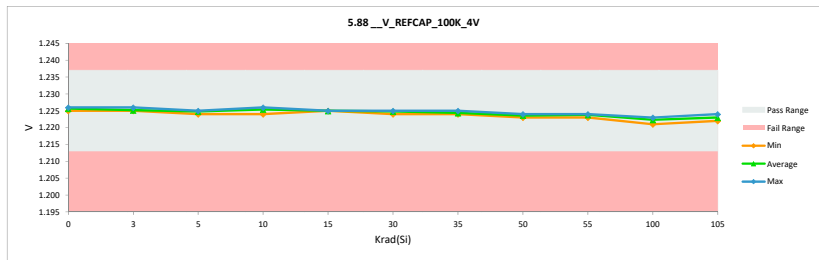


# HDR TID Report TPS7H5003-SP

5.88_V_REFCAP_100K_4V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.225	1.225	0.000	
0	139	1.226	1.226	0.000	
3	197	1.226	1.226	0.000	
3	198	1.225	1.225	0.000	
3	199	1.225	1.225	0.000	
3	202	1.225	1.225	0.000	
3	203	1.225	1.225	0.000	
5	204	1.225	1.225	0.000	
5	205	1.226	1.225	-0.001	
5	206	1.225	1.225	0.000	
5	208	1.225	1.225	0.000	
5	209	1.225	1.224	-0.001	
10	210	1.225	1.224	-0.001	
10	211	1.226	1.226	0.000	
10	212	1.226	1.226	0.000	
10	213	1.226	1.226	0.000	
10	214	1.226	1.225	-0.001	
10	215	1.226	1.225	-0.001	
15	217	1.225	1.225	0.000	
15	219	1.225	1.225	0.000	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.225	1.225	0.000	
30	224	1.226	1.225	-0.001	
30	225	1.225	1.225	0.000	
30	226	1.226	1.224	-0.002	
35	227	1.226	1.225	-0.001	
35	228	1.226	1.224	-0.002	
35	229	1.225	1.224	-0.001	
35	230	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.224	-0.002	
50	173	1.225	1.223	-0.002	
50	175	1.225	1.224	-0.001	
50	176	1.226	1.223	-0.003	
55	177	1.225	1.224	-0.001	
55	178	1.225	1.223	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.224	-0.002	
55	181	1.226	1.224	-0.002	
100	154	1.225	1.222	-0.003	
100	155	1.226	1.222	-0.004	
100	156	1.225	1.223	-0.002	
100	157	1.226	1.222	-0.004	
100	158	1.225	1.223	-0.002	
100	159	1.226	1.222	-0.004	
100	160	1.224	1.221	-0.003	
100	161	1.226	1.222	-0.004	
100	162	1.225	1.221	-0.004	
100	163	1.226	1.222	-0.004	
100	164	1.226	1.223	-0.003	
100	165	1.225	1.223	-0.002	
100	166	1.226	1.222	-0.004	
100	168	1.226	1.223	-0.003	
100	169	1.225	1.223	-0.002	
100	182	1.225	1.222	-0.003	
100	183	1.226	1.223	-0.003	
100	184	1.226	1.223	-0.003	
100	185	1.226	1.223	-0.003	
100	186	1.225	1.222	-0.003	
100	187	1.226	1.222	-0.004	
100	188	1.226	1.223	-0.003	
100	189	1.225	1.221	-0.004	
100	190	1.226	1.223	-0.003	
105	191	1.226	1.223	-0.003	
105	192	1.226	1.223	-0.003	
105	193	1.225	1.223	-0.002	
105	194	1.226	1.224	-0.002	
105	195	1.226	1.222	-0.004	
105	196	1.225	1.223	-0.002	
Max		1.226	1.226	0.000	
Average		1.226	1.224	-0.002	
Min		1.224	1.221	-0.004	
Std Dev		0.001	0.001	0.001	

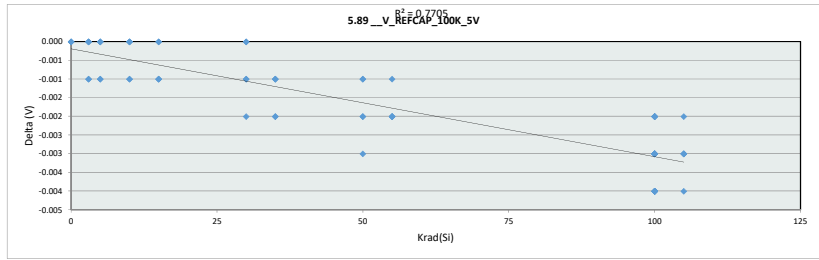


5.88_V_REFCAP											
Test Site											
Tester											
Test Number	1.237	V									
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.224	1.224	1.225	1.224	1.224	1.223	1.223	1.221	1.222
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.223
Max	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

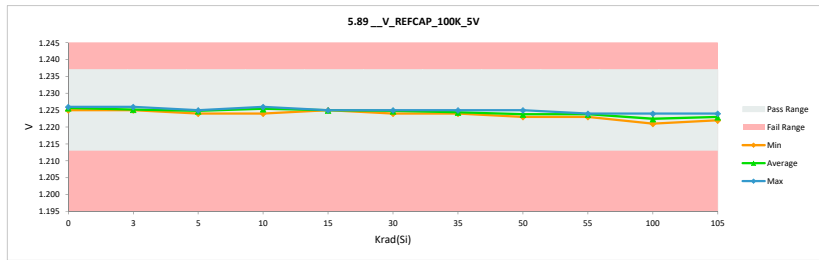


# HDR TID Report TPS7H5003-SP

5.89_V_REFCAP_100K_5V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.225	1.225	0.000	
0	139	1.226	1.226	0.000	
3	197	1.226	1.226	0.000	
3	198	1.226	1.225	-0.001	
3	199	1.226	1.225	-0.001	
3	202	1.225	1.225	0.000	
3	203	1.225	1.225	0.000	
5	204	1.225	1.225	0.000	
5	205	1.226	1.225	-0.001	
5	206	1.225	1.225	0.000	
5	208	1.225	1.225	0.000	
5	209	1.225	1.224	-0.001	
10	210	1.225	1.224	-0.001	
10	211	1.226	1.226	0.000	
10	212	1.226	1.226	0.000	
10	213	1.226	1.226	0.000	
10	214	1.226	1.225	-0.001	
10	215	1.226	1.225	-0.001	
15	217	1.225	1.225	0.000	
15	219	1.225	1.225	0.000	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.225	1.225	0.000	
30	224	1.226	1.225	-0.001	
30	225	1.225	1.225	0.000	
30	226	1.226	1.224	-0.002	
35	227	1.226	1.225	-0.001	
35	228	1.226	1.224	-0.002	
35	229	1.226	1.224	-0.002	
35	230	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.225	-0.001	
50	173	1.225	1.223	-0.002	
50	175	1.225	1.224	-0.001	
50	176	1.226	1.223	-0.003	
55	177	1.225	1.224	-0.001	
55	178	1.225	1.223	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.224	-0.002	
55	181	1.226	1.224	-0.002	
100	154	1.225	1.223	-0.002	
100	155	1.226	1.222	-0.004	
100	156	1.225	1.223	-0.002	
100	157	1.226	1.222	-0.004	
100	158	1.225	1.223	-0.002	
100	159	1.226	1.222	-0.004	
100	160	1.225	1.221	-0.004	
100	161	1.226	1.222	-0.004	
100	162	1.225	1.222	-0.003	
100	163	1.226	1.222	-0.004	
100	164	1.226	1.223	-0.003	
100	165	1.225	1.223	-0.002	
100	166	1.226	1.222	-0.004	
100	168	1.226	1.223	-0.003	
100	169	1.225	1.223	-0.002	
100	182	1.225	1.222	-0.003	
100	183	1.226	1.223	-0.003	
100	184	1.226	1.223	-0.003	
100	185	1.226	1.224	-0.002	
100	186	1.225	1.222	-0.003	
100	187	1.226	1.222	-0.004	
100	188	1.226	1.223	-0.003	
100	189	1.225	1.221	-0.004	
100	190	1.226	1.223	-0.003	
105	191	1.226	1.223	-0.003	
105	192	1.226	1.223	-0.003	
105	193	1.226	1.223	-0.003	
105	194	1.226	1.224	-0.002	
105	195	1.226	1.222	-0.004	
105	196	1.226	1.223	-0.003	
Max		1.226	1.226	0.000	
Average		1.226	1.224	-0.002	
Min		1.225	1.221	-0.004	
Std Dev		0.000	0.001	0.001	



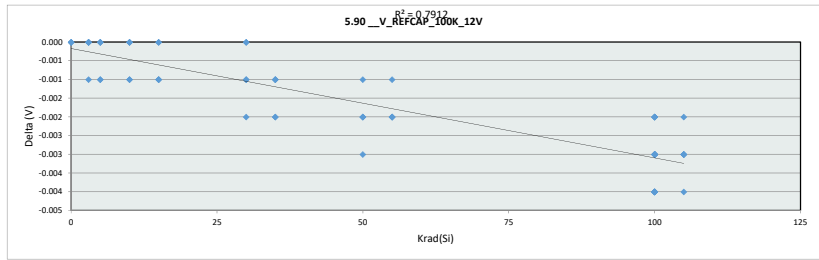
5.89_V_REFCAP											
Test Site											
Tester											
Test Number	1.237	V									
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.224	1.224	1.225	1.224	1.224	1.223	1.223	1.221	1.222
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.223
Max	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



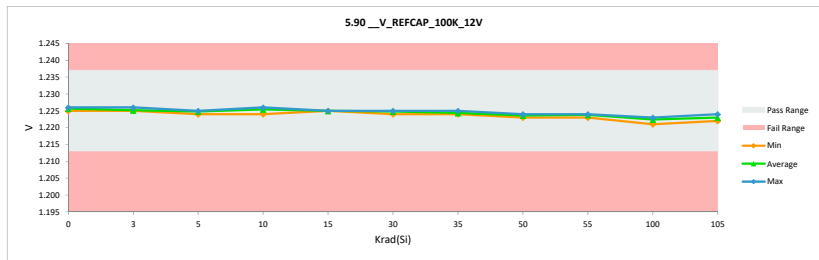


# HDR TID Report TPS7H5003-SP

5.90_V_REFCAP_100K_12V					
Test Site					
Tester					
Test Number					
Unit		V	V		
Max Limit		1.237	1.237		
Min Limit		1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.225	1.225	0.000	
0	139	1.226	1.226	0.000	
3	197	1.226	1.226	0.000	
3	198	1.225	1.225	0.000	
3	199	1.226	1.225	-0.001	
3	202	1.225	1.225	0.000	
3	203	1.225	1.225	0.000	
5	204	1.225	1.225	0.000	
5	205	1.226	1.225	-0.001	
5	206	1.225	1.225	0.000	
5	208	1.225	1.225	0.000	
5	209	1.225	1.224	-0.001	
10	210	1.225	1.224	-0.001	
10	211	1.226	1.226	0.000	
10	212	1.226	1.226	0.000	
10	213	1.226	1.226	0.000	
10	214	1.226	1.225	-0.001	
10	215	1.226	1.225	-0.001	
15	217	1.225	1.225	0.000	
15	219	1.225	1.225	0.000	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.225	1.225	0.000	
30	224	1.226	1.225	-0.001	
30	225	1.225	1.225	0.000	
30	226	1.226	1.224	-0.002	
35	227	1.226	1.225	-0.001	
35	228	1.226	1.224	-0.002	
35	229	1.226	1.224	-0.002	
35	230	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.224	-0.002	
50	173	1.225	1.223	-0.002	
50	175	1.225	1.224	-0.001	
50	176	1.226	1.223	-0.003	
55	177	1.225	1.224	-0.001	
55	178	1.225	1.223	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.224	-0.002	
55	181	1.226	1.224	-0.002	
100	154	1.225	1.223	-0.002	
100	155	1.226	1.222	-0.004	
100	156	1.225	1.223	-0.002	
100	157	1.226	1.222	-0.004	
100	158	1.225	1.223	-0.002	
100	159	1.226	1.222	-0.004	
100	160	1.224	1.221	-0.003	
100	161	1.226	1.222	-0.004	
100	162	1.225	1.222	-0.003	
100	163	1.226	1.222	-0.004	
100	164	1.226	1.223	-0.003	
100	165	1.225	1.223	-0.002	
100	166	1.226	1.222	-0.004	
100	168	1.226	1.223	-0.003	
100	169	1.225	1.223	-0.002	
100	182	1.225	1.222	-0.003	
100	183	1.226	1.223	-0.003	
100	184	1.226	1.223	-0.003	
100	185	1.226	1.223	-0.003	
100	186	1.225	1.222	-0.003	
100	187	1.226	1.222	-0.004	
100	188	1.226	1.223	-0.003	
100	189	1.225	1.221	-0.004	
100	190	1.226	1.223	-0.003	
105	191	1.226	1.223	-0.003	
105	192	1.226	1.223	-0.003	
105	193	1.226	1.223	-0.003	
105	194	1.226	1.224	-0.002	
105	195	1.226	1.222	-0.004	
105	196	1.226	1.223	-0.003	
Max		1.226	1.226	0.000	
Average		1.226	1.224	-0.002	
Min		1.224	1.221	-0.004	
Std Dev		0.001	0.001	0.001	

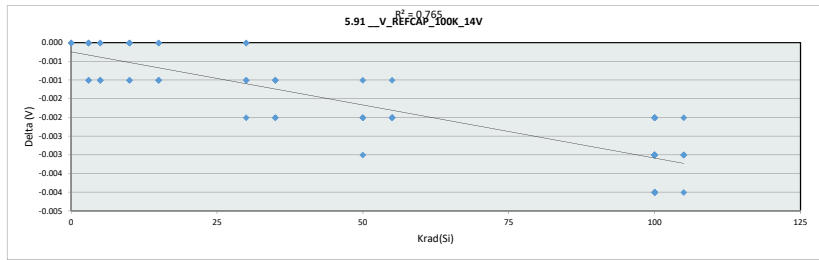


5.90_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit		1.237	V								
Min Limit		1.213	V								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.224	1.224	1.225	1.224	1.224	1.223	1.223	1.221	1.222
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.223
Max	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

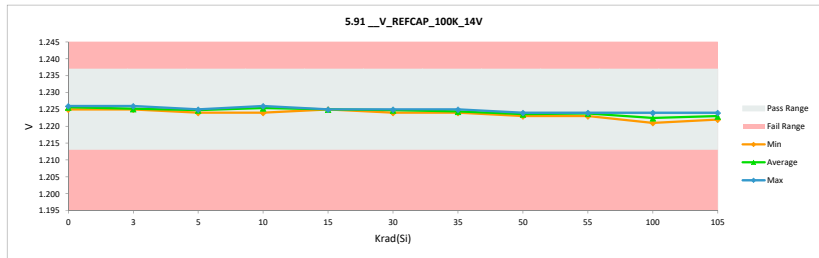


HDR TID Report  
TPS7H5003-SP

5.91_V_REFCAP_100K_14V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.225	1.225	0.000	
0	139	1.226	1.226	0.000	
3	197	1.226	1.226	0.000	
3	198	1.226	1.225	-0.001	
3	199	1.226	1.225	-0.001	
3	202	1.225	1.225	0.000	
3	203	1.225	1.225	0.000	
5	204	1.225	1.225	0.000	
5	205	1.226	1.225	-0.001	
5	206	1.225	1.225	0.000	
5	208	1.226	1.225	-0.001	
5	209	1.225	1.224	-0.001	
10	210	1.225	1.224	-0.001	
10	211	1.226	1.226	0.000	
10	212	1.226	1.226	0.000	
10	213	1.226	1.226	0.000	
10	214	1.226	1.225	-0.001	
15	215	1.226	1.225	-0.001	
15	217	1.225	1.225	0.000	
15	219	1.225	1.225	0.000	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.225	1.225	0.000	
30	224	1.226	1.225	-0.001	
30	225	1.225	1.225	0.000	
30	226	1.226	1.224	-0.002	
35	227	1.226	1.225	-0.001	
35	228	1.226	1.224	-0.002	
35	229	1.226	1.224	-0.002	
35	230	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.224	-0.002	
50	173	1.225	1.223	-0.002	
50	175	1.225	1.224	-0.001	
50	176	1.226	1.223	-0.003	
55	177	1.225	1.224	-0.001	
55	178	1.225	1.223	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.224	-0.002	
55	181	1.226	1.224	-0.002	
100	154	1.225	1.222	-0.003	
100	155	1.226	1.222	-0.004	
100	156	1.225	1.223	-0.002	
100	157	1.226	1.222	-0.004	
100	158	1.225	1.223	-0.002	
100	159	1.226	1.222	-0.004	
100	160	1.225	1.221	-0.004	
100	161	1.226	1.222	-0.004	
100	162	1.225	1.222	-0.003	
100	163	1.226	1.222	-0.004	
100	164	1.226	1.223	-0.003	
100	165	1.225	1.223	-0.002	
100	166	1.226	1.222	-0.004	
100	168	1.226	1.223	-0.003	
100	169	1.225	1.223	-0.002	
100	182	1.225	1.222	-0.003	
100	183	1.226	1.224	-0.002	
100	184	1.226	1.223	-0.003	
100	185	1.226	1.224	-0.002	
100	186	1.225	1.222	-0.003	
100	187	1.226	1.222	-0.004	
100	188	1.226	1.223	-0.003	
100	189	1.225	1.221	-0.004	
100	190	1.226	1.223	-0.003	
105	191	1.226	1.223	-0.003	
105	192	1.226	1.223	-0.003	
105	193	1.226	1.223	-0.003	
105	194	1.226	1.224	-0.002	
105	195	1.226	1.222	-0.004	
105	196	1.226	1.223	-0.003	
Max		1.226	1.226	0.000	
Average		1.226	1.224	-0.002	
Min		1.225	1.221	-0.004	
Std Dev		0.000	0.001	0.001	

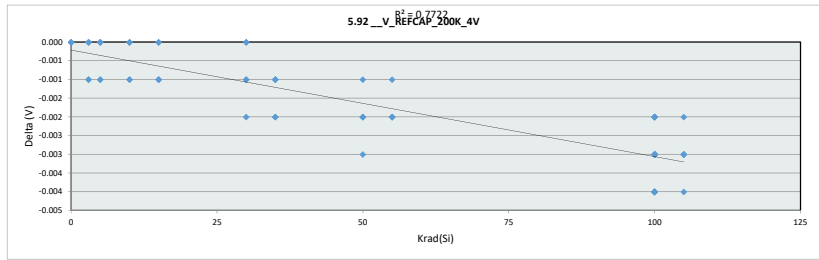


5.91_V_REFCAP											
Test Site											
Tester											
Test Number	1.237	V									
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.224	1.224	1.225	1.224	1.224	1.223	1.223	1.221	1.222
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.223
Max	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.224	1.224	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

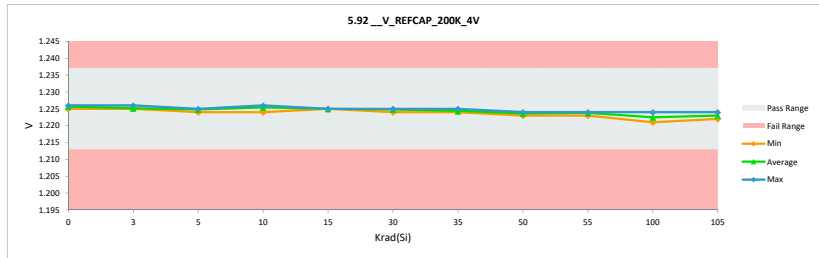


HDR TID Report  
TPS7H5003-SP

5.92_V_REFCAP_200K_4V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.225	1.225	0.000	
0	139	1.226	1.226	0.000	
3	197	1.226	1.226	0.000	
3	198	1.226	1.225	-0.001	
3	199	1.226	1.225	-0.001	
3	202	1.225	1.225	0.000	
3	203	1.225	1.225	0.000	
5	204	1.225	1.225	0.000	
5	205	1.226	1.225	-0.001	
5	206	1.225	1.225	0.000	
5	208	1.225	1.225	0.000	
5	209	1.225	1.224	-0.001	
10	210	1.225	1.224	-0.001	
10	211	1.226	1.226	0.000	
10	212	1.226	1.226	0.000	
10	213	1.226	1.226	0.000	
10	214	1.226	1.225	-0.001	
15	215	1.226	1.225	-0.001	
15	217	1.225	1.225	0.000	
15	219	1.225	1.225	0.000	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.225	1.225	0.000	
30	224	1.226	1.225	-0.001	
30	225	1.225	1.225	0.000	
30	226	1.226	1.224	-0.002	
35	227	1.226	1.225	-0.001	
35	228	1.226	1.224	-0.002	
35	229	1.226	1.224	-0.002	
35	230	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.224	-0.002	
50	173	1.225	1.223	-0.002	
50	175	1.225	1.224	-0.001	
50	176	1.226	1.223	-0.003	
55	177	1.225	1.224	-0.001	
55	178	1.225	1.223	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.224	-0.002	
55	181	1.226	1.224	-0.002	
100	154	1.225	1.222	-0.003	
100	155	1.226	1.222	-0.004	
100	156	1.225	1.223	-0.002	
100	157	1.226	1.222	-0.004	
100	158	1.225	1.223	-0.002	
100	159	1.226	1.222	-0.004	
100	160	1.224	1.221	-0.003	
100	161	1.226	1.222	-0.004	
100	162	1.225	1.222	-0.003	
100	163	1.226	1.222	-0.004	
100	164	1.226	1.223	-0.003	
100	165	1.225	1.223	-0.002	
100	166	1.226	1.222	-0.004	
100	168	1.226	1.223	-0.003	
100	169	1.225	1.223	-0.002	
100	182	1.225	1.222	-0.003	
100	183	1.226	1.224	-0.002	
100	184	1.226	1.223	-0.003	
100	185	1.226	1.224	-0.002	
100	186	1.225	1.222	-0.003	
100	187	1.226	1.222	-0.004	
100	188	1.226	1.223	-0.003	
100	189	1.225	1.221	-0.004	
100	190	1.226	1.223	-0.003	
105	191	1.226	1.223	-0.003	
105	192	1.226	1.223	-0.003	
105	193	1.226	1.223	-0.003	
105	194	1.226	1.224	-0.002	
105	195	1.226	1.222	-0.004	
105	196	1.226	1.223	-0.003	
Max		1.226	1.226	0.000	
Average		1.226	1.224	-0.002	
Min		1.224	1.221	-0.004	
Std Dev		0.001	0.001	0.001	

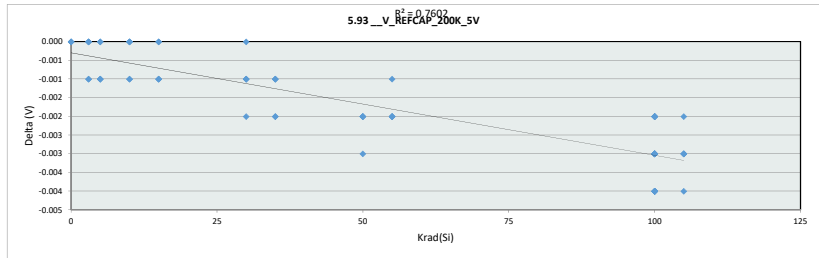


5.92_V_REFCAP											
Test Site											
Tester											
Test Number	1.237	V									
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.224	1.224	1.225	1.224	1.224	1.223	1.223	1.221	1.222
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.223
Max	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.224	1.224	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

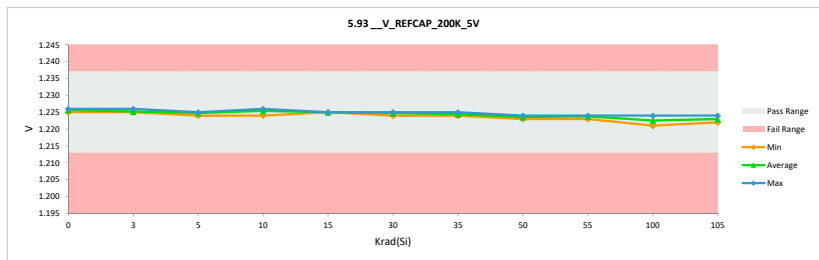


# HDR TID Report TPS7H5003-SP

5.93_V_REFCAP_200K_5V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.225	1.225	0.000	
0	139	1.226	1.226	0.000	
3	197	1.226	1.226	0.000	
3	198	1.226	1.225	-0.001	
3	199	1.226	1.225	-0.001	
3	202	1.225	1.225	0.000	
3	203	1.225	1.225	0.000	
5	204	1.225	1.225	0.000	
5	205	1.226	1.225	-0.001	
5	206	1.225	1.225	0.000	
5	208	1.226	1.225	-0.001	
5	209	1.225	1.224	-0.001	
10	210	1.225	1.224	-0.001	
10	211	1.226	1.226	0.000	
10	212	1.226	1.226	0.000	
10	213	1.226	1.226	0.000	
10	214	1.226	1.225	-0.001	
10	215	1.226	1.225	-0.001	
15	217	1.225	1.225	0.000	
15	219	1.225	1.225	0.000	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.225	1.225	0.000	
30	224	1.226	1.225	-0.001	
30	225	1.226	1.225	-0.001	
30	226	1.226	1.224	-0.002	
35	227	1.226	1.225	-0.001	
35	228	1.226	1.224	-0.002	
35	229	1.226	1.224	-0.002	
35	230	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.224	-0.002	
50	173	1.225	1.223	-0.002	
50	175	1.226	1.224	-0.002	
50	176	1.226	1.223	-0.003	
55	177	1.225	1.224	-0.001	
55	178	1.225	1.223	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.224	-0.002	
55	181	1.226	1.224	-0.002	
100	154	1.225	1.223	-0.002	
100	155	1.226	1.222	-0.004	
100	156	1.225	1.223	-0.002	
100	157	1.226	1.222	-0.004	
100	158	1.225	1.223	-0.002	
100	159	1.226	1.222	-0.004	
100	160	1.224	1.221	-0.003	
100	161	1.226	1.222	-0.004	
100	162	1.226	1.222	-0.004	
100	163	1.226	1.222	-0.004	
100	164	1.226	1.223	-0.003	
100	165	1.225	1.223	-0.002	
100	166	1.226	1.222	-0.004	
100	168	1.226	1.223	-0.003	
100	169	1.225	1.223	-0.002	
100	182	1.225	1.222	-0.003	
100	183	1.226	1.224	-0.002	
100	184	1.226	1.223	-0.003	
100	185	1.226	1.224	-0.002	
100	186	1.225	1.222	-0.003	
100	187	1.226	1.223	-0.003	
100	188	1.226	1.223	-0.003	
100	189	1.225	1.221	-0.004	
100	190	1.226	1.223	-0.003	
105	191	1.226	1.223	-0.003	
105	192	1.226	1.223	-0.003	
105	193	1.226	1.223	-0.003	
105	194	1.226	1.224	-0.002	
105	195	1.226	1.222	-0.004	
105	196	1.226	1.223	-0.003	
Max		1.226	1.226	0.000	
Average		1.226	1.224	-0.002	
Min		1.224	1.221	-0.004	
Std Dev		0.001	0.001	0.001	

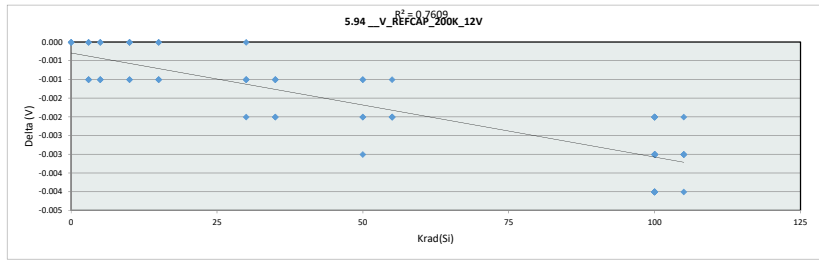


5.93_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.224	1.224	1.225	1.224	1.224	1.223	1.223	1.221	1.222
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.223
Max	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.224	1.224	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

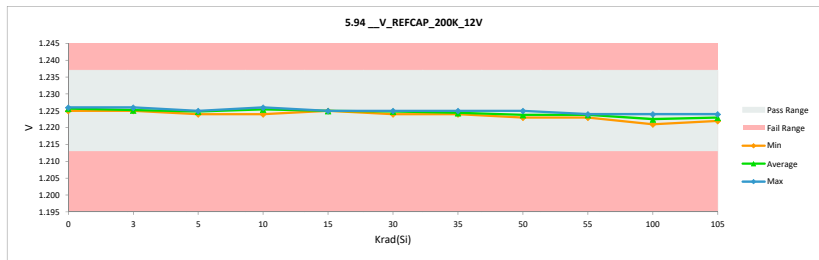


HDR TID Report  
TPS7H5003-SP

5.94_V_REFCAP_200K_12V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.225	1.225	0.000	
0	139	1.226	1.226	0.000	
3	197	1.226	1.226	0.000	
3	198	1.226	1.225	-0.001	
3	199	1.226	1.225	-0.001	
3	202	1.225	1.225	0.000	
3	203	1.226	1.225	-0.001	
5	204	1.225	1.225	0.000	
5	205	1.226	1.225	-0.001	
5	206	1.225	1.225	0.000	
5	208	1.226	1.225	-0.001	
5	209	1.225	1.224	-0.001	
10	210	1.225	1.224	-0.001	
10	211	1.226	1.226	0.000	
10	212	1.226	1.226	0.000	
10	213	1.226	1.226	0.000	
10	214	1.226	1.225	-0.001	
10	215	1.226	1.225	-0.001	
15	217	1.225	1.225	0.000	
15	219	1.225	1.225	0.000	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.225	1.225	0.000	
30	224	1.226	1.225	-0.001	
30	225	1.226	1.225	-0.001	
30	226	1.226	1.224	-0.002	
35	227	1.226	1.225	-0.001	
35	228	1.226	1.224	-0.002	
35	229	1.226	1.224	-0.002	
35	230	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.225	-0.001	
50	173	1.225	1.223	-0.002	
50	175	1.225	1.224	-0.001	
50	176	1.226	1.223	-0.003	
55	177	1.225	1.224	-0.001	
55	178	1.225	1.223	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.224	-0.002	
55	181	1.226	1.224	-0.002	
100	154	1.225	1.223	-0.002	
100	155	1.226	1.222	-0.004	
100	156	1.226	1.223	-0.003	
100	157	1.226	1.222	-0.004	
100	158	1.225	1.223	-0.002	
100	159	1.226	1.222	-0.004	
100	160	1.225	1.221	-0.004	
100	161	1.226	1.222	-0.004	
100	162	1.226	1.222	-0.004	
100	163	1.226	1.222	-0.004	
100	164	1.226	1.223	-0.003	
100	165	1.225	1.223	-0.002	
100	166	1.226	1.222	-0.004	
100	168	1.226	1.223	-0.003	
100	169	1.225	1.223	-0.002	
100	182	1.225	1.222	-0.003	
100	183	1.226	1.224	-0.002	
100	184	1.226	1.223	-0.003	
100	185	1.226	1.224	-0.002	
100	186	1.225	1.222	-0.003	
100	187	1.226	1.223	-0.003	
100	188	1.226	1.223	-0.003	
100	189	1.225	1.221	-0.004	
100	190	1.226	1.223	-0.003	
105	191	1.226	1.223	-0.003	
105	192	1.226	1.223	-0.003	
105	193	1.226	1.223	-0.003	
105	194	1.226	1.224	-0.002	
105	195	1.226	1.222	-0.004	
105	196	1.226	1.223	-0.003	
Max		1.226	1.226	0.000	
Average		1.226	1.224	-0.002	
Min		1.225	1.221	-0.004	
Std Dev		0.000	0.001	0.001	

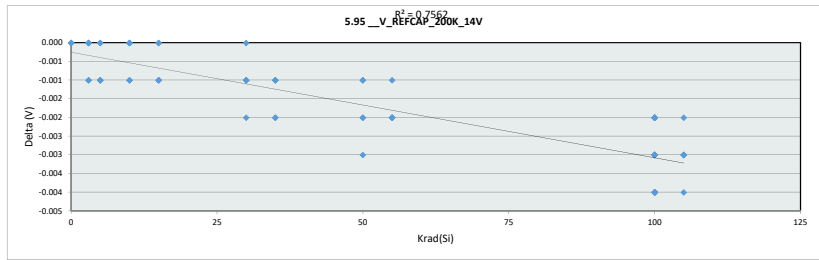


5.94_V_REFCAP											
Test Site											
Tester											
Test Number	1.237	V									
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.224	1.224	1.225	1.224	1.224	1.223	1.223	1.221	1.222
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.223
Max	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

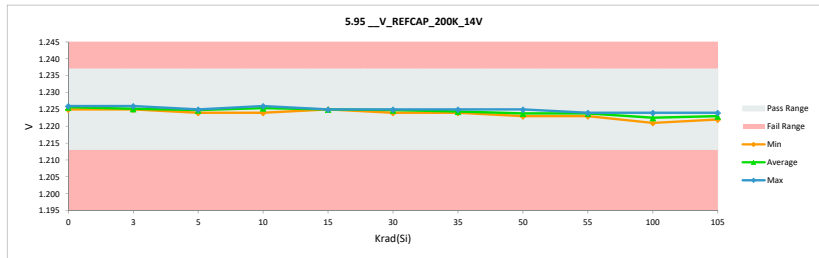


# HDR TID Report TPS7H5003-SP

5.95_V_REFCAP_200K_14V					
Test Site					
Tester					
Test Number					
Unit		V	V		
Max Limit		1.237	1.237		
Min Limit		1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.225	1.225	0.000	
0	139	1.226	1.226	0.000	
3	197	1.226	1.226	0.000	
3	198	1.226	1.225	-0.001	
3	199	1.226	1.225	-0.001	
3	202	1.225	1.225	0.000	
3	203	1.225	1.225	0.000	
5	204	1.225	1.225	0.000	
5	205	1.226	1.225	-0.001	
5	206	1.225	1.225	0.000	
5	208	1.226	1.225	-0.001	
5	209	1.225	1.224	-0.001	
10	210	1.225	1.224	-0.001	
10	211	1.226	1.226	0.000	
10	212	1.226	1.226	0.000	
10	213	1.226	1.226	0.000	
10	214	1.226	1.225	-0.001	
15	215	1.226	1.225	-0.001	
15	217	1.225	1.225	0.000	
15	219	1.225	1.225	0.000	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.225	1.225	0.000	
30	224	1.226	1.225	-0.001	
30	225	1.226	1.225	-0.001	
30	226	1.226	1.224	-0.002	
35	227	1.226	1.225	-0.001	
35	228	1.226	1.224	-0.002	
35	229	1.226	1.224	-0.002	
35	230	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.225	-0.001	
50	173	1.225	1.223	-0.002	
50	175	1.225	1.224	-0.001	
50	176	1.226	1.223	-0.003	
55	177	1.225	1.224	-0.001	
55	178	1.225	1.223	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.224	-0.002	
55	181	1.226	1.224	-0.002	
100	154	1.225	1.223	-0.002	
100	155	1.226	1.222	-0.004	
100	156	1.225	1.223	-0.002	
100	157	1.226	1.222	-0.004	
100	158	1.225	1.223	-0.002	
100	159	1.226	1.222	-0.004	
100	160	1.225	1.221	-0.004	
100	161	1.226	1.222	-0.004	
100	162	1.226	1.222	-0.004	
100	163	1.226	1.222	-0.004	
100	164	1.226	1.223	-0.003	
100	165	1.225	1.223	-0.002	
100	166	1.226	1.222	-0.004	
100	168	1.226	1.223	-0.003	
100	169	1.225	1.223	-0.002	
100	182	1.226	1.222	-0.004	
100	183	1.226	1.224	-0.002	
100	184	1.226	1.223	-0.003	
100	185	1.226	1.224	-0.002	
100	186	1.225	1.222	-0.003	
100	187	1.226	1.223	-0.003	
100	188	1.226	1.223	-0.003	
100	189	1.225	1.221	-0.004	
100	190	1.226	1.223	-0.003	
105	191	1.226	1.223	-0.003	
105	192	1.226	1.223	-0.003	
105	193	1.226	1.223	-0.003	
105	194	1.226	1.224	-0.002	
105	195	1.226	1.222	-0.004	
105	196	1.226	1.223	-0.003	
Max		1.226	1.226	0.000	
Average		1.226	1.224	-0.002	
Min		1.225	1.221	-0.004	
Std Dev		0.000	0.001	0.001	

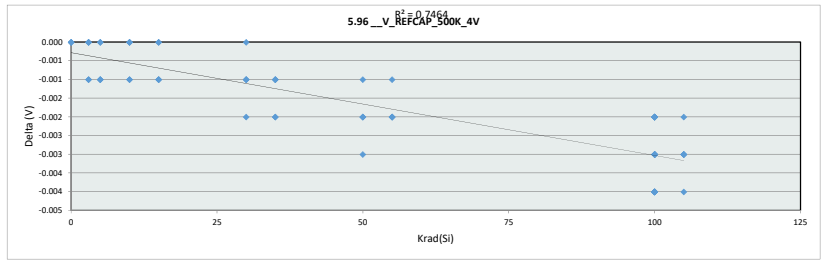


5.95_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit		1.237	V								
Min Limit		1.213	V								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.224	1.224	1.225	1.224	1.224	1.223	1.223	1.221	1.222
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.223
Max	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

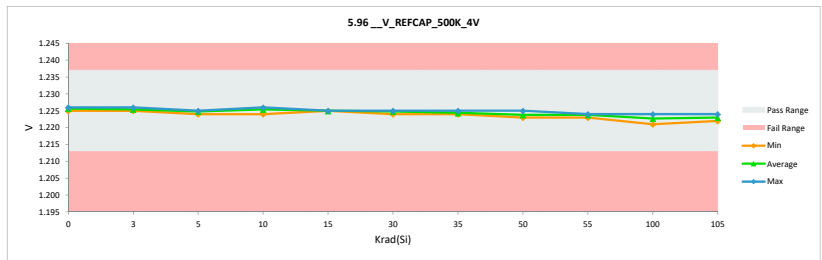


HDR TID Report  
TPS7H5003-SP

5.96_V_REFCAP_500K_4V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.225	1.225	0.000	
0	139	1.226	1.226	0.000	
3	197	1.226	1.226	0.000	
3	198	1.226	1.225	-0.001	
3	199	1.226	1.226	0.000	
3	202	1.225	1.225	0.000	
3	203	1.226	1.225	-0.001	
5	204	1.225	1.225	0.000	
5	205	1.226	1.225	-0.001	
5	206	1.225	1.225	0.000	
5	208	1.226	1.225	-0.001	
5	209	1.225	1.224	-0.001	
10	210	1.225	1.224	-0.001	
10	211	1.226	1.226	0.000	
10	212	1.226	1.226	0.000	
10	213	1.226	1.226	0.000	
10	214	1.226	1.225	-0.001	
15	215	1.226	1.225	-0.001	
15	217	1.225	1.225	0.000	
15	219	1.225	1.225	0.000	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.225	1.225	0.000	
30	224	1.226	1.225	-0.001	
30	225	1.226	1.225	-0.001	
30	226	1.226	1.224	-0.002	
35	227	1.226	1.225	-0.001	
35	228	1.226	1.224	-0.002	
35	229	1.226	1.224	-0.002	
35	230	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.225	-0.001	
50	173	1.225	1.223	-0.002	
50	175	1.226	1.224	-0.002	
50	176	1.226	1.223	-0.003	
55	177	1.225	1.224	-0.001	
55	178	1.225	1.223	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.224	-0.002	
55	181	1.226	1.224	-0.002	
100	154	1.225	1.223	-0.002	
100	155	1.226	1.222	-0.004	
100	156	1.226	1.223	-0.003	
100	157	1.226	1.222	-0.004	
100	158	1.225	1.223	-0.002	
100	159	1.226	1.223	-0.003	
100	160	1.225	1.221	-0.004	
100	161	1.226	1.222	-0.004	
100	162	1.226	1.222	-0.004	
100	163	1.226	1.222	-0.004	
100	164	1.226	1.223	-0.003	
100	165	1.226	1.223	-0.003	
100	166	1.226	1.222	-0.004	
100	168	1.226	1.224	-0.002	
100	169	1.225	1.223	-0.002	
100	182	1.226	1.222	-0.004	
100	183	1.226	1.224	-0.002	
100	184	1.226	1.224	-0.002	
100	185	1.226	1.224	-0.002	
100	186	1.225	1.223	-0.002	
100	187	1.226	1.223	-0.003	
100	188	1.226	1.223	-0.003	
100	189	1.225	1.221	-0.004	
100	190	1.226	1.223	-0.003	
105	191	1.226	1.223	-0.003	
105	192	1.226	1.223	-0.003	
105	193	1.226	1.223	-0.003	
105	194	1.226	1.224	-0.002	
105	195	1.226	1.222	-0.004	
105	196	1.226	1.223	-0.003	
Max		1.226	1.226	0.000	
Average		1.226	1.224	-0.002	
Min		1.225	1.221	-0.004	
Std Dev		0.000	0.001	0.001	

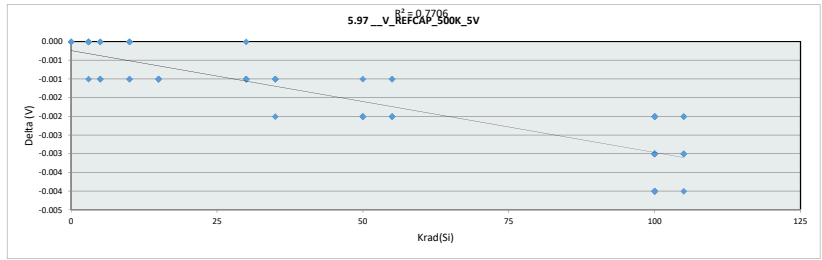


5.96_V_REFCAP											
Test Site											
Tester											
Test Number	1.237	V									
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.224	1.224	1.225	1.224	1.224	1.223	1.223	1.221	1.222
Average	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.223
Max	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

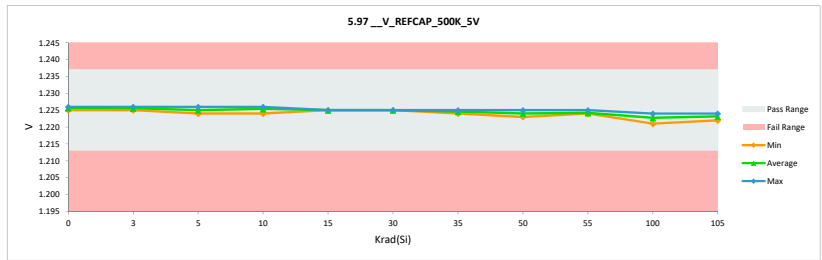


HDR TID Report  
TPS7H5003-SP

5.97_V_REFCAP_500K_5V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.225	1.225	0.000	
0	139	1.226	1.226	0.000	
3	197	1.226	1.226	0.000	
3	198	1.226	1.225	-0.001	
3	199	1.226	1.226	0.000	
3	202	1.225	1.225	0.000	
3	203	1.226	1.226	0.000	
5	204	1.226	1.225	-0.001	
5	205	1.226	1.225	-0.001	
5	206	1.225	1.225	0.000	
5	208	1.226	1.226	0.000	
5	209	1.225	1.224	-0.001	
10	210	1.225	1.224	-0.001	
10	211	1.226	1.226	0.000	
10	212	1.226	1.226	0.000	
10	213	1.226	1.226	0.000	
10	214	1.226	1.225	-0.001	
10	215	1.226	1.225	-0.001	
15	217	1.226	1.225	-0.001	
15	219	1.226	1.225	-0.001	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.225	1.225	0.000	
30	224	1.226	1.225	-0.001	
30	225	1.226	1.225	-0.001	
30	226	1.226	1.225	-0.001	
35	227	1.226	1.225	-0.001	
35	228	1.226	1.225	-0.001	
35	229	1.226	1.224	-0.002	
35	230	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.225	-0.001	
50	173	1.225	1.223	-0.002	
50	175	1.226	1.224	-0.002	
50	176	1.226	1.224	-0.002	
55	177	1.226	1.224	-0.002	
55	178	1.225	1.224	-0.001	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.225	-0.001	
55	181	1.226	1.224	-0.002	
100	154	1.226	1.223	-0.003	
100	155	1.226	1.222	-0.004	
100	156	1.226	1.223	-0.003	
100	157	1.226	1.222	-0.004	
100	158	1.225	1.223	-0.002	
100	159	1.226	1.223	-0.003	
100	160	1.225	1.221	-0.004	
100	161	1.226	1.222	-0.004	
100	162	1.226	1.222	-0.004	
100	163	1.226	1.223	-0.003	
100	164	1.226	1.223	-0.003	
100	165	1.226	1.223	-0.003	
100	166	1.226	1.222	-0.004	
100	168	1.226	1.224	-0.002	
100	169	1.225	1.223	-0.002	
100	182	1.226	1.222	-0.004	
100	183	1.226	1.224	-0.002	
100	184	1.226	1.224	-0.002	
100	185	1.226	1.224	-0.002	
100	186	1.225	1.223	-0.002	
100	187	1.226	1.223	-0.003	
100	188	1.226	1.223	-0.003	
100	189	1.225	1.221	-0.004	
100	190	1.226	1.223	-0.003	
105	191	1.226	1.223	-0.003	
105	192	1.226	1.223	-0.003	
105	193	1.226	1.223	-0.003	
105	194	1.226	1.224	-0.002	
105	195	1.226	1.222	-0.004	
105	196	1.226	1.224	-0.002	
Max		1.226	1.226	0.000	
Average		1.226	1.224	-0.002	
Min		1.225	1.221	-0.004	
Std Dev		0.000	0.001	0.001	



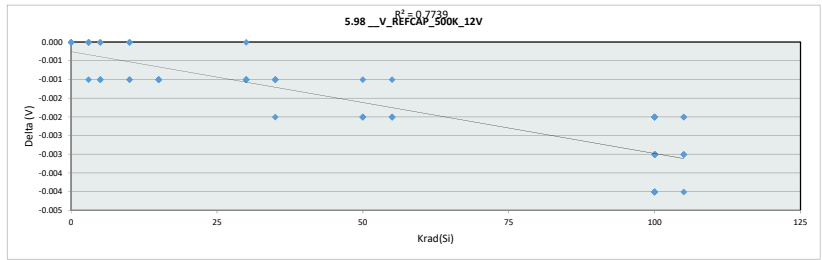
5.97_V_REFCAP											
Test Site											
Tester											
Test Number	1.237	V									
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.224	1.224	1.225	1.225	1.224	1.223	1.224	1.221	1.222
Average	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223
Max	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



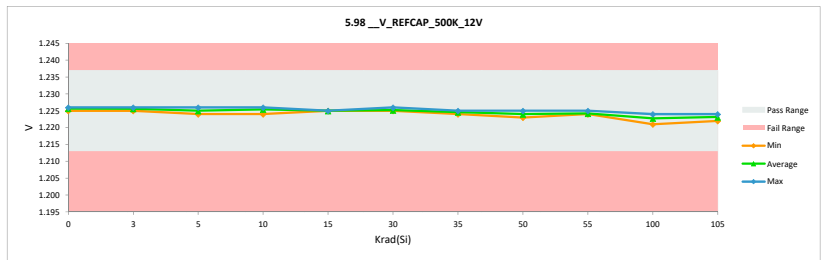


# HDR TID Report TPS7H5003-SP

5.98_V_REFCAP_500K_12V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.225	1.225	0.000	
0	139	1.226	1.226	0.000	
3	197	1.226	1.226	0.000	
3	198	1.226	1.225	-0.001	
3	199	1.226	1.226	0.000	
3	202	1.225	1.225	0.000	
3	203	1.226	1.226	0.000	
5	204	1.226	1.225	-0.001	
5	205	1.226	1.225	-0.001	
5	206	1.225	1.225	0.000	
5	208	1.226	1.226	0.000	
5	209	1.225	1.224	-0.001	
10	210	1.225	1.224	-0.001	
10	211	1.226	1.226	0.000	
10	212	1.226	1.226	0.000	
10	213	1.226	1.226	0.000	
10	214	1.226	1.225	-0.001	
10	215	1.226	1.225	-0.001	
15	217	1.226	1.225	-0.001	
15	219	1.226	1.225	-0.001	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.226	1.225	-0.001	
30	224	1.226	1.225	-0.001	
30	225	1.226	1.226	0.000	
30	226	1.226	1.225	-0.001	
35	227	1.226	1.225	-0.001	
35	228	1.226	1.225	-0.001	
35	229	1.226	1.224	-0.002	
35	230	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.225	-0.001	
50	173	1.225	1.223	-0.002	
50	175	1.226	1.224	-0.002	
50	176	1.226	1.224	-0.002	
55	177	1.226	1.224	-0.002	
55	178	1.226	1.224	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.225	-0.001	
55	181	1.226	1.224	-0.002	
100	154	1.226	1.223	-0.003	
100	155	1.226	1.222	-0.004	
100	156	1.226	1.223	-0.003	
100	157	1.226	1.222	-0.004	
100	158	1.225	1.223	-0.002	
100	159	1.226	1.223	-0.003	
100	160	1.225	1.221	-0.004	
100	161	1.226	1.222	-0.004	
100	162	1.226	1.222	-0.004	
100	163	1.226	1.223	-0.003	
100	164	1.226	1.223	-0.003	
100	165	1.226	1.223	-0.003	
100	166	1.226	1.222	-0.004	
100	168	1.226	1.224	-0.002	
100	169	1.225	1.223	-0.002	
100	182	1.226	1.222	-0.004	
100	183	1.226	1.224	-0.002	
100	184	1.226	1.224	-0.002	
100	185	1.226	1.224	-0.002	
100	186	1.225	1.223	-0.002	
100	187	1.226	1.223	-0.003	
100	188	1.226	1.223	-0.003	
100	189	1.225	1.221	-0.004	
100	190	1.226	1.223	-0.003	
105	191	1.226	1.223	-0.003	
105	192	1.226	1.223	-0.003	
105	193	1.226	1.223	-0.003	
105	194	1.226	1.224	-0.002	
105	195	1.226	1.222	-0.004	
105	196	1.226	1.224	-0.002	
Max		1.226	1.226	0.000	
Average		1.226	1.224	-0.002	
Min		1.225	1.221	-0.004	
Std Dev		0.000	0.001	0.001	

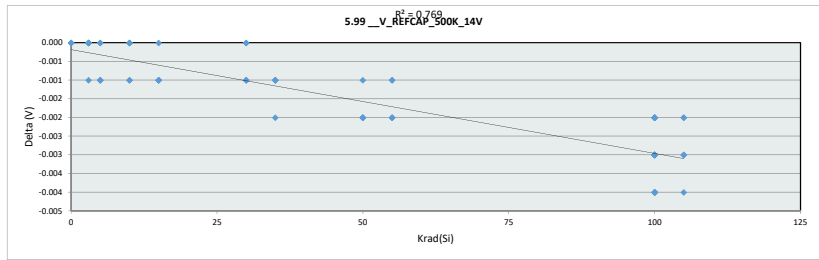


5.98_V_REFCAP											
Test Site											
Tester											
Test Number	1.237	V									
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.224	1.224	1.225	1.225	1.224	1.223	1.224	1.221	1.222
Average	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

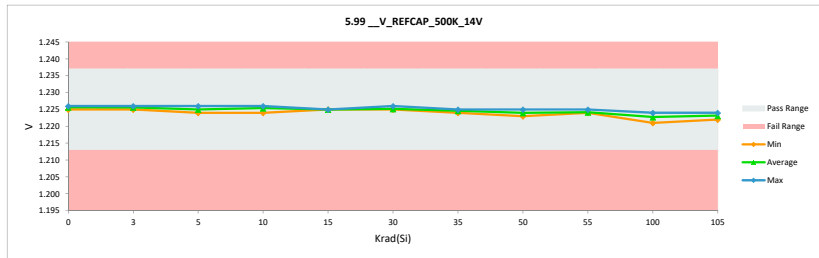


# HDR TID Report TPS7H5003-SP

5.99_V_REFCAP_500K_14V					
Test Site					
Tester					
Test Number					
Unit	V		V		
Max Limit	1.237		1.237		
Min Limit	1.213		1.213		
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.225	1.225	0.000	
0	139	1.226	1.226	0.000	
3	197	1.226	1.226	0.000	
3	198	1.226	1.225	-0.001	
3	199	1.226	1.226	0.000	
3	202	1.225	1.225	0.000	
3	203	1.226	1.226	0.000	
5	204	1.226	1.225	-0.001	
5	205	1.226	1.225	-0.001	
5	206	1.225	1.225	0.000	
5	208	1.226	1.226	0.000	
5	209	1.225	1.224	-0.001	
10	210	1.225	1.224	-0.001	
10	211	1.226	1.226	0.000	
10	212	1.226	1.226	0.000	
10	213	1.226	1.226	0.000	
10	214	1.226	1.225	-0.001	
10	215	1.226	1.225	-0.001	
15	217	1.225	1.225	0.000	
15	219	1.226	1.225	-0.001	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.225	1.225	0.000	
30	224	1.226	1.225	-0.001	
30	225	1.226	1.226	0.000	
30	226	1.226	1.225	-0.001	
35	227	1.226	1.225	-0.001	
35	228	1.226	1.225	-0.001	
35	229	1.226	1.224	-0.002	
35	230	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.225	-0.001	
50	173	1.225	1.223	-0.002	
50	175	1.226	1.224	-0.002	
50	176	1.226	1.224	-0.002	
55	177	1.225	1.224	-0.001	
55	178	1.226	1.224	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.225	-0.001	
55	181	1.226	1.224	-0.002	
100	154	1.226	1.223	-0.003	
100	155	1.226	1.222	-0.004	
100	156	1.226	1.223	-0.003	
100	157	1.226	1.222	-0.004	
100	158	1.225	1.223	-0.002	
100	159	1.226	1.223	-0.003	
100	160	1.225	1.221	-0.004	
100	161	1.226	1.222	-0.004	
100	162	1.226	1.222	-0.004	
100	163	1.226	1.223	-0.003	
100	164	1.226	1.223	-0.003	
100	165	1.226	1.223	-0.003	
100	166	1.226	1.222	-0.004	
100	168	1.226	1.224	-0.002	
100	169	1.225	1.223	-0.002	
100	182	1.226	1.222	-0.004	
100	183	1.226	1.224	-0.002	
100	184	1.226	1.224	-0.002	
100	185	1.226	1.224	-0.002	
100	186	1.225	1.223	-0.002	
100	187	1.226	1.223	-0.003	
100	188	1.226	1.223	-0.003	
100	189	1.225	1.221	-0.004	
100	190	1.226	1.223	-0.003	
105	191	1.226	1.223	-0.003	
105	192	1.226	1.223	-0.003	
105	193	1.226	1.223	-0.003	
105	194	1.226	1.224	-0.002	
105	195	1.226	1.222	-0.004	
105	196	1.226	1.224	-0.002	
Max		1.226	1.226	0.000	
Average		1.226	1.224	-0.002	
Min		1.225	1.221	-0.004	
Std Dev		0.000	0.001	0.001	

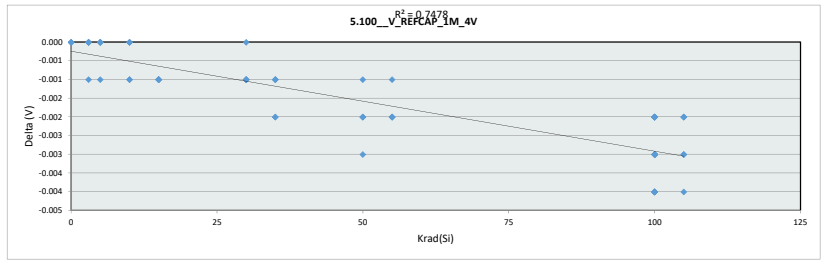


5.99_V_REFCAP											
Test Site											
Tester											
Test Number	1.237	V									
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.224	1.224	1.225	1.225	1.224	1.223	1.224	1.221	1.222
Average	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

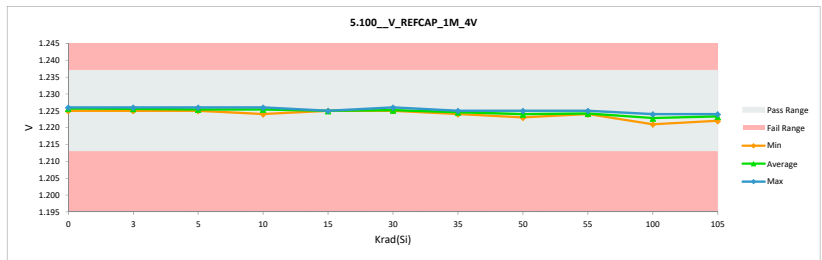


HDR TID Report  
TPS7H5003-SP

5.100_V_REFCAP_1M_4V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			V	1.237	1.213
			V	1.237	1.213
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.225	1.225	0.000	
0	139	1.226	1.226	0.000	
3	197	1.226	1.226	0.000	
3	198	1.226	1.225	-0.001	
3	199	1.226	1.226	0.000	
3	202	1.225	1.225	0.000	
3	203	1.226	1.226	0.000	
5	204	1.226	1.225	-0.001	
5	205	1.226	1.226	0.000	
5	206	1.225	1.225	0.000	
5	208	1.226	1.226	0.000	
5	209	1.225	1.225	0.000	
10	210	1.225	1.224	-0.001	
10	211	1.226	1.226	0.000	
10	212	1.226	1.226	0.000	
10	213	1.226	1.226	0.000	
10	214	1.226	1.225	-0.001	
15	215	1.226	1.225	-0.001	
15	217	1.226	1.225	-0.001	
15	219	1.226	1.225	-0.001	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.226	1.225	-0.001	
30	224	1.226	1.225	-0.001	
30	225	1.226	1.226	0.000	
30	226	1.226	1.225	-0.001	
35	227	1.227	1.225	-0.002	
35	228	1.226	1.225	-0.001	
35	229	1.226	1.224	-0.002	
35	230	1.225	1.224	-0.001	
35	231	1.226	1.225	-0.001	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.225	-0.001	
50	173	1.226	1.223	-0.003	
50	175	1.226	1.224	-0.002	
50	176	1.226	1.224	-0.002	
55	177	1.226	1.224	-0.002	
55	178	1.226	1.224	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.225	-0.001	
55	181	1.226	1.224	-0.002	
100	154	1.226	1.223	-0.003	
100	155	1.226	1.222	-0.004	
100	156	1.226	1.223	-0.003	
100	157	1.226	1.222	-0.004	
100	158	1.225	1.223	-0.002	
100	159	1.226	1.223	-0.003	
100	160	1.225	1.221	-0.004	
100	161	1.226	1.222	-0.004	
100	162	1.226	1.222	-0.004	
100	163	1.226	1.223	-0.003	
100	164	1.226	1.223	-0.003	
100	165	1.226	1.223	-0.003	
100	166	1.226	1.222	-0.004	
100	168	1.226	1.224	-0.002	
100	169	1.225	1.223	-0.002	
100	182	1.226	1.223	-0.003	
100	183	1.226	1.224	-0.002	
100	184	1.226	1.224	-0.002	
100	185	1.226	1.224	-0.002	
100	186	1.225	1.223	-0.002	
100	187	1.226	1.223	-0.003	
100	188	1.226	1.223	-0.003	
100	189	1.225	1.221	-0.004	
100	190	1.226	1.224	-0.002	
105	191	1.226	1.224	-0.002	
105	192	1.226	1.223	-0.003	
105	193	1.226	1.223	-0.003	
105	194	1.226	1.224	-0.002	
105	195	1.226	1.222	-0.004	
105	196	1.226	1.224	-0.002	
	Max	1.227	1.226	0.000	
	Average	1.226	1.224	-0.002	
	Min	1.225	1.221	-0.004	
	Std Dev	0.000	0.001	0.001	

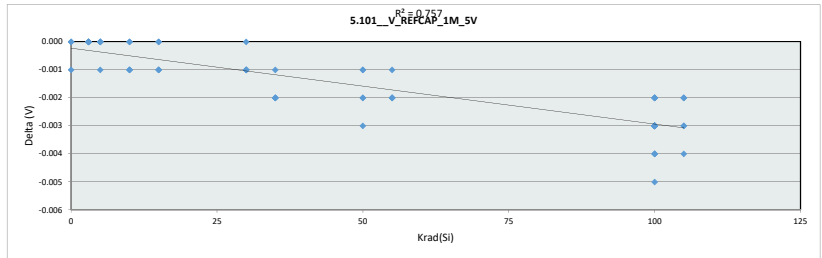


5.100_V_REFCAP											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	1.237	1.213						
			V	1.237	1.213						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.225	1.225	1.224	1.223	1.224	1.221	1.222
Average	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

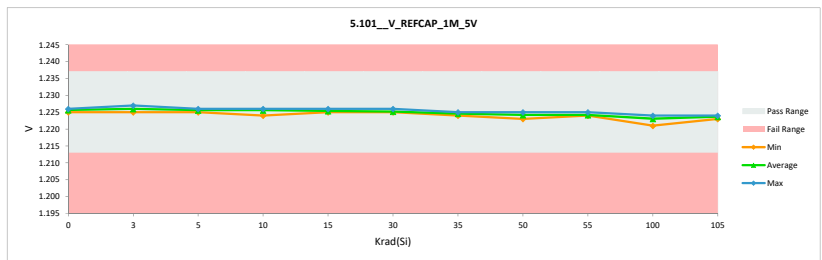


# HDR TID Report TPS7H5003-SP

5.101_V_REFCAP_1M_5V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.226	1.225	-0.001	
0	139	1.226	1.226	0.000	
3	197	1.227	1.227	0.000	
3	198	1.226	1.226	0.000	
3	199	1.226	1.226	0.000	
3	202	1.225	1.225	0.000	
3	203	1.226	1.226	0.000	
5	204	1.226	1.226	0.000	
5	205	1.226	1.226	0.000	
5	206	1.226	1.225	-0.001	
5	208	1.226	1.226	0.000	
5	209	1.225	1.225	0.000	
10	210	1.225	1.224	-0.001	
10	211	1.227	1.226	-0.001	
10	212	1.227	1.226	-0.001	
10	213	1.226	1.226	0.000	
10	214	1.226	1.226	0.000	
10	215	1.226	1.226	0.000	
15	217	1.226	1.225	-0.001	
15	219	1.226	1.225	-0.001	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.226	0.000	
30	222	1.226	1.225	-0.001	
30	223	1.226	1.225	-0.001	
30	224	1.226	1.225	-0.001	
30	225	1.226	1.226	0.000	
30	226	1.226	1.225	-0.001	
35	227	1.227	1.225	-0.002	
35	228	1.226	1.225	-0.001	
35	229	1.226	1.224	-0.002	
35	230	1.226	1.224	-0.002	
35	231	1.227	1.225	-0.002	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.225	-0.001	
50	173	1.226	1.223	-0.003	
50	175	1.226	1.225	-0.001	
50	176	1.226	1.224	-0.002	
55	177	1.226	1.224	-0.002	
55	178	1.226	1.224	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.225	-0.001	
55	181	1.226	1.224	-0.002	
100	154	1.226	1.223	-0.003	
100	155	1.227	1.222	-0.005	
100	156	1.226	1.223	-0.003	
100	157	1.226	1.223	-0.003	
100	158	1.226	1.223	-0.003	
100	159	1.226	1.223	-0.003	
100	160	1.225	1.221	-0.004	
100	161	1.226	1.223	-0.003	
100	162	1.226	1.222	-0.004	
100	163	1.226	1.223	-0.003	
100	164	1.226	1.223	-0.003	
100	165	1.226	1.224	-0.002	
100	166	1.226	1.223	-0.003	
100	168	1.226	1.224	-0.002	
100	169	1.226	1.223	-0.003	
100	182	1.226	1.223	-0.003	
100	183	1.227	1.224	-0.003	
100	184	1.226	1.224	-0.002	
100	185	1.227	1.224	-0.003	
100	186	1.226	1.223	-0.003	
100	187	1.226	1.223	-0.003	
100	188	1.226	1.224	-0.002	
100	189	1.226	1.222	-0.004	
100	190	1.226	1.224	-0.002	
105	191	1.227	1.224	-0.003	
105	192	1.226	1.224	-0.002	
105	193	1.226	1.223	-0.003	
105	194	1.226	1.224	-0.002	
105	195	1.227	1.223	-0.004	
105	196	1.226	1.224	-0.002	
Max		1.227	1.227	0.000	
Average		1.226	1.224	-0.002	
Min		1.225	1.221	-0.005	
Std Dev		0.000	0.001	0.001	

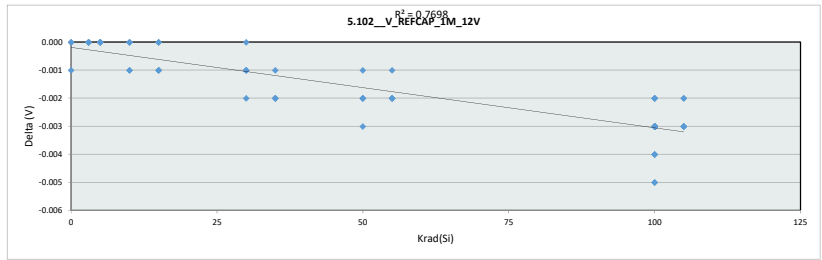


5.101_V_REFCAP											
Test Site											
Tester											
Test Number	1.237	V									
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.225	1.225	1.224	1.223	1.224	1.221	1.223
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224
Max	1.226	1.227	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

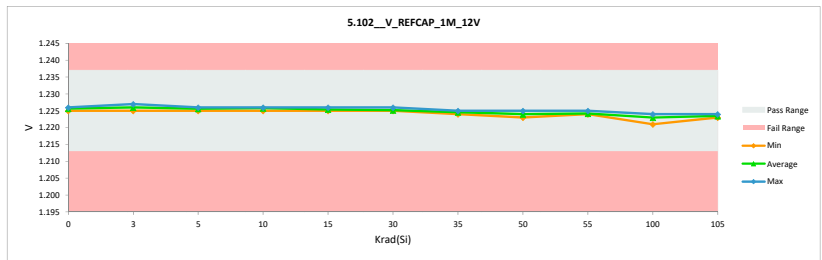


# HDR TID Report TPS7H5003-SP

		5.102_V_REFCAP_1M_12V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit		1.237	1.237	
Min Limit		1.213	1.213	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1.226	1.226	0.000
0	138	1.226	1.225	-0.001
0	139	1.226	1.226	0.000
3	197	1.227	1.227	0.000
3	198	1.226	1.226	0.000
3	199	1.226	1.226	0.000
3	202	1.225	1.225	0.000
3	203	1.226	1.226	0.000
5	204	1.226	1.226	0.000
5	205	1.226	1.226	0.000
5	206	1.225	1.225	0.000
5	208	1.226	1.226	0.000
5	209	1.225	1.225	0.000
10	210	1.225	1.225	0.000
10	211	1.227	1.226	-0.001
10	212	1.227	1.226	-0.001
10	213	1.226	1.226	0.000
10	214	1.226	1.226	0.000
15	215	1.226	1.226	0.000
15	217	1.226	1.225	-0.001
15	219	1.226	1.225	-0.001
15	220	1.226	1.225	-0.001
15	221	1.226	1.226	0.000
30	222	1.226	1.225	-0.001
30	223	1.226	1.225	-0.001
30	224	1.226	1.225	-0.001
30	225	1.226	1.226	0.000
30	226	1.227	1.225	-0.002
35	227	1.227	1.225	-0.002
35	228	1.226	1.225	-0.001
35	229	1.226	1.224	-0.002
35	230	1.226	1.224	-0.002
35	231	1.227	1.225	-0.002
50	170	1.226	1.224	-0.002
50	172	1.226	1.225	-0.001
50	173	1.226	1.223	-0.003
50	175	1.226	1.224	-0.002
50	176	1.226	1.224	-0.002
55	177	1.226	1.224	-0.002
55	178	1.226	1.224	-0.002
55	179	1.226	1.224	-0.002
55	180	1.226	1.225	-0.001
55	181	1.226	1.224	-0.002
100	154	1.226	1.223	-0.003
100	155	1.227	1.222	-0.005
100	156	1.226	1.223	-0.003
100	157	1.226	1.223	-0.003
100	158	1.226	1.223	-0.003
100	159	1.226	1.223	-0.003
100	160	1.225	1.221	-0.004
100	161	1.226	1.223	-0.003
100	162	1.226	1.222	-0.004
100	163	1.226	1.223	-0.003
100	164	1.226	1.223	-0.003
100	165	1.226	1.224	-0.002
100	166	1.226	1.222	-0.004
100	168	1.226	1.224	-0.002
100	169	1.226	1.223	-0.003
100	182	1.226	1.223	-0.003
100	183	1.227	1.224	-0.003
100	184	1.227	1.224	-0.003
100	185	1.227	1.224	-0.003
100	186	1.226	1.223	-0.003
100	187	1.226	1.223	-0.003
100	188	1.226	1.224	-0.002
100	189	1.226	1.221	-0.005
100	190	1.226	1.224	-0.002
105	191	1.227	1.224	-0.003
105	192	1.226	1.223	-0.003
105	193	1.226	1.223	-0.003
105	194	1.226	1.224	-0.002
105	195	1.226	1.223	-0.003
105	196	1.226	1.224	-0.002
	Max	1.227	1.227	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.001	0.001

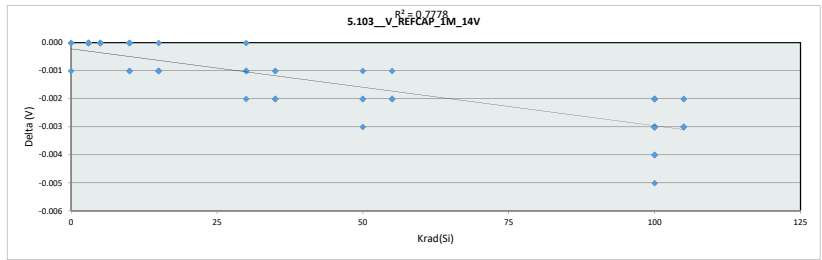


		5.102_V_REFCAP										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit		1.237	V									
Min Limit		1.213	V									
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min		1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.223	1.224	1.221	1.223
Average		1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224
Max		1.226	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224
UL		1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

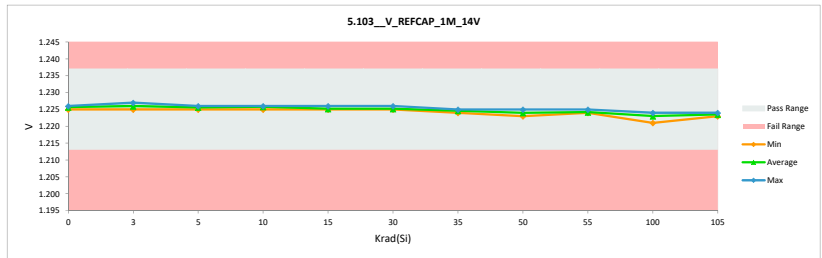


HDR TID Report  
TPS7H5003-SP

5.103_V_REFCAP_1M_14V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			V	1.237	1.237
			V	1.213	1.213
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.226	1.226	0.000	
0	138	1.226	1.225	-0.001	
0	139	1.226	1.226	0.000	
3	197	1.227	1.227	0.000	
3	198	1.226	1.226	0.000	
3	199	1.226	1.226	0.000	
3	202	1.225	1.225	0.000	
3	203	1.226	1.226	0.000	
5	204	1.226	1.226	0.000	
5	205	1.226	1.226	0.000	
5	206	1.225	1.225	0.000	
5	208	1.226	1.226	0.000	
5	209	1.225	1.225	0.000	
10	210	1.225	1.225	0.000	
10	211	1.227	1.226	-0.001	
10	212	1.227	1.226	-0.001	
10	213	1.226	1.226	0.000	
10	214	1.226	1.226	0.000	
15	215	1.226	1.226	0.000	
15	217	1.226	1.225	-0.001	
15	219	1.226	1.225	-0.001	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.225	-0.001	
30	222	1.226	1.225	-0.001	
30	223	1.226	1.225	-0.001	
30	224	1.226	1.225	-0.001	
30	225	1.226	1.226	0.000	
30	226	1.227	1.225	-0.002	
35	227	1.227	1.225	-0.002	
35	228	1.226	1.225	-0.001	
35	229	1.226	1.224	-0.002	
35	230	1.226	1.224	-0.002	
35	231	1.226	1.225	-0.001	
50	170	1.226	1.224	-0.002	
50	172	1.226	1.225	-0.001	
50	173	1.226	1.223	-0.003	
50	175	1.226	1.224	-0.002	
50	176	1.226	1.224	-0.002	
55	177	1.226	1.224	-0.002	
55	178	1.226	1.224	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.226	1.225	-0.001	
55	181	1.226	1.224	-0.002	
100	154	1.226	1.223	-0.003	
100	155	1.227	1.222	-0.005	
100	156	1.226	1.223	-0.003	
100	157	1.226	1.223	-0.003	
100	158	1.226	1.223	-0.003	
100	159	1.226	1.223	-0.003	
100	160	1.225	1.221	-0.004	
100	161	1.226	1.223	-0.003	
100	162	1.226	1.222	-0.004	
100	163	1.226	1.223	-0.003	
100	164	1.226	1.223	-0.003	
100	165	1.226	1.224	-0.002	
100	166	1.226	1.222	-0.004	
100	168	1.226	1.224	-0.002	
100	169	1.226	1.223	-0.003	
100	182	1.226	1.223	-0.003	
100	183	1.227	1.224	-0.003	
100	184	1.226	1.224	-0.002	
100	185	1.227	1.224	-0.003	
100	186	1.226	1.223	-0.003	
100	187	1.226	1.223	-0.003	
100	188	1.226	1.224	-0.002	
100	189	1.225	1.222	-0.003	
100	190	1.226	1.224	-0.002	
105	191	1.227	1.224	-0.003	
105	192	1.226	1.223	-0.003	
105	193	1.226	1.223	-0.003	
105	194	1.226	1.224	-0.002	
105	195	1.226	1.223	-0.003	
105	196	1.226	1.224	-0.002	
	Max	1.227	1.227	0.000	
	Average	1.226	1.224	-0.002	
	Min	1.225	1.221	-0.005	
	Std Dev	0.000	0.001	0.001	

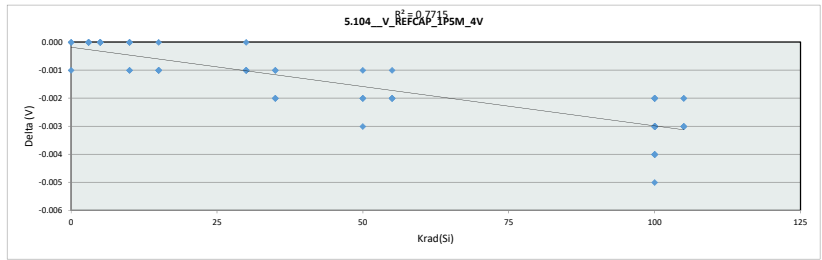


5.103_V_REFCAP											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	1.237	1.237						
			V	1.213	1.213						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.223	1.224	1.221	1.223
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224
Max	1.226	1.227	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

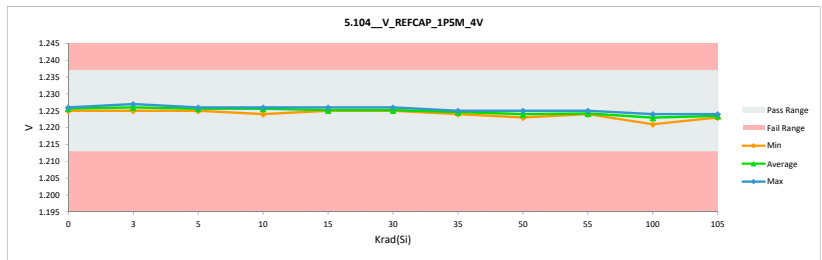


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		5.104_V_REFCAP_1P5M_4V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit		1.237	1.237	
Min Limit		1.213	1.213	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1.226	1.226	0.000
0	138	1.226	1.225	-0.001
0	139	1.226	1.226	0.000
3	197	1.227	1.227	0.000
3	198	1.226	1.226	0.000
3	199	1.226	1.226	0.000
3	202	1.225	1.225	0.000
3	203	1.226	1.226	0.000
5	204	1.226	1.226	0.000
5	205	1.226	1.226	0.000
5	206	1.225	1.225	0.000
5	208	1.226	1.226	0.000
5	209	1.225	1.225	0.000
10	210	1.225	1.224	-0.001
10	211	1.227	1.226	-0.001
10	212	1.226	1.226	0.000
10	213	1.226	1.226	0.000
10	214	1.226	1.226	0.000
10	215	1.226	1.226	0.000
15	217	1.226	1.225	-0.001
15	219	1.226	1.225	-0.001
15	220	1.226	1.225	-0.001
15	221	1.226	1.225	-0.001
30	222	1.226	1.225	-0.001
30	223	1.226	1.225	-0.001
30	224	1.226	1.225	-0.001
30	225	1.226	1.226	0.000
30	226	1.226	1.225	-0.001
35	227	1.227	1.225	-0.002
35	228	1.226	1.225	-0.001
35	229	1.226	1.224	-0.002
35	230	1.226	1.224	-0.002
35	231	1.226	1.225	-0.001
50	170	1.226	1.224	-0.002
50	172	1.226	1.225	-0.001
50	173	1.226	1.223	-0.003
50	175	1.226	1.224	-0.002
50	176	1.226	1.224	-0.002
55	177	1.226	1.224	-0.002
55	178	1.226	1.224	-0.002
55	179	1.226	1.224	-0.002
55	180	1.226	1.225	-0.001
55	181	1.226	1.224	-0.002
100	154	1.226	1.223	-0.003
100	155	1.227	1.222	-0.005
100	156	1.226	1.223	-0.003
100	157	1.226	1.223	-0.003
100	158	1.226	1.223	-0.003
100	159	1.226	1.223	-0.003
100	160	1.225	1.221	-0.004
100	161	1.226	1.222	-0.004
100	162	1.226	1.222	-0.004
100	163	1.226	1.223	-0.003
100	164	1.226	1.223	-0.003
100	165	1.226	1.224	-0.002
100	166	1.226	1.222	-0.004
100	168	1.226	1.224	-0.002
100	169	1.226	1.223	-0.003
100	182	1.226	1.223	-0.003
100	183	1.226	1.224	-0.002
100	184	1.226	1.224	-0.002
100	185	1.227	1.224	-0.003
100	186	1.226	1.223	-0.003
100	187	1.226	1.223	-0.003
100	188	1.226	1.224	-0.002
100	189	1.225	1.221	-0.004
100	190	1.226	1.224	-0.002
105	191	1.227	1.224	-0.003
105	192	1.226	1.223	-0.003
105	193	1.226	1.223	-0.003
105	194	1.226	1.224	-0.002
105	195	1.226	1.223	-0.003
105	196	1.226	1.224	-0.002
	Max	1.227	1.227	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.001	0.001

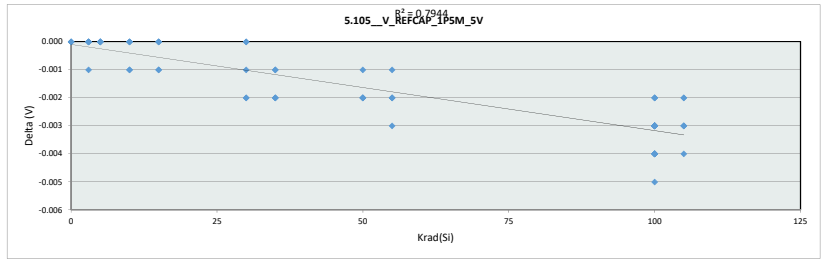


		5.104_V_REFCAP										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit		1.237	V									
Min Limit		1.213	V									
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min		1.225	1.225	1.225	1.224	1.225	1.225	1.224	1.223	1.224	1.221	1.223
Average		1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224
Max		1.226	1.227	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224
UL		1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

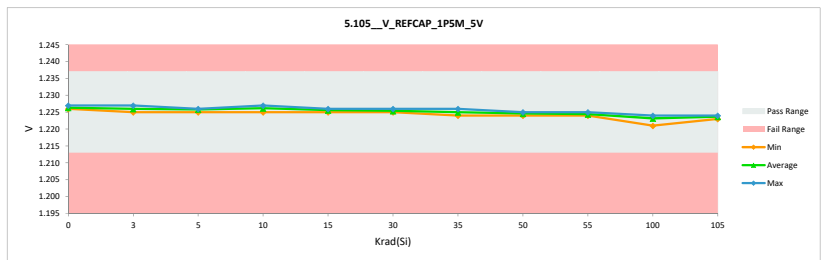


HDR TID Report  
TPS7H5003-SP

5.105_V_REFCAP_1P5M_5V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			V	1.237	1.213
			V	1.237	1.213
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.227	1.227	0.000	
0	138	1.226	1.226	0.000	
0	139	1.226	1.226	0.000	
3	197	1.227	1.227	0.000	
3	198	1.226	1.226	0.000	
3	199	1.226	1.226	0.000	
3	202	1.226	1.225	-0.001	
3	203	1.226	1.226	0.000	
5	204	1.226	1.226	0.000	
5	205	1.226	1.226	0.000	
5	206	1.226	1.226	0.000	
5	208	1.226	1.226	0.000	
5	209	1.225	1.225	0.000	
10	210	1.226	1.225	-0.001	
10	211	1.227	1.227	0.000	
10	212	1.227	1.227	0.000	
10	213	1.227	1.226	-0.001	
10	214	1.226	1.226	0.000	
15	215	1.226	1.226	0.000	
15	217	1.226	1.226	0.000	
15	219	1.226	1.225	-0.001	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.226	0.000	
30	222	1.227	1.225	-0.002	
30	223	1.226	1.225	-0.001	
30	224	1.226	1.226	0.000	
30	225	1.226	1.226	0.000	
30	226	1.227	1.225	-0.002	
35	227	1.227	1.226	-0.001	
35	228	1.227	1.225	-0.002	
35	229	1.226	1.225	-0.001	
35	230	1.226	1.224	-0.002	
35	231	1.227	1.225	-0.002	
50	170	1.227	1.225	-0.002	
50	172	1.227	1.225	-0.002	
50	173	1.226	1.224	-0.002	
50	175	1.226	1.225	-0.001	
50	176	1.226	1.224	-0.002	
55	177	1.226	1.225	-0.001	
55	178	1.226	1.224	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.227	1.225	-0.002	
55	181	1.227	1.224	-0.003	
100	154	1.226	1.223	-0.003	
100	155	1.227	1.222	-0.005	
100	156	1.226	1.224	-0.002	
100	157	1.226	1.223	-0.003	
100	158	1.226	1.223	-0.003	
100	159	1.227	1.223	-0.004	
100	160	1.225	1.221	-0.004	
100	161	1.227	1.223	-0.004	
100	162	1.226	1.222	-0.004	
100	163	1.226	1.223	-0.003	
100	164	1.227	1.224	-0.003	
100	165	1.226	1.224	-0.002	
100	166	1.227	1.223	-0.004	
100	168	1.227	1.224	-0.003	
100	169	1.226	1.223	-0.003	
100	182	1.226	1.223	-0.003	
100	183	1.227	1.224	-0.003	
100	184	1.227	1.224	-0.003	
100	185	1.227	1.224	-0.003	
100	186	1.226	1.223	-0.003	
100	187	1.227	1.223	-0.004	
100	188	1.226	1.224	-0.002	
100	189	1.226	1.222	-0.004	
100	190	1.227	1.224	-0.003	
105	191	1.227	1.224	-0.003	
105	192	1.226	1.224	-0.002	
105	193	1.226	1.223	-0.003	
105	194	1.227	1.224	-0.003	
105	195	1.227	1.223	-0.004	
105	196	1.226	1.224	-0.002	
	Max	1.227	1.227	0.000	
	Average	1.226	1.225	-0.002	
	Min	1.225	1.221	-0.005	
	Std Dev	0.001	0.001	0.001	



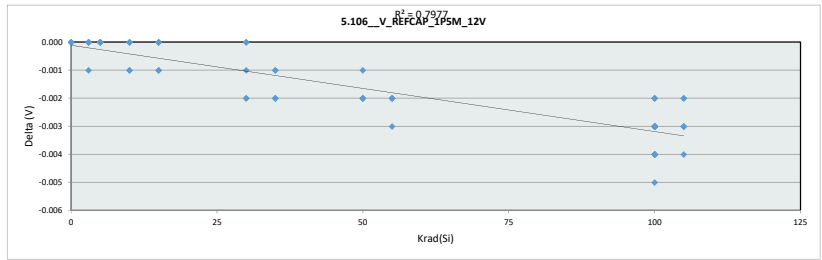
5.105_V_REFCAP											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	1.237	1.213						
			V	1.237	1.213						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.221	1.223
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.223	1.224
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.226	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



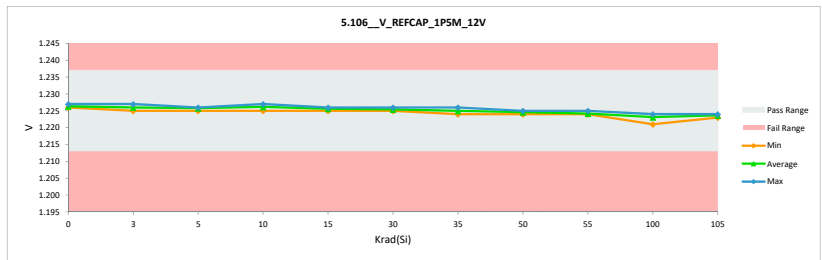


HDR TID Report  
TPS7H5003-SP

5.106_V_REFCAP_1P5M_12V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.227	1.227	0.000	
0	138	1.226	1.226	0.000	
0	139	1.226	1.226	0.000	
3	197	1.227	1.227	0.000	
3	198	1.226	1.226	0.000	
3	199	1.226	1.226	0.000	
3	202	1.226	1.225	-0.001	
3	203	1.226	1.226	0.000	
5	204	1.226	1.226	0.000	
5	205	1.226	1.226	0.000	
5	206	1.226	1.226	0.000	
5	208	1.226	1.226	0.000	
5	209	1.225	1.225	0.000	
10	210	1.226	1.225	-0.001	
10	211	1.227	1.227	0.000	
10	212	1.227	1.227	0.000	
10	213	1.227	1.226	-0.001	
10	214	1.226	1.226	0.000	
15	215	1.226	1.226	0.000	
15	217	1.226	1.226	0.000	
15	219	1.226	1.225	-0.001	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.226	0.000	
30	222	1.227	1.225	-0.002	
30	223	1.226	1.225	-0.001	
30	224	1.226	1.226	0.000	
30	225	1.226	1.226	0.000	
30	226	1.227	1.225	-0.002	
35	227	1.227	1.226	-0.001	
35	228	1.227	1.225	-0.002	
35	229	1.226	1.225	-0.001	
35	230	1.226	1.224	-0.002	
35	231	1.227	1.225	-0.002	
50	170	1.227	1.225	-0.002	
50	172	1.227	1.225	-0.002	
50	173	1.226	1.224	-0.002	
50	175	1.226	1.225	-0.001	
50	176	1.226	1.224	-0.002	
55	177	1.226	1.224	-0.002	
55	178	1.226	1.224	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.227	1.225	-0.002	
55	181	1.227	1.224	-0.003	
100	154	1.226	1.223	-0.003	
100	155	1.227	1.222	-0.005	
100	156	1.226	1.224	-0.002	
100	157	1.226	1.223	-0.003	
100	158	1.226	1.223	-0.003	
100	159	1.227	1.223	-0.004	
100	160	1.225	1.221	-0.004	
100	161	1.227	1.223	-0.004	
100	162	1.226	1.222	-0.004	
100	163	1.226	1.223	-0.003	
100	164	1.226	1.223	-0.003	
100	165	1.226	1.224	-0.002	
100	166	1.227	1.223	-0.004	
100	168	1.227	1.224	-0.003	
100	169	1.226	1.223	-0.003	
100	182	1.226	1.223	-0.003	
100	183	1.227	1.224	-0.003	
100	184	1.227	1.224	-0.003	
100	185	1.227	1.224	-0.003	
100	186	1.226	1.223	-0.003	
100	187	1.227	1.223	-0.004	
100	188	1.226	1.224	-0.002	
100	189	1.226	1.222	-0.004	
100	190	1.227	1.224	-0.003	
105	191	1.227	1.224	-0.003	
105	192	1.226	1.224	-0.002	
105	193	1.226	1.223	-0.003	
105	194	1.227	1.224	-0.003	
105	195	1.227	1.223	-0.004	
105	196	1.226	1.224	-0.002	
	Max	1.227	1.227	0.000	
	Average	1.226	1.225	-0.002	
	Min	1.225	1.221	-0.005	
	Std Dev	0.001	0.001	0.001	

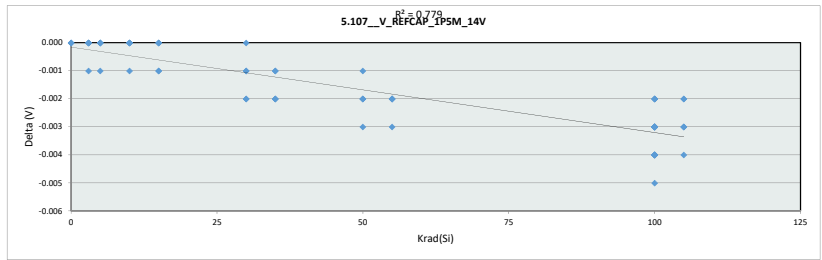


5.106_V_REFCAP											
Test Site											
Tester											
Test Number	1.237	V									
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.221	1.223
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.223	1.224
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.226	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

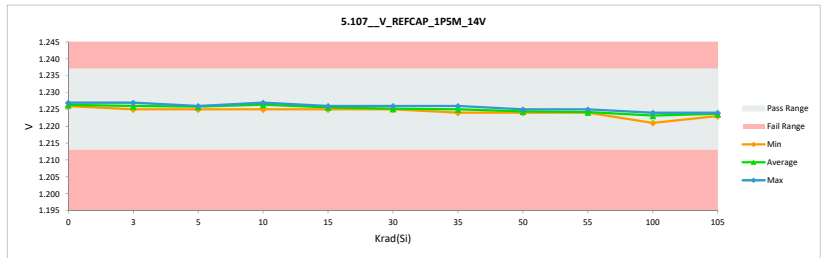


# HDR TID Report TPS7H5003-SP

		5.107_V_REFCAP_1P5M_14V		
Test Site				
Tester				
Test Number				
Unit		V	V	
Max Limit		1.237	1.237	
Min Limit		1.213	1.213	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1.227	1.227	0.000
0	138	1.226	1.226	0.000
0	139	1.226	1.226	0.000
3	197	1.227	1.227	0.000
3	198	1.226	1.226	0.000
3	199	1.226	1.226	0.000
3	202	1.226	1.225	-0.001
3	203	1.226	1.226	0.000
5	204	1.226	1.226	0.000
5	205	1.226	1.226	0.000
5	206	1.226	1.226	0.000
5	208	1.226	1.226	0.000
5	209	1.226	1.225	-0.001
10	210	1.226	1.225	-0.001
10	211	1.227	1.227	0.000
10	212	1.227	1.227	0.000
10	213	1.227	1.227	0.000
10	214	1.226	1.226	0.000
15	215	1.226	1.226	0.000
15	217	1.226	1.226	0.000
15	219	1.226	1.225	-0.001
15	220	1.226	1.225	-0.001
15	221	1.226	1.226	0.000
30	222	1.227	1.225	-0.002
30	223	1.226	1.225	-0.001
30	224	1.226	1.225	-0.001
30	225	1.226	1.226	0.000
30	226	1.227	1.225	-0.002
35	227	1.227	1.226	-0.001
35	228	1.227	1.225	-0.002
35	229	1.226	1.225	-0.001
35	230	1.226	1.224	-0.002
35	231	1.227	1.225	-0.002
50	170	1.227	1.224	-0.003
50	172	1.227	1.225	-0.002
50	173	1.226	1.224	-0.002
50	175	1.226	1.225	-0.001
50	176	1.226	1.224	-0.002
55	177	1.226	1.224	-0.002
55	178	1.226	1.224	-0.002
55	179	1.226	1.224	-0.002
55	180	1.227	1.225	-0.002
55	181	1.227	1.224	-0.003
100	154	1.226	1.223	-0.003
100	155	1.227	1.222	-0.005
100	156	1.226	1.224	-0.002
100	157	1.226	1.223	-0.003
100	158	1.226	1.223	-0.003
100	159	1.227	1.223	-0.004
100	160	1.225	1.221	-0.004
100	161	1.227	1.223	-0.004
100	162	1.226	1.222	-0.004
100	163	1.226	1.223	-0.003
100	164	1.227	1.223	-0.004
100	165	1.226	1.224	-0.002
100	166	1.227	1.223	-0.004
100	168	1.227	1.224	-0.003
100	169	1.226	1.224	-0.002
100	182	1.226	1.223	-0.003
100	183	1.227	1.224	-0.003
100	184	1.227	1.224	-0.003
100	185	1.227	1.224	-0.003
100	186	1.226	1.223	-0.003
100	187	1.227	1.223	-0.004
100	188	1.226	1.224	-0.002
100	189	1.226	1.222	-0.004
100	190	1.227	1.224	-0.003
105	191	1.227	1.224	-0.003
105	192	1.226	1.224	-0.002
105	193	1.226	1.223	-0.003
105	194	1.227	1.224	-0.003
105	195	1.227	1.223	-0.004
105	196	1.226	1.224	-0.002
	Max	1.227	1.227	0.000
	Average	1.226	1.225	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.001	0.001	0.001

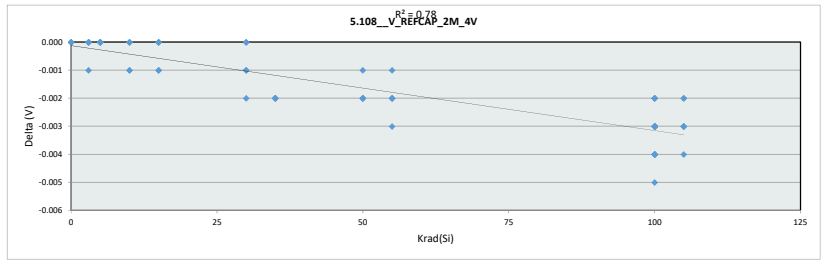


		5.107_V_REFCAP											
Test Site													
Tester													
Test Number		1.237	V										
Max Limit		1.237	V										
Min Limit		1.213	V										
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105	
LL		1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	
Min		1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.221	1.223	
Average		1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223	1.224	
Max		1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224	1.224	
UL		1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	

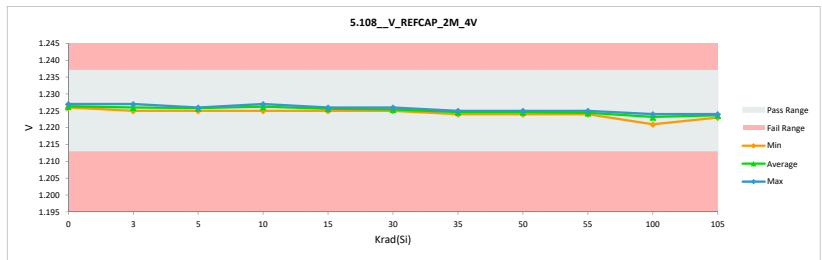


HDR TID Report  
TPS7H5003-SP

5.108_V_REFCAP_2M_4V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.227	1.227	0.000	
0	138	1.226	1.226	0.000	
0	139	1.226	1.226	0.000	
3	197	1.227	1.227	0.000	
3	198	1.226	1.226	0.000	
3	199	1.226	1.226	0.000	
3	202	1.226	1.225	-0.001	
3	203	1.226	1.226	0.000	
5	204	1.226	1.226	0.000	
5	205	1.226	1.226	0.000	
5	206	1.226	1.226	0.000	
5	208	1.226	1.226	0.000	
5	209	1.225	1.225	0.000	
10	210	1.226	1.225	-0.001	
10	211	1.227	1.227	0.000	
10	212	1.227	1.227	0.000	
10	213	1.227	1.226	-0.001	
10	214	1.226	1.226	0.000	
15	215	1.226	1.226	0.000	
15	217	1.226	1.226	0.000	
15	219	1.226	1.225	-0.001	
15	220	1.226	1.225	-0.001	
15	221	1.226	1.226	0.000	
30	222	1.226	1.225	-0.001	
30	223	1.226	1.225	-0.001	
30	224	1.226	1.226	0.000	
30	225	1.226	1.226	0.000	
30	226	1.227	1.225	-0.002	
35	227	1.227	1.225	-0.002	
35	228	1.227	1.225	-0.002	
35	229	1.226	1.224	-0.002	
35	230	1.226	1.224	-0.002	
35	231	1.227	1.225	-0.002	
50	170	1.227	1.225	-0.002	
50	172	1.227	1.225	-0.002	
50	173	1.226	1.224	-0.002	
50	175	1.226	1.225	-0.001	
50	176	1.226	1.224	-0.002	
55	177	1.226	1.225	-0.001	
55	178	1.226	1.224	-0.002	
55	179	1.226	1.224	-0.002	
55	180	1.227	1.225	-0.002	
55	181	1.227	1.224	-0.003	
100	154	1.226	1.223	-0.003	
100	155	1.227	1.222	-0.005	
100	156	1.226	1.224	-0.002	
100	157	1.226	1.223	-0.003	
100	158	1.226	1.223	-0.003	
100	159	1.227	1.223	-0.004	
100	160	1.225	1.221	-0.004	
100	161	1.227	1.223	-0.004	
100	162	1.226	1.222	-0.004	
100	163	1.226	1.223	-0.003	
100	164	1.226	1.224	-0.002	
100	165	1.226	1.224	-0.002	
100	166	1.227	1.223	-0.004	
100	168	1.227	1.224	-0.003	
100	169	1.226	1.223	-0.003	
100	182	1.226	1.223	-0.003	
100	183	1.227	1.224	-0.003	
100	184	1.227	1.224	-0.003	
100	185	1.227	1.224	-0.003	
100	186	1.226	1.223	-0.003	
100	187	1.227	1.223	-0.004	
100	188	1.226	1.224	-0.002	
100	189	1.226	1.222	-0.004	
100	190	1.227	1.224	-0.003	
105	191	1.227	1.224	-0.003	
105	192	1.226	1.224	-0.002	
105	193	1.226	1.223	-0.003	
105	194	1.227	1.224	-0.003	
105	195	1.227	1.223	-0.004	
105	196	1.226	1.224	-0.002	
Max		1.227	1.227	0.000	
Average		1.226	1.225	-0.002	
Min		1.225	1.221	-0.005	
Std Dev		0.001	0.001	0.001	

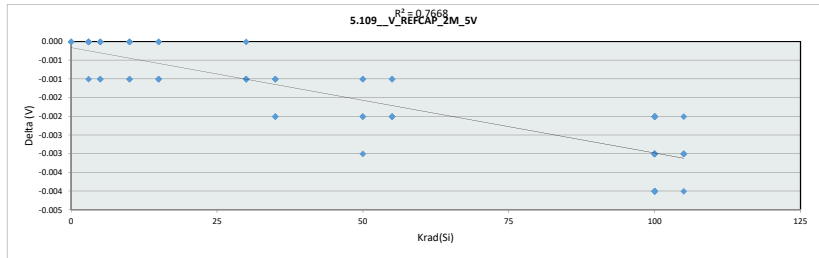


5.108_V_REFCAP											
Test Site											
Tester											
Test Number	1.237	V									
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.221	1.223
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.223	1.224
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.225	1.225	1.225	1.224	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

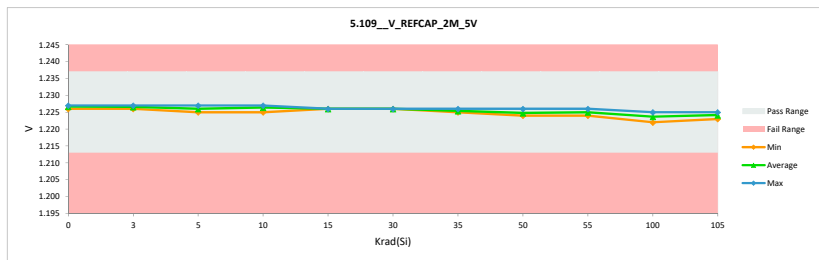


# HDR TID Report TPS7H5003-SP

5.109_V_REFCAP_2M_5V					
Test Site	Tester	Test Number	Unit	V	V
Max Limit				1.237	1.237
Min Limit				1.213	1.213
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.227	1.227	0.000	
0	138	1.226	1.226	0.000	
0	139	1.227	1.227	0.000	
3	197	1.227	1.227	0.000	
3	198	1.227	1.226	-0.001	
3	199	1.227	1.227	0.000	
3	202	1.226	1.226	0.000	
3	203	1.227	1.227	0.000	
5	204	1.226	1.226	0.000	
5	205	1.227	1.226	-0.001	
5	206	1.226	1.226	0.000	
5	208	1.227	1.227	0.000	
5	209	1.226	1.225	-0.001	
10	210	1.226	1.225	-0.001	
10	211	1.227	1.227	0.000	
10	212	1.227	1.227	0.000	
10	213	1.227	1.227	0.000	
10	214	1.227	1.226	-0.001	
10	215	1.227	1.226	-0.001	
15	217	1.226	1.226	0.000	
15	219	1.226	1.226	0.000	
15	220	1.227	1.226	-0.001	
15	221	1.227	1.226	-0.001	
30	222	1.227	1.226	-0.001	
30	223	1.226	1.226	0.000	
30	224	1.227	1.226	-0.001	
30	225	1.227	1.226	-0.001	
30	226	1.227	1.226	-0.001	
35	227	1.227	1.226	-0.001	
35	228	1.227	1.225	-0.002	
35	229	1.227	1.225	-0.002	
35	230	1.226	1.225	-0.001	
35	231	1.227	1.226	-0.001	
50	170	1.227	1.225	-0.002	
50	172	1.227	1.226	-0.001	
50	173	1.226	1.224	-0.002	
50	175	1.226	1.225	-0.001	
50	176	1.227	1.224	-0.003	
55	177	1.226	1.225	-0.001	
55	178	1.226	1.224	-0.002	
55	179	1.227	1.225	-0.002	
55	180	1.227	1.226	-0.001	
55	181	1.227	1.225	-0.002	
100	154	1.226	1.224	-0.002	
100	155	1.227	1.223	-0.004	
100	156	1.227	1.224	-0.003	
100	157	1.227	1.223	-0.004	
100	158	1.226	1.224	-0.002	
100	159	1.227	1.224	-0.003	
100	160	1.226	1.222	-0.004	
100	161	1.227	1.223	-0.004	
100	162	1.227	1.223	-0.004	
100	163	1.227	1.223	-0.004	
100	164	1.227	1.224	-0.003	
100	165	1.226	1.224	-0.002	
100	166	1.227	1.223	-0.004	
100	168	1.227	1.225	-0.002	
100	169	1.226	1.224	-0.002	
100	182	1.226	1.223	-0.003	
100	183	1.227	1.225	-0.002	
100	184	1.227	1.224	-0.003	
100	185	1.227	1.225	-0.002	
100	186	1.226	1.224	-0.002	
100	187	1.227	1.224	-0.003	
100	188	1.227	1.224	-0.003	
100	189	1.226	1.222	-0.004	
100	190	1.227	1.224	-0.003	
105	191	1.228	1.225	-0.003	
105	192	1.227	1.224	-0.003	
105	193	1.227	1.224	-0.003	
105	194	1.227	1.225	-0.002	
105	195	1.227	1.223	-0.004	
105	196	1.227	1.224	-0.003	
Max		1.228	1.227	0.000	
Average		1.227	1.225	-0.002	
Min		1.226	1.222	-0.004	
Std Dev		0.000	0.001	0.001	



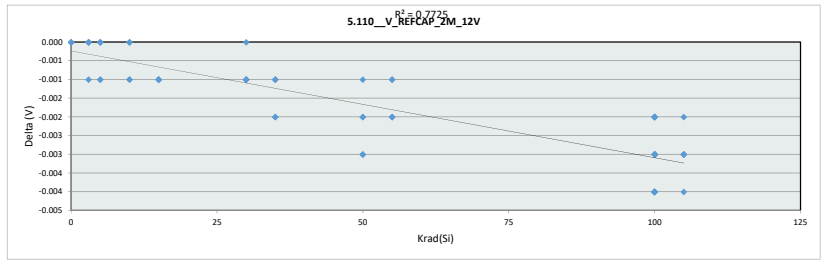
5.109_V_REFCAP											
Test Site	Tester	Test Number	Unit	V	V						
Max Limit				1.237	1.237						
Min Limit				1.213	1.213						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.225	1.225	1.226	1.226	1.225	1.224	1.224	1.222	1.223
Average	1.227	1.227	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224
Max	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



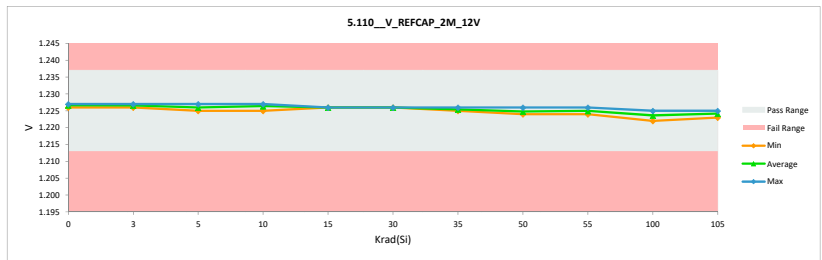
# HDR TID Report

## TPS7H5003-SP

5.110_V_REFCAP_2M_12V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			V	1.237	1.213
			V	1.237	1.213
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.227	1.227	0.000	
0	138	1.226	1.226	0.000	
0	139	1.227	1.227	0.000	
3	197	1.227	1.227	0.000	
3	198	1.227	1.226	-0.001	
3	199	1.227	1.227	0.000	
3	202	1.226	1.226	0.000	
3	203	1.227	1.227	0.000	
5	204	1.226	1.226	0.000	
5	205	1.227	1.226	-0.001	
5	206	1.226	1.226	0.000	
5	208	1.227	1.227	0.000	
5	209	1.226	1.225	-0.001	
10	210	1.226	1.225	-0.001	
10	211	1.227	1.227	0.000	
10	212	1.227	1.227	0.000	
10	213	1.227	1.227	0.000	
10	214	1.227	1.226	-0.001	
10	215	1.227	1.226	-0.001	
15	217	1.227	1.226	-0.001	
15	219	1.227	1.226	-0.001	
15	220	1.227	1.226	-0.001	
15	221	1.227	1.226	-0.001	
30	222	1.227	1.226	-0.001	
30	223	1.226	1.226	0.000	
30	224	1.227	1.226	-0.001	
30	225	1.227	1.226	-0.001	
30	226	1.227	1.226	-0.001	
35	227	1.227	1.226	-0.001	
35	228	1.227	1.225	-0.002	
35	229	1.227	1.225	-0.002	
35	230	1.226	1.225	-0.001	
35	231	1.227	1.226	-0.001	
50	170	1.227	1.225	-0.002	
50	172	1.227	1.226	-0.001	
50	173	1.227	1.224	-0.003	
50	175	1.227	1.225	-0.002	
50	176	1.227	1.224	-0.003	
55	177	1.226	1.225	-0.001	
55	178	1.226	1.224	-0.002	
55	179	1.227	1.225	-0.002	
55	180	1.227	1.226	-0.001	
55	181	1.227	1.225	-0.002	
100	154	1.226	1.224	-0.002	
100	155	1.227	1.223	-0.004	
100	156	1.227	1.224	-0.003	
100	157	1.227	1.223	-0.004	
100	158	1.226	1.224	-0.002	
100	159	1.227	1.224	-0.003	
100	160	1.226	1.222	-0.004	
100	161	1.227	1.223	-0.004	
100	162	1.227	1.223	-0.004	
100	163	1.227	1.223	-0.004	
100	164	1.227	1.224	-0.003	
100	165	1.227	1.224	-0.003	
100	166	1.227	1.223	-0.004	
100	168	1.227	1.225	-0.002	
100	169	1.226	1.224	-0.002	
100	182	1.227	1.223	-0.004	
100	183	1.227	1.224	-0.003	
100	184	1.227	1.224	-0.003	
100	185	1.227	1.225	-0.002	
100	186	1.226	1.224	-0.002	
100	187	1.227	1.224	-0.003	
100	188	1.227	1.224	-0.003	
100	189	1.226	1.222	-0.004	
100	190	1.227	1.224	-0.003	
105	191	1.228	1.225	-0.003	
105	192	1.227	1.224	-0.003	
105	193	1.227	1.224	-0.003	
105	194	1.227	1.225	-0.002	
105	195	1.227	1.223	-0.004	
105	196	1.227	1.224	-0.003	
	Max	1.228	1.227	0.000	
	Average	1.227	1.225	-0.002	
	Min	1.226	1.222	-0.004	
	Std Dev	0.000	0.001	0.001	

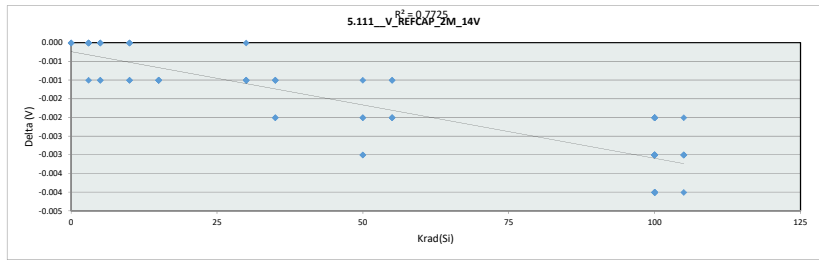


5.110_V_REFCAP											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			V	1.237	1.213						
			V	1.237	1.213						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.225	1.225	1.226	1.226	1.225	1.224	1.224	1.222	1.223
Average	1.227	1.227	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224
Max	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

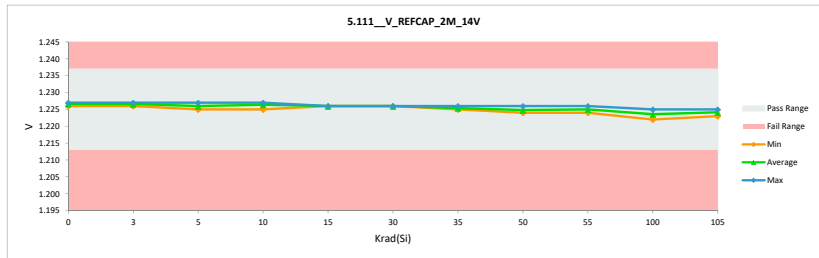


# HDR TID Report TPS7H5003-SP

5.111_V_REFCAP_2M_14V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	1.227	1.227	0.000	
0	138	1.226	1.226	0.000	
0	139	1.227	1.227	0.000	
3	197	1.227	1.227	0.000	
3	198	1.227	1.226	-0.001	
3	199	1.227	1.227	0.000	
3	202	1.226	1.226	0.000	
3	203	1.227	1.227	0.000	
5	204	1.226	1.226	0.000	
5	205	1.227	1.226	-0.001	
5	206	1.226	1.226	0.000	
5	208	1.227	1.227	0.000	
5	209	1.226	1.225	-0.001	
10	210	1.226	1.225	-0.001	
10	211	1.227	1.227	0.000	
10	212	1.227	1.227	0.000	
10	213	1.227	1.227	0.000	
10	214	1.227	1.226	-0.001	
10	215	1.227	1.226	-0.001	
15	217	1.227	1.226	-0.001	
15	219	1.227	1.226	-0.001	
15	220	1.227	1.226	-0.001	
15	221	1.227	1.226	-0.001	
30	222	1.227	1.226	-0.001	
30	223	1.226	1.226	0.000	
30	224	1.227	1.226	-0.001	
30	225	1.227	1.226	-0.001	
30	226	1.227	1.226	-0.001	
35	227	1.227	1.226	-0.001	
35	228	1.227	1.225	-0.002	
35	229	1.227	1.225	-0.002	
35	230	1.226	1.225	-0.001	
35	231	1.227	1.226	-0.001	
50	170	1.227	1.225	-0.002	
50	172	1.227	1.226	-0.001	
50	173	1.227	1.224	-0.003	
50	175	1.227	1.225	-0.002	
50	176	1.227	1.224	-0.003	
55	177	1.226	1.225	-0.001	
55	178	1.226	1.224	-0.002	
55	179	1.227	1.225	-0.002	
55	180	1.227	1.226	-0.001	
55	181	1.227	1.225	-0.002	
100	154	1.226	1.224	-0.002	
100	155	1.227	1.223	-0.004	
100	156	1.227	1.224	-0.003	
100	157	1.227	1.223	-0.004	
100	158	1.226	1.224	-0.002	
100	159	1.227	1.224	-0.003	
100	160	1.226	1.222	-0.004	
100	161	1.227	1.223	-0.004	
100	162	1.227	1.223	-0.004	
100	163	1.227	1.223	-0.004	
100	164	1.227	1.224	-0.003	
100	165	1.226	1.224	-0.002	
100	166	1.227	1.223	-0.004	
100	168	1.227	1.225	-0.002	
100	169	1.226	1.224	-0.002	
100	182	1.227	1.223	-0.004	
100	183	1.227	1.224	-0.003	
100	184	1.227	1.224	-0.003	
100	185	1.227	1.224	-0.003	
100	186	1.226	1.224	-0.002	
100	187	1.227	1.224	-0.003	
100	188	1.227	1.224	-0.003	
100	189	1.226	1.222	-0.004	
100	190	1.227	1.224	-0.003	
105	191	1.228	1.225	-0.003	
105	192	1.227	1.224	-0.003	
105	193	1.227	1.224	-0.003	
105	194	1.227	1.225	-0.002	
105	195	1.227	1.223	-0.004	
105	196	1.227	1.224	-0.003	
Max		1.228	1.227	0.000	
Average		1.227	1.225	-0.002	
Min		1.226	1.222	-0.004	
Std Dev		0.000	0.001	0.001	

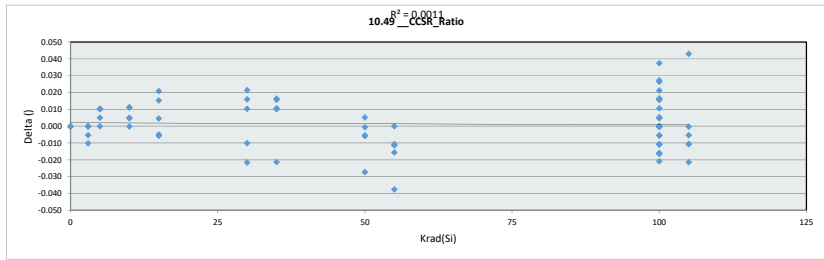


5.111_V_REFCAP											
Test Site											
Tester											
Test Number	1.237	V									
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.225	1.225	1.226	1.226	1.225	1.224	1.224	1.222	1.223
Average	1.227	1.227	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224
Max	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

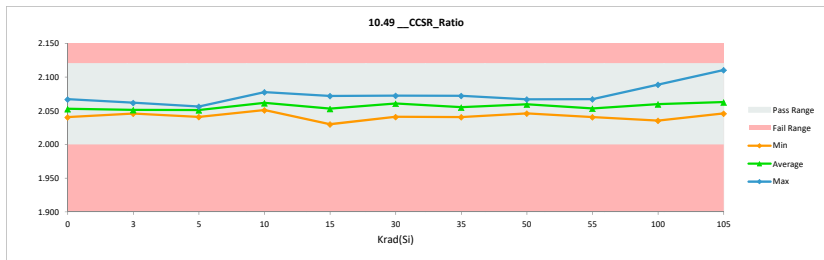


HDR TID Report  
TPS7H5003-SP

		10.49_CCSR_Ratio		
Test Site	Tester			
Test Number	Unit			
Max Limit	Min Limit	2.12	2.12	
Max Limit	Min Limit	2	2	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	2.041	2.041	0.000
0	138	2.051	2.051	0.000
0	139	2.067	2.067	0.000
3	197	2.051	2.051	0.000
3	198	2.057	2.046	-0.010
3	199	2.051	2.051	0.000
3	202	2.062	2.062	0.000
3	203	2.051	2.046	-0.005
5	204	2.041	2.041	0.000
5	205	2.046	2.056	0.010
5	206	2.040	2.051	0.010
5	208	2.046	2.056	0.010
5	209	2.046	2.051	0.005
10	210	2.040	2.052	0.011
10	211	2.062	2.061	0.000
10	212	2.046	2.051	0.005
10	213	2.062	2.067	0.005
10	214	2.066	2.078	0.011
15	215	2.067	2.072	0.005
15	217	2.036	2.056	0.021
15	219	2.062	2.056	-0.006
15	220	2.035	2.030	-0.005
15	221	2.036	2.051	0.015
30	222	2.094	2.072	-0.021
30	223	2.051	2.041	-0.010
30	224	2.051	2.062	0.011
30	225	2.056	2.072	0.016
30	226	2.035	2.057	0.022
35	227	2.040	2.051	0.011
35	228	2.062	2.041	-0.021
35	229	2.040	2.056	0.016
35	230	2.046	2.056	0.010
35	231	2.056	2.072	0.017
50	170	2.089	2.062	-0.027
50	172	2.073	2.067	-0.006
50	173	2.051	2.057	0.005
50	175	2.067	2.067	0.000
50	176	2.051	2.046	-0.005
55	177	2.051	2.041	-0.010
55	178	2.089	2.051	-0.037
55	179	2.067	2.067	0.000
55	180	2.078	2.067	-0.011
55	181	2.056	2.041	-0.015
100	154	2.046	2.046	0.000
100	155	2.051	2.046	-0.005
100	156	2.051	2.051	0.000
100	157	2.062	2.061	0.000
100	158	2.041	2.040	0.000
100	159	2.067	2.062	-0.005
100	160	2.051	2.072	0.021
100	161	2.062	2.088	0.027
100	162	2.046	2.035	-0.011
100	163	2.057	2.046	-0.011
100	164	2.057	2.073	0.016
100	165	2.051	2.089	0.038
100	166	2.062	2.041	-0.021
100	168	2.051	2.057	0.005
100	169	2.089	2.073	-0.016
100	182	2.062	2.046	-0.016
100	183	2.035	2.051	0.016
100	184	2.056	2.084	0.027
100	185	2.062	2.067	0.005
100	186	2.072	2.089	0.017
100	187	2.056	2.041	-0.016
100	188	2.067	2.072	0.006
100	189	2.051	2.062	0.011
100	190	2.057	2.046	-0.011
105	191	2.056	2.046	-0.010
105	192	2.057	2.046	-0.010
105	193	2.083	2.062	-0.021
105	194	2.056	2.051	-0.005
105	195	2.067	2.110	0.043
105	196	2.062	2.062	0.000
Max		2.094	2.110	0.043
Average		2.056	2.058	0.001
Min		2.035	2.030	-0.037
Std Dev		0.013	0.014	0.015

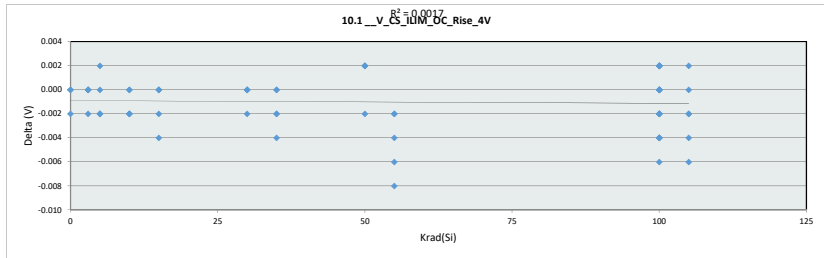


		10.49_CCSR_Rat										
Test Site	Tester											
Test Number	Unit											
Max Limit	Min Limit	2.12										
Max Limit	Min Limit	2										
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000
Min		2.041	2.046	2.041	2.051	2.030	2.041	2.041	2.046	2.041	2.035	2.046
Average		2.053	2.051	2.051	2.062	2.053	2.061	2.055	2.060	2.053	2.060	2.063
Max		2.067	2.062	2.056	2.078	2.072	2.072	2.072	2.067	2.067	2.089	2.110
UL		2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120

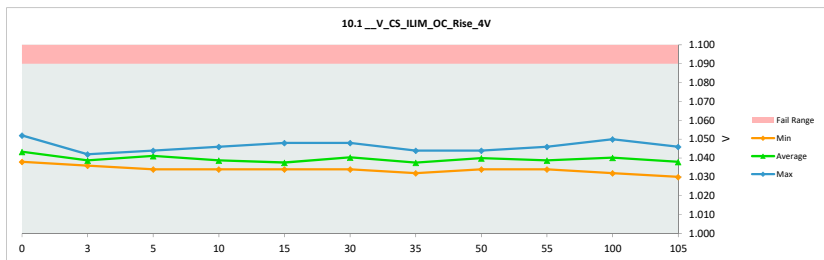


# HDR TID Report TPS7H5003-SP

10.1_V_CS_ILIM_OC_Rise_4V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1.040	1.040	0.000
0	138	1.038	1.038	0.000
0	139	1.054	1.052	-0.002
3	197	1.038	1.038	0.000
3	198	1.038	1.038	0.000
3	199	1.042	1.042	0.000
3	202	1.038	1.036	-0.002
3	203	1.040	1.040	0.000
5	204	1.042	1.044	0.002
5	205	1.046	1.044	-0.002
5	206	1.042	1.040	-0.002
5	208	1.044	1.044	0.000
5	209	1.036	1.034	-0.002
10	210	1.048	1.046	-0.002
10	211	1.042	1.042	0.000
10	212	1.038	1.036	-0.002
10	213	1.036	1.036	0.000
10	214	1.036	1.034	-0.002
15	215	1.034	1.034	0.000
15	217	1.036	1.034	-0.002
15	219	1.036	1.036	0.000
15	220	1.036	1.036	0.000
15	221	1.052	1.048	-0.004
30	222	1.042	1.042	0.000
30	223	1.040	1.040	0.000
30	224	1.036	1.034	-0.002
30	225	1.038	1.038	0.000
30	226	1.048	1.048	0.000
35	227	1.040	1.036	-0.004
35	228	1.044	1.044	0.000
35	229	1.034	1.032	-0.002
35	230	1.038	1.036	-0.002
35	231	1.040	1.040	0.000
50	170	1.042	1.044	0.002
50	172	1.042	1.044	0.002
50	173	1.040	1.042	0.002
50	175	1.034	1.036	0.002
50	176	1.036	1.034	-0.002
55	177	1.044	1.038	-0.006
55	178	1.042	1.034	-0.008
55	179	1.038	1.036	-0.002
55	180	1.044	1.040	-0.004
55	181	1.048	1.046	-0.002
100	154	1.040	1.040	0.000
100	155	1.040	1.042	0.002
100	156	1.042	1.040	-0.002
100	157	1.044	1.044	0.000
100	158	1.038	1.038	0.000
100	159	1.042	1.038	-0.004
100	160	1.040	1.036	-0.004
100	161	1.036	1.036	0.000
100	162	1.046	1.042	-0.004
100	163	1.040	1.042	0.002
100	164	1.042	1.044	0.002
100	165	1.036	1.038	0.002
100	166	1.038	1.038	0.000
100	168	1.032	1.032	0.000
100	169	1.044	1.042	-0.002
100	182	1.048	1.050	0.002
100	183	1.038	1.036	-0.002
100	184	1.046	1.048	0.002
100	185	1.042	1.038	-0.004
100	186	1.046	1.044	-0.002
100	187	1.042	1.042	0.000
100	188	1.036	1.034	-0.002
100	189	1.034	1.036	0.002
100	190	1.052	1.046	-0.006
105	191	1.036	1.030	-0.006
105	192	1.046	1.046	0.000
105	193	1.040	1.038	-0.002
105	194	1.042	1.038	-0.004
105	195	1.040	1.042	0.002
105	196	1.036	1.034	-0.002
Max		1.054	1.052	0.002
Average		1.041	1.040	-0.001
Min		1.032	1.030	-0.008
Std Dev		0.005	0.005	0.002



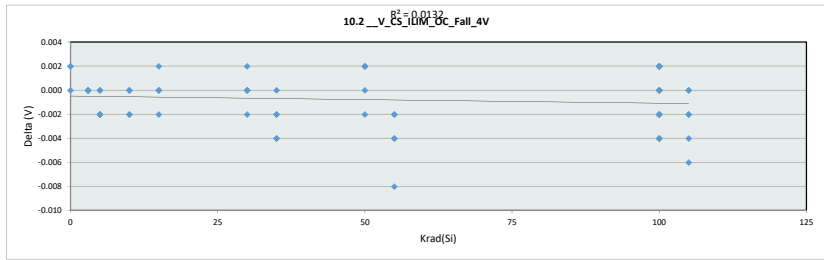
10.1_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.038	1.036	1.034	1.034	1.034	1.034	1.032	1.034	1.034	1.032	1.030
Min	1.043	1.039	1.041	1.039	1.038	1.040	1.038	1.040	1.039	1.040	1.038
Average	1.052	1.042	1.044	1.046	1.048	1.048	1.044	1.044	1.046	1.050	1.046
Max	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



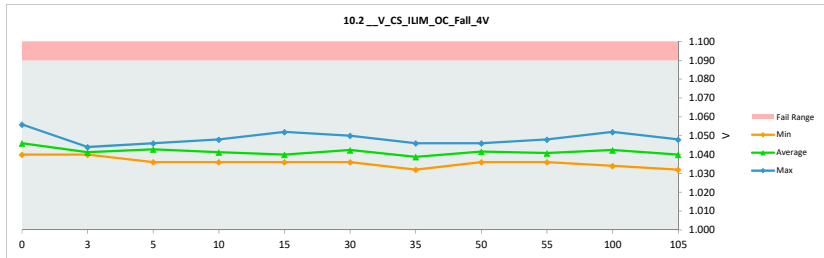


HDR TID Report  
TPS7H5003-SP

10.2_V_CS_ILIM_OC_Fall_4V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1.040	1.042	0.002
0	138	1.040	1.040	0.000
0	139	1.054	1.056	0.002
3	197	1.040	1.040	0.000
3	198	1.040	1.040	0.000
3	199	1.044	1.044	0.000
3	202	1.040	1.040	0.000
3	203	1.042	1.042	0.000
5	204	1.046	1.044	-0.002
5	205	1.048	1.046	-0.002
5	206	1.042	1.042	0.000
5	208	1.046	1.046	0.000
5	209	1.038	1.036	-0.002
10	210	1.050	1.048	-0.002
10	211	1.044	1.044	0.000
10	212	1.040	1.040	0.000
10	213	1.038	1.038	0.000
10	214	1.038	1.036	-0.002
15	215	1.036	1.036	0.000
15	217	1.038	1.036	-0.002
15	219	1.038	1.038	0.000
15	220	1.036	1.038	0.002
15	221	1.052	1.052	0.000
30	222	1.044	1.044	0.000
30	223	1.040	1.042	0.002
30	224	1.038	1.036	-0.002
30	225	1.040	1.040	0.000
30	226	1.050	1.050	0.000
35	227	1.042	1.038	-0.004
35	228	1.046	1.046	0.000
35	229	1.036	1.032	-0.004
35	230	1.040	1.038	-0.002
35	231	1.042	1.040	-0.002
50	170	1.044	1.046	0.002
50	172	1.044	1.046	0.002
50	173	1.042	1.042	0.000
50	175	1.036	1.038	0.002
50	176	1.038	1.036	-0.002
55	177	1.044	1.040	-0.004
55	178	1.044	1.036	-0.008
55	179	1.040	1.038	-0.002
55	180	1.046	1.042	-0.004
55	181	1.050	1.048	-0.002
100	154	1.042	1.042	0.000
100	155	1.042	1.044	0.002
100	156	1.044	1.042	-0.002
100	157	1.048	1.046	-0.002
100	158	1.040	1.040	0.000
100	159	1.044	1.040	-0.004
100	160	1.042	1.040	-0.002
100	161	1.038	1.038	0.000
100	162	1.046	1.044	-0.002
100	163	1.042	1.044	0.002
100	164	1.044	1.046	0.002
100	165	1.038	1.040	0.002
100	166	1.040	1.040	0.000
100	168	1.032	1.034	0.002
100	169	1.046	1.044	-0.002
100	182	1.050	1.052	0.002
100	183	1.042	1.038	-0.004
100	184	1.048	1.050	0.002
100	185	1.044	1.040	-0.004
100	186	1.046	1.046	0.000
100	187	1.044	1.042	-0.002
100	188	1.038	1.038	0.000
100	189	1.036	1.038	0.002
100	190	1.054	1.050	-0.004
105	191	1.038	1.032	-0.006
105	192	1.048	1.048	0.000
105	193	1.042	1.040	-0.002
105	194	1.044	1.040	-0.004
105	195	1.044	1.044	0.000
105	196	1.038	1.036	-0.002
Max		1.054	1.056	0.002
Average		1.042	1.042	-0.001
Min		1.032	1.032	-0.008
Std Dev		0.005	0.005	0.002

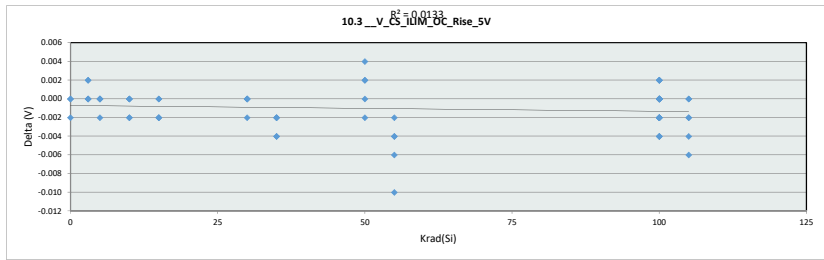


10.2_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.040	1.040	1.036	1.036	1.036	1.036	1.032	1.036	1.036	1.034	1.032
Min	1.046	1.041	1.043	1.041	1.040	1.042	1.039	1.042	1.041	1.042	1.040
Average	1.056	1.044	1.046	1.048	1.052	1.050	1.046	1.046	1.048	1.052	1.048
Max	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090
UL											

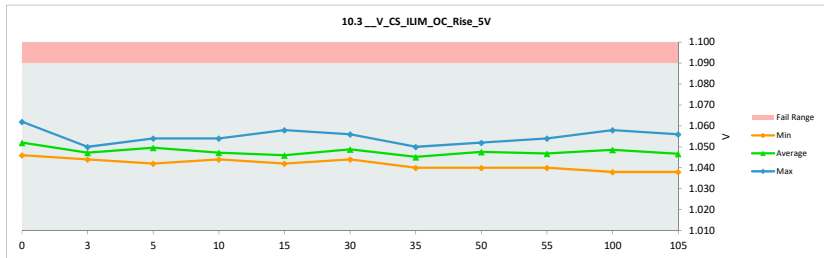


HDR TID Report  
TPS7H5003-SP

10.3_V_CS_ILIM_OC_Rise_SV				
Test Site				
Tester				
Test Number				
Unit	V		V	
Max Limit	1.09		1.09	
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1.050	1.048	-0.002
0	138	1.046	1.046	0.000
0	139	1.062	1.062	0.000
3	197	1.044	1.044	0.000
3	198	1.046	1.046	0.000
3	199	1.048	1.050	0.002
3	202	1.046	1.046	0.000
3	203	1.048	1.050	0.002
5	204	1.052	1.050	-0.002
5	205	1.054	1.054	0.000
5	206	1.050	1.050	0.000
5	208	1.052	1.052	0.000
5	209	1.042	1.042	0.000
10	210	1.056	1.054	-0.002
10	211	1.050	1.048	-0.002
10	212	1.046	1.046	0.000
10	213	1.044	1.044	0.000
10	214	1.044	1.044	0.000
15	215	1.044	1.042	-0.002
15	217	1.044	1.042	-0.002
15	219	1.044	1.044	0.000
15	220	1.044	1.044	0.000
15	221	1.060	1.058	-0.002
30	222	1.050	1.050	0.000
30	223	1.048	1.046	-0.002
30	224	1.044	1.044	0.000
30	225	1.048	1.048	0.000
30	226	1.056	1.056	0.000
35	227	1.048	1.044	-0.004
35	228	1.052	1.050	-0.002
35	229	1.044	1.040	-0.004
35	230	1.048	1.046	-0.002
35	231	1.048	1.046	-0.002
50	170	1.050	1.052	0.002
50	172	1.048	1.052	0.004
50	173	1.050	1.050	0.000
50	175	1.042	1.044	0.002
50	176	1.042	1.040	-0.002
55	177	1.054	1.048	-0.006
55	178	1.050	1.040	-0.010
55	179	1.046	1.044	-0.002
55	180	1.052	1.048	-0.004
55	181	1.058	1.054	-0.004
100	154	1.046	1.046	0.000
100	155	1.048	1.050	0.002
100	156	1.050	1.048	-0.002
100	157	1.054	1.054	0.000
100	158	1.046	1.046	0.000
100	159	1.050	1.046	-0.004
100	160	1.048	1.046	-0.002
100	161	1.042	1.042	0.000
100	162	1.052	1.050	-0.002
100	163	1.050	1.052	0.002
100	164	1.054	1.054	0.000
100	165	1.044	1.044	0.000
100	166	1.046	1.044	-0.002
100	168	1.038	1.038	0.000
100	169	1.054	1.052	-0.002
100	182	1.058	1.058	0.000
100	183	1.048	1.044	-0.004
100	184	1.056	1.058	0.002
100	185	1.052	1.048	-0.004
100	186	1.054	1.052	-0.002
100	187	1.048	1.048	0.000
100	188	1.044	1.044	0.000
100	189	1.044	1.046	0.002
100	190	1.058	1.056	-0.002
105	191	1.044	1.038	-0.006
105	192	1.056	1.056	0.000
105	193	1.048	1.046	-0.002
105	194	1.052	1.048	-0.004
105	195	1.050	1.050	0.000
105	196	1.044	1.042	-0.002
Max		1.062	1.062	0.004
Average		1.049	1.048	-0.001
Min		1.038	1.038	-0.010
Std Dev		0.005	0.005	0.002



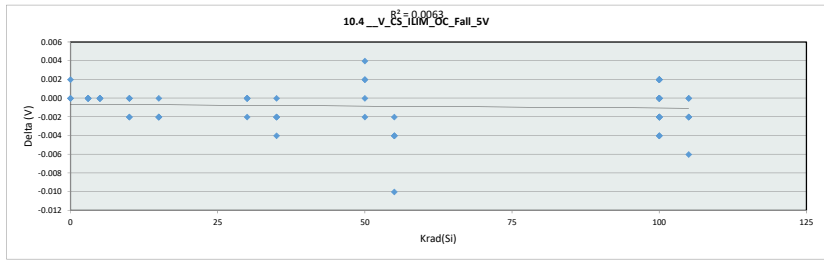
10.3_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09		V								
Min Limit			V								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.046	1.044	1.042	1.044	1.042	1.044	1.040	1.040	1.040	1.038	1.038
Min	1.052	1.047	1.050	1.047	1.046	1.049	1.045	1.048	1.047	1.049	1.047
Average	1.062	1.050	1.054	1.054	1.058	1.056	1.050	1.052	1.054	1.058	1.056
Max	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



# HDR TID Report TPS7H5003-SP

10.4_V_CS_ILIM_OC_Fall_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

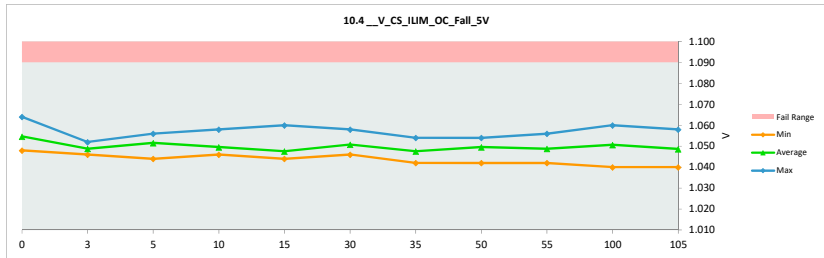
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1.052	1.052	0.000
0	138	1.048	1.048	0.000
0	139	1.062	1.064	0.002
3	197	1.046	1.046	0.000
3	198	1.048	1.048	0.000
3	199	1.052	1.052	0.000
3	202	1.048	1.048	0.000
3	203	1.050	1.050	0.000
5	204	1.052	1.052	0.000
5	205	1.056	1.056	0.000
5	206	1.052	1.052	0.000
5	208	1.054	1.054	0.000
5	209	1.044	1.044	0.000
10	210	1.058	1.058	0.000
10	211	1.052	1.050	-0.002
10	212	1.048	1.048	0.000
10	213	1.046	1.046	0.000
10	214	1.048	1.046	-0.002
15	215	1.046	1.044	-0.002
15	217	1.046	1.044	-0.002
15	219	1.046	1.046	0.000
15	220	1.046	1.044	-0.002
15	221	1.062	1.060	-0.002
30	222	1.052	1.052	0.000
30	223	1.048	1.048	0.000
30	224	1.048	1.046	-0.002
30	225	1.050	1.050	0.000
30	226	1.058	1.058	0.000
35	227	1.050	1.046	-0.004
35	228	1.054	1.054	0.000
35	229	1.044	1.042	-0.002
35	230	1.050	1.048	-0.002
35	231	1.050	1.048	-0.002
50	170	1.052	1.054	0.002
50	172	1.052	1.054	0.002
50	173	1.052	1.052	0.000
50	175	1.042	1.046	0.004
50	176	1.044	1.042	-0.002
55	177	1.054	1.050	-0.004
55	178	1.052	1.042	-0.010
55	179	1.048	1.046	-0.002
55	180	1.054	1.050	-0.004
55	181	1.060	1.056	-0.004
100	154	1.048	1.048	0.000
100	155	1.050	1.052	0.002
100	156	1.050	1.050	0.000
100	157	1.056	1.056	0.000
100	158	1.048	1.048	0.000
100	159	1.052	1.048	-0.004
100	160	1.050	1.048	-0.002
100	161	1.044	1.044	0.000
100	162	1.054	1.052	-0.002
100	163	1.052	1.054	0.002
100	164	1.056	1.056	0.000
100	165	1.046	1.046	0.000
100	166	1.048	1.046	-0.002
100	168	1.040	1.040	0.000
100	169	1.056	1.054	-0.002
100	182	1.058	1.060	0.002
100	183	1.048	1.046	-0.002
100	184	1.058	1.060	0.002
100	185	1.054	1.050	-0.004
100	186	1.056	1.056	0.000
100	187	1.048	1.050	0.002
100	188	1.048	1.046	-0.002
100	189	1.046	1.048	0.002
100	190	1.052	1.058	-0.004
105	191	1.046	1.040	-0.006
105	192	1.058	1.058	0.000
105	193	1.050	1.048	-0.002
105	194	1.052	1.050	-0.002
105	195	1.052	1.052	0.000
105	196	1.046	1.044	-0.002
Max		1.062	1.064	0.004
Average		1.051	1.050	-0.001
Min		1.040	1.040	-0.010
Std Dev		0.005	0.005	0.002



## 10.4\_V\_CS\_ILIM

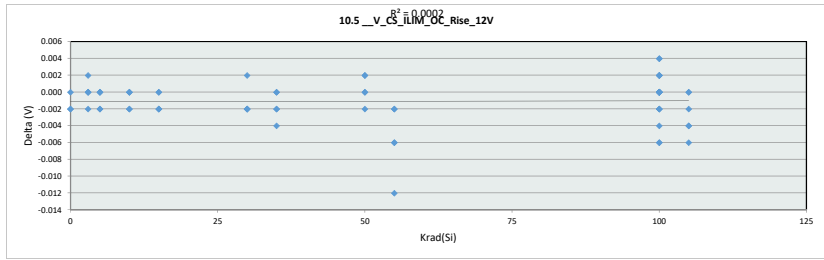
Test Site	
Tester	
Test Number	
Max Limit	1.09 V
Min Limit	V

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.048	1.046	1.044	1.046	1.044	1.046	1.042	1.042	1.042	1.040	1.040
Min	1.055	1.049	1.052	1.050	1.048	1.051	1.048	1.050	1.049	1.051	1.049
Average	1.064	1.052	1.056	1.058	1.060	1.058	1.054	1.054	1.056	1.060	1.058
Max	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

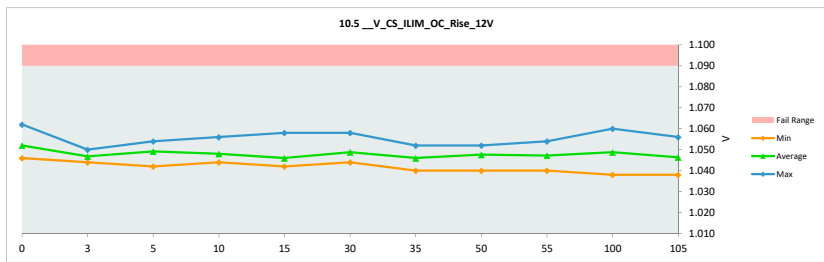


HDR TID Report  
TPS7H5003-SP

10.5_V_CS_ILIM_OC_Rise_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1.050	1.048	-0.002
0	138	1.048	1.046	-0.002
0	139	1.062	1.062	0.000
3	197	1.046	1.044	-0.002
3	198	1.046	1.046	0.000
3	199	1.048	1.050	0.002
3	202	1.046	1.046	0.000
3	203	1.048	1.048	0.000
5	204	1.050	1.050	0.000
5	205	1.056	1.054	-0.002
5	206	1.050	1.050	0.000
5	208	1.052	1.050	-0.002
5	209	1.042	1.042	0.000
10	210	1.058	1.056	-0.002
10	211	1.050	1.050	0.000
10	212	1.046	1.046	0.000
10	213	1.046	1.044	-0.002
10	214	1.044	1.044	0.000
15	215	1.044	1.042	-0.002
15	217	1.044	1.042	-0.002
15	219	1.044	1.044	0.000
15	220	1.044	1.044	0.000
15	221	1.060	1.058	-0.002
30	222	1.052	1.050	-0.002
30	223	1.048	1.046	-0.002
30	224	1.046	1.044	-0.002
30	225	1.048	1.046	-0.002
30	226	1.056	1.058	0.002
35	227	1.048	1.044	-0.004
35	228	1.052	1.052	0.000
35	229	1.042	1.040	-0.002
35	230	1.048	1.046	-0.002
35	231	1.048	1.048	0.000
50	170	1.052	1.052	0.000
50	172	1.050	1.052	0.002
50	173	1.050	1.050	0.000
50	175	1.042	1.044	0.002
50	176	1.042	1.040	-0.002
55	177	1.054	1.048	-0.006
55	178	1.052	1.040	-0.012
55	179	1.046	1.044	-0.002
55	180	1.052	1.050	-0.002
55	181	1.060	1.054	-0.006
100	154	1.044	1.044	0.000
100	155	1.048	1.050	0.002
100	156	1.048	1.048	0.000
100	157	1.054	1.054	0.000
100	158	1.046	1.046	0.000
100	159	1.052	1.046	-0.006
100	160	1.048	1.048	0.000
100	161	1.042	1.042	0.000
100	162	1.052	1.050	-0.002
100	163	1.050	1.052	0.002
100	164	1.054	1.054	0.000
100	165	1.044	1.044	0.000
100	166	1.044	1.044	0.000
100	168	1.038	1.038	0.000
100	169	1.052	1.052	0.000
100	182	1.056	1.058	0.002
100	183	1.046	1.044	-0.002
100	184	1.056	1.060	0.004
100	185	1.052	1.048	-0.004
100	186	1.054	1.052	-0.002
100	187	1.048	1.048	0.000
100	188	1.044	1.046	0.002
100	189	1.044	1.048	0.004
100	190	1.060	1.054	-0.006
105	191	1.044	1.038	-0.006
105	192	1.056	1.056	0.000
105	193	1.048	1.046	-0.002
105	194	1.052	1.048	-0.004
105	195	1.050	1.050	0.000
105	196	1.044	1.040	-0.004
Max		1.062	1.062	0.004
Average		1.049	1.048	-0.001
Min		1.038	1.038	-0.012
Std Dev		0.005	0.005	0.003

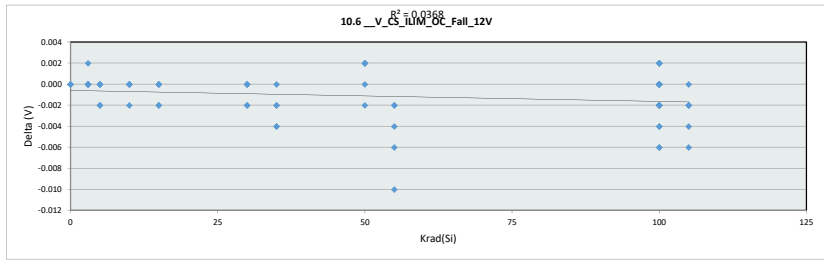


10.5_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit	V										
LL	0	3	5	10	15	30	35	50	55	100	105
Min	1.046	1.044	1.042	1.044	1.042	1.044	1.040	1.040	1.040	1.038	1.038
Average	1.052	1.047	1.049	1.048	1.046	1.049	1.046	1.048	1.047	1.049	1.046
Max	1.062	1.050	1.054	1.056	1.058	1.058	1.052	1.052	1.054	1.060	1.056
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

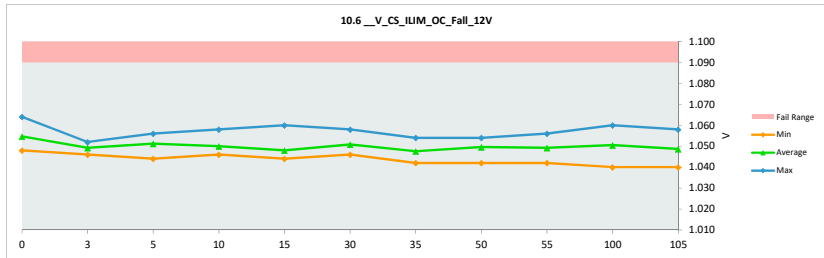


# HDR TID Report TPS7H5003-SP

10.6_V_CS_ILIM_OC_Fall_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1.052	1.052	0.000
0	138	1.048	1.048	0.000
0	139	1.064	1.064	0.000
3	197	1.046	1.046	0.000
3	198	1.048	1.048	0.000
3	199	1.052	1.052	0.000
3	202	1.048	1.048	0.000
3	203	1.050	1.052	0.002
5	204	1.052	1.052	0.000
5	205	1.058	1.056	-0.002
5	206	1.052	1.052	0.000
5	208	1.054	1.052	-0.002
5	209	1.044	1.044	0.000
10	210	1.060	1.058	-0.002
10	211	1.052	1.052	0.000
10	212	1.048	1.048	0.000
10	213	1.046	1.046	0.000
10	214	1.046	1.046	0.000
15	215	1.046	1.044	-0.002
15	217	1.044	1.044	0.000
15	219	1.046	1.046	0.000
15	220	1.046	1.046	0.000
15	221	1.062	1.060	-0.002
30	222	1.052	1.052	0.000
30	223	1.050	1.048	-0.002
30	224	1.048	1.046	-0.002
30	225	1.050	1.050	0.000
30	226	1.058	1.058	0.000
35	227	1.050	1.046	-0.004
35	228	1.054	1.054	0.000
35	229	1.046	1.042	-0.004
35	230	1.050	1.048	-0.002
35	231	1.050	1.048	-0.002
50	170	1.052	1.054	0.002
50	172	1.052	1.054	0.002
50	173	1.052	1.052	0.000
50	175	1.044	1.046	0.002
50	176	1.044	1.042	-0.002
55	177	1.056	1.050	-0.006
55	178	1.052	1.042	-0.010
55	179	1.048	1.046	-0.002
55	180	1.054	1.052	-0.002
55	181	1.060	1.056	-0.004
100	154	1.048	1.048	0.000
100	155	1.050	1.052	0.002
100	156	1.050	1.050	0.000
100	157	1.056	1.056	0.000
100	158	1.048	1.048	0.000
100	159	1.054	1.048	-0.006
100	160	1.050	1.048	-0.002
100	161	1.046	1.044	-0.002
100	162	1.054	1.052	-0.002
100	163	1.052	1.054	0.002
100	164	1.056	1.056	0.000
100	165	1.046	1.046	0.000
100	166	1.048	1.046	-0.002
100	168	1.040	1.040	0.000
100	169	1.056	1.054	-0.002
100	182	1.060	1.060	0.000
100	183	1.050	1.046	-0.004
100	184	1.058	1.060	0.002
100	185	1.054	1.050	-0.004
100	186	1.056	1.054	-0.002
100	187	1.050	1.050	0.000
100	188	1.048	1.046	-0.002
100	189	1.046	1.048	0.002
100	190	1.062	1.056	-0.006
105	191	1.046	1.040	-0.006
105	192	1.060	1.058	-0.002
105	193	1.050	1.048	-0.002
105	194	1.054	1.050	-0.004
105	195	1.052	1.052	0.000
105	196	1.046	1.044	-0.002
Max		1.064	1.064	0.002
Average		1.051	1.050	-0.001
Min		1.040	1.040	-0.010
Std Dev		0.005	0.005	0.002

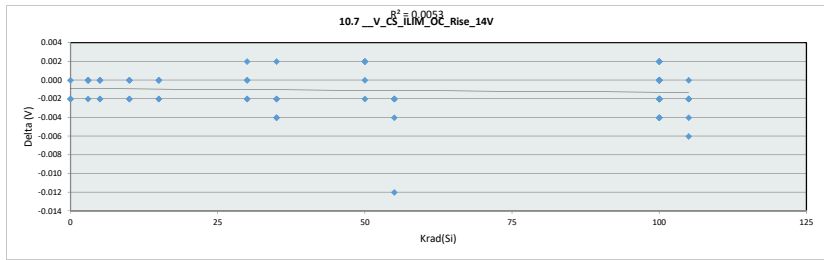


10.6_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit	V										
LL	0	3	5	10	15	30	35	50	55	100	105
Min	1.048	1.046	1.044	1.046	1.044	1.046	1.042	1.042	1.042	1.040	1.040
Average	1.055	1.049	1.051	1.050	1.048	1.051	1.048	1.050	1.049	1.051	1.049
Max	1.064	1.052	1.056	1.058	1.060	1.058	1.054	1.054	1.056	1.060	1.058
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

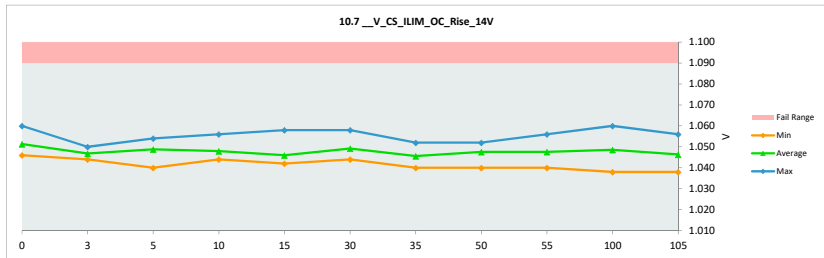


# HDR TID Report TPS7H5003-SP

10.7_V_CS_ILIM_OC_Rise_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1.050	1.048	-0.002
0	138	1.046	1.046	0.000
0	139	1.062	1.060	-0.002
3	197	1.046	1.044	-0.002
3	198	1.046	1.046	0.000
3	199	1.050	1.050	0.000
3	202	1.046	1.046	0.000
3	203	1.048	1.048	0.000
5	204	1.050	1.050	0.000
5	205	1.054	1.054	0.000
5	206	1.052	1.050	-0.002
5	208	1.050	1.050	0.000
5	209	1.042	1.040	-0.002
10	210	1.058	1.056	-0.002
10	211	1.052	1.050	-0.002
10	212	1.046	1.046	0.000
10	213	1.044	1.044	0.000
10	214	1.044	1.044	0.000
15	215	1.042	1.042	0.000
15	217	1.044	1.042	-0.002
15	219	1.044	1.044	0.000
15	220	1.044	1.044	0.000
15	221	1.060	1.058	-0.002
30	222	1.050	1.050	0.000
30	223	1.048	1.046	-0.002
30	224	1.046	1.044	-0.002
30	225	1.048	1.048	0.000
30	226	1.056	1.058	0.002
35	227	1.048	1.044	-0.004
35	228	1.050	1.052	0.002
35	229	1.044	1.040	-0.004
35	230	1.048	1.046	-0.002
35	231	1.048	1.046	-0.002
50	170	1.050	1.052	0.002
50	172	1.050	1.052	0.002
50	173	1.050	1.050	0.000
50	175	1.042	1.044	0.002
50	176	1.042	1.040	-0.002
55	177	1.052	1.048	-0.004
55	178	1.052	1.040	-0.012
55	179	1.046	1.044	-0.002
55	180	1.052	1.050	-0.002
55	181	1.058	1.056	-0.002
100	154	1.046	1.046	0.000
100	155	1.048	1.050	0.002
100	156	1.048	1.048	0.000
100	157	1.054	1.054	0.000
100	158	1.046	1.046	0.000
100	159	1.050	1.046	-0.004
100	160	1.048	1.046	-0.002
100	161	1.042	1.042	0.000
100	162	1.052	1.050	-0.002
100	163	1.050	1.052	0.002
100	164	1.054	1.054	0.000
100	165	1.044	1.044	0.000
100	166	1.046	1.044	-0.002
100	168	1.038	1.038	0.000
100	169	1.054	1.052	-0.002
100	182	1.058	1.060	0.002
100	183	1.048	1.044	-0.004
100	184	1.056	1.058	0.002
100	185	1.052	1.048	-0.004
100	186	1.054	1.052	-0.002
100	187	1.048	1.048	0.000
100	188	1.046	1.044	-0.002
100	189	1.044	1.046	0.002
100	190	1.058	1.054	-0.004
105	191	1.044	1.038	-0.006
105	192	1.056	1.056	0.000
105	193	1.048	1.046	-0.002
105	194	1.050	1.046	-0.004
105	195	1.052	1.050	-0.002
105	196	1.044	1.042	-0.002
Max		1.062	1.060	0.002
Average		1.049	1.048	-0.001
Min		1.038	1.038	-0.012
Std Dev		0.005	0.005	0.002

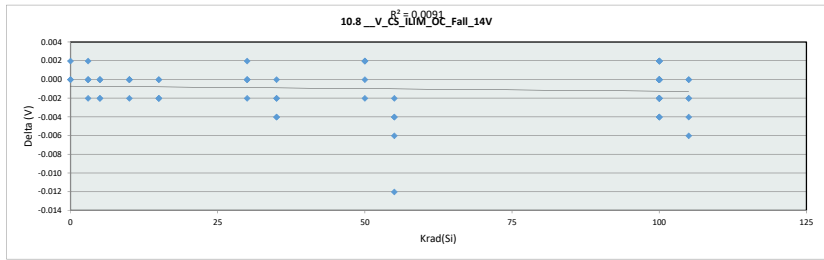


10.7_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.046	1.044	1.040	1.044	1.042	1.044	1.040	1.040	1.040	1.038	1.038
Min	1.051	1.047	1.049	1.048	1.046	1.049	1.046	1.048	1.048	1.049	1.046
Average	1.060	1.050	1.054	1.056	1.058	1.058	1.052	1.052	1.056	1.060	1.056
Max	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

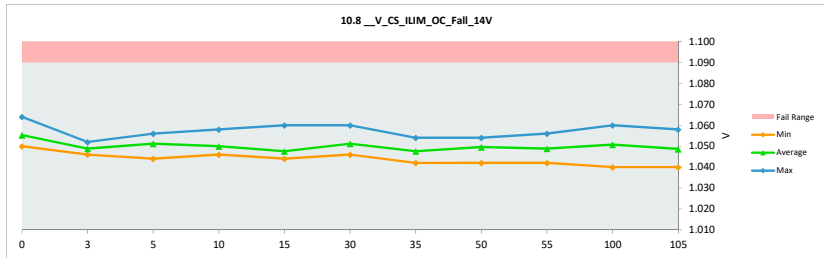


# HDR TID Report TPS7H5003-SP

10.8_V_CS_ILIM_OC_Fall_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.09	1.09		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	1.052	1.052	0.000
0	138	1.048	1.050	0.002
0	139	1.064	1.064	0.000
3	197	1.046	1.046	0.000
3	198	1.048	1.048	0.000
3	199	1.052	1.050	-0.002
3	202	1.048	1.048	0.000
3	203	1.050	1.052	0.002
5	204	1.054	1.052	-0.002
5	205	1.058	1.056	-0.002
5	206	1.052	1.052	0.000
5	208	1.052	1.052	0.000
5	209	1.044	1.044	0.000
10	210	1.060	1.058	-0.002
10	211	1.052	1.052	0.000
10	212	1.048	1.048	0.000
10	213	1.046	1.046	0.000
10	214	1.046	1.046	0.000
15	215	1.044	1.044	0.000
15	217	1.046	1.044	-0.002
15	219	1.046	1.046	0.000
15	220	1.046	1.044	-0.002
15	221	1.062	1.060	-0.002
30	222	1.052	1.052	0.000
30	223	1.048	1.048	0.000
30	224	1.048	1.046	-0.002
30	225	1.050	1.050	0.000
30	226	1.058	1.060	0.002
35	227	1.050	1.046	-0.004
35	228	1.054	1.054	0.000
35	229	1.046	1.042	-0.004
35	230	1.050	1.048	-0.002
35	231	1.050	1.048	-0.002
50	170	1.052	1.054	0.002
50	172	1.052	1.054	0.002
50	173	1.052	1.052	0.000
50	175	1.044	1.046	0.002
50	176	1.044	1.042	-0.002
55	177	1.056	1.050	-0.006
55	178	1.054	1.042	-0.012
55	179	1.048	1.046	-0.002
55	180	1.054	1.050	-0.004
55	181	1.060	1.056	-0.004
100	154	1.048	1.048	0.000
100	155	1.050	1.052	0.002
100	156	1.052	1.050	-0.002
100	157	1.056	1.056	0.000
100	158	1.048	1.048	0.000
100	159	1.052	1.050	-0.002
100	160	1.050	1.048	-0.002
100	161	1.044	1.044	0.000
100	162	1.054	1.052	-0.002
100	163	1.052	1.054	0.002
100	164	1.056	1.056	0.000
100	165	1.044	1.046	0.002
100	166	1.046	1.046	0.000
100	168	1.040	1.040	0.000
100	169	1.056	1.054	-0.002
100	182	1.058	1.060	0.002
100	183	1.050	1.046	-0.004
100	184	1.060	1.060	0.000
100	185	1.054	1.050	-0.004
100	186	1.056	1.056	0.000
100	187	1.050	1.050	0.000
100	188	1.048	1.046	-0.002
100	189	1.046	1.048	0.002
100	190	1.062	1.058	-0.004
105	191	1.046	1.040	-0.006
105	192	1.058	1.058	0.000
105	193	1.050	1.048	-0.002
105	194	1.054	1.050	-0.004
105	195	1.052	1.052	0.000
105	196	1.046	1.044	-0.002
Max		1.064	1.064	0.002
Average		1.051	1.050	-0.001
Min		1.040	1.040	-0.012
Std Dev		0.005	0.005	0.002

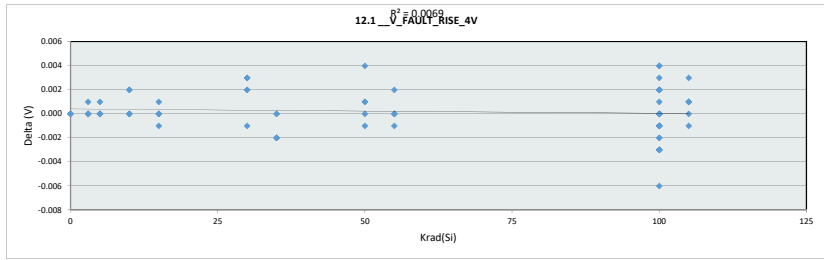


10.8_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.050	1.046	1.044	1.046	1.044	1.044	1.042	1.042	1.042	1.040	1.040
Min	1.055	1.049	1.051	1.050	1.048	1.051	1.048	1.050	1.049	1.051	1.049
Average	1.064	1.052	1.056	1.058	1.060	1.060	1.054	1.054	1.056	1.060	1.058
Max	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

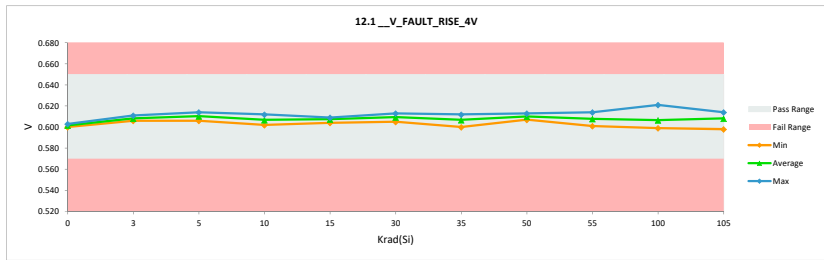


HDR TID Report  
TPS7H5003-SP

12.1_V_FAULT_RISE_4V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			0.65	0.65
Min Limit			0.57	0.57
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.602	0.602	0.000
0	138	0.600	0.600	0.000
0	139	0.603	0.603	0.000
3	197	0.606	0.606	0.000
3	198	0.609	0.609	0.000
3	199	0.611	0.611	0.000
3	202	0.605	0.606	0.001
3	203	0.609	0.609	0.000
5	204	0.613	0.613	0.000
5	205	0.609	0.609	0.000
5	206	0.606	0.606	0.000
5	208	0.614	0.614	0.000
5	209	0.609	0.610	0.001
10	210	0.608	0.608	0.000
10	211	0.612	0.612	0.000
10	212	0.600	0.602	0.002
10	213	0.601	0.603	0.002
10	214	0.609	0.609	0.000
15	215	0.607	0.607	0.000
15	217	0.603	0.604	0.001
15	219	0.609	0.609	0.000
15	220	0.608	0.608	0.000
15	221	0.610	0.609	-0.001
30	222	0.602	0.605	0.003
30	223	0.608	0.610	0.002
30	224	0.614	0.613	-0.001
30	225	0.610	0.612	0.002
30	226	0.604	0.607	0.003
35	227	0.613	0.611	-0.002
35	228	0.612	0.612	0.000
35	229	0.608	0.608	0.000
35	230	0.602	0.600	-0.002
35	231	0.605	0.603	-0.002
50	170	0.611	0.611	0.000
50	172	0.607	0.611	0.004
50	173	0.609	0.608	-0.001
50	175	0.606	0.607	0.001
50	176	0.612	0.613	0.001
55	177	0.608	0.607	-0.001
55	178	0.612	0.612	0.000
55	179	0.614	0.614	0.000
55	180	0.599	0.601	0.002
55	181	0.605	0.605	0.000
100	154	0.606	0.608	0.002
100	155	0.604	0.603	-0.001
100	156	0.607	0.604	-0.003
100	157	0.613	0.607	-0.006
100	158	0.607	0.608	0.001
100	159	0.609	0.606	-0.003
100	160	0.613	0.615	0.002
100	161	0.602	0.604	0.002
100	162	0.618	0.621	0.003
100	163	0.600	0.602	0.002
100	164	0.606	0.606	0.000
100	165	0.602	0.599	-0.003
100	166	0.604	0.604	0.000
100	168	0.607	0.607	0.000
100	169	0.603	0.603	0.000
100	182	0.616	0.614	-0.002
100	183	0.611	0.608	-0.003
100	184	0.605	0.604	-0.001
100	185	0.608	0.606	-0.002
100	186	0.602	0.606	0.004
100	187	0.603	0.602	-0.001
100	188	0.604	0.608	0.004
100	189	0.605	0.604	-0.001
100	190	0.609	0.609	0.000
105	191	0.607	0.608	0.001
105	192	0.607	0.608	0.001
105	193	0.611	0.614	0.003
105	194	0.613	0.614	0.001
105	195	0.607	0.607	0.000
105	196	0.599	0.598	-0.001
Max		0.618	0.621	0.004
Average		0.607	0.607	0.000
Min		0.599	0.598	-0.006
Std Dev		0.004	0.004	0.002



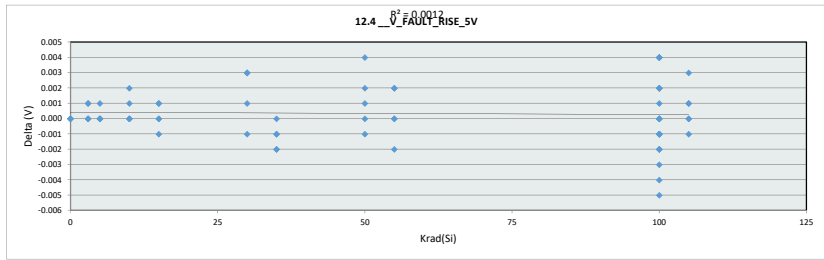
12.1_V_FAULT_RISE_4V											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			0.65	0.57							
			V	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.600	0.606	0.606	0.602	0.604	0.605	0.600	0.607	0.601	0.599	0.598
Average	0.602	0.608	0.610	0.607	0.607	0.609	0.607	0.610	0.608	0.607	0.608
Max	0.603	0.611	0.614	0.612	0.609	0.613	0.612	0.613	0.614	0.621	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



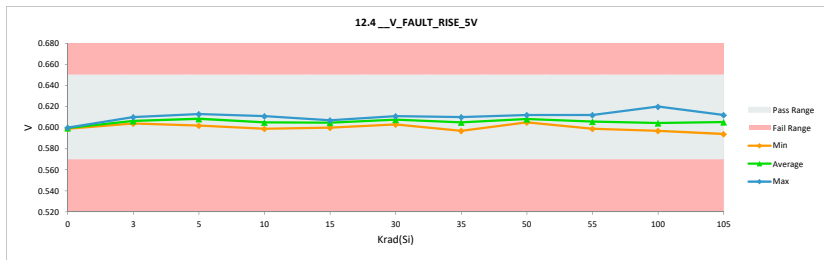


HDR TID Report  
TPS7H5003-SP

12.4 __V_FAULT_RISE_5V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			0.65	0.65
Min Limit			0.57	0.57
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.600	0.600	0.000
0	138	0.599	0.599	0.000
0	139	0.600	0.600	0.000
3	197	0.604	0.604	0.000
3	198	0.606	0.607	0.001
3	199	0.610	0.610	0.000
3	202	0.603	0.604	0.001
3	203	0.607	0.607	0.000
5	204	0.612	0.612	0.000
5	205	0.606	0.606	0.000
5	206	0.602	0.602	0.000
5	208	0.613	0.613	0.000
5	209	0.608	0.609	0.001
10	210	0.606	0.606	0.000
10	211	0.611	0.611	0.000
10	212	0.598	0.600	0.002
10	213	0.598	0.599	0.001
10	214	0.609	0.609	0.000
15	215	0.604	0.605	0.001
15	217	0.599	0.600	0.001
15	219	0.607	0.607	0.000
15	220	0.605	0.605	0.000
15	221	0.608	0.607	-0.001
30	222	0.600	0.603	0.003
30	223	0.608	0.609	0.001
30	224	0.612	0.611	-0.001
30	225	0.607	0.610	0.003
30	226	0.602	0.605	0.003
35	227	0.612	0.610	-0.002
35	228	0.610	0.610	0.000
35	229	0.607	0.606	-0.001
35	230	0.599	0.597	-0.002
35	231	0.603	0.602	-0.001
50	170	0.609	0.609	0.000
50	172	0.604	0.608	0.004
50	173	0.608	0.607	-0.001
50	175	0.604	0.605	0.001
50	176	0.610	0.612	0.002
55	177	0.607	0.605	-0.002
55	178	0.610	0.612	0.002
55	179	0.611	0.611	0.000
55	180	0.597	0.599	0.002
55	181	0.602	0.602	0.000
100	154	0.604	0.606	0.002
100	155	0.600	0.599	-0.001
100	156	0.605	0.601	-0.004
100	157	0.610	0.605	-0.005
100	158	0.605	0.607	0.002
100	159	0.606	0.603	-0.003
100	160	0.612	0.614	0.002
100	161	0.600	0.602	0.002
100	162	0.616	0.620	0.004
100	163	0.597	0.601	0.004
100	164	0.603	0.604	0.001
100	165	0.599	0.597	-0.002
100	166	0.603	0.602	-0.001
100	168	0.604	0.604	0.000
100	169	0.601	0.601	0.000
100	182	0.616	0.615	-0.001
100	183	0.608	0.606	-0.002
100	184	0.601	0.601	0.000
100	185	0.605	0.603	-0.002
100	186	0.599	0.603	0.004
100	187	0.600	0.599	-0.001
100	188	0.602	0.606	0.004
100	189	0.602	0.601	-0.001
100	190	0.608	0.608	0.000
105	191	0.604	0.604	0.000
105	192	0.605	0.606	0.001
105	193	0.608	0.611	0.003
105	194	0.611	0.612	0.001
105	195	0.605	0.605	0.000
105	196	0.595	0.594	-0.001
Max		0.616	0.620	0.004
Average		0.605	0.605	0.000
Min		0.595	0.594	-0.005
Std Dev		0.005	0.005	0.002

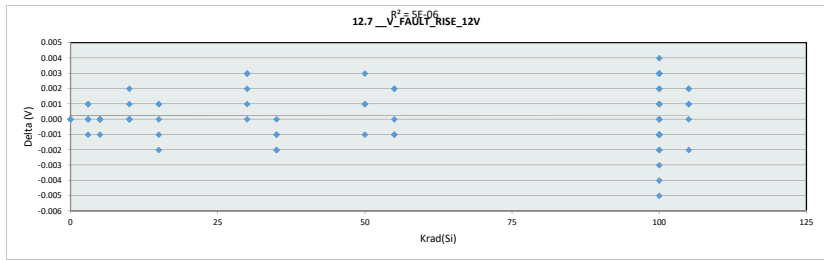


12.4 __V_FAULT_RISE_5V											
Test Site	Tester	Test Number	Unit								
			V	V							
Max Limit			0.65	0.65							
Min Limit			0.57	0.57							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.599	0.604	0.602	0.599	0.600	0.603	0.597	0.605	0.599	0.597	0.594
Average	0.600	0.606	0.608	0.605	0.605	0.608	0.605	0.608	0.606	0.605	0.605
Max	0.600	0.610	0.613	0.611	0.607	0.611	0.610	0.612	0.612	0.620	0.612
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

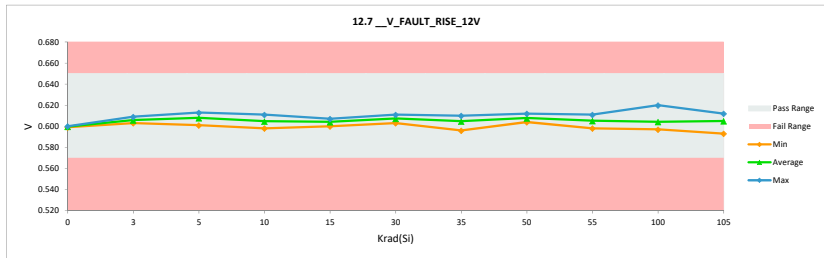


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12.7 __V_FAULT_RISE_12V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			V	0.65	0.57
			V	0.65	0.57
Krad(Si)	Serial #	PRE	POST	Delta	
0	137	0.600	0.600	0.000	
0	138	0.599	0.599	0.000	
0	139	0.600	0.600	0.000	
3	197	0.603	0.603	0.000	
3	198	0.606	0.607	0.001	
3	199	0.610	0.609	-0.001	
3	202	0.603	0.603	0.000	
3	203	0.606	0.607	0.001	
5	204	0.612	0.612	0.000	
5	205	0.606	0.606	0.000	
5	206	0.602	0.601	-0.001	
5	208	0.613	0.613	0.000	
5	209	0.608	0.608	0.000	
10	210	0.606	0.606	0.000	
10	211	0.611	0.611	0.000	
10	212	0.598	0.600	0.002	
10	213	0.597	0.598	0.001	
10	214	0.609	0.609	0.000	
15	215	0.603	0.604	0.001	
15	217	0.599	0.600	0.001	
15	219	0.607	0.607	0.000	
15	220	0.605	0.604	-0.001	
15	221	0.608	0.606	-0.002	
30	222	0.600	0.603	0.003	
30	223	0.608	0.609	0.001	
30	224	0.611	0.611	0.000	
30	225	0.607	0.609	0.002	
30	226	0.602	0.605	0.003	
35	227	0.612	0.610	-0.002	
35	228	0.610	0.610	0.000	
35	229	0.607	0.606	-0.001	
35	230	0.598	0.596	-0.002	
35	231	0.603	0.602	-0.001	
50	170	0.608	0.609	0.001	
50	172	0.604	0.607	0.003	
50	173	0.608	0.607	-0.001	
50	175	0.603	0.604	0.001	
50	176	0.611	0.612	0.001	
55	177	0.606	0.605	-0.001	
55	178	0.609	0.611	0.002	
55	179	0.611	0.610	-0.001	
55	180	0.596	0.598	0.002	
55	181	0.602	0.602	0.000	
100	154	0.603	0.606	0.003	
100	155	0.600	0.599	-0.001	
100	156	0.605	0.601	-0.004	
100	157	0.610	0.605	-0.005	
100	158	0.605	0.606	0.001	
100	159	0.605	0.602	-0.003	
100	160	0.612	0.614	0.002	
100	161	0.601	0.602	0.001	
100	162	0.616	0.620	0.004	
100	163	0.597	0.600	0.003	
100	164	0.602	0.604	0.002	
100	165	0.599	0.597	-0.002	
100	166	0.603	0.602	-0.001	
100	168	0.603	0.603	0.000	
100	169	0.601	0.601	0.000	
100	182	0.616	0.615	-0.001	
100	183	0.608	0.606	-0.002	
100	184	0.601	0.601	0.000	
100	185	0.604	0.603	-0.001	
100	186	0.599	0.602	0.003	
100	187	0.600	0.599	-0.001	
100	188	0.602	0.605	0.003	
100	189	0.602	0.601	-0.001	
100	190	0.607	0.608	0.001	
105	191	0.603	0.604	0.001	
105	192	0.604	0.606	0.002	
105	193	0.608	0.610	0.002	
105	194	0.611	0.612	0.001	
105	195	0.605	0.605	0.000	
105	196	0.595	0.593	-0.002	
	Max	0.616	0.620	0.004	
	Average	0.605	0.605	0.000	
	Min	0.595	0.593	-0.005	
	Std Dev	0.005	0.005	0.002	

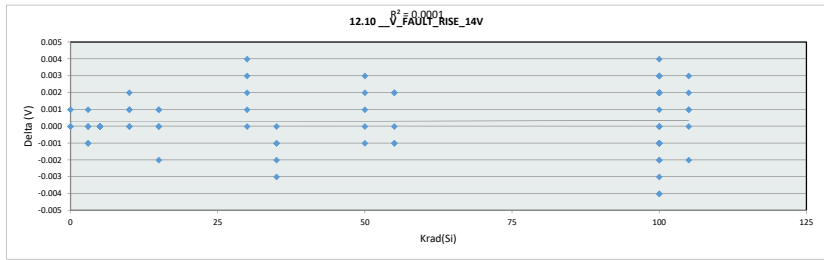


12.7 __V_FAULT_RISE_12V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.599	0.603	0.601	0.598	0.600	0.603	0.596	0.604	0.598	0.597	0.593
Average	0.600	0.606	0.608	0.605	0.604	0.607	0.605	0.608	0.605	0.604	0.605
Max	0.600	0.609	0.613	0.611	0.607	0.611	0.610	0.612	0.611	0.620	0.612
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

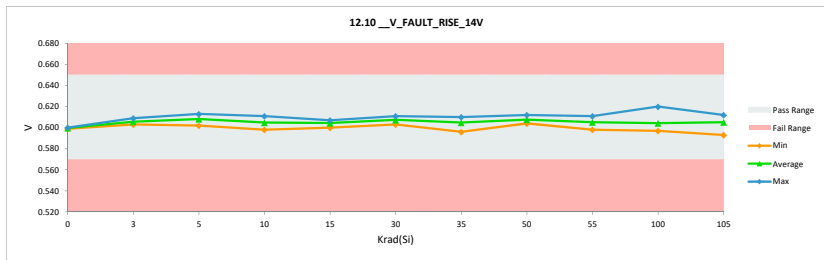


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12.10_V_FAULT_RISE_14V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			0.65	0.65
Min Limit			0.57	0.57
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.599	0.600	0.001
0	138	0.599	0.599	0.000
0	139	0.600	0.600	0.000
3	197	0.603	0.603	0.000
3	198	0.606	0.607	0.001
3	199	0.610	0.609	-0.001
3	202	0.603	0.603	0.000
3	203	0.607	0.606	-0.001
5	204	0.612	0.612	0.000
5	205	0.606	0.606	0.000
5	206	0.602	0.602	0.000
5	208	0.613	0.613	0.000
5	209	0.608	0.608	0.000
10	210	0.606	0.606	0.000
10	211	0.610	0.611	0.001
10	212	0.598	0.600	0.002
10	213	0.597	0.598	0.001
10	214	0.609	0.609	0.000
15	215	0.603	0.604	0.001
15	217	0.599	0.600	0.001
15	219	0.607	0.607	0.000
15	220	0.605	0.605	0.000
15	221	0.608	0.606	-0.002
30	222	0.599	0.603	0.004
30	223	0.608	0.609	0.001
30	224	0.611	0.611	0.000
30	225	0.607	0.609	0.002
30	226	0.602	0.605	0.003
35	227	0.612	0.610	-0.002
35	228	0.610	0.610	0.000
35	229	0.607	0.606	-0.001
35	230	0.599	0.596	-0.003
35	231	0.603	0.602	-0.001
50	170	0.608	0.608	0.000
50	172	0.604	0.607	0.003
50	173	0.608	0.607	-0.001
50	175	0.603	0.604	0.001
50	176	0.610	0.612	0.002
55	177	0.606	0.605	-0.001
55	178	0.609	0.611	0.002
55	179	0.611	0.610	-0.001
55	180	0.596	0.598	0.002
55	181	0.602	0.602	0.000
100	154	0.604	0.606	0.002
100	155	0.600	0.599	-0.001
100	156	0.605	0.601	-0.004
100	157	0.609	0.605	-0.004
100	158	0.605	0.607	0.002
100	159	0.605	0.602	-0.003
100	160	0.612	0.614	0.002
100	161	0.600	0.602	0.002
100	162	0.616	0.620	0.004
100	163	0.597	0.600	0.003
100	164	0.602	0.604	0.002
100	165	0.599	0.597	-0.002
100	166	0.603	0.602	-0.001
100	168	0.603	0.603	0.000
100	169	0.601	0.601	0.000
100	182	0.616	0.615	-0.001
100	183	0.608	0.606	-0.002
100	184	0.601	0.601	0.000
100	185	0.604	0.603	-0.001
100	186	0.599	0.602	0.003
100	187	0.600	0.599	-0.001
100	188	0.602	0.605	0.003
100	189	0.602	0.601	-0.001
100	190	0.607	0.608	0.001
105	191	0.603	0.604	0.001
105	192	0.604	0.606	0.002
105	193	0.608	0.611	0.003
105	194	0.611	0.612	0.001
105	195	0.605	0.605	0.000
105	196	0.595	0.593	-0.002
Max		0.616	0.620	0.004
Average		0.605	0.605	0.000
Min		0.595	0.593	-0.004
Std Dev		0.005	0.005	0.002

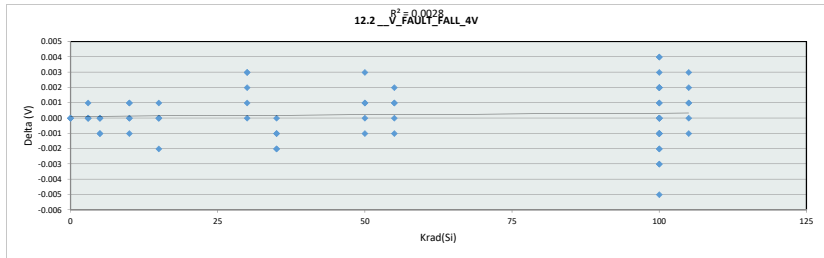


12.10_V_FAULT											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			0.65	0.57	V	V					
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.599	0.603	0.602	0.598	0.600	0.603	0.596	0.604	0.598	0.597	0.593
Average	0.600	0.606	0.608	0.605	0.604	0.607	0.605	0.608	0.605	0.604	0.605
Max	0.600	0.609	0.613	0.611	0.607	0.611	0.610	0.612	0.611	0.620	0.612
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

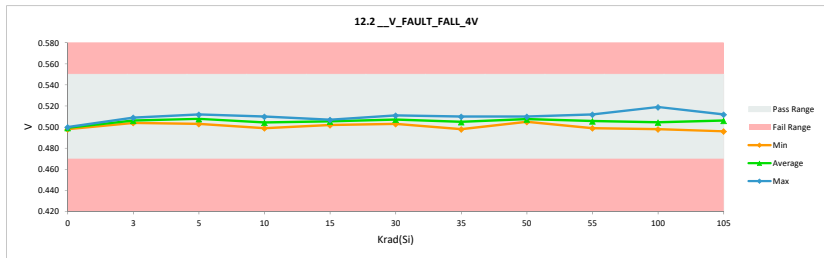


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		12.2_V_FAULT_FALL_4V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	0.55	0.55		
Min Limit	0.47	0.47		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.500	0.500	0.000
0	138	0.498	0.498	0.000
0	139	0.500	0.500	0.000
3	197	0.504	0.504	0.000
3	198	0.507	0.507	0.000
3	199	0.509	0.509	0.000
3	202	0.503	0.504	0.001
3	203	0.507	0.507	0.000
5	204	0.511	0.510	-0.001
5	205	0.507	0.507	0.000
5	206	0.504	0.503	-0.001
5	208	0.512	0.512	0.000
5	209	0.507	0.507	0.000
10	210	0.506	0.505	-0.001
10	211	0.510	0.510	0.000
10	212	0.498	0.499	0.001
10	213	0.499	0.500	0.001
10	214	0.508	0.508	0.000
15	215	0.505	0.505	0.000
15	217	0.501	0.502	0.001
15	219	0.507	0.507	0.000
15	220	0.506	0.506	0.000
15	221	0.509	0.507	-0.002
30	222	0.500	0.503	0.003
30	223	0.506	0.507	0.001
30	224	0.511	0.511	0.000
30	225	0.508	0.510	0.002
30	226	0.502	0.505	0.003
35	227	0.511	0.509	-0.002
35	228	0.510	0.510	0.000
35	229	0.507	0.506	-0.001
35	230	0.500	0.498	-0.002
35	231	0.503	0.502	-0.001
50	170	0.509	0.509	0.000
50	172	0.505	0.508	0.003
50	173	0.507	0.506	-0.001
50	175	0.504	0.505	0.001
50	176	0.509	0.510	0.001
55	177	0.506	0.505	-0.001
55	178	0.510	0.510	0.000
55	179	0.511	0.512	0.001
55	180	0.497	0.499	0.002
55	181	0.502	0.503	0.001
100	154	0.503	0.505	0.002
100	155	0.502	0.501	-0.001
100	156	0.505	0.502	-0.003
100	157	0.511	0.506	-0.005
100	158	0.505	0.506	0.001
100	159	0.507	0.504	-0.003
100	160	0.511	0.513	0.002
100	161	0.500	0.502	0.002
100	162	0.515	0.519	0.004
100	163	0.498	0.500	0.002
100	164	0.504	0.505	0.001
100	165	0.499	0.498	-0.001
100	166	0.502	0.502	0.000
100	168	0.504	0.504	0.000
100	169	0.501	0.501	0.000
100	182	0.514	0.513	-0.001
100	183	0.508	0.506	-0.002
100	184	0.502	0.501	-0.001
100	185	0.506	0.504	-0.002
100	186	0.500	0.504	0.004
100	187	0.501	0.500	-0.001
100	188	0.502	0.505	0.003
100	189	0.502	0.502	0.000
100	190	0.507	0.507	0.000
105	191	0.505	0.506	0.001
105	192	0.505	0.507	0.002
105	193	0.508	0.511	0.003
105	194	0.511	0.512	0.001
105	195	0.505	0.505	0.000
105	196	0.497	0.496	-0.001
	Max	0.515	0.519	0.004
	Average	0.505	0.505	0.000
	Min	0.497	0.496	-0.005
	Std Dev	0.004	0.004	0.002

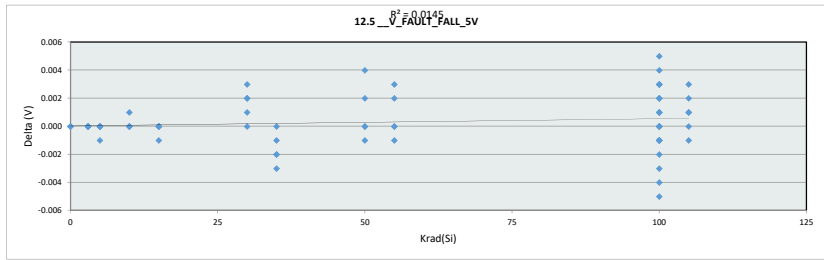


		12.2_V_FAULT_FALL_4V										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit	0.55	0.55										
Min Limit	0.47	0.47										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.498	0.504	0.503	0.499	0.502	0.503	0.498	0.505	0.499	0.498	0.496	0.496
Average	0.499	0.506	0.508	0.504	0.505	0.507	0.505	0.508	0.506	0.505	0.506	0.506
Max	0.500	0.509	0.512	0.510	0.507	0.511	0.510	0.510	0.512	0.519	0.512	0.512
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

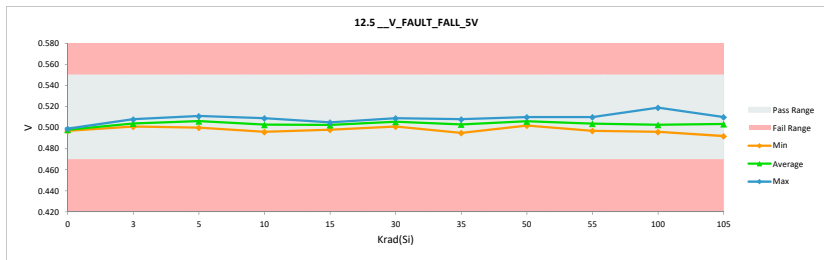


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		12.5_V_FAULT_FALL_5V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	0.55	0.55		
Min Limit	0.47	0.47		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.498	0.498	0.000
0	138	0.497	0.497	0.000
0	139	0.499	0.499	0.000
3	197	0.501	0.501	0.000
3	198	0.505	0.505	0.000
3	199	0.508	0.508	0.000
3	202	0.501	0.501	0.000
3	203	0.505	0.505	0.000
5	204	0.511	0.510	-0.001
5	205	0.504	0.504	0.000
5	206	0.500	0.500	0.000
5	208	0.511	0.511	0.000
5	209	0.506	0.506	0.000
10	210	0.504	0.504	0.000
10	211	0.509	0.509	0.000
10	212	0.497	0.498	0.001
10	213	0.496	0.496	0.000
10	214	0.507	0.507	0.000
15	215	0.502	0.502	0.000
15	217	0.498	0.498	0.000
15	219	0.505	0.505	0.000
15	220	0.503	0.503	0.000
15	221	0.506	0.505	-0.001
30	222	0.498	0.501	0.003
30	223	0.506	0.507	0.001
30	224	0.509	0.509	0.000
30	225	0.506	0.508	0.002
30	226	0.501	0.503	0.002
35	227	0.511	0.508	-0.003
35	228	0.508	0.508	0.000
35	229	0.505	0.504	-0.001
35	230	0.497	0.495	-0.002
35	231	0.502	0.500	-0.002
50	170	0.507	0.507	0.000
50	172	0.502	0.506	0.004
50	173	0.506	0.505	-0.001
50	175	0.502	0.502	0.000
50	176	0.508	0.510	0.002
55	177	0.504	0.503	-0.001
55	178	0.508	0.510	0.002
55	179	0.509	0.509	0.000
55	180	0.494	0.497	0.003
55	181	0.500	0.500	0.000
100	154	0.501	0.504	0.003
100	155	0.499	0.498	-0.001
100	156	0.503	0.499	-0.004
100	157	0.508	0.503	-0.005
100	158	0.503	0.505	0.002
100	159	0.504	0.501	-0.003
100	160	0.510	0.512	0.002
100	161	0.499	0.501	0.002
100	162	0.514	0.519	0.005
100	163	0.495	0.498	0.003
100	164	0.501	0.502	0.001
100	165	0.497	0.496	-0.001
100	166	0.501	0.501	0.000
100	168	0.501	0.501	0.000
100	169	0.499	0.499	0.000
100	182	0.514	0.513	-0.001
100	183	0.506	0.504	-0.002
100	184	0.499	0.500	0.001
100	185	0.503	0.502	-0.001
100	186	0.497	0.501	0.004
100	187	0.498	0.498	0.000
100	188	0.500	0.503	0.003
100	189	0.500	0.499	-0.001
100	190	0.505	0.506	0.001
105	191	0.501	0.503	0.002
105	192	0.503	0.504	0.001
105	193	0.506	0.509	0.003
105	194	0.509	0.510	0.001
105	195	0.503	0.503	0.000
105	196	0.493	0.492	-0.001
	Max	0.514	0.519	0.005
	Average	0.503	0.503	0.000
	Min	0.493	0.492	-0.005
	Std Dev	0.005	0.005	0.002

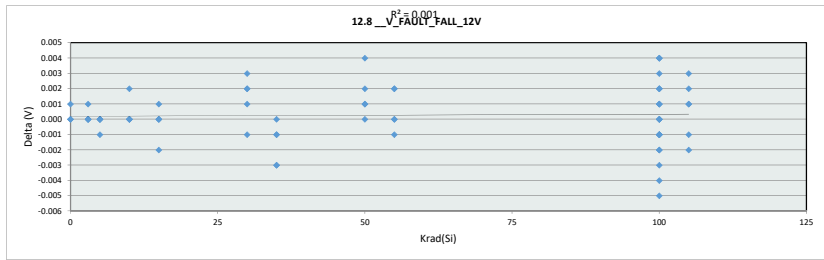


		12.5_V_FAULT_FALL_5V										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit	0.55	0.55										
Min Limit	0.47	0.47										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.497	0.501	0.500	0.496	0.498	0.501	0.495	0.502	0.497	0.496	0.492	
Average	0.498	0.504	0.506	0.503	0.503	0.506	0.503	0.506	0.504	0.503	0.504	
Max	0.499	0.508	0.511	0.509	0.505	0.509	0.508	0.510	0.510	0.519	0.510	
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	

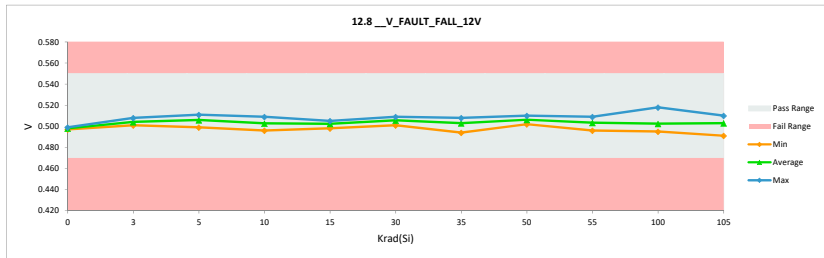


HDR TID Report  
TPS7H5003-SP

12.8_V_FAULT_FALL_12V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			0.55	0.55
Min Limit			0.47	0.47
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.498	0.498	0.000
0	138	0.497	0.497	0.000
0	139	0.498	0.499	0.001
3	197	0.501	0.501	0.000
3	198	0.505	0.506	0.001
3	199	0.508	0.508	0.000
3	202	0.501	0.501	0.000
3	203	0.505	0.505	0.000
5	204	0.510	0.510	0.000
5	205	0.504	0.504	0.000
5	206	0.500	0.499	-0.001
5	208	0.511	0.511	0.000
5	209	0.506	0.506	0.000
10	210	0.504	0.504	0.000
10	211	0.509	0.509	0.000
10	212	0.496	0.498	0.002
10	213	0.496	0.496	0.000
10	214	0.507	0.507	0.000
15	215	0.502	0.502	0.000
15	217	0.497	0.498	0.001
15	219	0.505	0.505	0.000
15	220	0.503	0.503	0.000
15	221	0.506	0.504	-0.002
30	222	0.498	0.501	0.003
30	223	0.506	0.507	0.001
30	224	0.510	0.509	-0.001
30	225	0.506	0.508	0.002
30	226	0.501	0.503	0.002
35	227	0.511	0.508	-0.003
35	228	0.508	0.508	0.000
35	229	0.505	0.504	-0.001
35	230	0.497	0.494	-0.003
35	231	0.502	0.501	-0.001
50	170	0.506	0.507	0.001
50	172	0.502	0.506	0.004
50	173	0.506	0.506	0.000
50	175	0.501	0.502	0.001
50	176	0.508	0.510	0.002
55	177	0.504	0.503	-0.001
55	178	0.507	0.509	0.002
55	179	0.509	0.509	0.000
55	180	0.494	0.496	0.002
55	181	0.500	0.500	0.000
100	154	0.502	0.504	0.002
100	155	0.498	0.497	-0.001
100	156	0.502	0.499	-0.003
100	157	0.508	0.503	-0.005
100	158	0.504	0.505	0.001
100	159	0.504	0.500	-0.004
100	160	0.510	0.512	0.002
100	161	0.498	0.500	0.002
100	162	0.514	0.518	0.004
100	163	0.495	0.498	0.003
100	164	0.501	0.502	0.001
100	165	0.497	0.495	-0.002
100	166	0.501	0.501	0.000
100	168	0.501	0.501	0.000
100	169	0.499	0.499	0.000
100	182	0.514	0.513	-0.001
100	183	0.506	0.504	-0.002
100	184	0.499	0.499	0.000
100	185	0.503	0.502	-0.001
100	186	0.497	0.501	0.004
100	187	0.498	0.497	-0.001
100	188	0.500	0.504	0.004
100	189	0.500	0.499	-0.001
100	190	0.505	0.506	0.001
105	191	0.501	0.502	0.001
105	192	0.502	0.504	0.002
105	193	0.506	0.509	0.003
105	194	0.509	0.510	0.001
105	195	0.503	0.502	-0.001
105	196	0.493	0.491	-0.002
Max		0.514	0.518	0.004
Average		0.503	0.503	0.000
Min		0.493	0.491	-0.005
Std Dev		0.005	0.005	0.002

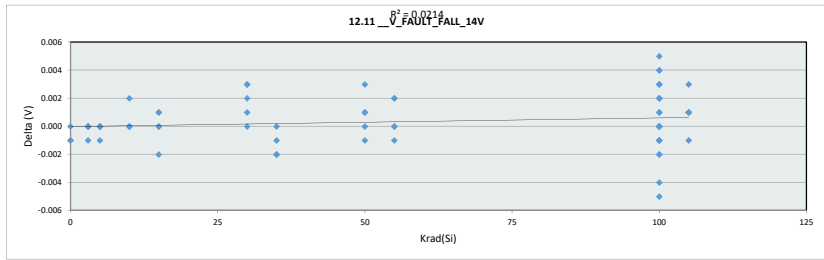


12.8_V_FAULT_FALL_12V											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			0.55	0.47							
			V	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.497	0.501	0.499	0.496	0.498	0.501	0.494	0.502	0.496	0.495	0.491
Average	0.498	0.504	0.506	0.503	0.502	0.506	0.503	0.506	0.503	0.502	0.503
Max	0.499	0.508	0.511	0.509	0.505	0.509	0.508	0.510	0.509	0.518	0.510
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

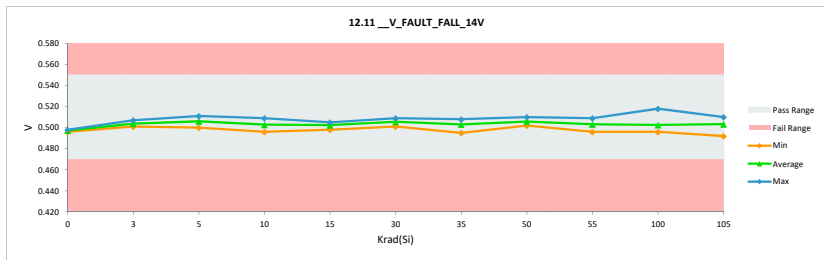


HDR TID Report  
TPS7H5003-SP

12.11_V_FAULT_FALL_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	0.55	0.55		
Min Limit	0.47	0.47		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.498	0.498	0.000
0	138	0.497	0.496	-0.001
0	139	0.499	0.498	-0.001
3	197	0.501	0.501	0.000
3	198	0.505	0.505	0.000
3	199	0.508	0.507	-0.001
3	202	0.501	0.501	0.000
3	203	0.505	0.505	0.000
5	204	0.510	0.509	-0.001
5	205	0.504	0.504	0.000
5	206	0.500	0.500	0.000
5	208	0.511	0.511	0.000
5	209	0.506	0.506	0.000
10	210	0.504	0.504	0.000
10	211	0.509	0.509	0.000
10	212	0.496	0.498	0.002
10	213	0.496	0.496	0.000
10	214	0.507	0.507	0.000
15	215	0.502	0.502	0.000
15	217	0.497	0.498	0.001
15	219	0.504	0.505	0.001
15	220	0.503	0.503	0.000
15	221	0.506	0.504	-0.002
30	222	0.498	0.501	0.003
30	223	0.506	0.507	0.001
30	224	0.509	0.509	0.000
30	225	0.506	0.508	0.002
30	226	0.500	0.503	0.003
35	227	0.510	0.508	-0.002
35	228	0.508	0.508	0.000
35	229	0.505	0.504	-0.001
35	230	0.497	0.495	-0.002
35	231	0.502	0.500	-0.002
50	170	0.507	0.507	0.000
50	172	0.502	0.505	0.003
50	173	0.506	0.505	-0.001
50	175	0.501	0.502	0.001
50	176	0.509	0.510	0.001
55	177	0.504	0.503	-0.001
55	178	0.507	0.509	0.002
55	179	0.508	0.508	0.000
55	180	0.494	0.496	0.002
55	181	0.500	0.500	0.000
100	154	0.501	0.504	0.003
100	155	0.498	0.497	-0.001
100	156	0.503	0.499	-0.004
100	157	0.508	0.503	-0.005
100	158	0.503	0.505	0.002
100	159	0.503	0.501	-0.002
100	160	0.510	0.512	0.002
100	161	0.498	0.500	0.002
100	162	0.514	0.518	0.004
100	163	0.495	0.498	0.003
100	164	0.501	0.502	0.001
100	165	0.497	0.496	-0.001
100	166	0.501	0.501	0.000
100	168	0.501	0.501	0.000
100	169	0.499	0.499	0.000
100	182	0.514	0.513	-0.001
100	183	0.506	0.504	-0.002
100	184	0.499	0.499	0.000
100	185	0.502	0.502	0.000
100	186	0.497	0.501	0.004
100	187	0.498	0.498	0.000
100	188	0.499	0.504	0.005
100	189	0.500	0.499	-0.001
100	190	0.505	0.506	0.001
105	191	0.501	0.502	0.001
105	192	0.503	0.504	0.001
105	193	0.506	0.509	0.003
105	194	0.509	0.510	0.001
105	195	0.502	0.503	0.001
105	196	0.493	0.492	-0.001
Max		0.514	0.518	0.005
Average		0.503	0.503	0.000
Min		0.493	0.492	-0.005
Std Dev		0.005	0.005	0.002

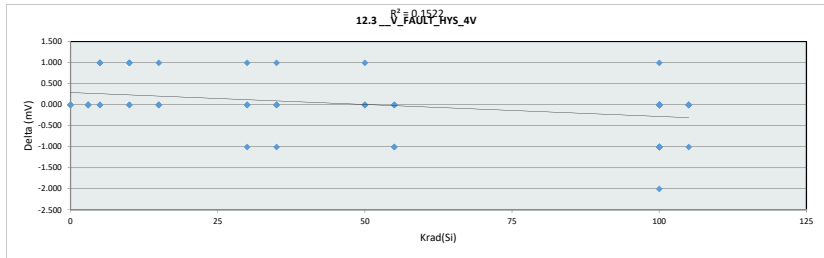


12.11_V_FAULT											
Test Site											
Tester											
Test Number											
Max Limit	0.55	V									
Min Limit	0.47	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.496	0.501	0.500	0.496	0.498	0.501	0.495	0.502	0.496	0.496	0.492
Average	0.497	0.504	0.506	0.503	0.502	0.506	0.503	0.506	0.503	0.503	0.503
Max	0.498	0.507	0.511	0.509	0.505	0.509	0.508	0.510	0.509	0.518	0.510
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

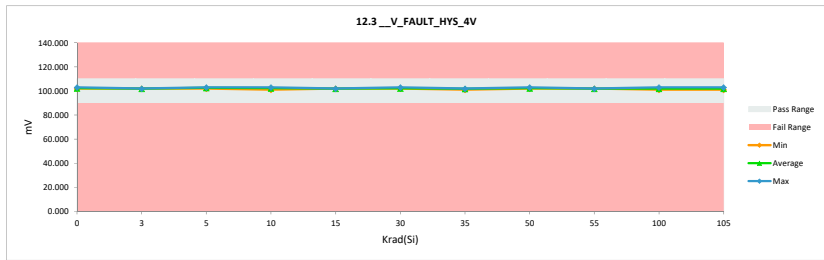


HDR TID Report  
TPS7H5003-SP

12.3_V_FAULT_HYS_4V				
Test Site	Tester	Test Number	Unit	
			mV	mV
Max Limit	110	110		
Min Limit	90	90		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	102.000	102.000	0.000
0	138	102.000	102.000	0.000
0	139	103.000	103.000	0.000
3	197	102.000	102.000	0.000
3	198	102.000	102.000	0.000
3	199	102.000	102.000	0.000
3	202	102.000	102.000	0.000
3	203	102.000	102.000	0.000
5	204	102.000	103.000	1.000
5	205	102.000	102.000	0.000
5	206	102.000	103.000	1.000
5	208	102.000	102.000	0.000
5	209	102.000	103.000	1.000
10	210	102.000	103.000	1.000
10	211	102.000	102.000	0.000
10	212	102.000	103.000	1.000
10	213	102.000	103.000	1.000
10	214	102.000	101.000	-1.000
10	214	101.000	101.000	0.000
15	215	102.000	102.000	0.000
15	217	102.000	102.000	0.000
15	219	102.000	102.000	0.000
15	220	102.000	102.000	0.000
15	221	101.000	102.000	1.000
30	222	102.000	102.000	0.000
30	223	102.000	103.000	1.000
30	224	103.000	102.000	-1.000
30	225	102.000	102.000	0.000
30	226	102.000	102.000	0.000
35	227	102.000	102.000	0.000
35	228	102.000	102.000	0.000
35	229	101.000	102.000	1.000
35	230	102.000	102.000	0.000
35	231	102.000	101.000	-1.000
50	170	102.000	102.000	0.000
50	172	102.000	103.000	1.000
50	173	102.000	102.000	0.000
50	175	102.000	102.000	0.000
50	176	103.000	103.000	0.000
55	177	102.000	102.000	0.000
55	178	102.000	102.000	0.000
55	179	103.000	102.000	-1.000
55	180	102.000	102.000	0.000
55	181	103.000	102.000	-1.000
100	154	103.000	103.000	0.000
100	155	102.000	102.000	0.000
100	156	102.000	102.000	0.000
100	157	102.000	101.000	-1.000
100	158	102.000	102.000	0.000
100	159	102.000	102.000	0.000
100	160	102.000	102.000	0.000
100	161	102.000	102.000	0.000
100	162	103.000	102.000	-1.000
100	163	102.000	102.000	0.000
100	164	102.000	101.000	-1.000
100	165	103.000	101.000	-2.000
100	166	102.000	102.000	0.000
100	168	103.000	103.000	0.000
100	169	102.000	102.000	0.000
100	182	102.000	101.000	-1.000
100	183	103.000	102.000	-1.000
100	184	103.000	103.000	0.000
100	185	102.000	102.000	0.000
100	186	102.000	102.000	0.000
100	187	102.000	102.000	0.000
100	188	102.000	103.000	1.000
100	189	103.000	102.000	-1.000
100	190	102.000	102.000	0.000
105	191	102.000	102.000	0.000
105	192	102.000	101.000	-1.000
105	193	103.000	103.000	0.000
105	194	102.000	102.000	0.000
105	195	102.000	102.000	0.000
105	196	102.000	102.000	0.000
Max		103.000	103.000	1.000
Average		102.137	102.110	-0.027
Min		101.000	101.000	-2.000
Std Dev		0.451	0.542	0.600



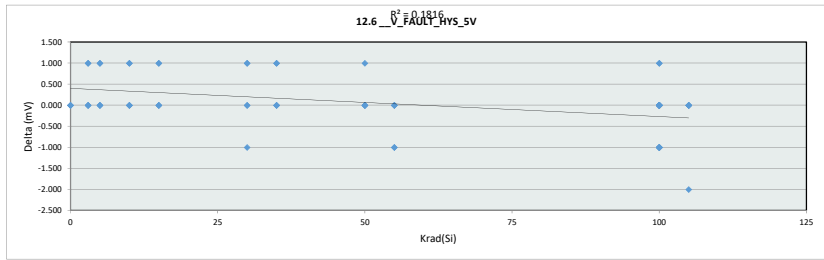
12.3_V_FAULT_HYS_4V											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			110	90							
			mV	mV							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	102.000	102.000	101.000	102.000	102.000	101.000	102.000	102.000	101.000	101.000
Average	102.333	102.000	102.600	102.400	102.000	102.200	101.800	102.400	102.000	102.000	102.000
Max	103.000	102.000	103.000	103.000	102.000	103.000	102.000	103.000	102.000	103.000	103.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



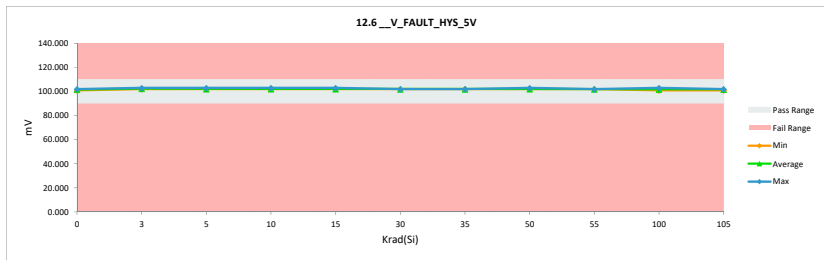


# HDR TID Report TPS7H5003-SP

12.6_V_FAULT_HYS_5V				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	102.000	102.000	0.000
0	138	102.000	102.000	0.000
0	139	101.000	101.000	0.000
3	197	103.000	103.000	0.000
3	198	101.000	102.000	1.000
3	199	102.000	102.000	0.000
3	202	102.000	103.000	1.000
3	203	102.000	102.000	0.000
5	204	101.000	102.000	1.000
5	205	102.000	102.000	0.000
5	206	102.000	102.000	0.000
5	208	102.000	102.000	0.000
5	209	102.000	103.000	1.000
10	210	102.000	102.000	0.000
10	211	102.000	102.000	0.000
10	212	101.000	102.000	1.000
10	213	102.000	103.000	1.000
10	214	102.000	102.000	0.000
15	215	102.000	103.000	1.000
15	217	101.000	102.000	1.000
15	219	102.000	102.000	0.000
15	220	102.000	102.000	0.000
15	221	102.000	102.000	0.000
30	222	102.000	102.000	0.000
30	223	102.000	102.000	0.000
30	224	103.000	102.000	-1.000
30	225	101.000	102.000	1.000
30	226	101.000	102.000	1.000
35	227	101.000	102.000	1.000
35	228	102.000	102.000	0.000
35	229	102.000	102.000	0.000
35	230	102.000	102.000	0.000
35	231	101.000	102.000	1.000
50	170	102.000	102.000	0.000
50	172	102.000	102.000	0.000
50	173	102.000	102.000	0.000
50	175	102.000	103.000	1.000
50	176	102.000	102.000	0.000
55	177	103.000	102.000	-1.000
55	178	102.000	102.000	0.000
55	179	102.000	102.000	0.000
55	180	103.000	102.000	-1.000
55	181	102.000	102.000	0.000
100	154	103.000	102.000	-1.000
100	155	101.000	101.000	0.000
100	156	102.000	102.000	0.000
100	157	102.000	102.000	0.000
100	158	102.000	102.000	0.000
100	159	102.000	102.000	0.000
100	160	102.000	102.000	0.000
100	161	101.000	101.000	0.000
100	162	102.000	101.000	-1.000
100	163	102.000	103.000	1.000
100	164	102.000	102.000	0.000
100	165	102.000	101.000	-1.000
100	166	102.000	101.000	-1.000
100	168	103.000	103.000	0.000
100	169	102.000	102.000	0.000
100	182	102.000	102.000	0.000
100	183	102.000	102.000	0.000
100	184	102.000	101.000	-1.000
100	185	102.000	101.000	-1.000
100	186	102.000	102.000	0.000
100	187	102.000	101.000	-1.000
100	188	102.000	103.000	1.000
100	189	102.000	102.000	0.000
100	190	103.000	102.000	-1.000
105	191	103.000	101.000	-2.000
105	192	102.000	102.000	0.000
105	193	102.000	102.000	0.000
105	194	102.000	102.000	0.000
105	195	102.000	102.000	0.000
105	196	102.000	102.000	0.000
Max		103.000	103.000	1.000
Average		101.959	101.986	0.027
Min		101.000	101.000	-2.000
Std Dev		0.512	0.514	0.645

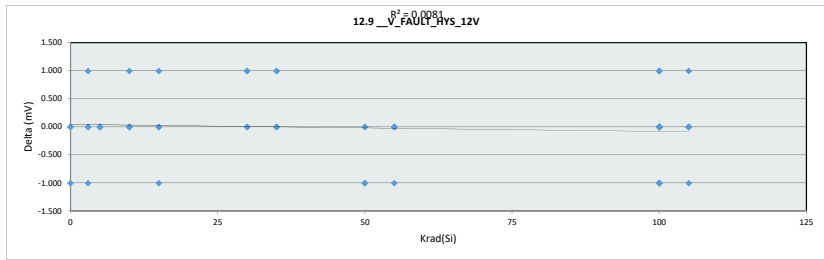


12.6_V_FAULT_HYS_5V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	101.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	101.000	101.000
Average	101.667	102.400	102.200	102.200	102.200	102.000	102.000	102.200	102.000	101.792	101.833
Max	102.000	103.000	103.000	103.000	103.000	102.000	102.000	103.000	102.000	103.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

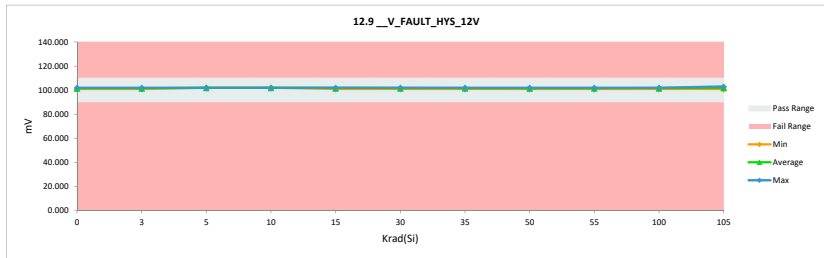


# HDR TID Report TPS7H5003-SP

12.9_V_FAULT_HYS_12V				
Test Site				
Tester				
Test Number				
Unit	mV	mV		
Max Limit	110	110		
Min Limit	90	90		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	102.000	102.000	0.000
0	138	102.000	102.000	0.000
0	139	102.000	101.000	-1.000
3	197	102.000	102.000	0.000
3	198	101.000	101.000	0.000
3	199	102.000	101.000	-1.000
3	202	102.000	102.000	0.000
3	203	101.000	102.000	1.000
5	204	102.000	102.000	0.000
5	205	102.000	102.000	0.000
5	206	102.000	102.000	0.000
5	208	102.000	102.000	0.000
5	209	102.000	102.000	0.000
10	210	102.000	102.000	0.000
10	211	102.000	102.000	0.000
10	212	102.000	102.000	0.000
10	213	101.000	102.000	1.000
10	214	102.000	102.000	0.000
15	215	101.000	102.000	1.000
15	217	102.000	102.000	0.000
15	219	102.000	102.000	0.000
15	220	102.000	101.000	-1.000
15	221	102.000	102.000	0.000
30	222	102.000	102.000	0.000
30	223	102.000	102.000	0.000
30	224	101.000	102.000	1.000
30	225	101.000	101.000	0.000
30	226	101.000	102.000	1.000
35	227	101.000	102.000	1.000
35	228	102.000	102.000	0.000
35	229	102.000	102.000	0.000
35	230	101.000	102.000	1.000
35	231	101.000	101.000	0.000
50	170	102.000	102.000	0.000
50	172	102.000	101.000	-1.000
50	173	102.000	101.000	-1.000
50	175	102.000	102.000	0.000
50	176	103.000	102.000	-1.000
55	177	102.000	102.000	0.000
55	178	102.000	102.000	0.000
55	179	102.000	101.000	-1.000
55	180	102.000	102.000	0.000
55	181	102.000	102.000	0.000
100	154	101.000	102.000	1.000
100	155	102.000	102.000	0.000
100	156	103.000	102.000	-1.000
100	157	102.000	102.000	0.000
100	158	101.000	101.000	0.000
100	159	101.000	102.000	1.000
100	160	102.000	102.000	0.000
100	161	103.000	102.000	-1.000
100	162	102.000	102.000	0.000
100	163	102.000	102.000	0.000
100	164	101.000	102.000	1.000
100	165	102.000	102.000	0.000
100	166	102.000	101.000	-1.000
100	168	102.000	102.000	0.000
100	169	102.000	102.000	0.000
100	182	102.000	102.000	0.000
100	183	102.000	102.000	0.000
100	184	102.000	102.000	0.000
100	185	101.000	101.000	0.000
100	186	102.000	101.000	-1.000
100	187	102.000	102.000	0.000
100	188	102.000	101.000	-1.000
100	189	102.000	102.000	0.000
100	190	102.000	102.000	0.000
105	191	102.000	102.000	0.000
105	192	102.000	102.000	0.000
105	193	102.000	101.000	-1.000
105	194	102.000	102.000	0.000
105	195	102.000	103.000	1.000
105	196	102.000	102.000	0.000
	Max	103.000	103.000	1.000
	Average	101.836	101.808	-0.027
	Min	101.000	101.000	-1.000
	Std Dev	0.472	0.430	0.577

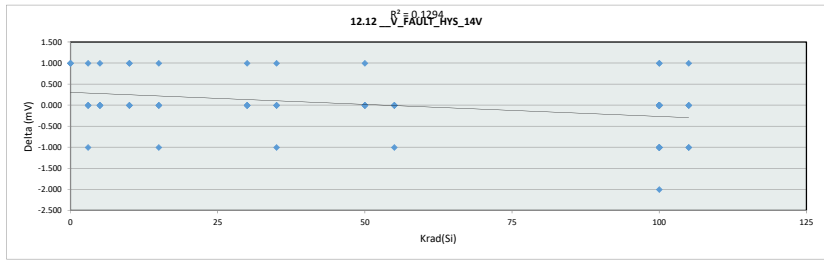


12.9_V_FAULT_HYS_12V											
Test Site											
Tester											
Test Number											
Max Limit	110	mV									
Min Limit	90	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	101.000	101.000	102.000	102.000	101.000	101.000	101.000	101.000	101.000	101.000	101.000
Average	101.667	101.600	102.000	102.000	101.800	101.800	101.800	101.600	101.800	101.792	102.000
Max	102.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	103.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

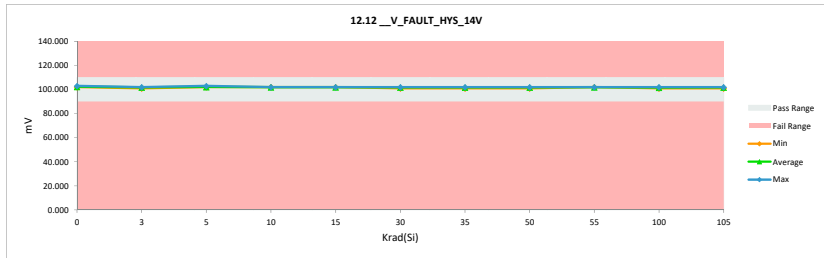


HDR TID Report  
TPS7H5003-SP

12.12_V_FAULT_HYS_14V				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	101.000	102.000	1.000
0	138	102.000	103.000	1.000
0	139	101.000	102.000	1.000
3	197	102.000	102.000	0.000
3	198	101.000	102.000	1.000
3	199	102.000	102.000	0.000
3	202	102.000	102.000	0.000
3	203	102.000	101.000	-1.000
5	204	102.000	103.000	1.000
5	205	102.000	102.000	0.000
5	206	102.000	102.000	0.000
5	208	102.000	102.000	0.000
5	209	102.000	102.000	0.000
10	210	102.000	102.000	0.000
10	211	101.000	102.000	1.000
10	212	102.000	102.000	0.000
10	213	101.000	102.000	1.000
10	214	101.000	102.000	1.000
10	215	101.000	102.000	1.000
15	217	102.000	102.000	0.000
15	219	103.000	102.000	-1.000
15	220	102.000	102.000	0.000
15	221	102.000	102.000	0.000
30	222	101.000	102.000	1.000
30	223	102.000	102.000	0.000
30	224	102.000	102.000	0.000
30	225	101.000	101.000	0.000
30	226	102.000	102.000	0.000
35	227	102.000	102.000	0.000
35	228	102.000	102.000	0.000
35	229	102.000	102.000	0.000
35	230	102.000	101.000	-1.000
35	231	101.000	102.000	1.000
50	170	101.000	101.000	0.000
50	172	102.000	102.000	0.000
50	173	102.000	102.000	0.000
50	175	102.000	102.000	0.000
50	176	101.000	102.000	1.000
55	177	102.000	102.000	0.000
55	178	102.000	102.000	0.000
55	179	103.000	102.000	-1.000
55	180	102.000	102.000	0.000
55	181	102.000	102.000	0.000
100	154	103.000	102.000	-1.000
100	155	102.000	102.000	0.000
100	156	102.000	102.000	0.000
100	157	101.000	102.000	1.000
100	158	102.000	102.000	0.000
100	159	102.000	101.000	-1.000
100	160	102.000	102.000	0.000
100	161	102.000	102.000	0.000
100	162	102.000	102.000	0.000
100	163	102.000	102.000	0.000
100	164	101.000	102.000	1.000
100	165	102.000	101.000	-1.000
100	166	102.000	101.000	-1.000
100	168	102.000	102.000	0.000
100	169	102.000	102.000	0.000
100	182	102.000	102.000	0.000
100	183	102.000	102.000	0.000
100	184	102.000	102.000	0.000
100	185	102.000	101.000	-1.000
100	186	102.000	101.000	-1.000
100	187	102.000	101.000	-1.000
100	188	103.000	101.000	-2.000
100	189	102.000	102.000	0.000
100	190	102.000	102.000	0.000
105	191	102.000	102.000	0.000
105	192	101.000	102.000	1.000
105	193	102.000	102.000	0.000
105	194	102.000	102.000	0.000
105	195	103.000	102.000	-1.000
105	196	102.000	101.000	-1.000
Max		103.000	103.000	1.000
Average		101.877	101.863	-0.014
Min		101.000	101.000	-2.000
Std Dev		0.498	0.419	0.656

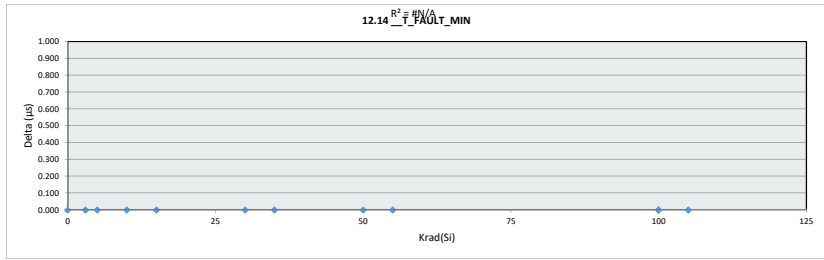


12.12_V_FAULT											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	101.000	102.000	102.000	102.000	101.000	101.000	101.000	102.000	101.000	101.000
Average	102.333	101.800	102.200	102.000	102.000	101.800	101.800	101.800	102.000	101.708	101.833
Max	103.000	102.000	103.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

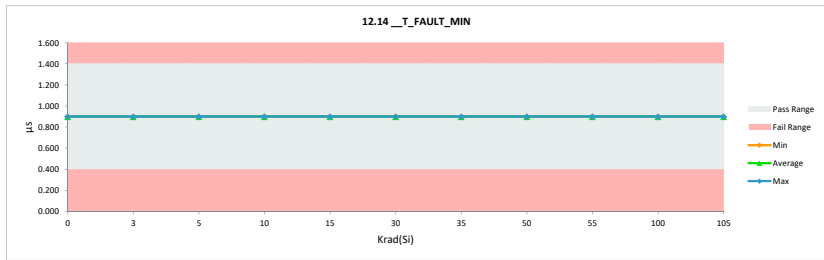


HDR TID Report  
TPS7H5003-SP

		12.14 __T_FAULT_MIN		
Test Site				
Tester				
Test Number				
Unit		µs	µs	
Max Limit		1.4	1.4	
Min Limit		0.4	0.4	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	0.900	0.900	0.000
0	138	0.900	0.900	0.000
0	139	0.900	0.900	0.000
3	197	0.900	0.900	0.000
3	198	0.900	0.900	0.000
3	199	0.900	0.900	0.000
3	202	0.900	0.900	0.000
3	203	0.900	0.900	0.000
5	204	0.900	0.900	0.000
5	205	0.900	0.900	0.000
5	206	0.900	0.900	0.000
5	208	0.900	0.900	0.000
5	209	0.900	0.900	0.000
10	210	0.900	0.900	0.000
10	211	0.900	0.900	0.000
10	212	0.900	0.900	0.000
10	213	0.900	0.900	0.000
10	214	0.900	0.900	0.000
10	215	0.900	0.900	0.000
15	217	0.900	0.900	0.000
15	219	0.900	0.900	0.000
15	220	0.900	0.900	0.000
15	221	0.900	0.900	0.000
30	222	0.900	0.900	0.000
30	223	0.900	0.900	0.000
30	224	0.900	0.900	0.000
30	225	0.900	0.900	0.000
30	226	0.900	0.900	0.000
35	227	0.900	0.900	0.000
35	228	0.900	0.900	0.000
35	229	0.900	0.900	0.000
35	230	0.900	0.900	0.000
35	231	0.900	0.900	0.000
50	170	0.900	0.900	0.000
50	172	0.900	0.900	0.000
50	173	0.900	0.900	0.000
50	175	0.900	0.900	0.000
50	176	0.900	0.900	0.000
55	177	0.900	0.900	0.000
55	178	0.900	0.900	0.000
55	179	0.900	0.900	0.000
55	180	0.900	0.900	0.000
55	181	0.900	0.900	0.000
100	154	0.900	0.900	0.000
100	155	0.900	0.900	0.000
100	156	0.900	0.900	0.000
100	157	0.900	0.900	0.000
100	158	0.900	0.900	0.000
100	159	0.900	0.900	0.000
100	160	0.900	0.900	0.000
100	161	0.900	0.900	0.000
100	162	0.900	0.900	0.000
100	163	0.900	0.900	0.000
100	164	0.900	0.900	0.000
100	165	0.900	0.900	0.000
100	166	0.900	0.900	0.000
100	168	0.900	0.900	0.000
100	169	0.900	0.900	0.000
100	182	0.900	0.900	0.000
100	183	0.900	0.900	0.000
100	184	0.900	0.900	0.000
100	185	0.900	0.900	0.000
100	186	0.900	0.900	0.000
100	187	0.900	0.900	0.000
100	188	0.900	0.900	0.000
100	189	0.900	0.900	0.000
100	190	0.900	0.900	0.000
105	191	0.900	0.900	0.000
105	192	0.900	0.900	0.000
105	193	0.900	0.900	0.000
105	194	0.900	0.900	0.000
105	195	0.900	0.900	0.000
105	196	0.900	0.900	0.000
	Max	0.900	0.900	0.000
	Average	0.900	0.900	0.000
	Min	0.900	0.900	0.000
	Std Dev	0.000	0.000	0.000

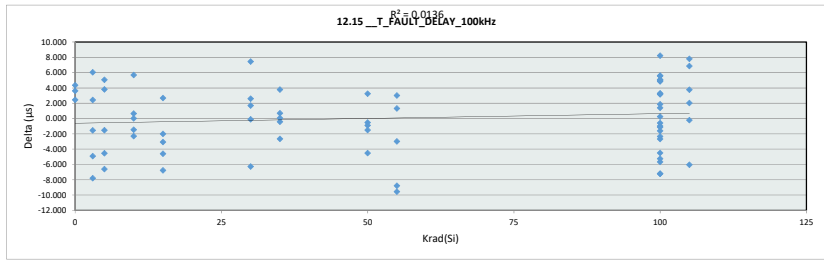


		12.14 T_FAULT											
Test Site													
Tester													
Test Number													
Max Limit		1.4	µs										
Min Limit		0.4	µs										
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105	
LL		0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	
Min		0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	
Average		0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	
Max		0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	
UL		1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	

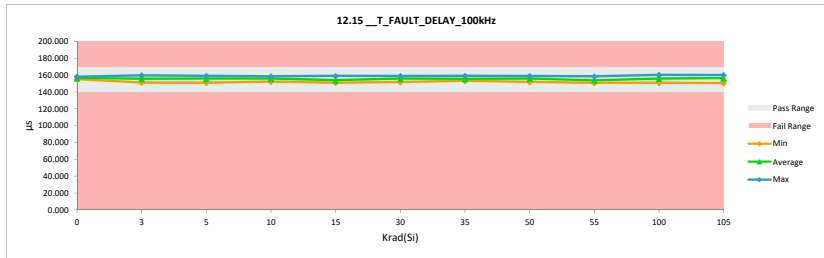


HDR TID Report  
TPS7H5003-SP

		12.15 _T_FAULT_DELAY_100kHz		
Test Site	Tester			
Test Number	Unit	µs	µs	
Max Limit	169	169		
Min Limit	140	140		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	151.320	154.985	3.665
0	138	153.765	158.150	4.385
0	139	154.237	156.730	2.493
3	197	152.898	155.366	2.468
3	198	153.679	159.768	6.089
3	199	160.066	158.568	-1.498
3	202	158.830	151.064	-7.766
3	203	158.603	153.740	-4.863
5	204	152.911	158.007	5.096
5	205	158.177	153.685	-4.492
5	206	157.618	151.048	-6.570
5	208	155.211	159.058	3.847
5	209	159.365	157.887	-1.478
10	210	152.272	152.324	0.052
10	211	159.867	157.617	-2.250
10	212	157.756	156.327	-1.429
10	213	152.833	158.562	5.729
10	214	154.309	155.002	0.693
15	215	152.997	151.039	-1.958
15	217	156.444	159.161	2.717
15	219	158.180	151.450	-6.730
15	220	159.136	154.586	-4.550
15	221	157.127	154.093	-3.034
30	222	156.368	159.000	2.632
30	223	150.877	158.357	7.480
30	224	158.032	151.809	-6.223
30	225	153.725	153.650	-0.075
30	226	155.026	156.758	1.732
35	227	155.309	159.131	3.822
35	228	156.940	154.331	-2.609
35	229	154.187	154.919	0.732
35	230	153.533	153.150	-0.383
35	231	156.377	156.478	0.101
50	170	159.667	155.185	-4.482
50	172	153.595	156.873	3.278
50	173	152.314	151.834	-0.480
50	175	157.221	156.354	-0.867
50	176	160.358	158.897	-1.461
55	177	155.565	152.615	-2.950
55	178	160.291	150.784	-9.507
55	179	159.788	151.055	-8.733
55	180	154.302	155.661	1.359
55	181	155.411	158.464	3.053
100	154	159.848	152.668	-7.180
100	155	159.714	157.421	-2.293
100	156	157.295	156.353	-0.942
100	157	154.244	153.165	-1.079
100	158	150.924	159.176	8.252
100	159	151.118	156.003	4.885
100	160	152.610	158.243	5.633
100	161	160.007	154.371	-5.636
100	162	153.655	158.633	4.978
100	163	153.066	151.487	-1.579
100	164	153.907	157.239	3.332
100	165	152.291	157.409	5.118
100	166	156.210	151.002	-5.208
100	168	156.817	158.248	1.431
100	169	157.405	152.948	-4.457
100	182	153.851	155.761	1.910
100	183	156.903	160.182	3.279
100	184	151.293	151.584	0.291
100	185	152.009	157.612	5.603
100	186	155.153	160.234	5.081
100	187	155.817	159.017	3.200
100	188	158.541	151.371	-7.170
100	189	157.245	156.692	-0.553
100	190	155.795	153.169	-2.626
105	191	154.706	158.512	3.806
105	192	156.419	156.249	-0.170
105	193	156.503	150.509	-5.994
105	194	152.391	160.235	7.844
105	195	152.437	159.334	6.897
105	196	151.335	153.402	2.067
	Max	160.358	160.235	8.252
	Average	155.562	155.640	0.079
	Min	150.877	150.509	-9.507
	Std Dev	2.752	2.920	4.379

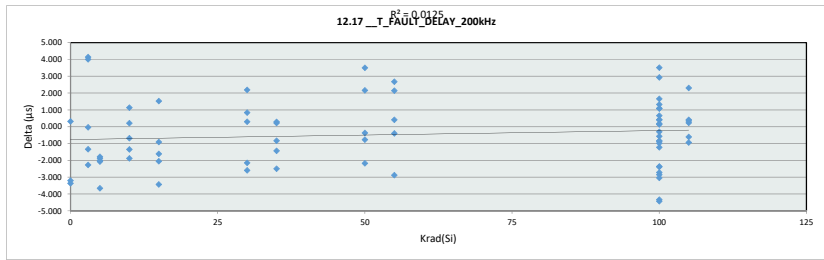


		12.15 T_FAULT										
Test Site	Tester											
Test Number	Unit	µs	µs									
Max Limit	169	µs										
Min Limit	140	µs										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000
Min	154.985	151.064	151.048	152.324	151.039	151.809	153.150	151.834	150.784	151.002	150.509	
Average	156.622	155.701	155.937	155.966	154.066	155.915	155.602	155.829	153.716	155.833	156.374	
Max	158.150	159.768	159.058	158.562	159.161	159.003	159.131	158.897	158.464	160.234	160.235	
UL	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	

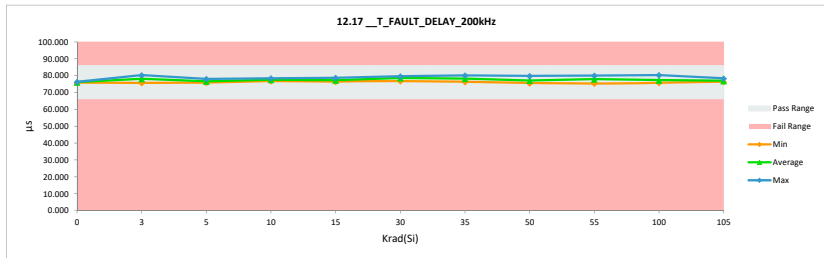


# HDR TID Report TPS7H5003-SP

		12.17 __ T_FAULT_DELAY_200kHz		
Test Site	Tester			
Test Number	Unit	µs	µs	
Max Limit	Min Limit	86	66	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	75.522	75.854	0.332
0	138	79.661	76.315	-3.346
0	139	79.518	76.330	-3.188
3	197	76.324	76.308	-0.016
3	198	76.133	80.296	4.163
3	199	77.992	75.740	-2.252
3	202	79.540	78.223	-1.317
3	203	76.286	80.314	4.028
5	204	78.529	76.537	-1.992
5	205	79.663	76.018	-3.645
5	206	80.180	78.127	-2.053
5	208	78.495	76.731	-1.764
5	209	77.798	75.932	-1.866
10	210	79.080	77.752	-1.328
10	211	76.052	77.203	1.151
10	212	78.240	78.472	0.232
10	213	78.712	76.849	-1.863
10	214	78.543	77.880	-0.663
10	215	78.093	76.495	-1.598
15	217	77.224	78.765	1.541
15	219	79.832	77.798	-2.034
15	220	80.099	76.685	-3.414
15	221	77.800	76.907	-0.893
30	222	79.238	79.555	0.317
30	223	79.428	76.849	-2.579
30	224	79.762	77.631	-2.131
30	225	78.756	79.614	0.858
30	226	77.480	79.687	2.207
35	227	79.709	77.228	-2.481
35	228	79.895	80.197	0.302
35	229	80.414	79.601	-0.813
35	230	77.770	76.354	-1.416
35	231	77.839	78.083	0.244
50	170	76.316	79.842	3.526
50	172	76.715	75.963	-0.752
50	173	75.608	77.788	2.180
50	175	78.622	76.461	-2.161
50	176	75.956	75.603	-0.353
55	177	78.310	75.450	-2.860
55	178	75.937	78.096	2.159
55	179	79.682	80.115	0.433
55	180	77.038	76.662	-0.376
55	181	77.019	79.713	2.694
100	154	78.886	76.186	-2.700
100	155	77.055	76.502	-0.553
100	156	79.043	76.021	-3.022
100	157	80.250	75.839	-4.411
100	158	80.208	75.895	-4.313
100	159	79.198	78.371	-0.827
100	160	79.941	77.112	-2.829
100	161	75.833	76.943	1.110
100	162	76.335	77.436	1.101
100	163	77.175	76.336	-0.839
100	164	76.204	77.547	1.343
100	165	75.818	77.496	1.678
100	166	79.517	79.945	0.428
100	168	77.333	80.287	2.954
100	169	76.307	79.839	3.532
100	182	75.497	76.174	0.677
100	183	79.311	76.960	-2.351
100	184	80.215	80.374	0.159
100	185	77.004	77.209	0.205
100	186	78.492	77.285	-1.207
100	187	79.660	78.698	-0.962
100	188	79.062	76.697	-2.365
100	189	75.603	76.038	0.435
100	190	76.898	76.617	-0.281
105	191	76.453	76.885	0.432
105	192	76.613	76.865	0.252
105	193	77.354	76.438	-0.916
105	194	76.361	76.726	0.365
105	195	77.190	76.598	-0.592
105	196	76.129	78.452	2.323
Max		80.414	80.374	4.163
Average		77.969	77.504	-0.465
Min		75.497	75.450	-4.411
Std Dev		1.514	1.428	1.991



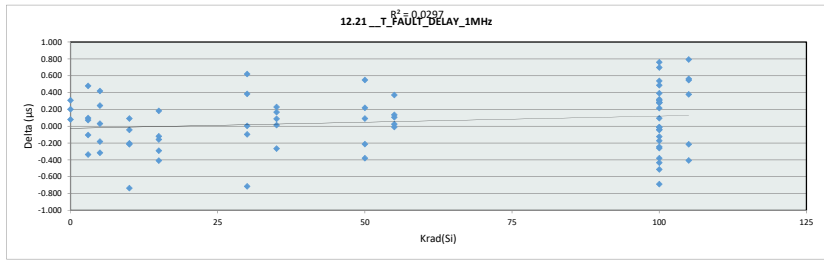
		12.17 __ T_FAULT										
Test Site	Tester											
Test Number	Unit	µs	µs									
Max Limit	Min Limit	86	66									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	
Min	75.854	75.740	75.932	76.849	76.495	76.849	76.354	75.603	75.450	75.839	76.438	
Average	76.166	78.176	76.669	77.631	77.330	78.667	78.293	77.131	78.007	77.409	76.994	
Max	76.330	80.314	78.127	78.472	78.765	79.687	80.197	79.842	80.115	80.374	78.452	
UL	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	



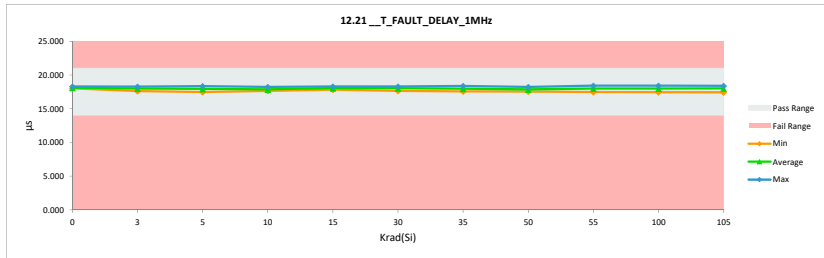
# HDR TID Report

## TPS7H5003-SP

		12.21_T_FAULT_DELAY_1MHz		
Test Site	Tester			
Test Number	Unit	µs	µs	
Max Limit	21	21		
Min Limit	14	14		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	17.965	18.049	0.084
0	138	18.002	18.313	0.311
0	139	17.827	18.031	0.204
3	197	18.196	18.297	0.101
3	198	18.088	17.754	-0.334
3	199	18.246	18.145	-0.101
3	202	17.562	17.637	0.075
3	203	17.771	18.253	0.482
5	204	17.918	17.738	-0.180
5	205	17.948	18.370	0.422
5	206	17.792	17.480	-0.312
5	208	17.813	18.061	0.248
5	209	18.121	18.154	0.033
10	210	18.225	18.024	-0.201
10	211	17.712	17.806	0.094
10	212	17.755	17.715	-0.040
10	213	18.390	17.657	-0.733
10	214	18.451	18.239	-0.212
15	215	18.122	18.308	0.186
15	217	18.431	18.315	-0.116
15	219	18.421	18.015	-0.406
15	220	18.166	17.879	-0.287
15	221	18.003	17.849	-0.154
30	222	17.981	17.989	0.008
30	223	18.373	17.661	-0.712
30	224	18.404	18.310	-0.094
30	225	17.728	18.115	0.387
30	226	17.652	18.275	0.623
35	227	17.941	18.112	0.171
35	228	17.707	17.797	0.090
35	229	17.800	18.032	0.232
35	230	18.366	18.382	0.016
35	231	17.847	17.584	-0.263
50	170	17.697	18.250	0.553
50	172	17.469	17.563	0.094
50	173	17.524	17.746	0.222
50	175	18.307	17.931	-0.376
50	176	18.087	17.877	-0.210
55	177	17.493	17.486	-0.007
55	178	18.059	18.432	0.373
55	179	18.081	18.219	0.138
55	180	17.849	17.959	0.110
55	181	17.932	17.960	0.028
100	154	18.166	17.735	-0.431
100	155	17.471	17.751	0.280
100	156	18.212	18.203	-0.009
100	157	18.080	18.403	0.323
100	158	18.404	18.234	-0.170
100	159	18.158	17.474	-0.684
100	160	17.611	18.374	0.763
100	161	18.077	17.835	-0.242
100	162	18.441	18.065	-0.376
100	163	17.629	17.939	0.310
100	164	17.580	17.679	0.099
100	165	17.910	17.790	-0.120
100	166	17.711	18.253	0.542
100	168	17.924	18.414	0.490
100	169	18.049	18.336	0.287
100	182	17.619	18.013	0.394
100	183	18.273	17.763	-0.510
100	184	18.361	18.104	-0.257
100	185	17.599	17.817	0.218
100	186	17.750	17.713	-0.037
100	187	17.733	18.434	0.701
100	188	17.628	17.912	0.284
100	189	18.356	18.315	-0.041
100	190	17.702	17.694	-0.008
105	191	17.733	18.114	0.381
105	192	17.734	18.285	0.551
105	193	17.850	17.447	-0.403
105	194	17.686	18.251	0.565
105	195	17.622	18.418	0.796
105	196	17.971	17.760	-0.211
Max		18.451	18.434	0.796
Average		17.949	18.004	0.055
Min		17.469	17.447	-0.733
Std Dev		0.283	0.279	0.347

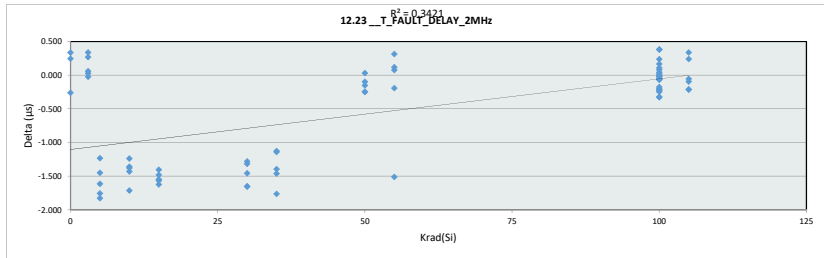


		12.21_T_FAULT										
Test Site	Tester											
Test Number	Unit	µs	µs									
Max Limit	21	µs										
Min Limit	14	µs										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	
Min	18.031	17.637	17.480	17.657	17.849	17.661	17.584	17.563	17.486	17.474	17.447	
Average	18.131	18.017	17.961	17.888	18.073	18.070	17.981	17.873	18.011	18.010	18.046	
Max	18.313	18.297	18.370	18.239	18.315	18.310	18.382	18.250	18.432	18.434	18.418	
UL	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	

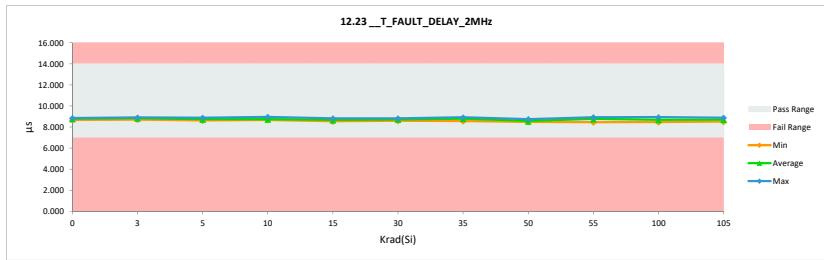


HDR TID Report  
TPS7H5003-SP

		12.23 __ T_FAULT_DELAY_2MHz		
Test Site	Tester			
Test Number	Unit	µs	µs	
Max Limit	Min Limit	14	7	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.607	8.856	0.249
0	138	8.932	8.674	-0.258
0	139	8.498	8.837	0.339
3	197	8.907	8.888	-0.019
3	198	8.547	8.889	0.342
3	199	8.634	8.907	0.273
3	202	8.731	8.795	0.064
3	203	8.683	8.714	0.031
5	204	10.415	8.662	-1.753
5	205	10.302	8.692	-1.610
5	206	10.075	8.846	-1.229
5	208	10.336	8.890	-1.446
5	209	10.450	8.625	-1.825
10	210	10.096	8.740	-1.356
10	211	10.385	8.675	-1.710
10	212	10.140	8.712	-1.428
10	213	10.046	8.672	-1.374
10	214	10.197	8.963	-1.234
10	215	10.027	8.549	-1.478
15	217	10.276	8.732	-1.544
15	219	10.134	8.571	-1.563
15	220	10.229	8.829	-1.400
15	221	10.335	8.714	-1.621
30	222	10.043	8.589	-1.454
30	223	10.087	8.809	-1.278
30	224	10.329	8.678	-1.651
30	225	10.141	8.828	-1.313
30	226	10.348	8.700	-1.648
35	227	10.327	8.566	-1.761
35	228	10.061	8.923	-1.138
35	229	10.209	8.817	-1.392
35	230	10.008	8.886	-1.122
35	231	10.241	8.783	-1.458
50	170	8.725	8.481	-0.244
50	172	8.842	8.748	-0.094
50	173	8.742	8.500	-0.242
50	175	8.527	8.563	0.036
50	176	8.753	8.606	-0.147
55	177	10.423	8.915	-1.508
55	178	8.652	8.464	-0.188
55	179	8.614	8.933	0.319
55	180	8.776	8.857	0.081
55	181	8.557	8.690	0.133
100	154	8.594	8.574	-0.020
100	155	8.622	8.865	0.243
100	156	8.791	8.877	0.086
100	157	8.647	8.695	0.048
100	158	8.871	8.653	-0.218
100	159	8.675	8.626	-0.049
100	160	8.496	8.501	0.005
100	161	8.529	8.915	0.386
100	162	8.937	8.737	-0.200
100	163	8.687	8.516	-0.171
100	164	8.690	8.640	-0.050
100	165	8.620	8.790	0.170
100	166	8.697	8.487	-0.210
100	168	8.958	8.894	-0.064
100	169	8.823	8.937	0.114
100	182	8.800	8.480	-0.320
100	183	8.582	8.528	-0.054
100	184	8.550	8.548	-0.002
100	185	8.714	8.660	-0.054
100	186	8.826	8.584	-0.242
100	187	8.748	8.732	-0.016
100	188	8.824	8.500	-0.324
100	189	8.513	8.901	0.388
100	190	8.917	8.940	0.023
105	191	8.840	8.789	-0.051
105	192	8.643	8.887	0.244
105	193	8.726	8.517	-0.209
105	194	8.892	8.685	-0.207
105	195	8.483	8.826	0.343
105	196	8.626	8.535	-0.091
Max		10.450	8.963	0.388
Average		9.243	8.720	-0.522
Min		8.483	8.464	-1.825
Std Dev		0.742	0.145	0.739



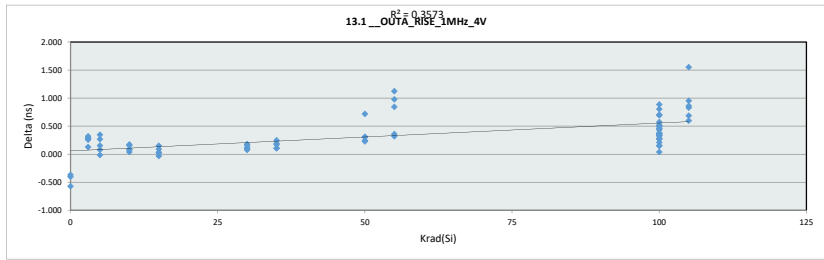
		12.23 __ T_FAULT										
Test Site	Tester											
Test Number	Unit	µs	µs									
Max Limit	Min Limit	14	7									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	
Min	8.674	8.714	8.625	8.672	8.549	8.589	8.566	8.481	8.464	8.480	8.517	
Average	8.789	8.839	8.743	8.752	8.679	8.721	8.795	8.580	8.772	8.691	8.707	
Max	8.856	8.907	8.890	8.963	8.829	8.828	8.923	8.748	8.933	8.940	8.887	
UL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	



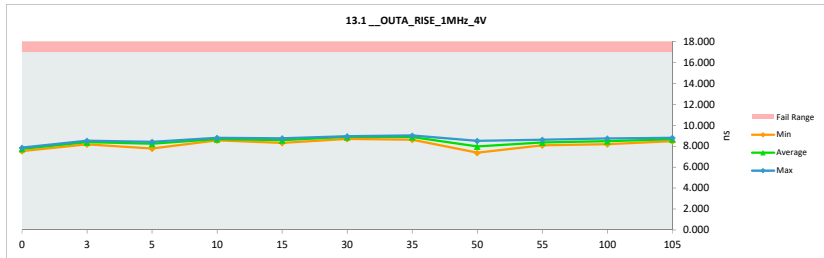


HDR TID Report  
TPS7H5003-SP

13.1_OUTA_RISE_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.223	7.862	-0.361
0	138	8.226	7.829	-0.397
0	139	8.106	7.543	-0.563
3	197	8.106	8.431	0.325
3	198	8.123	8.414	0.291
3	199	8.084	8.371	0.287
3	202	8.269	8.535	0.266
3	203	8.041	8.174	0.133
5	204	8.039	8.392	0.353
5	205	8.262	8.422	0.160
5	206	7.502	7.776	0.274
5	208	8.258	8.343	0.085
5	209	8.258	8.250	-0.008
10	210	8.623	8.802	0.179
10	211	8.513	8.672	0.159
10	212	8.605	8.700	0.095
10	213	8.596	8.682	0.086
10	214	8.509	8.555	0.046
15	215	8.399	8.497	0.098
15	217	8.163	8.319	0.156
15	219	8.659	8.696	0.037
15	220	8.726	8.757	0.031
15	221	8.597	8.572	-0.025
30	222	8.731	8.918	0.187
30	223	8.531	8.697	0.166
30	224	8.831	8.914	0.083
30	225	8.828	8.955	0.127
30	226	8.858	8.964	0.106
35	227	8.692	8.947	0.255
35	228	8.675	8.871	0.196
35	229	8.845	9.023	0.178
35	230	8.830	8.942	0.112
35	231	8.501	8.616	0.115
50	170	7.126	7.375	0.249
50	172	6.728	7.452	0.724
50	173	7.781	8.099	0.318
50	175	8.227	8.460	0.233
50	176	8.206	8.515	0.309
55	177	8.167	8.533	0.366
55	178	7.620	8.471	0.851
55	179	6.992	8.122	1.130
55	180	7.100	8.083	0.983
55	181	8.298	8.624	0.326
100	154	8.340	8.385	0.045
100	155	8.119	8.395	0.276
100	156	8.231	8.386	0.155
100	157	7.493	8.192	0.699
100	158	8.374	8.591	0.217
100	159	8.233	8.610	0.377
100	160	8.312	8.650	0.338
100	161	8.466	8.742	0.276
100	162	8.310	8.598	0.288
100	163	7.975	8.484	0.509
100	164	7.950	8.300	0.350
100	165	7.872	8.310	0.438
100	166	7.968	8.302	0.334
100	168	8.392	8.552	0.160
100	169	8.187	8.569	0.382
100	182	7.817	8.526	0.709
100	183	7.939	8.518	0.579
100	184	8.127	8.595	0.468
100	185	7.408	8.301	0.893
100	186	7.412	8.222	0.810
100	187	8.263	8.631	0.368
100	188	7.973	8.496	0.523
100	189	8.063	8.532	0.469
100	190	8.113	8.630	0.517
105	191	6.920	8.476	1.556
105	192	7.559	8.518	0.959
105	193	7.904	8.739	0.835
105	194	8.064	8.758	0.694
105	195	8.205	8.809	0.604
105	196	7.877	8.743	0.866
Max		8.858	9.023	1.556
Average		8.141	8.476	0.334
Min		6.728	7.375	-0.563
Std Dev		0.471	0.336	0.342



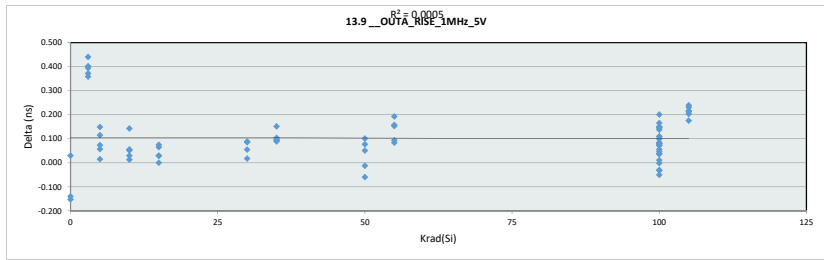
13.1_OUTA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.543	8.174	7.776	8.555	8.319	8.697	8.616	7.375	8.083	8.192	8.476
Average	7.745	8.385	8.237	8.682	8.568	8.890	8.880	7.980	8.367	8.480	8.674
Max	7.862	8.535	8.422	8.802	8.757	8.964	9.023	8.515	8.624	8.742	8.809
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



# HDR TID Report TPS7H5003-SP

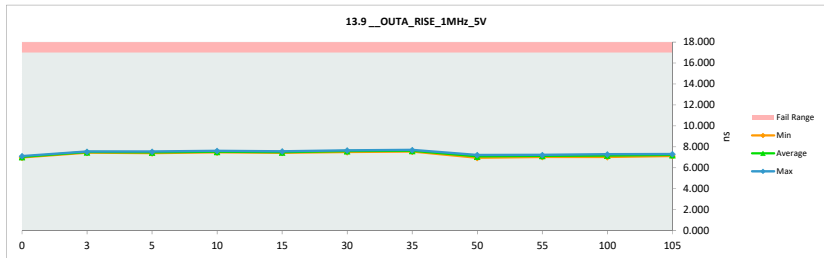
13.9_OUTA_RISE_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.088	7.119	0.031
0	138	7.169	7.031	-0.138
0	139	7.141	6.990	-0.151
3	197	7.147	7.550	0.403
3	198	7.079	7.475	0.396
3	199	7.115	7.557	0.442
3	202	7.155	7.528	0.373
3	203	7.090	7.450	0.360
5	204	7.410	7.560	0.150
5	205	7.399	7.457	0.058
5	206	7.326	7.401	0.075
5	208	7.406	7.522	0.116
5	209	7.418	7.434	0.016
10	210	7.482	7.626	0.144
10	211	7.470	7.484	0.014
10	212	7.488	7.541	0.053
10	213	7.488	7.545	0.057
10	214	7.451	7.482	0.031
15	215	7.423	7.454	0.031
15	217	7.377	7.443	0.066
15	219	7.491	7.567	0.076
15	220	7.547	7.577	0.030
15	221	7.448	7.450	0.002
30	222	7.497	7.553	0.056
30	223	7.486	7.505	0.019
30	224	7.580	7.669	0.089
30	225	7.569	7.656	0.087
30	226	7.567	7.656	0.089
35	227	7.525	7.615	0.090
35	228	7.502	7.597	0.095
35	229	7.553	7.706	0.153
35	230	7.551	7.650	0.099
35	231	7.444	7.549	0.105
50	170	7.008	6.997	-0.011
50	172	6.898	6.950	0.052
50	173	7.097	7.039	-0.058
50	175	7.075	7.177	0.102
50	176	7.151	7.229	0.078
55	177	7.048	7.207	0.159
55	178	7.027	7.221	0.194
55	179	6.972	7.057	0.085
55	180	6.947	7.042	0.095
55	181	7.079	7.233	0.154
100	154	7.179	7.179	0.000
100	155	7.123	7.074	-0.049
100	156	7.161	7.132	-0.029
100	157	6.963	7.037	0.074
100	158	7.226	7.238	0.012
100	159	7.122	7.202	0.080
100	160	7.127	7.238	0.111
100	161	7.207	7.280	0.073
100	162	7.187	7.267	0.080
100	163	7.044	7.195	0.151
100	164	7.101	7.070	-0.031
100	165	7.078	7.135	0.057
100	166	7.115	7.163	0.048
100	168	7.145	7.228	0.083
100	169	7.132	7.172	0.040
100	182	7.052	7.191	0.139
100	183	7.083	7.194	0.111
100	184	7.084	7.234	0.150
100	185	7.034	7.071	0.037
100	186	6.992	7.092	0.100
100	187	7.087	7.254	0.167
100	188	7.114	7.199	0.085
100	189	7.063	7.209	0.146
100	190	7.092	7.294	0.202
105	191	6.923	7.137	0.214
105	192	6.995	7.172	0.177
105	193	7.012	7.217	0.205
105	194	7.077	7.318	0.241
105	195	7.088	7.306	0.218
105	196	7.040	7.272	0.232
Max		7.580	7.706	0.442
Average		7.217	7.319	0.103
Min		6.898	6.950	-0.151
Std Dev		0.201	0.207	0.110



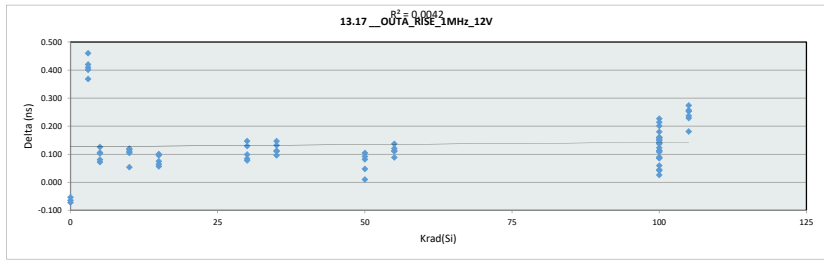
13.9_OUTA_RISE	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

LL	0	3	5	10	15	30	35	50	55	100	105
Min	6.990	7.450	7.401	7.482	7.443	7.505	7.549	6.950	7.042	7.037	7.137
Average	7.047	7.512	7.475	7.536	7.498	7.608	7.623	7.078	7.152	7.181	7.237
Max	7.119	7.557	7.560	7.626	7.577	7.669	7.706	7.229	7.233	7.294	7.318
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

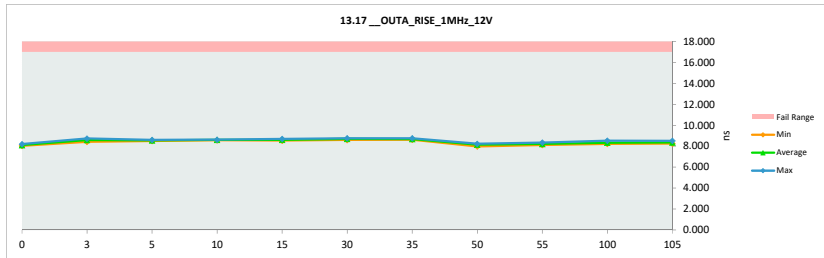


HDR TID Report  
TPS7H5003-SP

13.17 __OUTA_RISE_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.264	8.201	-0.063
0	138	8.157	8.086	-0.071
0	139	8.090	8.038	-0.052
3	197	8.256	8.658	0.402
3	198	8.212	8.581	0.369
3	199	8.165	8.575	0.410
3	202	8.314	8.735	0.421
3	203	7.960	8.421	0.461
5	204	8.486	8.559	0.073
5	205	8.393	8.497	0.104
5	206	8.448	8.575	0.127
5	208	8.501	8.608	0.107
5	209	8.464	8.546	0.082
10	210	8.489	8.599	0.110
10	211	8.523	8.629	0.106
10	212	8.449	8.566	0.117
10	213	8.501	8.622	0.121
10	214	8.562	8.617	0.055
15	215	8.601	8.698	0.097
15	217	8.488	8.564	0.076
15	219	8.478	8.536	0.058
15	220	8.550	8.652	0.102
15	221	8.486	8.552	0.066
30	222	8.583	8.731	0.148
30	223	8.652	8.731	0.079
30	224	8.599	8.699	0.100
30	225	8.625	8.755	0.130
30	226	8.512	8.597	0.085
35	227	8.557	8.690	0.133
35	228	8.647	8.759	0.112
35	229	8.499	8.647	0.148
35	230	8.587	8.700	0.113
35	231	8.527	8.624	0.097
50	170	8.080	8.129	0.049
50	172	7.968	7.979	0.011
50	173	8.122	8.215	0.093
50	175	8.125	8.231	0.106
50	176	8.095	8.178	0.083
55	177	8.230	8.342	0.112
55	178	8.022	8.160	0.138
55	179	8.021	8.111	0.090
55	180	8.007	8.120	0.113
55	181	8.218	8.340	0.122
100	154	8.229	8.256	0.027
100	155	8.192	8.283	0.091
100	156	8.248	8.294	0.046
100	157	8.176	8.318	0.142
100	158	8.270	8.384	0.114
100	159	8.168	8.292	0.124
100	160	8.220	8.381	0.161
100	161	8.346	8.527	0.181
100	162	8.200	8.288	0.088
100	163	8.145	8.347	0.202
100	164	8.229	8.338	0.109
100	165	8.294	8.337	0.043
100	166	8.124	8.273	0.149
100	168	8.158	8.219	0.061
100	169	8.300	8.413	0.113
100	182	8.163	8.378	0.215
100	183	8.275	8.362	0.087
100	184	8.080	8.308	0.228
100	185	8.085	8.238	0.153
100	186	8.189	8.329	0.140
100	187	8.201	8.362	0.161
100	188	8.134	8.294	0.160
100	189	8.267	8.405	0.138
100	190	8.260	8.420	0.160
105	191	8.116	8.346	0.230
105	192	8.064	8.246	0.182
105	193	8.041	8.279	0.238
105	194	8.126	8.401	0.275
105	195	8.261	8.515	0.254
105	196	8.152	8.410	0.258
Max		8.652	8.759	0.461
Average		8.291	8.426	0.135
Min		7.960	7.979	-0.071
Std Dev		0.192	0.196	0.100



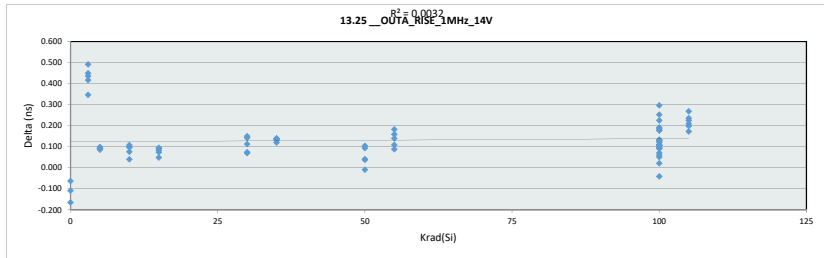
13.17 __OUTA_RISE_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL (Krad(Si))	0	3	5	10	15	30	35	50	55	100	105
Min	8.038	8.421	8.497	8.566	8.536	8.597	8.624	7.979	8.111	8.219	8.246
Average	8.108	8.594	8.557	8.607	8.600	8.703	8.684	8.146	8.215	8.335	8.366
Max	8.201	8.735	8.608	8.629	8.698	8.755	8.759	8.231	8.342	8.527	8.515
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



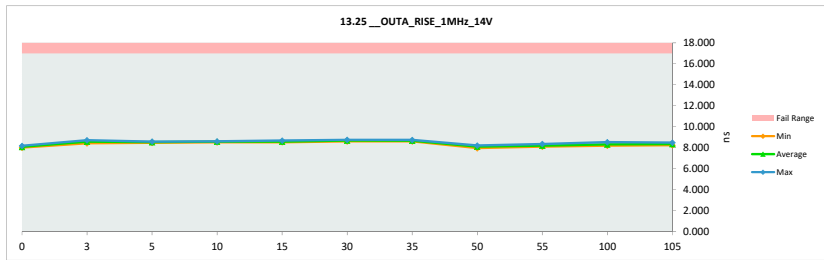
# HDR TID Report TPS7H5003-SP

13.25_OUTA_RISE_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(S)	Serial #	PRE	POST	Delta
0	137	8.287	8.178	-0.109
0	138	8.097	8.034	-0.063
0	139	8.172	8.007	-0.165
3	197	8.251	8.688	0.437
3	198	8.177	8.524	0.347
3	199	8.038	8.530	0.492
3	202	8.288	8.705	0.417
3	203	7.973	8.423	0.450
5	204	8.453	8.540	0.087
5	205	8.366	8.459	0.093
5	206	8.404	8.496	0.092
5	208	8.478	8.577	0.099
5	209	8.443	8.531	0.088
10	210	8.464	8.572	0.108
10	211	8.499	8.598	0.099
10	212	8.428	8.504	0.076
10	213	8.495	8.592	0.097
10	214	8.544	8.584	0.040
15	215	8.579	8.674	0.095
15	217	8.446	8.537	0.091
15	219	8.447	8.521	0.074
15	220	8.523	8.607	0.084
15	221	8.467	8.516	0.049
30	222	8.564	8.706	0.142
30	223	8.633	8.746	0.113
30	224	8.579	8.653	0.074
30	225	8.615	8.685	0.070
30	226	8.451	8.601	0.150
35	227	8.532	8.669	0.137
35	228	8.615	8.735	0.120
35	229	8.469	8.601	0.132
35	230	8.563	8.701	0.138
35	231	8.482	8.622	0.140
50	170	8.078	8.069	-0.009
50	172	7.911	7.949	0.038
50	173	8.166	8.207	0.041
50	175	8.037	8.131	0.094
50	176	8.017	8.121	0.104
55	177	8.157	8.297	0.140
55	178	7.954	8.137	0.183
55	179	8.006	8.094	0.088
55	180	7.986	8.095	0.109
55	181	8.190	8.349	0.159
100	154	8.287	8.246	-0.041
100	155	8.171	8.231	0.060
100	156	8.219	8.270	0.051
100	157	8.164	8.284	0.120
100	158	8.235	8.365	0.130
100	159	8.140	8.249	0.109
100	160	8.224	8.358	0.134
100	161	8.301	8.526	0.225
100	162	8.185	8.293	0.108
100	163	8.156	8.247	0.091
100	164	8.171	8.267	0.096
100	165	8.252	8.273	0.021
100	166	8.114	8.244	0.130
100	168	8.144	8.215	0.071
100	169	8.283	8.391	0.108
100	182	8.168	8.346	0.178
100	183	8.180	8.477	0.297
100	184	8.188	8.281	0.093
100	185	8.084	8.188	0.104
100	186	8.143	8.320	0.177
100	187	8.206	8.334	0.128
100	188	8.083	8.275	0.192
100	189	8.237	8.423	0.186
100	190	8.185	8.438	0.253
105	191	8.075	8.300	0.225
105	192	8.030	8.229	0.199
105	193	8.084	8.257	0.173
105	194	8.171	8.380	0.209
105	195	8.241	8.477	0.236
105	196	8.149	8.418	0.269
Max		8.633	8.746	0.492
Average		8.268	8.400	0.131
Min		7.911	7.949	-0.165
Std Dev		0.190	0.200	0.112



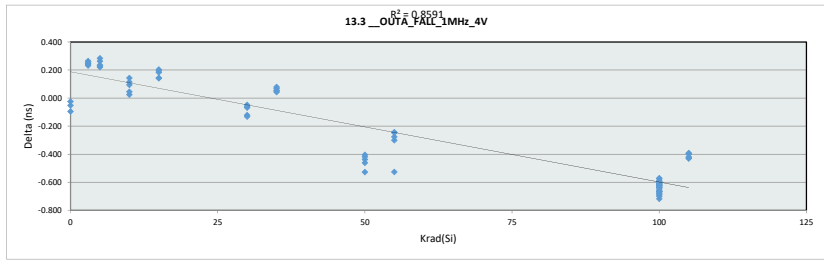
13.25_OUTA_RISE		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.007	8.423	8.459	8.504	8.516	8.601	8.601	7.949	8.094	8.188	8.229
Average		8.073	8.574	8.521	8.570	8.571	8.678	8.666	8.095	8.194	8.314	8.344
Max		8.178	8.705	8.577	8.598	8.674	8.746	8.735	8.207	8.349	8.526	8.477
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



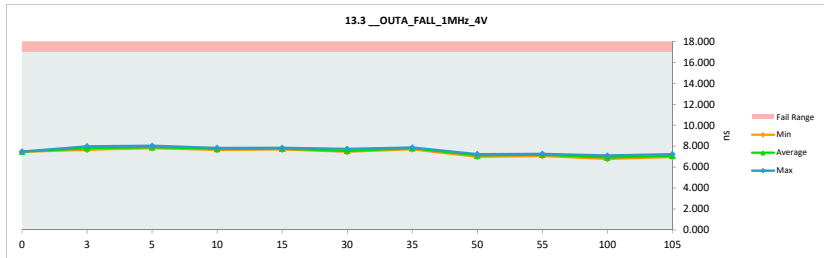
# HDR TID Report TPS7H5003-SP

13.3_OUTA_FALL_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.475	7.451	-0.024
0	138	7.523	7.472	-0.051
0	139	7.587	7.493	-0.094
3	197	7.753	7.997	0.244
3	198	7.471	7.729	0.258
3	199	7.500	7.765	0.265
3	202	7.673	7.919	0.246
3	203	7.426	7.659	0.233
5	204	7.595	7.817	0.222
5	205	7.578	7.862	0.284
5	206	7.691	7.955	0.264
5	208	7.805	8.042	0.237
5	209	7.590	7.821	0.231
10	210	7.545	7.689	0.144
10	211	7.698	7.724	0.026
10	212	7.720	7.836	0.116
10	213	7.737	7.832	0.095
10	214	7.590	7.636	0.046
15	215	7.541	7.725	0.184
15	217	7.500	7.695	0.195
15	219	7.544	7.687	0.143
15	220	7.693	7.837	0.144
15	221	7.642	7.847	0.205
30	222	7.571	7.504	-0.067
30	223	7.812	7.751	-0.061
30	224	7.587	7.467	-0.120
30	225	7.578	7.531	-0.047
30	226	7.639	7.508	-0.131
35	227	7.643	7.700	0.057
35	228	7.796	7.867	0.071
35	229	7.693	7.773	0.080
35	230	7.833	7.877	0.044
35	231	7.779	7.831	0.052
50	170	7.511	6.984	-0.527
50	172	7.502	7.042	-0.460
50	173	7.679	7.243	-0.436
50	175	7.641	7.224	-0.417
50	176	7.483	7.080	-0.403
55	177	7.516	7.241	-0.275
55	178	7.588	7.063	-0.525
55	179	7.518	7.272	-0.246
55	180	7.445	7.203	-0.242
55	181	7.455	7.153	-0.300
100	154	7.555	6.866	-0.689
100	155	7.512	6.813	-0.699
100	156	7.516	6.799	-0.717
100	157	7.533	6.923	-0.610
100	158	7.611	6.948	-0.663
100	159	7.484	6.860	-0.624
100	160	7.634	7.009	-0.625
100	161	7.589	6.914	-0.675
100	162	7.581	6.962	-0.619
100	163	7.515	6.876	-0.639
100	164	7.591	6.909	-0.682
100	165	7.457	6.803	-0.654
100	166	7.510	6.900	-0.610
100	168	7.429	6.826	-0.603
100	169	7.760	7.095	-0.665
100	182	7.771	7.107	-0.664
100	183	7.555	6.981	-0.574
100	184	7.432	6.862	-0.570
100	185	7.417	6.779	-0.638
100	186	7.647	7.054	-0.593
100	187	7.668	7.033	-0.635
100	188	7.508	6.883	-0.625
100	189	7.728	7.112	-0.616
100	190	7.548	6.959	-0.589
105	191	7.370	6.973	-0.397
105	192	7.520	7.129	-0.391
105	193	7.639	7.243	-0.396
105	194	7.469	7.043	-0.426
105	195	7.427	7.009	-0.418
105	196	7.481	7.051	-0.430
Max		7.833	8.042	0.284
Average		7.584	7.336	-0.248
Min		7.370	6.779	-0.717
Std Dev		0.110	0.395	0.349

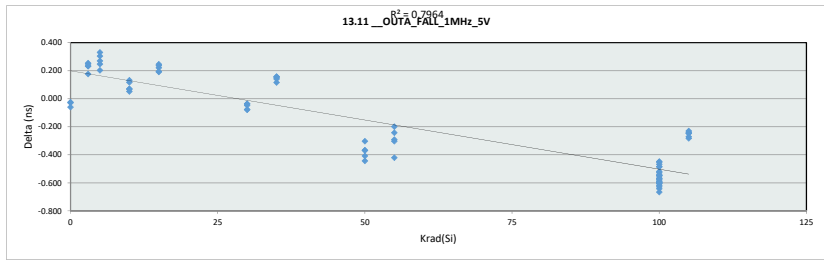


13.3_OUTA_FALL		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		7.451	7.659	7.817	7.636	7.687	7.467	7.700	6.984	7.063	6.779	6.973
Average		7.472	7.814	7.899	7.743	7.758	7.552	7.810	7.115	7.187	6.928	7.075
Max		7.493	7.997	8.042	7.836	7.847	7.751	7.877	7.243	7.272	7.112	7.243
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

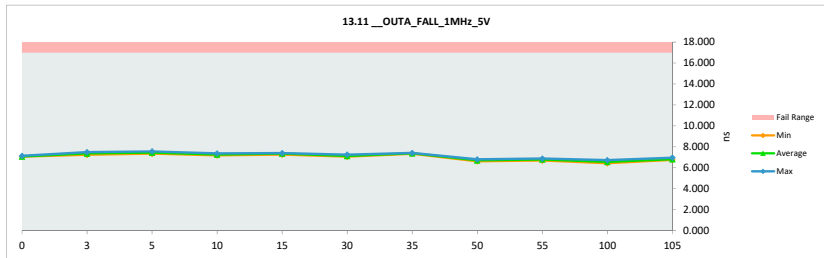


# HDR TID Report TPS7H5003-SP

13.11_OUTA_FALL_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.160	7.136	-0.024
0	138	7.091	7.063	-0.028
0	139	7.119	7.060	-0.059
3	197	7.249	7.498	0.249
3	198	7.114	7.291	0.177
3	199	7.142	7.396	0.254
3	202	7.196	7.430	0.234
3	203	7.011	7.244	0.233
5	204	7.169	7.500	0.331
5	205	7.134	7.382	0.248
5	206	7.172	7.443	0.271
5	208	7.259	7.564	0.305
5	209	7.143	7.346	0.203
10	210	7.119	7.237	0.118
10	211	7.185	7.254	0.069
10	212	7.212	7.284	0.072
10	213	7.230	7.363	0.133
10	214	7.135	7.187	0.052
15	215	7.099	7.337	0.238
15	217	7.064	7.259	0.195
15	219	7.130	7.377	0.247
15	220	7.213	7.405	0.192
15	221	7.166	7.387	0.221
30	222	7.115	7.079	-0.036
30	223	7.290	7.255	-0.035
30	224	7.181	7.103	-0.078
30	225	7.136	7.060	-0.076
30	226	7.164	7.117	-0.047
35	227	7.184	7.340	0.156
35	228	7.274	7.415	0.141
35	229	7.194	7.353	0.159
35	230	7.278	7.394	0.116
35	231	7.262	7.412	0.150
50	170	7.026	6.619	-0.407
50	172	7.025	6.657	-0.368
50	173	7.105	6.803	-0.302
50	175	7.210	6.767	-0.443
50	176	7.097	6.730	-0.367
55	177	7.112	6.821	-0.291
55	178	7.116	6.696	-0.420
55	179	7.079	6.880	-0.199
55	180	7.029	6.787	-0.242
55	181	7.100	6.798	-0.302
100	154	7.148	6.483	-0.665
100	155	7.059	6.532	-0.527
100	156	7.137	6.509	-0.628
100	157	7.109	6.533	-0.576
100	158	7.174	6.557	-0.617
100	159	7.045	6.500	-0.545
100	160	7.162	6.613	-0.549
100	161	7.113	6.570	-0.543
100	162	7.151	6.583	-0.568
100	163	7.141	6.499	-0.642
100	164	7.120	6.517	-0.603
100	165	7.065	6.478	-0.587
100	166	7.124	6.526	-0.598
100	168	7.037	6.432	-0.605
100	169	7.218	6.625	-0.593
100	182	7.197	6.730	-0.467
100	183	7.096	6.649	-0.447
100	184	7.054	6.570	-0.484
100	185	6.920	6.435	-0.485
100	186	7.148	6.630	-0.518
100	187	7.200	6.634	-0.566
100	188	7.106	6.513	-0.593
100	189	7.217	6.668	-0.549
100	190	7.106	6.654	-0.452
105	191	6.998	6.769	-0.229
105	192	7.076	6.829	-0.247
105	193	7.210	6.965	-0.245
105	194	7.029	6.759	-0.270
105	195	7.020	6.781	-0.239
105	196	7.073	6.791	-0.282
Max		7.290	7.564	0.331
Average		7.133	6.943	-0.190
Min		6.920	6.432	-0.665
Std Dev		0.073	0.355	0.324



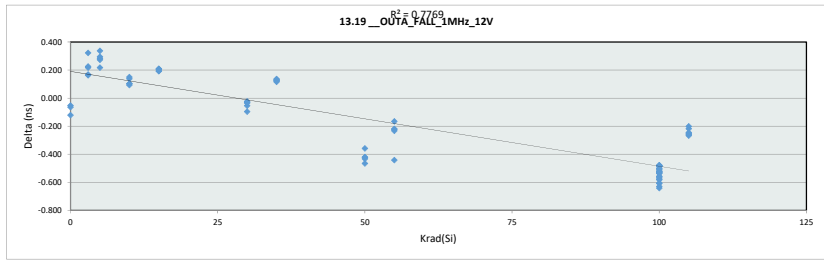
13.11_OUTA_FALL_1MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.060	7.244	7.346	7.187	7.259	7.060	7.340	6.619	6.696	6.432	6.759
Average	7.086	7.372	7.447	7.265	7.353	7.123	7.383	6.715	6.796	6.560	6.816
Max	7.136	7.498	7.564	7.363	7.405	7.255	7.415	6.803	6.880	6.730	6.965
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



# HDR TID Report TPS7H5003-SP

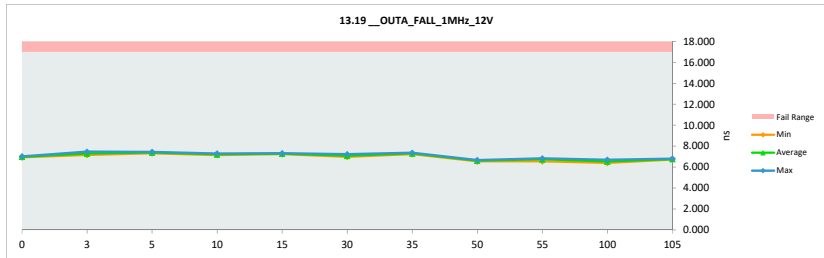
13.19 __OUTA_FALL_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.095	7.032	-0.063
0	138	7.052	6.932	-0.120
0	139	7.048	6.996	-0.052
3	197	7.156	7.479	0.323
3	198	7.082	7.245	0.163
3	199	6.995	7.221	0.226
3	202	7.215	7.384	0.169
3	203	6.950	7.168	0.218
5	204	7.103	7.387	0.284
5	205	7.031	7.327	0.296
5	206	7.122	7.460	0.338
5	208	7.222	7.440	0.218
5	209	7.090	7.364	0.274
10	210	7.047	7.150	0.103
10	211	7.105	7.256	0.151
10	212	7.107	7.211	0.104
10	213	7.163	7.301	0.138
10	214	7.098	7.192	0.094
15	215	7.114	7.322	0.208
15	217	7.027	7.233	0.206
15	219	7.035	7.245	0.210
15	220	7.135	7.329	0.194
15	221	7.104	7.297	0.193
30	222	7.082	7.060	-0.022
30	223	7.268	7.238	-0.030
30	224	7.105	7.052	-0.053
30	225	7.100	7.066	-0.034
30	226	7.077	6.982	-0.095
35	227	7.121	7.254	0.133
35	228	7.254	7.371	0.117
35	229	7.114	7.238	0.124
35	230	7.223	7.360	0.137
35	231	7.201	7.327	0.126
50	170	7.004	6.585	-0.419
50	172	6.968	6.547	-0.421
50	173	7.147	6.682	-0.465
50	175	7.107	6.675	-0.432
50	176	6.928	6.570	-0.358
55	177	7.002	6.837	-0.165
55	178	6.997	6.556	-0.441
55	179	7.023	6.803	-0.220
55	180	6.953	6.721	-0.232
55	181	7.036	6.816	-0.220
100	154	7.066	6.437	-0.629
100	155	7.021	6.453	-0.568
100	156	7.069	6.462	-0.607
100	157	7.062	6.525	-0.537
100	158	7.127	6.567	-0.560
100	159	6.973	6.488	-0.485
100	160	7.136	6.580	-0.556
100	161	7.122	6.589	-0.533
100	162	7.051	6.549	-0.502
100	163	7.091	6.451	-0.640
100	164	7.083	6.506	-0.577
100	165	7.066	6.462	-0.604
100	166	7.031	6.504	-0.527
100	168	6.985	6.404	-0.581
100	169	7.159	6.670	-0.489
100	182	7.213	6.709	-0.504
100	183	7.141	6.612	-0.529
100	184	6.982	6.470	-0.512
100	185	6.897	6.417	-0.480
100	186	7.153	6.631	-0.522
100	187	7.086	6.608	-0.478
100	188	6.993	6.510	-0.483
100	189	7.226	6.703	-0.523
100	190	7.076	6.599	-0.477
105	191	6.923	6.723	-0.200
105	192	7.022	6.763	-0.259
105	193	7.064	6.812	-0.252
105	194	6.992	6.775	-0.217
105	195	7.051	6.806	-0.245
105	196	7.027	6.761	-0.266
Max		7.268	7.479	0.338
Average		7.078	6.894	-0.184
Min		6.897	6.404	-0.640
Std Dev		0.080	0.344	0.316



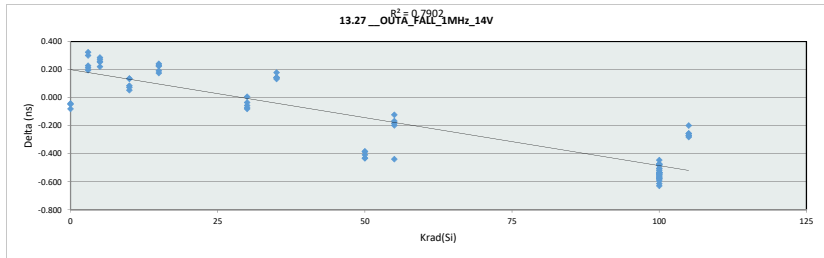
13.19 __OUTA_FALL_1MHz_12V	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.932	7.168	7.327	7.150	7.233	6.982	7.238	6.547	6.556	6.404	6.723
Average	6.987	7.299	7.396	7.222	7.285	7.080	7.310	6.612	6.747	6.538	6.773
Max	7.032	7.479	7.460	7.301	7.329	7.238	7.371	6.682	6.837	6.709	6.812
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

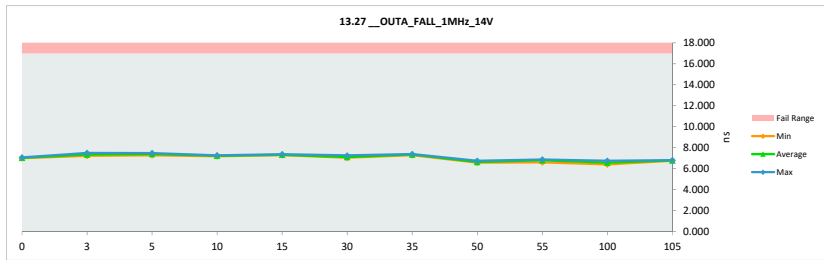


# HDR TID Report TPS7H5003-SP

13.27 __OUTA_FALL_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.119	7.072	-0.047
0	138	7.080	7.000	-0.080
0	139	7.046	7.005	-0.041
3	197	7.287	7.500	0.213
3	198	7.004	7.328	0.324
3	199	7.045	7.241	0.196
3	202	7.201	7.429	0.228
3	203	6.928	7.229	0.301
5	204	7.123	7.382	0.259
5	205	7.054	7.275	0.221
5	206	7.147	7.421	0.274
5	208	7.234	7.489	0.255
5	209	7.101	7.387	0.286
10	210	7.065	7.202	0.137
10	211	7.138	7.273	0.135
10	212	7.126	7.180	0.054
10	213	7.178	7.264	0.086
10	214	7.120	7.194	0.074
15	215	7.142	7.334	0.192
15	217	7.042	7.284	0.242
15	219	7.050	7.282	0.232
15	220	7.157	7.382	0.225
15	221	7.115	7.291	0.176
30	222	7.090	7.034	-0.056
30	223	7.297	7.263	-0.034
30	224	7.128	7.048	-0.080
30	225	7.118	7.046	-0.072
30	226	7.093	7.100	0.007
35	227	7.140	7.285	0.145
35	228	7.260	7.394	0.134
35	229	7.108	7.287	0.179
35	230	7.239	7.383	0.144
35	231	7.224	7.366	0.142
50	170	7.011	6.579	-0.432
50	172	6.973	6.565	-0.408
50	173	7.177	6.746	-0.431
50	175	7.070	6.686	-0.384
50	176	6.956	6.571	-0.385
55	177	7.049	6.865	-0.184
55	178	7.037	6.599	-0.438
55	179	7.035	6.868	-0.167
55	180	6.973	6.851	-0.122
55	181	7.017	6.819	-0.198
100	154	7.079	6.450	-0.629
100	155	7.034	6.458	-0.576
100	156	7.106	6.493	-0.613
100	157	7.061	6.587	-0.474
100	158	7.117	6.582	-0.535
100	159	6.996	6.459	-0.537
100	160	7.181	6.623	-0.558
100	161	7.115	6.595	-0.520
100	162	7.110	6.574	-0.536
100	163	7.076	6.528	-0.548
100	164	7.148	6.589	-0.559
100	165	7.094	6.513	-0.581
100	166	7.113	6.519	-0.594
100	168	6.985	6.412	-0.573
100	169	7.202	6.719	-0.483
100	182	7.210	6.733	-0.477
100	183	7.209	6.638	-0.571
100	184	7.033	6.588	-0.445
100	185	6.947	6.401	-0.546
100	186	7.153	6.645	-0.508
100	187	7.109	6.576	-0.533
100	188	7.036	6.499	-0.537
100	189	7.221	6.748	-0.473
100	190	7.095	6.592	-0.503
105	191	6.947	6.748	-0.199
105	192	7.038	6.782	-0.256
105	193	7.100	6.819	-0.281
105	194	7.021	6.749	-0.272
105	195	7.051	6.795	-0.256
105	196	7.065	6.796	-0.269
Max		7.297	7.500	0.324
Average		7.098	6.918	-0.180
Min		6.928	6.401	-0.629
Std Dev		0.082	0.342	0.317



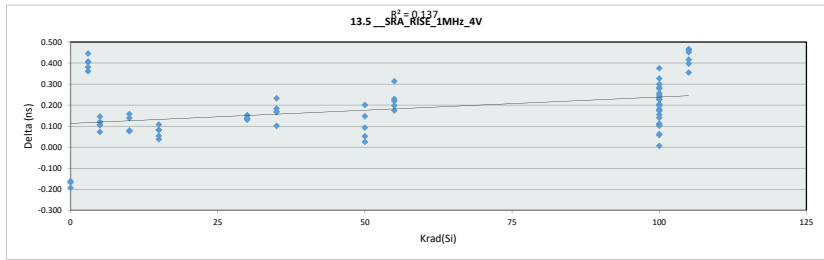
13.27 __OUTA_FALL_1MHz_14V											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.000	7.229	7.275	7.180	7.282	7.034	7.285	6.565	6.599	6.401	6.748
Average	7.026	7.345	7.391	7.223	7.315	7.098	7.343	6.629	6.800	6.563	6.782
Max	7.072	7.500	7.489	7.273	7.382	7.263	7.394	6.746	6.868	6.748	6.819
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



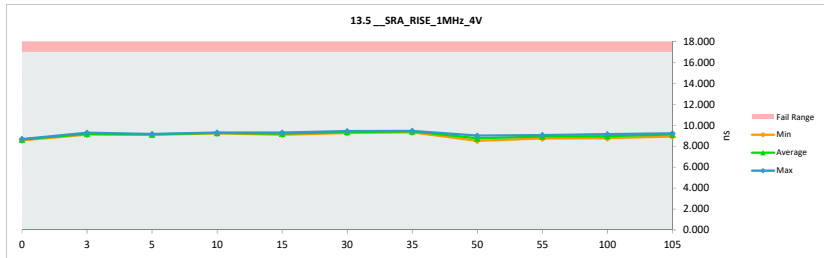


# HDR TID Report TPS7H5003-SP

13.5_SRA_RISE_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.834	8.667	-0.167
0	138	8.842	8.682	-0.160
0	139	8.743	8.550	-0.193
3	197	8.663	9.109	0.446
3	198	8.740	9.144	0.404
3	199	8.848	9.209	0.361
3	202	8.875	9.283	0.408
3	203	8.761	9.142	0.381
5	204	9.021	9.142	0.121
5	205	9.054	9.162	0.108
5	206	8.924	9.070	0.146
5	208	9.048	9.153	0.105
5	209	9.058	9.131	0.073
10	210	9.135	9.293	0.158
10	211	9.123	9.262	0.139
10	212	9.191	9.269	0.078
10	213	9.138	9.218	0.080
10	214	9.184	9.252	0.078
15	215	9.063	9.145	0.082
15	217	9.011	9.093	0.082
15	219	9.185	9.293	0.108
15	220	9.159	9.197	0.038
15	221	9.151	9.205	0.054
30	222	9.204	9.343	0.139
30	223	9.098	9.251	0.153
30	224	9.241	9.372	0.131
30	225	9.298	9.436	0.138
30	226	9.206	9.341	0.135
35	227	9.175	9.408	0.233
35	228	9.239	9.408	0.169
35	229	9.271	9.455	0.184
35	230	9.292	9.460	0.168
35	231	9.214	9.316	0.102
50	170	8.555	8.581	0.026
50	172	8.417	8.511	0.094
50	173	8.677	8.730	0.053
50	175	8.740	8.888	0.148
50	176	8.803	9.005	0.202
55	177	8.782	9.012	0.230
55	178	8.644	8.958	0.314
55	179	8.633	8.808	0.175
55	180	8.517	8.737	0.220
55	181	8.852	9.061	0.199
100	154	8.848	8.855	0.007
100	155	8.802	8.915	0.113
100	156	8.879	8.938	0.059
100	157	8.579	8.760	0.181
100	158	8.830	8.970	0.140
100	159	8.760	8.989	0.229
100	160	8.933	9.110	0.177
100	161	8.969	9.142	0.173
100	162	8.885	9.087	0.202
100	163	8.761	8.916	0.155
100	164	8.693	8.800	0.107
100	165	8.794	8.857	0.063
100	166	8.735	8.838	0.103
100	168	8.874	9.072	0.198
100	169	8.836	9.041	0.205
100	182	8.624	8.903	0.279
100	183	8.687	9.063	0.376
100	184	8.679	8.963	0.284
100	185	8.624	8.873	0.249
100	186	8.579	8.836	0.257
100	187	8.789	9.028	0.239
100	188	8.688	8.988	0.300
100	189	8.690	8.917	0.227
100	190	8.688	9.015	0.327
105	191	8.567	8.922	0.355
105	192	8.596	8.994	0.398
105	193	8.759	9.220	0.461
105	194	8.710	9.127	0.417
105	195	8.766	9.218	0.452
105	196	8.714	9.181	0.467
Max		9.298	9.460	0.467
Average		8.876	9.059	0.183
Min		8.417	8.511	-0.193
Std Dev		0.225	0.221	0.139

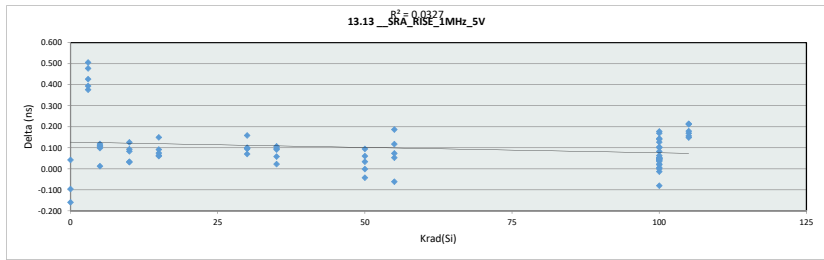


13.5_SRA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.550	9.109	9.070	9.218	9.093	9.251	9.316	8.511	8.737	8.760	8.922
Average	8.633	9.177	9.132	9.261	9.187	9.349	9.409	8.743	8.915	8.953	9.110
Max	8.682	9.283	9.162	9.293	9.293	9.436	9.460	9.005	9.061	9.142	9.220
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

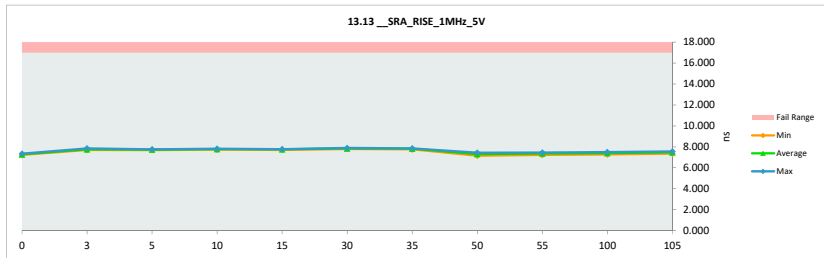


HDR TID Report  
TPS7H5003-SP

13.13_SRA_RISE_1MHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.307	7.350	0.043
0	138	7.372	7.276	-0.096
0	139	7.381	7.222	-0.159
3	197	7.280	7.707	0.427
3	198	7.331	7.725	0.394
3	199	7.355	7.861	0.506
3	202	7.365	7.742	0.377
3	203	7.284	7.762	0.478
5	204	7.697	7.710	0.013
5	205	7.643	7.742	0.099
5	206	7.586	7.693	0.107
5	208	7.644	7.763	0.119
5	209	7.658	7.770	0.112
10	210	7.701	7.827	0.126
10	211	7.697	7.729	0.032
10	212	7.715	7.749	0.034
10	213	7.694	7.778	0.084
10	214	7.704	7.798	0.094
15	215	7.626	7.776	0.150
15	217	7.616	7.708	0.092
15	219	7.719	7.783	0.064
15	220	7.720	7.781	0.061
15	221	7.679	7.754	0.075
30	222	7.725	7.884	0.159
30	223	7.703	7.800	0.097
30	224	7.767	7.838	0.071
30	225	7.796	7.892	0.096
30	226	7.721	7.822	0.101
35	227	7.747	7.839	0.092
35	228	7.744	7.851	0.107
35	229	7.750	7.848	0.098
35	230	7.777	7.836	0.059
35	231	7.733	7.756	0.023
50	170	7.204	7.299	0.095
50	172	7.119	7.153	0.034
50	173	7.333	7.291	-0.042
50	175	7.339	7.338	-0.001
50	176	7.388	7.449	0.061
55	177	7.315	7.433	0.118
55	178	7.269	7.456	0.187
55	179	7.231	7.284	0.053
55	180	7.159	7.233	0.074
55	181	7.502	7.441	-0.061
100	154	7.454	7.374	-0.080
100	155	7.371	7.358	-0.013
100	156	7.380	7.386	0.006
100	157	7.230	7.282	0.052
100	158	7.369	7.412	0.043
100	159	7.388	7.408	0.020
100	160	7.420	7.503	0.083
100	161	7.447	7.498	0.051
100	162	7.428	7.482	0.054
100	163	7.338	7.388	0.050
100	164	7.295	7.336	0.041
100	165	7.327	7.327	0.000
100	166	7.338	7.361	0.023
100	168	7.375	7.424	0.049
100	169	7.357	7.402	0.045
100	182	7.287	7.414	0.127
100	183	7.263	7.408	0.145
100	184	7.281	7.459	0.178
100	185	7.231	7.332	0.101
100	186	7.216	7.320	0.104
100	187	7.392	7.428	0.036
100	188	7.293	7.434	0.141
100	189	7.309	7.372	0.063
100	190	7.295	7.465	0.170
105	191	7.180	7.350	0.170
105	192	7.223	7.435	0.212
105	193	7.412	7.561	0.149
105	194	7.317	7.474	0.157
105	195	7.306	7.521	0.215
105	196	7.304	7.484	0.180
Max		7.796	7.892	0.506
Average		7.451	7.550	0.099
Min		7.119	7.153	-0.159
Std Dev		0.196	0.211	0.115



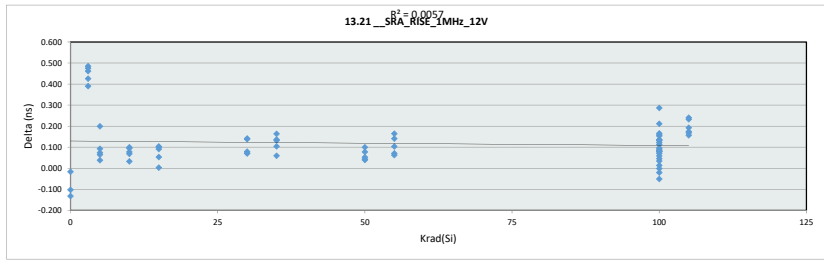
13.13_SRA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.222	7.707	7.693	7.729	7.708	7.800	7.756	7.153	7.233	7.282	7.350
Average	7.283	7.759	7.736	7.776	7.760	7.847	7.826	7.306	7.369	7.399	7.471
Max	7.350	7.861	7.770	7.827	7.783	7.892	7.851	7.449	7.456	7.503	7.561
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



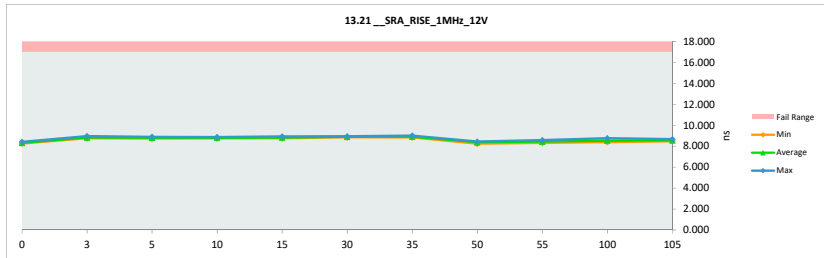
HDR TID Report  
TPS7H5003-SP

13.21_SRA_RISE_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.514	8.412	-0.102
0	138	8.311	8.295	-0.016
0	139	8.384	8.252	-0.132
3	197	8.467	8.858	0.391
3	198	8.356	8.782	0.426
3	199	8.335	8.797	0.462
3	202	8.487	8.963	0.476
3	203	8.277	8.763	0.486
5	204	8.733	8.772	0.039
5	205	8.660	8.734	0.074
5	206	8.690	8.890	0.200
5	208	8.761	8.854	0.093
5	209	8.732	8.797	0.065
10	210	8.719	8.752	0.033
10	211	8.732	8.832	0.100
10	212	8.716	8.812	0.096
10	213	8.733	8.811	0.078
10	214	8.804	8.873	0.069
15	215	8.831	8.933	0.102
15	217	8.730	8.834	0.104
15	219	8.719	8.811	0.092
15	220	8.748	8.802	0.054
15	221	8.760	8.764	0.004
30	222	8.804	8.943	0.139
30	223	8.855	8.926	0.071
30	224	8.802	8.882	0.080
30	225	8.843	8.915	0.072
30	226	8.700	8.842	0.142
35	227	8.781	8.918	0.137
35	228	8.851	9.015	0.164
35	229	8.730	8.863	0.133
35	230	8.800	8.905	0.105
35	231	8.777	8.837	0.060
50	170	8.372	8.416	0.044
50	172	8.188	8.241	0.053
50	173	8.406	8.446	0.040
50	175	8.308	8.408	0.100
50	176	8.302	8.380	0.078
55	177	8.425	8.529	0.104
55	178	8.226	8.391	0.165
55	179	8.310	8.373	0.063
55	180	8.242	8.313	0.071
55	181	8.436	8.577	0.141
100	154	8.494	8.474	-0.020
100	155	8.435	8.470	0.035
100	156	8.496	8.542	0.046
100	157	8.392	8.489	0.097
100	158	8.451	8.565	0.114
100	159	8.394	8.465	0.071
100	160	8.451	8.610	0.159
100	161	8.569	8.649	0.080
100	162	8.441	8.500	0.059
100	163	8.513	8.463	-0.050
100	164	8.419	8.554	0.135
100	165	8.537	8.551	0.014
100	166	8.396	8.477	0.081
100	168	8.401	8.400	-0.001
100	169	8.560	8.646	0.086
100	182	8.408	8.531	0.123
100	183	8.473	8.760	0.287
100	184	8.339	8.551	0.212
100	185	8.342	8.422	0.080
100	186	8.429	8.561	0.132
100	187	8.430	8.522	0.092
100	188	8.372	8.509	0.137
100	189	8.488	8.642	0.154
100	190	8.424	8.590	0.166
105	191	8.380	8.555	0.175
105	192	8.309	8.478	0.169
105	193	8.385	8.542	0.157
105	194	8.355	8.548	0.193
105	195	8.426	8.667	0.241
105	196	8.386	8.619	0.233
Max		8.855	9.015	0.486
Average		8.524	8.642	0.118
Min		8.188	8.241	-0.132
Std Dev		0.187	0.199	0.115

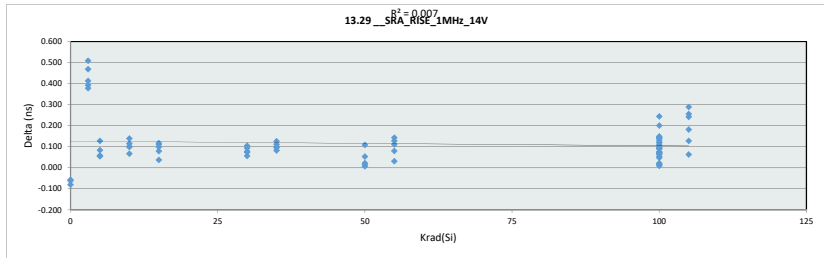


13.21_SRA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.252	8.763	8.734	8.752	8.764	8.842	8.837	8.241	8.313	8.400	8.478
Average	8.320	8.833	8.809	8.816	8.829	8.902	8.908	8.378	8.437	8.539	8.568
Max	8.412	8.963	8.890	8.873	8.933	8.943	9.015	8.446	8.577	8.760	8.667
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

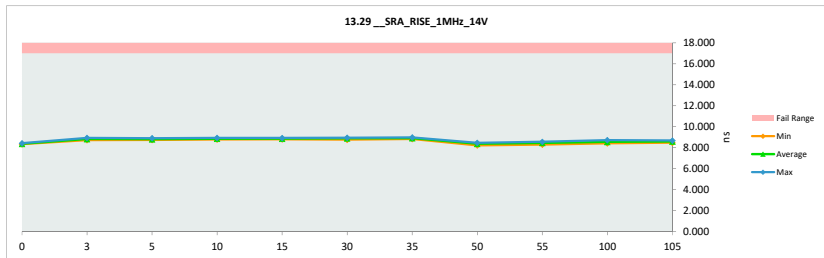


HDR TID Report  
TPS7H5003-SP

13.29 _SRA_RISE_1MHz_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	8.503	8.422	-0.081
0	138	8.385	8.328	-0.057
0	139	8.400	8.338	-0.062
3	197	8.470	8.848	0.378
3	198	8.386	8.779	0.393
3	199	8.313	8.822	0.509
3	202	8.524	8.937	0.413
3	203	8.249	8.718	0.469
5	204	8.728	8.784	0.056
5	205	8.676	8.731	0.055
5	206	8.720	8.778	0.058
5	208	8.771	8.899	0.128
5	209	8.752	8.836	0.084
10	210	8.717	8.856	0.139
10	211	8.735	8.836	0.101
10	212	8.715	8.782	0.067
10	213	8.708	8.807	0.099
10	214	8.819	8.934	0.115
15	215	8.844	8.923	0.079
15	217	8.719	8.818	0.099
15	219	8.718	8.835	0.117
15	220	8.762	8.799	0.037
15	221	8.750	8.861	0.111
30	222	8.797	8.902	0.105
30	223	8.871	8.945	0.074
30	224	8.809	8.903	0.094
30	225	8.859	8.937	0.078
30	226	8.700	8.756	0.056
35	227	8.792	8.892	0.100
35	228	8.869	8.981	0.112
35	229	8.749	8.831	0.082
35	230	8.822	8.917	0.095
35	231	8.795	8.921	0.126
50	170	8.367	8.389	0.022
50	172	8.215	8.222	0.007
50	173	8.440	8.455	0.015
50	175	8.284	8.337	0.053
50	176	8.300	8.409	0.109
55	177	8.411	8.522	0.111
55	178	8.269	8.412	0.143
55	179	8.333	8.413	0.080
55	180	8.266	8.297	0.031
55	181	8.430	8.558	0.128
100	154	8.469	8.486	0.017
100	155	8.467	8.476	0.009
100	156	8.526	8.543	0.017
100	157	8.411	8.482	0.071
100	158	8.480	8.553	0.073
100	159	8.373	8.437	0.064
100	160	8.483	8.632	0.149
100	161	8.576	8.646	0.070
100	162	8.425	8.515	0.090
100	163	8.408	8.462	0.054
100	164	8.460	8.555	0.095
100	165	8.487	8.534	0.047
100	166	8.408	8.500	0.092
100	168	8.387	8.409	0.022
100	169	8.565	8.674	0.109
100	182	8.418	8.550	0.132
100	183	8.479	8.723	0.244
100	184	8.417	8.493	0.076
100	185	8.321	8.422	0.101
100	186	8.426	8.569	0.143
100	187	8.436	8.558	0.122
100	188	8.382	8.501	0.119
100	189	8.478	8.619	0.141
100	190	8.466	8.667	0.201
105	191	8.404	8.467	0.063
105	192	8.309	8.491	0.182
105	193	8.301	8.590	0.289
105	194	8.362	8.604	0.242
105	195	8.527	8.655	0.128
105	196	8.424	8.680	0.256
Max		8.871	8.981	0.509
Average		8.532	8.646	0.114
Min		8.215	8.222	-0.081
Std Dev		0.187	0.198	0.109

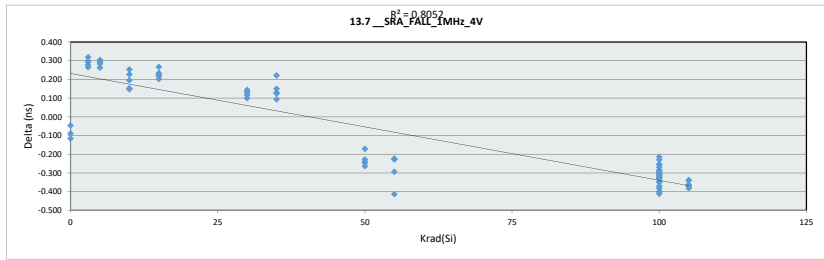


13.29 _SRA_RISE											
Test Site											
Tester											
Test Number	17	ns									
Max Limit	17	ns									
Min Limit											
LL	0	3	5	10	15	30	35	50	55	100	105
Min	8.328	8.718	8.731	8.782	8.799	8.756	8.831	8.222	8.297	8.409	8.467
Average	8.363	8.821	8.806	8.843	8.847	8.889	8.908	8.362	8.440	8.542	8.581
Max	8.422	8.937	8.899	8.934	8.923	8.945	8.981	8.455	8.558	8.723	8.680
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

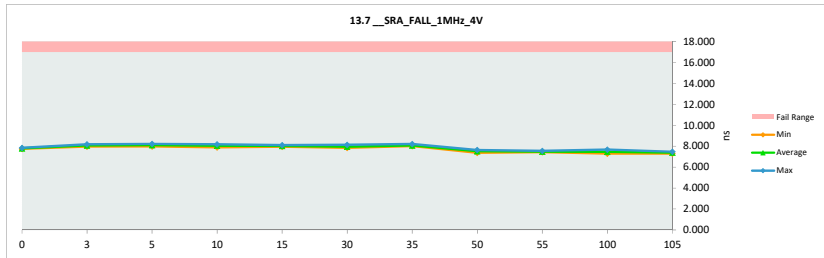


# HDR TID Report TPS7H5003-SP

13.7_SRA_FALL_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17	17		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.830	7.785	-0.045
0	138	7.868	7.752	-0.116
0	139	7.928	7.839	-0.089
3	197	7.884	8.178	0.294
3	198	7.813	8.091	0.278
3	199	7.731	8.029	0.298
3	202	7.854	8.119	0.265
3	203	7.646	7.965	0.319
5	204	7.737	8.021	0.284
5	205	7.738	8.034	0.296
5	206	7.926	8.219	0.293
5	208	7.952	8.216	0.264
5	209	7.683	7.988	0.305
10	210	7.701	7.929	0.228
10	211	7.824	8.077	0.253
10	212	7.989	8.185	0.196
10	213	7.995	8.148	0.153
10	214	7.743	7.891	0.148
15	215	7.840	8.075	0.235
15	217	7.736	8.003	0.267
15	219	7.730	7.955	0.225
15	220	7.879	8.097	0.218
15	221	7.827	8.030	0.203
30	222	7.778	7.914	0.136
30	223	7.993	8.138	0.145
30	224	7.737	7.854	0.117
30	225	7.720	7.849	0.129
30	226	7.924	8.024	0.100
35	227	7.782	8.004	0.222
35	228	7.949	8.043	0.094
35	229	7.889	8.040	0.151
35	230	8.079	8.204	0.125
35	231	7.947	8.077	0.130
50	170	7.614	7.375	-0.239
50	172	7.719	7.548	-0.171
50	173	7.884	7.638	-0.246
50	175	7.841	7.614	-0.227
50	176	7.693	7.430	-0.263
55	177	7.713	7.488	-0.225
55	178	7.859	7.446	-0.413
55	179	7.773	7.550	-0.223
55	180	7.767	7.474	-0.293
55	181	7.660	7.433	-0.227
100	154	7.702	7.303	-0.399
100	155	7.759	7.389	-0.370
100	156	7.708	7.309	-0.399
100	157	7.774	7.448	-0.326
100	158	7.810	7.508	-0.302
100	159	7.731	7.477	-0.254
100	160	7.909	7.589	-0.320
100	161	7.803	7.455	-0.348
100	162	7.744	7.445	-0.299
100	163	7.754	7.408	-0.346
100	164	7.778	7.470	-0.308
100	165	7.777	7.367	-0.410
100	166	7.714	7.400	-0.314
100	168	7.732	7.441	-0.291
100	169	7.937	7.634	-0.303
100	182	7.894	7.513	-0.381
100	183	7.853	7.505	-0.348
100	184	7.758	7.504	-0.254
100	185	7.629	7.300	-0.329
100	186	7.871	7.547	-0.324
100	187	7.840	7.611	-0.229
100	188	7.670	7.402	-0.268
100	189	7.962	7.678	-0.284
100	190	7.729	7.515	-0.214
105	191	7.636	7.298	-0.338
105	192	7.837	7.475	-0.362
105	193	7.855	7.474	-0.381
105	194	7.689	7.325	-0.364
105	195	7.709	7.339	-0.370
105	196	7.697	7.357	-0.340
	Max	8.079	8.219	0.319
	Average	7.800	7.716	-0.085
	Min	7.614	7.298	-0.413
	Std Dev	0.102	0.302	0.262

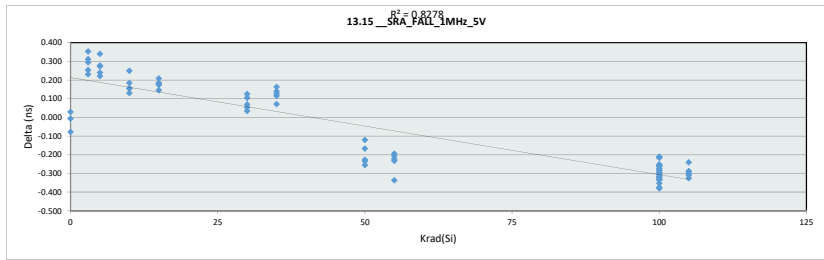


13.7_SRA_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.752	7.965	7.988	7.891	7.955	7.849	8.004	7.375	7.433	7.300	7.298
Average	7.792	8.076	8.096	8.046	8.032	7.956	8.074	7.521	7.478	7.467	7.378
Max	7.839	8.178	8.219	8.185	8.097	8.138	8.204	7.638	7.550	7.678	7.475
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

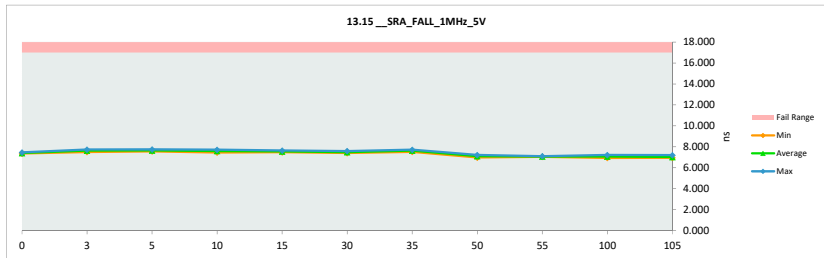


HDR TID Report  
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		13.15_SRA_FALL_1MHz_5V		
Test Site	Tester			
Test Number	Unit	ns	ns	
Max Limit	Min Limit	17	17	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.371	7.401	0.030
0	138	7.432	7.355	-0.077
0	139	7.470	7.464	-0.006
3	197	7.429	7.741	0.312
3	198	7.309	7.662	0.353
3	199	7.374	7.605	0.231
3	202	7.391	7.645	0.254
3	203	7.212	7.507	0.295
5	204	7.349	7.571	0.222
5	205	7.338	7.612	0.274
5	206	7.443	7.683	0.240
5	208	7.474	7.752	0.278
5	209	7.297	7.638	0.341
10	210	7.327	7.483	0.156
10	211	7.408	7.658	0.250
10	212	7.508	7.664	0.156
10	213	7.541	7.726	0.185
10	214	7.326	7.457	0.131
15	215	7.420	7.629	0.209
15	217	7.310	7.496	0.186
15	219	7.332	7.478	0.146
15	220	7.470	7.647	0.177
15	221	7.371	7.546	0.175
30	222	7.353	7.407	0.054
30	223	7.525	7.595	0.070
30	224	7.355	7.481	0.126
30	225	7.345	7.450	0.105
30	226	7.467	7.502	0.035
35	227	7.448	7.520	0.072
35	228	7.513	7.653	0.140
35	229	7.464	7.627	0.163
35	230	7.608	7.723	0.115
35	231	7.517	7.644	0.127
50	170	7.232	6.978	-0.254
50	172	7.308	7.075	-0.233
50	173	7.380	7.214	-0.166
50	175	7.339	7.219	-0.120
50	176	7.270	7.044	-0.226
55	177	7.251	7.057	-0.194
55	178	7.380	7.044	-0.336
55	179	7.341	7.109	-0.232
55	180	7.294	7.088	-0.206
55	181	7.286	7.064	-0.222
100	154	7.308	6.935	-0.373
100	155	7.373	6.994	-0.379
100	156	7.252	6.935	-0.317
100	157	7.345	7.012	-0.333
100	158	7.396	7.114	-0.282
100	159	7.387	7.055	-0.332
100	160	7.476	7.222	-0.254
100	161	7.379	7.048	-0.331
100	162	7.347	7.042	-0.305
100	163	7.349	6.997	-0.352
100	164	7.346	7.095	-0.251
100	165	7.285	6.989	-0.296
100	166	7.332	7.012	-0.320
100	168	7.274	7.001	-0.273
100	169	7.453	7.160	-0.293
100	182	7.416	7.206	-0.210
100	183	7.393	7.134	-0.259
100	184	7.346	7.084	-0.262
100	185	7.167	6.955	-0.212
100	186	7.423	7.092	-0.331
100	187	7.397	7.114	-0.283
100	188	7.245	7.029	-0.216
100	189	7.525	7.219	-0.306
100	190	7.377	7.117	-0.260
105	191	7.238	6.950	-0.288
105	192	7.377	7.069	-0.308
105	193	7.444	7.204	-0.240
105	194	7.269	6.981	-0.288
105	195	7.275	6.974	-0.301
105	196	7.289	6.964	-0.325
Max		7.608	7.752	0.353
Average		7.371	7.296	-0.075
Min		7.167	6.935	-0.379
Std Dev		0.087	0.273	0.235

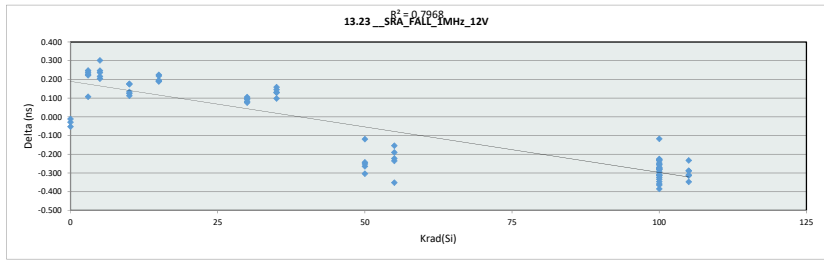


		13.15_SRA_FALL										
Test Site	Tester											
Test Number	Unit	17	ns									
Max Limit	Min Limit	17	ns									
Krad(Si)	LL	0	3	5	10	15	30	35	50	55	100	105
Min		7.355	7.507	7.571	7.457	7.478	7.407	7.520	6.978	7.044	6.935	6.950
Average		7.407	7.632	7.651	7.598	7.559	7.487	7.633	7.106	7.072	7.065	7.024
Max		7.464	7.741	7.752	7.726	7.647	7.595	7.723	7.219	7.109	7.222	7.204
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

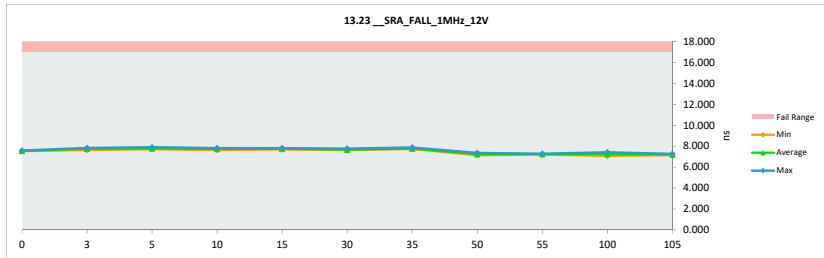


# HDR TID Report TPS7H5003-SP

13.23_SRA_FALL_1MHZ_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	17			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.604	7.593	-0.011
0	138	7.550	7.522	-0.028
0	139	7.613	7.561	-0.052
3	197	7.600	7.848	0.248
3	198	7.535	7.774	0.239
3	199	7.520	7.749	0.229
3	202	7.573	7.796	0.223
3	203	7.541	7.649	0.108
5	204	7.543	7.780	0.237
5	205	7.523	7.770	0.247
5	206	7.609	7.911	0.302
5	208	7.647	7.852	0.205
5	209	7.482	7.699	0.217
10	210	7.508	7.685	0.177
10	211	7.560	7.735	0.175
10	212	7.654	7.767	0.113
10	213	7.693	7.827	0.134
10	214	7.522	7.647	0.125
15	215	7.605	7.830	0.225
15	217	7.505	7.695	0.190
15	219	7.526	7.745	0.219
15	220	7.634	7.825	0.191
15	221	7.556	7.752	0.196
30	222	7.564	7.640	0.076
30	223	7.694	7.789	0.095
30	224	7.542	7.625	0.083
30	225	7.519	7.625	0.106
30	226	7.618	7.717	0.099
35	227	7.591	7.750	0.159
35	228	7.682	7.828	0.146
35	229	7.602	7.731	0.129
35	230	7.755	7.887	0.132
35	231	7.651	7.750	0.099
50	170	7.407	7.143	-0.264
50	172	7.483	7.180	-0.303
50	173	7.554	7.311	-0.243
50	175	7.493	7.374	-0.119
50	176	7.459	7.208	-0.251
55	177	7.478	7.256	-0.222
55	178	7.545	7.194	-0.351
55	179	7.476	7.286	-0.190
55	180	7.467	7.232	-0.235
55	181	7.425	7.221	-0.154
100	154	7.450	7.086	-0.364
100	155	7.518	7.205	-0.313
100	156	7.463	7.078	-0.385
100	157	7.490	7.217	-0.273
100	158	7.577	7.296	-0.281
100	159	7.475	7.250	-0.225
100	160	7.628	7.398	-0.230
100	161	7.528	7.254	-0.274
100	162	7.497	7.215	-0.282
100	163	7.537	7.204	-0.333
100	164	7.504	7.226	-0.278
100	165	7.537	7.179	-0.358
100	166	7.430	7.161	-0.269
100	168	7.479	7.155	-0.324
100	169	7.657	7.404	-0.253
100	182	7.581	7.353	-0.228
100	183	7.640	7.333	-0.307
100	184	7.503	7.386	-0.117
100	185	7.427	7.081	-0.346
100	186	7.619	7.306	-0.313
100	187	7.551	7.304	-0.247
100	188	7.478	7.184	-0.294
100	189	7.692	7.436	-0.256
100	190	7.522	7.288	-0.234
105	191	7.440	7.208	-0.232
105	192	7.561	7.273	-0.288
105	193	7.548	7.260	-0.288
105	194	7.463	7.149	-0.314
105	195	7.500	7.190	-0.310
105	196	7.511	7.164	-0.347
	Max	7.755	7.911	0.302
	Average	7.547	7.466	-0.080
	Min	7.407	7.078	-0.385
	Std Dev	0.074	0.264	0.224



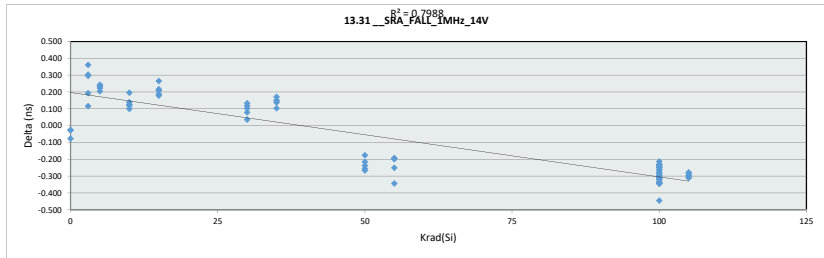
13.23_SRA_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
LL	0	3	5	10	15	30	35	50	55	100	105
Min	7.522	7.649	7.699	7.647	7.695	7.625	7.731	7.143	7.194	7.078	7.149
Average	7.559	7.763	7.802	7.732	7.769	7.679	7.789	7.243	7.248	7.250	7.207
Max	7.593	7.848	7.911	7.827	7.830	7.789	7.887	7.374	7.286	7.436	7.273
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



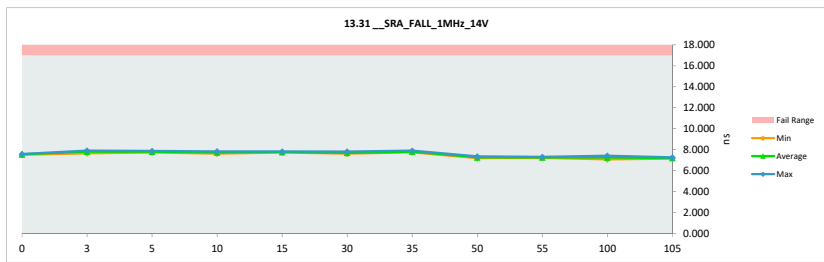
HDR TID Report  
TPS7H5003-SP

13.31_SRA_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	137	7.609	7.583	-0.026
0	138	7.583	7.506	-0.077
0	139	7.593	7.568	-0.025
3	197	7.550	7.912	0.362
3	198	7.539	7.843	0.304
3	199	7.468	7.765	0.297
3	202	7.630	7.823	0.193
3	203	7.530	7.646	0.116
5	204	7.542	7.780	0.238
5	205	7.532	7.766	0.234
5	206	7.621	7.826	0.205
5	208	7.650	7.874	0.224
5	209	7.484	7.728	0.244
10	210	7.516	7.656	0.140
10	211	7.561	7.757	0.196
10	212	7.657	7.776	0.119
10	213	7.703	7.829	0.126
10	214	7.516	7.616	0.100
15	215	7.608	7.817	0.209
15	217	7.473	7.739	0.266
15	219	7.506	7.723	0.217
15	220	7.636	7.824	0.188
15	221	7.550	7.729	0.179
30	222	7.547	7.680	0.133
30	223	7.696	7.814	0.118
30	224	7.541	7.620	0.079
30	225	7.511	7.614	0.103
30	226	7.635	7.671	0.036
35	227	7.602	7.753	0.151
35	228	7.680	7.851	0.171
35	229	7.638	7.742	0.104
35	230	7.749	7.898	0.149
35	231	7.640	7.778	0.138
50	170	7.421	7.184	-0.237
50	172	7.456	7.190	-0.266
50	173	7.570	7.355	-0.215
50	175	7.528	7.352	-0.176
50	176	7.478	7.223	-0.255
55	177	7.424	7.232	-0.192
55	178	7.558	7.215	-0.343
55	179	7.508	7.309	-0.199
55	180	7.477	7.281	-0.196
55	181	7.486	7.236	-0.250
100	154	7.519	7.074	-0.445
100	155	7.524	7.184	-0.340
100	156	7.473	7.130	-0.343
100	157	7.526	7.232	-0.294
100	158	7.550	7.268	-0.282
100	159	7.519	7.201	-0.318
100	160	7.618	7.358	-0.260
100	161	7.535	7.286	-0.249
100	162	7.489	7.242	-0.247
100	163	7.481	7.140	-0.341
100	164	7.555	7.259	-0.296
100	165	7.511	7.166	-0.345
100	166	7.519	7.178	-0.341
100	168	7.515	7.183	-0.332
100	169	7.697	7.380	-0.317
100	182	7.613	7.383	-0.230
100	183	7.618	7.308	-0.310
100	184	7.536	7.323	-0.213
100	185	7.382	7.120	-0.262
100	186	7.596	7.295	-0.301
100	187	7.561	7.285	-0.276
100	188	7.459	7.221	-0.238
100	189	7.661	7.417	-0.244
100	190	7.516	7.285	-0.231
105	191	7.449	7.171	-0.278
105	192	7.563	7.253	-0.310
105	193	7.540	7.258	-0.282
105	194	7.452	7.146	-0.306
105	195	7.487	7.192	-0.295
105	196	7.463	7.166	-0.297
Max		7.749	7.912	0.362
Average		7.550	7.468	-0.081
Min		7.382	7.074	-0.445
Std Dev		0.075	0.266	0.231



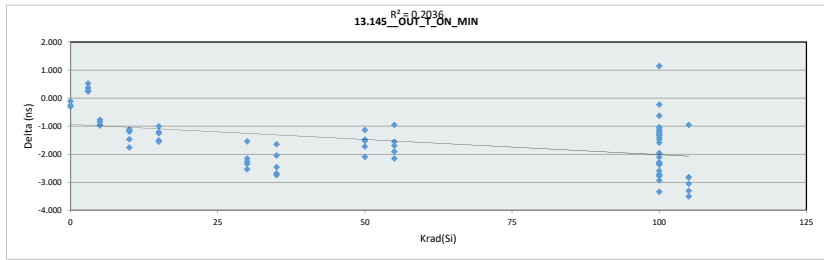
13.31_SRA_FALL		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		7.506	7.646	7.728	7.616	7.723	7.614	7.742	7.184	7.215	7.074	7.146
Average		7.552	7.798	7.795	7.727	7.766	7.680	7.804	7.261	7.255	7.247	7.198
Max		7.583	7.912	7.874	7.829	7.824	7.814	7.898	7.355	7.309	7.417	7.258
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



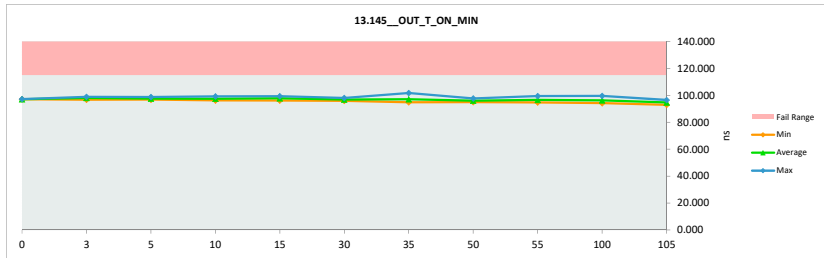


# HDR TID Report TPS7H5003-SP

13.145_OUT_T_ON_MIN				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	115			
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	97.219	97.110	-0.109
0	138	97.581	97.281	-0.300
0	139	97.367	97.120	-0.247
3	197	96.489	96.728	0.239
3	198	98.249	98.606	0.357
3	199	96.785	97.164	0.379
3	202	98.678	98.943	0.265
3	203	98.171	98.695	0.524
5	204	97.600	96.831	-0.769
5	205	97.852	96.899	-0.953
5	206	99.801	98.834	-0.967
5	208	98.058	97.209	-0.849
5	209	98.984	98.027	-0.957
10	210	100.393	99.230	-1.163
10	211	98.528	97.340	-1.188
10	212	97.688	96.227	-1.461
10	213	99.581	98.473	-1.108
10	214	97.977	96.222	-1.755
15	215	100.352	99.349	-1.003
15	217	97.386	96.182	-1.204
15	219	99.571	98.024	-1.547
15	220	100.868	99.362	-1.506
15	221	97.455	96.208	-1.247
30	222	98.614	96.466	-2.148
30	223	99.613	98.075	-1.538
30	224	99.726	97.471	-2.255
30	225	99.221	96.691	-2.530
30	226	98.303	95.969	-2.334
35	227	99.609	96.933	-2.676
35	228	98.940	96.484	-2.456
35	229	97.622	94.879	-2.743
35	230	97.591	95.552	-2.039
35	231	103.366	101.726	-1.640
50	170	97.842	96.368	-1.474
50	172	97.380	95.290	-2.090
50	173	96.489	94.967	-1.522
50	175	97.373	95.655	-1.718
50	176	98.876	97.744	-1.132
55	177	100.415	99.463	-0.952
55	178	99.240	97.692	-1.548
55	179	96.681	94.776	-1.905
55	180	97.872	95.724	-2.148
55	181	97.124	95.432	-1.692
100	154	98.301	95.565	-2.736
100	155	98.116	95.411	-2.705
100	156	97.269	94.992	-2.277
100	157	98.255	94.923	-3.332
100	158	96.778	95.557	-1.221
100	159	97.018	95.974	-1.044
100	160	95.378	94.207	-1.171
100	161	97.499	96.463	-1.036
100	162	96.545	95.263	-1.282
100	163	99.289	97.191	-2.098
100	164	96.698	95.581	-1.117
100	165	99.174	97.220	-1.954
100	166	97.460	95.993	-1.467
100	168	96.589	95.965	-0.624
100	169	98.571	98.343	-0.228
100	182	98.932	97.614	-1.318
100	183	99.197	97.614	-1.583
100	184	98.698	97.303	-1.395
100	185	97.571	94.796	-2.775
100	186	99.824	97.236	-2.588
100	187	98.998	96.674	-2.324
100	188	98.475	99.624	1.149
100	189	97.919	95.551	-2.368
100	190	99.010	96.080	-2.930
105	191	97.489	94.676	-2.813
105	192	97.352	94.529	-2.823
105	193	97.931	94.434	-3.497
105	194	96.255	92.958	-3.297
105	195	97.523	96.571	-0.952
105	196	98.193	95.142	-3.051
Max		103.366	101.726	1.149
Average		98.231	96.697	-1.534
Min		95.378	92.958	-3.497
Std Dev		1.278	1.552	0.990

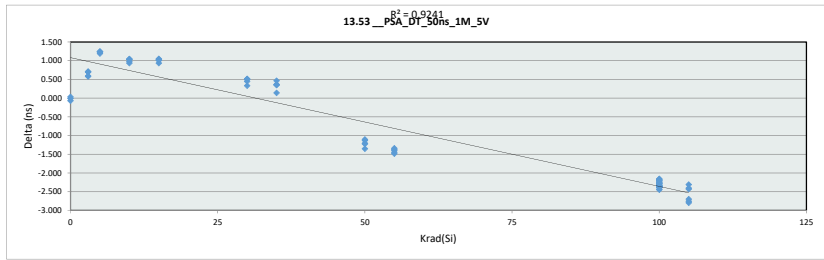


13.145_OUT_T_ON_MIN											
Test Site											
Tester											
Test Number											
Max Limit	115	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	97.110	96.728	96.831	96.222	96.182	95.969	94.879	94.967	94.776	94.207	92.958
Average	97.170	98.027	97.560	97.498	97.825	96.934	97.115	96.005	96.617	96.298	94.718
Max	97.281	98.943	98.834	99.230	99.362	98.075	101.726	97.744	99.463	99.624	96.571
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

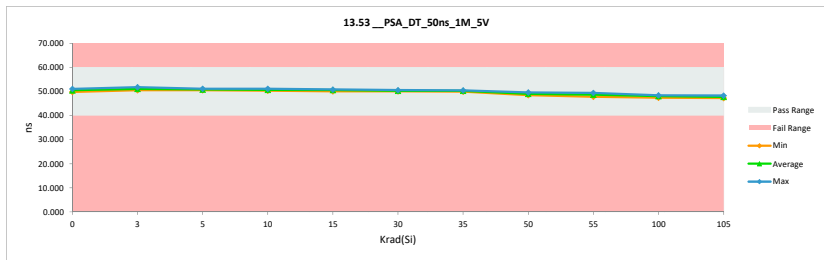


HDR TID Report  
TPS7H5003-SP

13.53_PSA_DT_50ns_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	60	60		
Min Limit	40	40		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	51.174	51.107	-0.067
0	138	49.804	49.839	0.035
0	139	50.652	50.645	-0.007
3	197	51.102	51.807	0.705
3	198	49.858	50.569	0.711
3	199	50.384	51.081	0.697
3	202	50.339	50.923	0.584
3	203	50.518	51.113	0.595
5	204	49.658	50.853	1.195
5	205	49.908	51.146	1.238
5	206	49.665	50.913	1.248
5	208	49.632	50.879	1.247
5	209	49.302	50.539	1.237
10	210	49.378	50.404	1.026
10	211	49.300	50.343	1.043
10	212	49.912	50.961	1.049
10	213	50.199	51.176	0.977
10	214	49.682	50.620	0.938
15	215	49.791	50.815	1.024
15	217	49.043	50.080	1.037
15	219	49.871	50.810	0.939
15	220	49.802	50.856	1.054
15	221	49.444	50.453	1.009
30	222	49.586	50.091	0.505
30	223	49.688	50.195	0.507
30	224	50.245	50.582	0.337
30	225	49.992	50.442	0.450
30	226	49.609	50.129	0.520
35	227	49.786	50.252	0.466
35	228	49.933	50.284	0.351
35	229	50.142	50.502	0.360
35	230	50.111	50.480	0.369
35	231	49.831	49.975	0.144
50	170	49.740	48.518	-1.222
50	172	49.824	48.618	-1.206
50	173	50.081	48.979	-1.102
50	175	50.989	49.641	-1.348
50	176	50.546	49.419	-1.127
55	177	50.388	49.049	-1.339
55	178	50.803	49.409	-1.394
55	179	49.253	47.809	-1.444
55	180	49.586	48.218	-1.368
55	181	50.514	49.033	-1.481
100	154	50.689	48.358	-2.331
100	155	50.128	47.975	-2.153
100	156	50.482	48.113	-2.369
100	157	50.229	47.999	-2.230
100	158	50.270	47.932	-2.338
100	159	49.735	47.452	-2.283
100	160	50.209	47.931	-2.278
100	161	50.266	48.005	-2.261
100	162	50.497	48.140	-2.357
100	163	50.570	48.140	-2.430
100	164	50.250	48.059	-2.191
100	165	49.765	47.597	-2.168
100	166	50.542	48.248	-2.294
100	168	50.798	48.352	-2.446
100	169	50.407	48.085	-2.322
100	182	50.390	48.029	-2.361
100	183	50.704	48.350	-2.354
100	184	50.776	48.391	-2.385
100	185	49.890	47.633	-2.257
100	186	50.276	48.092	-2.184
100	187	50.395	48.190	-2.205
100	188	50.639	48.260	-2.379
100	189	50.807	48.441	-2.366
100	190	50.613	48.260	-2.353
105	191	50.115	47.686	-2.429
105	192	50.412	48.105	-2.307
105	193	50.066	47.666	-2.400
105	194	50.095	47.301	-2.794
105	195	51.060	48.309	-2.751
105	196	50.134	47.438	-2.696
Max		51.174	51.807	1.248
Average		50.141	49.316	-0.824
Min		49.043	47.301	-2.794
Std Dev		0.475	1.297	1.474

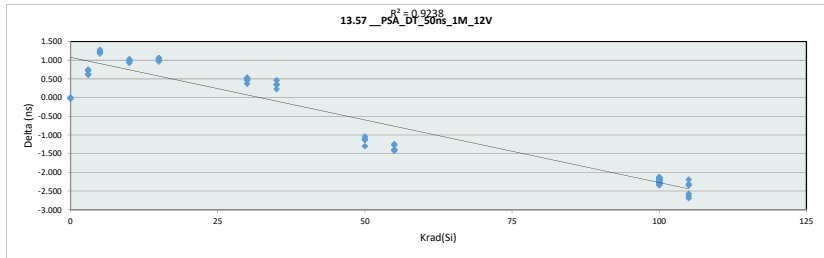


13.53_PSA_DT_50ns_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	60	ns									
Min Limit	40	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000
Min	49.839	50.569	50.539	50.343	50.080	50.091	49.975	48.518	47.809	47.452	47.301
Average	50.530	51.099	50.866	50.701	50.603	50.288	50.299	49.035	48.704	48.085	47.751
Max	51.107	51.807	51.146	51.176	50.856	50.582	50.502	49.641	49.409	48.441	48.309
UL	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000

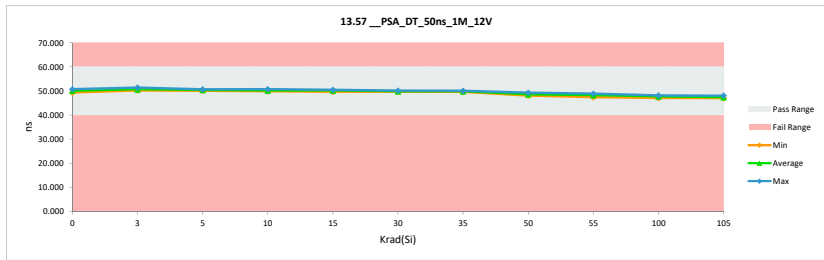


# HDR TID Report TPS7H5003-SP

13.57_PSA_DT_50ns_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	60	60		
Min Limit	40	40		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	50.733	50.742	0.009
0	138	49.320	49.305	-0.015
0	139	50.179	50.150	-0.029
3	197	50.686	51.404	0.718
3	198	49.552	50.172	0.620
3	199	49.890	50.644	0.754
3	202	49.972	50.597	0.625
3	203	49.969	50.596	0.627
5	204	49.195	50.415	1.220
5	205	49.406	50.634	1.228
5	206	49.265	50.548	1.283
5	208	49.204	50.386	1.182
5	209	48.844	50.084	1.240
10	210	48.900	49.883	0.983
10	211	48.853	49.851	0.998
10	212	49.410	50.441	1.031
10	213	49.723	50.698	0.975
10	214	49.284	50.215	0.931
15	215	49.424	50.475	1.051
15	217	48.623	49.679	1.056
15	219	49.384	50.384	1.000
15	220	49.348	50.411	1.063
15	221	48.989	49.955	0.966
30	222	49.184	49.688	0.504
30	223	49.329	49.823	0.494
30	224	49.767	50.138	0.371
30	225	49.557	50.017	0.460
30	226	49.124	49.664	0.540
35	227	49.325	49.791	0.466
35	228	49.527	49.887	0.360
35	229	49.636	49.988	0.352
35	230	49.690	50.035	0.345
35	231	49.373	49.606	0.233
50	170	49.216	48.094	-1.122
50	172	49.267	48.162	-1.105
50	173	49.651	48.610	-1.041
50	175	50.562	49.265	-1.297
50	176	49.992	48.865	-1.127
55	177	49.901	48.658	-1.243
55	178	50.291	48.870	-1.421
55	179	48.790	47.396	-1.394
55	180	49.083	47.818	-1.265
55	181	50.079	48.687	-1.392
100	154	50.226	47.978	-2.248
100	155	49.718	47.540	-2.178
100	156	50.026	47.776	-2.250
100	157	49.874	47.661	-2.213
100	158	49.876	47.533	-2.343
100	159	49.222	47.094	-2.128
100	160	49.817	47.564	-2.253
100	161	49.804	47.644	-2.160
100	162	49.989	47.804	-2.185
100	163	50.119	47.825	-2.294
100	164	49.914	47.755	-2.159
100	165	49.396	47.263	-2.133
100	166	50.072	47.921	-2.151
100	168	50.317	47.986	-2.331
100	169	50.011	47.755	-2.256
100	182	49.968	47.665	-2.303
100	183	50.352	48.087	-2.265
100	184	50.332	48.032	-2.300
100	185	49.453	47.238	-2.215
100	186	49.898	47.769	-2.129
100	187	49.962	47.818	-2.144
100	188	50.183	47.857	-2.326
100	189	50.390	48.146	-2.244
100	190	50.109	47.921	-2.188
105	191	49.693	47.347	-2.346
105	192	49.969	47.776	-2.193
105	193	49.577	47.266	-2.311
105	194	49.557	46.990	-2.567
105	195	50.671	48.047	-2.624
105	196	49.755	47.065	-2.690
Max		50.733	51.404	1.283
Average		49.695	48.916	-0.779
Min		48.623	46.990	-2.690
Std Dev		0.478	1.259	1.435

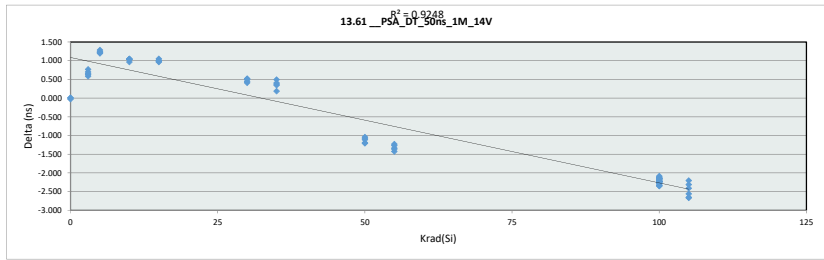


13.57_PSA_DT_50ns_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	60	ns									
Min Limit	40	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000
Min	49.305	50.172	50.084	49.851	49.679	49.664	49.606	48.094	47.396	47.094	46.990
Average	50.066	50.683	50.413	50.218	50.181	49.866	49.861	48.599	48.286	47.735	47.415
Max	50.742	51.404	50.634	50.698	50.475	50.138	50.035	49.265	48.870	48.146	48.047
UL	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000

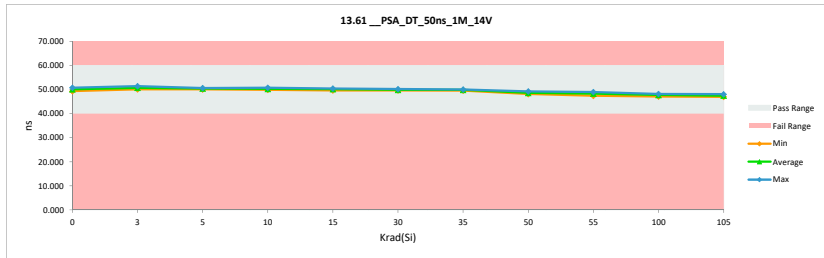


# HDR TID Report TPS7H5003-SP

13.61_PSA_DT_50ns_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	60	60		
Min Limit	40	40		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	50.748	50.752	0.004
0	138	49.301	49.314	0.013
0	139	50.198	50.174	-0.024
3	197	50.710	51.414	0.704
3	198	49.519	50.106	0.587
3	199	49.827	50.599	0.772
3	202	49.984	50.616	0.632
3	203	49.982	50.638	0.656
5	204	49.191	50.414	1.223
5	205	49.410	50.611	1.201
5	206	49.272	50.562	1.290
5	208	49.187	50.427	1.240
5	209	48.832	50.067	1.235
10	210	48.893	49.937	1.044
10	211	48.836	49.881	1.045
10	212	49.409	50.462	1.053
10	213	49.721	50.751	1.030
10	214	49.273	50.240	0.967
15	215	49.445	50.425	0.980
15	217	48.618	49.619	1.001
15	219	49.374	50.341	0.967
15	220	49.345	50.400	1.055
15	221	48.989	50.000	1.011
30	222	49.179	49.677	0.498
30	223	49.306	49.819	0.513
30	224	49.768	50.181	0.413
30	225	49.559	50.005	0.446
30	226	49.099	49.623	0.524
35	227	49.324	49.820	0.496
35	228	49.521	49.900	0.379
35	229	49.622	50.028	0.406
35	230	49.682	50.031	0.349
35	231	49.376	49.564	0.188
50	170	49.205	48.168	-1.037
50	172	49.325	48.121	-1.204
50	173	49.666	48.599	-1.067
50	175	50.402	49.209	-1.193
50	176	49.990	48.883	-1.107
55	177	49.925	48.698	-1.227
55	178	50.274	48.941	-1.333
55	179	48.761	47.404	-1.357
55	180	49.098	47.833	-1.265
55	181	50.073	48.651	-1.422
100	154	50.217	47.981	-2.236
100	155	49.700	47.548	-2.152
100	156	50.015	47.797	-2.218
100	157	49.845	47.739	-2.106
100	158	49.927	47.583	-2.344
100	159	49.208	47.080	-2.128
100	160	49.785	47.573	-2.212
100	161	49.847	47.623	-2.224
100	162	49.979	47.775	-2.204
100	163	50.146	47.800	-2.346
100	164	49.896	47.749	-2.147
100	165	49.376	47.228	-2.148
100	166	50.065	47.927	-2.138
100	168	50.285	48.013	-2.272
100	169	50.070	47.803	-2.267
100	182	49.981	47.660	-2.321
100	183	50.354	48.028	-2.326
100	184	50.280	48.024	-2.256
100	185	49.437	47.205	-2.232
100	186	49.860	47.777	-2.083
100	187	49.972	47.809	-2.163
100	188	50.131	47.913	-2.218
100	189	50.366	48.143	-2.223
100	190	50.119	47.954	-2.165
105	191	49.753	47.348	-2.405
105	192	49.965	47.763	-2.202
105	193	49.564	47.255	-2.309
105	194	49.629	46.968	-2.661
105	195	50.623	48.069	-2.554
105	196	49.756	47.105	-2.651
Max		50.748	51.414	1.290
Average		49.690	48.920	-0.770
Min		48.618	46.968	-2.661
Std Dev		0.475	1.259	1.435

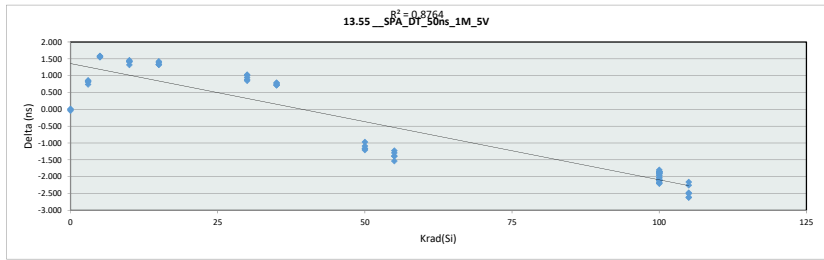


13.61_PSA_DT_50ns_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	60	ns									
Min Limit	40	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000
Min	49.314	50.106	50.067	49.881	49.619	49.623	49.564	48.121	47.404	47.080	46.968
Average	50.080	50.675	50.416	50.254	50.157	49.861	49.869	48.596	48.305	47.739	47.418
Max	50.752	51.414	50.611	50.751	50.425	50.181	50.031	49.209	48.941	48.143	48.069
UL	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000

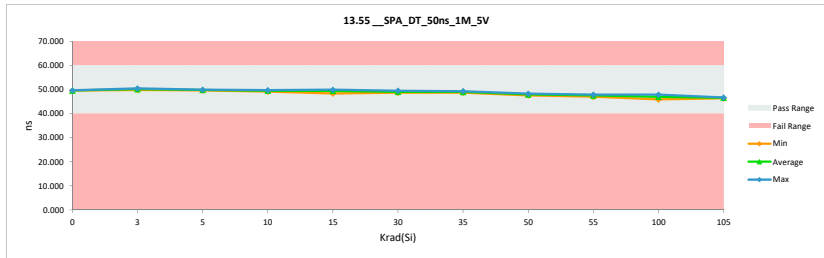


# HDR TID Report TPS7H5003-SP

13.55_SPA_DT_50ns_1M_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	60	60		
Min Limit	40	40		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	49.550	49.516	-0.034
0	138	49.650	49.666	0.016
0	139	49.520	49.523	0.003
3	197	49.616	50.442	0.826
3	198	49.458	50.202	0.744
3	199	49.238	50.100	0.862
3	202	48.968	49.815	0.847
3	203	49.352	50.170	0.818
5	204	48.308	49.899	1.591
5	205	48.279	49.840	1.561
5	206	48.292	49.856	1.564
5	208	48.063	49.615	1.552
5	209	48.257	49.844	1.587
10	210	47.961	49.375	1.414
10	211	47.681	49.141	1.460
10	212	48.330	49.651	1.321
10	213	48.122	49.555	1.433
10	214	48.298	49.714	1.416
15	215	48.293	49.630	1.337
15	217	46.969	48.361	1.392
15	219	48.145	49.477	1.332
15	220	48.604	49.934	1.330
15	221	48.289	49.718	1.429
30	222	47.884	48.768	0.884
30	223	47.658	48.690	1.032
30	224	48.617	49.471	0.854
30	225	48.119	49.127	1.008
30	226	48.138	49.087	0.949
35	227	47.962	48.680	0.718
35	228	48.233	49.031	0.798
35	229	48.573	49.293	0.720
35	230	48.298	49.077	0.779
35	231	48.169	48.908	0.739
50	170	48.876	47.907	-0.969
50	172	48.657	47.572	-1.085
50	173	49.375	48.217	-1.158
50	175	49.416	48.245	-1.171
50	176	49.103	47.898	-1.205
55	177	49.130	47.839	-1.291
55	178	49.253	47.869	-1.384
55	179	48.470	47.085	-1.385
55	180	48.209	46.986	-1.223
55	181	48.355	47.823	-1.532
100	154	49.265	47.421	-1.844
100	155	49.288	47.436	-1.852
100	156	49.025	47.129	-1.896
100	157	48.285	46.381	-1.904
100	158	49.504	47.405	-2.099
100	159	47.738	45.903	-1.835
100	160	49.094	46.907	-2.187
100	161	49.129	47.257	-1.872
100	162	48.400	46.497	-1.903
100	163	49.006	46.805	-2.201
100	164	48.718	46.602	-2.116
100	165	48.287	46.176	-2.111
100	166	49.093	47.221	-1.872
100	168	48.662	46.803	-1.859
100	169	49.662	47.866	-1.796
100	182	49.189	47.243	-1.946
100	183	49.345	47.333	-2.012
100	184	49.575	47.403	-2.172
100	185	48.508	46.408	-2.100
100	186	48.613	46.543	-2.070
100	187	48.963	47.120	-1.843
100	188	48.943	46.914	-2.029
100	189	49.159	47.200	-1.959
100	190	49.339	47.227	-2.112
105	191	48.929	46.678	-2.251
105	192	48.811	46.657	-2.154
105	193	49.249	46.642	-2.607
105	194	48.948	46.330	-2.618
105	195	49.096	46.625	-2.471
105	196	49.127	46.626	-2.501
Max		49.662	50.442	1.591
Average		48.735	48.183	-0.552
Min		46.969	45.903	-2.618
Std Dev		0.585	1.309	1.519

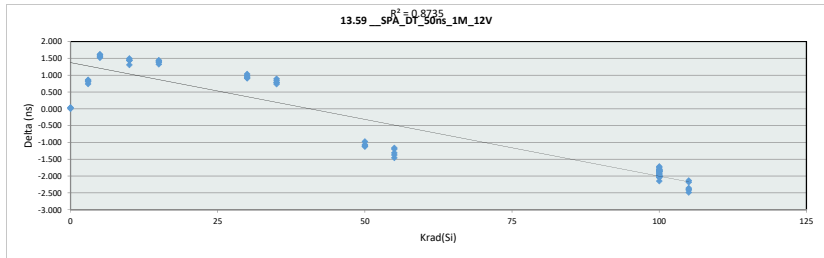


13.55_SPA_DT_50ns_1M_5V											
Test Site											
Tester											
Test Number											
Max Limit	60	ns									
Min Limit	40	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000
Min	49.516	49.815	49.615	49.141	48.361	48.690	48.680	47.572	46.986	45.903	46.330
Average	49.568	50.146	49.811	49.487	49.424	49.029	48.998	47.968	47.520	46.967	46.593
Max	49.666	50.442	49.899	49.714	49.934	49.471	49.293	48.245	47.869	47.866	46.678
UL	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000

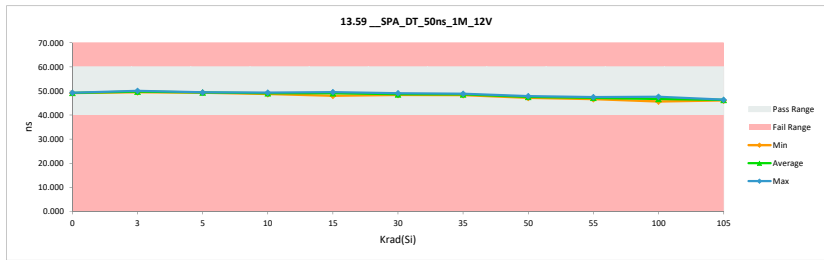


HDR TID Report  
TPS7H5003-SP

13.59_SPA_DT_50ns_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	60	60		
Min Limit	40	40		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	49.143	49.150	0.007
0	138	49.203	49.235	0.032
0	139	49.049	49.091	0.042
3	197	49.199	50.043	0.844
3	198	48.985	49.806	0.821
3	199	48.831	49.641	0.810
3	202	48.602	49.467	0.865
3	203	48.949	49.693	0.744
5	204	47.866	49.415	1.549
5	205	47.824	49.400	1.576
5	206	47.871	49.394	1.523
5	208	47.637	49.248	1.611
5	209	47.787	49.408	1.621
10	210	47.480	48.915	1.435
10	211	47.243	48.692	1.449
10	212	47.844	49.153	1.309
10	213	47.678	49.176	1.498
10	214	47.893	49.346	1.453
15	215	47.926	49.311	1.385
15	217	46.579	48.006	1.427
15	219	47.667	49.065	1.398
15	220	48.177	49.506	1.329
15	221	47.847	49.295	1.448
30	222	47.483	48.409	0.926
30	223	47.288	48.326	1.038
30	224	48.163	49.074	0.911
30	225	47.715	48.666	0.951
30	226	47.648	48.640	0.992
35	227	47.517	48.254	0.737
35	228	47.828	48.722	0.894
35	229	48.050	48.826	0.776
35	230	47.870	48.724	0.854
35	231	47.743	48.536	0.793
50	170	48.448	47.479	-0.969
50	172	48.231	47.114	-1.117
50	173	48.937	47.876	-1.061
50	175	48.908	47.809	-1.099
50	176	48.569	47.460	-1.109
55	177	48.681	47.486	-1.195
55	178	48.769	47.461	-1.308
55	179	48.106	46.735	-1.371
55	180	47.774	46.613	-1.161
55	181	48.935	47.479	-1.456
100	154	48.877	47.060	-1.817
100	155	48.886	47.064	-1.822
100	156	48.626	46.771	-1.855
100	157	47.846	46.046	-1.800
100	158	48.986	47.117	-1.869
100	159	47.334	45.621	-1.713
100	160	48.680	46.661	-2.019
100	161	48.794	46.938	-1.856
100	162	47.986	46.115	-1.871
100	163	48.609	46.463	-2.146
100	164	48.362	46.341	-2.021
100	165	47.935	45.894	-2.041
100	166	48.660	46.835	-1.825
100	168	48.185	46.395	-1.790
100	169	49.312	47.567	-1.745
100	182	48.810	46.907	-1.903
100	183	49.016	47.072	-1.944
100	184	49.159	47.143	-2.016
100	185	48.102	46.122	-1.980
100	186	48.267	46.287	-1.980
100	187	48.590	46.770	-1.820
100	188	48.482	46.489	-1.993
100	189	48.810	46.949	-1.861
100	190	48.860	46.932	-1.928
105	191	48.533	46.355	-2.178
105	192	48.429	46.297	-2.132
105	193	48.731	46.318	-2.413
105	194	48.519	46.036	-2.483
105	195	48.715	46.360	-2.355
105	196	48.745	46.360	-2.385
Max		49.312	50.043	1.621
Average		48.312	47.814	-0.498
Min		46.579	45.621	-2.483
Std Dev		0.586	1.267	1.488

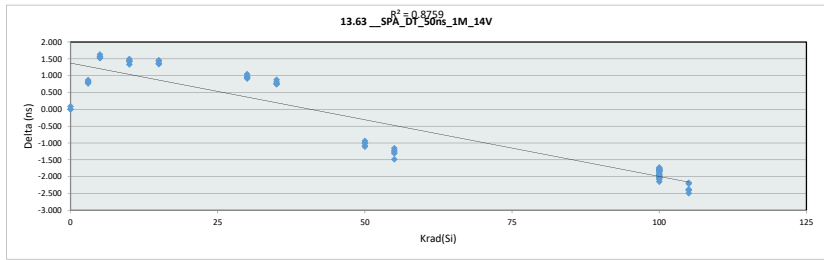


13.59_SPA_DT_50ns_1M_12V											
Test Site											
Tester											
Test Number											
Max Limit	60	ns									
Min Limit	40	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000
Min	49.091	49.467	49.248	48.692	48.006	48.326	48.254	47.114	46.613	45.621	46.036
Average	49.159	49.730	49.373	49.056	49.037	48.623	48.612	47.548	47.155	46.648	46.288
Max	49.235	50.043	49.415	49.346	49.506	49.074	48.826	47.876	47.486	47.567	46.360
UL	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000

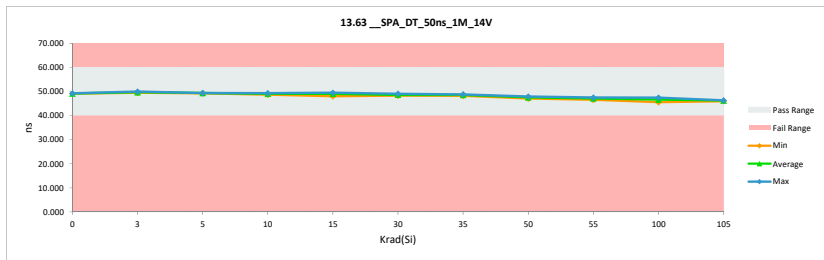


# HDR TID Report TPS7H5003-SP

13.63 SPA_DT_50ns_1M_14V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	60	60		
Min Limit	40	40		
Krad(Si)	Serial #	PRE	POST	Delta
0	137	49.126	49.134	0.008
0	138	49.175	49.267	0.092
0	139	49.103	49.110	0.007
3	197	49.213	49.985	0.772
3	198	49.017	49.841	0.824
3	199	48.819	49.620	0.801
3	202	48.589	49.460	0.871
3	203	48.830	49.655	0.825
5	204	47.865	49.404	1.539
5	205	47.815	49.391	1.576
5	206	47.861	49.385	1.524
5	208	47.630	49.264	1.634
5	209	47.803	49.364	1.561
10	210	47.484	48.982	1.498
10	211	47.243	48.701	1.458
10	212	47.850	49.189	1.339
10	213	47.684	49.110	1.426
10	214	47.889	49.313	1.424
15	215	47.913	49.278	1.365
15	217	46.570	48.032	1.462
15	219	47.669	49.035	1.366
15	220	48.174	49.519	1.345
15	221	47.853	49.290	1.437
30	222	47.479	48.397	0.918
30	223	47.286	48.333	1.047
30	224	48.150	49.084	0.934
30	225	47.707	48.658	0.951
30	226	47.659	48.639	0.980
35	227	47.510	48.272	0.762
35	228	47.827	48.643	0.816
35	229	48.064	48.815	0.751
35	230	47.838	48.720	0.882
35	231	47.722	48.473	0.751
50	170	48.449	47.514	-0.935
50	172	48.106	47.113	-0.993
50	173	48.941	47.935	-1.006
50	175	48.913	47.838	-1.075
50	176	48.595	47.488	-1.107
55	177	48.692	47.476	-1.216
55	178	48.745	47.440	-1.305
55	179	48.012	46.752	-1.260
55	180	47.760	46.608	-1.152
55	181	48.885	47.503	-1.482
100	154	48.849	47.073	-1.776
100	155	48.843	47.070	-1.773
100	156	48.619	46.820	-1.799
100	157	47.887	46.099	-1.788
100	158	48.976	47.135	-1.841
100	159	47.328	45.577	-1.751
100	160	48.688	46.658	-2.030
100	161	48.742	46.907	-1.835
100	162	47.965	46.125	-1.840
100	163	48.611	46.465	-2.146
100	164	48.355	46.342	-2.013
100	165	47.909	45.878	-2.031
100	166	48.679	46.869	-1.810
100	168	48.144	46.422	-1.722
100	169	49.248	47.476	-1.772
100	182	48.799	46.944	-1.855
100	183	49.008	47.064	-1.944
100	184	49.150	47.073	-2.077
100	185	48.149	46.170	-1.979
100	186	48.224	46.281	-1.943
100	187	48.578	46.785	-1.793
100	188	48.445	46.535	-1.910
100	189	48.828	46.987	-1.841
100	190	48.873	46.896	-1.977
105	191	48.508	46.299	-2.209
105	192	48.442	46.265	-2.177
105	193	48.735	46.335	-2.400
105	194	48.506	46.018	-2.488
105	195	48.714	46.341	-2.373
105	196	48.698	46.311	-2.387
Max		49.248	49.985	1.634
Average		48.303	47.812	-0.491
Min		46.570	45.577	-2.488
Std Dev		0.585	1.262	1.481

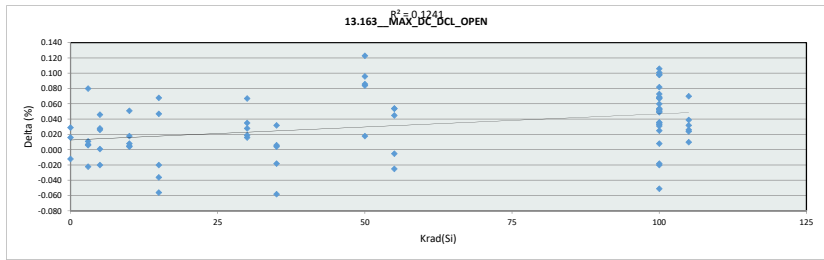


13.63 SPA_DT_50ns_1M_14V											
Test Site											
Tester											
Test Number											
Max Limit	60	ns									
Min Limit	40	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000	40.000
Min	49.110	49.460	49.264	48.701	48.032	48.333	48.272	47.113	46.608	45.577	46.018
Average	49.170	49.712	49.362	49.059	49.031	48.622	48.585	47.578	47.156	46.652	46.262
Max	49.267	49.985	49.404	49.313	49.519	49.084	48.815	47.935	47.503	47.476	46.341
UL	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000	60.000

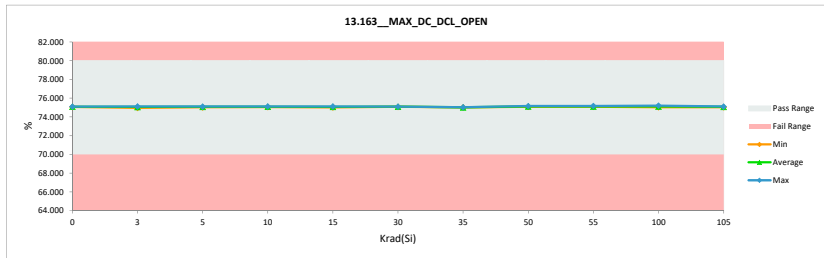


HDR TID Report  
TPS7H5003-SP

13.163_MAX_DC_DCL_OPEN				
Test Site				
Tester				
Test Number				
Unit		%	%	
Max Limit		80	80	
Min Limit		70	70	
Krad(Si)	Serial #	PRE	POST	Delta
0	137	75.058	75.074	0.016
0	138	75.069	75.098	0.029
0	139	75.088	75.076	-0.012
3	197	74.964	74.971	0.007
3	198	75.045	75.125	0.080
3	199	75.073	75.079	0.006
3	202	75.053	75.031	-0.022
3	203	75.098	75.109	0.011
5	204	75.060	75.086	0.026
5	205	75.110	75.111	0.001
5	206	75.048	75.094	0.046
5	208	75.045	75.025	-0.020
5	209	75.050	75.078	0.028
10	210	75.085	75.093	0.008
10	211	75.048	75.052	0.004
10	212	75.072	75.077	0.005
10	213	75.071	75.122	0.051
10	214	75.063	75.081	0.018
15	215	75.052	75.099	0.047
15	217	75.086	75.066	-0.020
15	219	75.076	75.020	-0.056
15	220	75.049	75.117	0.068
15	221	75.081	75.045	-0.036
30	222	75.079	75.095	0.016
30	223	75.011	75.078	0.067
30	224	75.074	75.093	0.019
30	225	75.068	75.096	0.028
30	226	75.069	75.104	0.035
35	227	75.030	75.012	-0.018
35	228	75.000	75.032	0.032
35	229	75.075	75.017	-0.058
35	230	75.031	75.035	0.004
35	231	75.010	75.016	0.006
50	170	75.008	75.104	0.096
50	172	75.040	75.163	0.123
50	173	75.002	75.086	0.084
50	175	75.065	75.083	0.018
50	176	75.056	75.142	0.086
55	177	75.108	75.083	-0.025
55	178	75.114	75.168	0.054
55	179	75.091	75.086	-0.005
55	180	75.087	75.132	0.045
55	181	75.045	75.099	0.054
100	154	75.081	75.148	0.067
100	155	75.046	75.099	0.053
100	156	75.073	75.142	0.069
100	157	75.077	75.102	0.025
100	158	74.987	75.093	0.106
100	159	75.088	75.140	0.052
100	160	75.026	75.059	0.033
100	161	74.982	75.050	0.068
100	162	75.054	75.152	0.098
100	163	75.061	75.111	0.050
100	164	75.055	75.104	0.049
100	165	75.097	75.157	0.060
100	166	75.119	75.099	-0.020
100	168	75.148	75.202	0.054
100	169	75.033	75.115	0.082
100	182	75.087	75.036	-0.051
100	183	75.031	75.104	0.073
100	184	75.029	75.130	0.101
100	185	75.130	75.138	0.008
100	186	75.066	75.097	0.031
100	187	75.032	75.130	0.098
100	188	75.095	75.130	0.035
100	189	75.058	75.040	-0.018
100	190	75.040	75.076	0.036
105	191	75.031	75.101	0.070
105	192	75.082	75.114	0.032
105	193	75.021	75.031	0.010
105	194	75.044	75.083	0.039
105	195	75.055	75.081	0.026
105	196	75.034	75.058	0.024
Max		75.148	75.202	0.123
Average		75.058	75.090	0.032
Min		74.964	74.971	-0.058
Std Dev		0.034	0.042	0.040



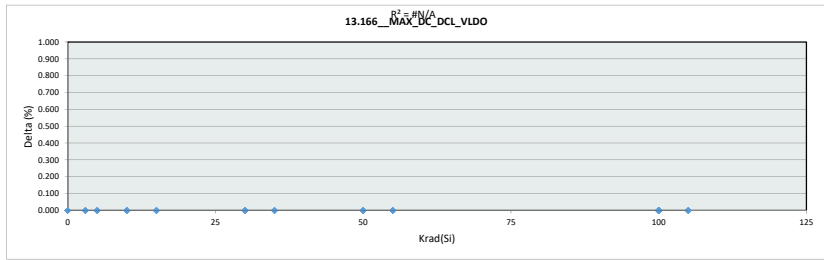
13.163_MAX_DC											
Test Site											
Tester											
Test Number											
Max Limit	80	%									
Min Limit	70	%									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000
Min	75.074	74.971	75.025	75.052	75.020	75.078	75.012	75.083	75.083	75.036	75.031
Average	75.083	75.063	75.079	75.085	75.069	75.093	75.022	75.116	75.114	75.111	75.078
Max	75.098	75.125	75.111	75.122	75.117	75.104	75.035	75.163	75.168	75.202	75.114
UL	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000



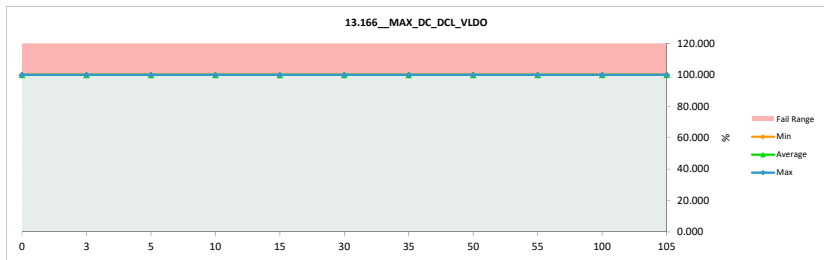


# HDR TID Report TPS7H5003-SP

13.166_MAX_DC_DCL_VLDO				
Test Site				
Tester				
Test Number				
Unit	%	%		
Max Limit	100	100		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	137	100.000	100.000	0.000
0	138	100.000	100.000	0.000
0	139	100.000	100.000	0.000
3	197	100.000	100.000	0.000
3	198	100.000	100.000	0.000
3	199	100.000	100.000	0.000
3	202	100.000	100.000	0.000
3	203	100.000	100.000	0.000
5	204	100.000	100.000	0.000
5	205	100.000	100.000	0.000
5	206	100.000	100.000	0.000
5	208	100.000	100.000	0.000
5	209	100.000	100.000	0.000
10	210	100.000	100.000	0.000
10	211	100.000	100.000	0.000
10	212	100.000	100.000	0.000
10	213	100.000	100.000	0.000
10	214	100.000	100.000	0.000
15	215	100.000	100.000	0.000
15	217	100.000	100.000	0.000
15	219	100.000	100.000	0.000
15	220	100.000	100.000	0.000
15	221	100.000	100.000	0.000
30	222	100.000	100.000	0.000
30	223	100.000	100.000	0.000
30	224	100.000	100.000	0.000
30	225	100.000	100.000	0.000
30	226	100.000	100.000	0.000
35	227	100.000	100.000	0.000
35	228	100.000	100.000	0.000
35	229	100.000	100.000	0.000
35	230	100.000	100.000	0.000
35	231	100.000	100.000	0.000
50	170	100.000	100.000	0.000
50	172	100.000	100.000	0.000
50	173	100.000	100.000	0.000
50	175	100.000	100.000	0.000
50	176	100.000	100.000	0.000
55	177	100.000	100.000	0.000
55	178	100.000	100.000	0.000
55	179	100.000	100.000	0.000
55	180	100.000	100.000	0.000
55	181	100.000	100.000	0.000
100	154	100.000	100.000	0.000
100	155	100.000	100.000	0.000
100	156	100.000	100.000	0.000
100	157	100.000	100.000	0.000
100	158	100.000	100.000	0.000
100	159	100.000	100.000	0.000
100	160	100.000	100.000	0.000
100	161	100.000	100.000	0.000
100	162	100.000	100.000	0.000
100	163	100.000	100.000	0.000
100	164	100.000	100.000	0.000
100	165	100.000	100.000	0.000
100	166	100.000	100.000	0.000
100	168	100.000	100.000	0.000
100	169	100.000	100.000	0.000
100	182	100.000	100.000	0.000
100	183	100.000	100.000	0.000
100	184	100.000	100.000	0.000
100	185	100.000	100.000	0.000
100	186	100.000	100.000	0.000
100	187	100.000	100.000	0.000
100	188	100.000	100.000	0.000
100	189	100.000	100.000	0.000
100	190	100.000	100.000	0.000
105	191	100.000	100.000	0.000
105	192	100.000	100.000	0.000
105	193	100.000	100.000	0.000
105	194	100.000	100.000	0.000
105	195	100.000	100.000	0.000
105	196	100.000	100.000	0.000
Max		100.000	100.000	0.000
Average		100.000	100.000	0.000
Min		100.000	100.000	0.000
Std Dev		0.000	0.000	0.000



13.166_MAX_DC											
Test Site											
Tester											
Test Number											
Max Limit	100	%									
Min Limit		%									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Average	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Max	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
UL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000



## C Appendix: LDR TID Report Data

### Description of identifiers for LDR TID report

IDENTIFIER - Krad(Si)	DESCRIPTION
0	Control Unit
3	3 krad(Si) biased
5	3 krad(Si) unbiased
10	10 krad(Si) biased
15	10 krad(Si) unbiased
30	30 krad(Si) biased
35	30 krad(Si) unbiased
50	50 krad(Si) biased
55	50 krad(Si) unbiased
100	100 krad(Si) biased
105	100 krad(Si) unbiased

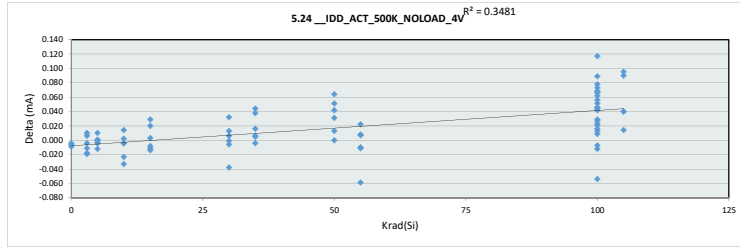
LDR TID Report  
TPS7H5001-SP

LDR TID Report  
TPS7H5001-SP

5.24 IDD\_ACT\_500K\_NOLOAD\_4V

Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	8	8
Min Limit		

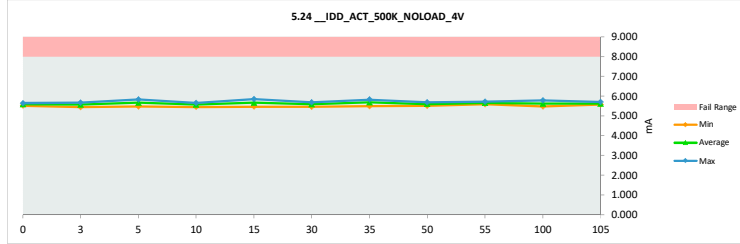
Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.527	5.521	-0.006
0	118	5.630	5.626	-0.004
0	122	5.659	5.650	-0.009
3	61	5.652	5.633	-0.019
3	62	5.685	5.681	-0.004
3	63	5.604	5.610	0.006
3	64	5.652	5.641	-0.011
3	65	5.460	5.442	-0.018
3	66	5.478	5.488	0.010
5	67	5.562	5.562	0.000
5	68	5.709	5.705	-0.004
5	69	5.487	5.488	0.001
5	70	5.834	5.844	0.010
5	71	5.678	5.666	-0.012
5	72	5.758	5.755	-0.003
10	73	5.652	5.619	-0.033
10	74	5.685	5.662	-0.023
10	75	5.604	5.618	0.014
10	76	5.652	5.648	-0.004
10	77	5.460	5.456	-0.004
10	78	5.478	5.480	0.002
15	79	5.562	5.591	0.029
15	80	5.709	5.701	-0.008
15	81	5.487	5.473	-0.014
15	82	5.834	5.854	0.020
15	83	5.678	5.667	-0.011
15	84	5.758	5.761	0.003
30	86	5.652	5.665	0.013
30	89	5.685	5.691	0.006
30	91	5.604	5.636	0.032
30	92	5.652	5.614	-0.038
30	94	5.460	5.459	-0.001
30	95	5.478	5.472	-0.006
35	100	5.562	5.606	0.044
35	101	5.709	5.747	0.038
35	102	5.487	5.493	0.006
35	103	5.834	5.830	-0.004
35	104	5.678	5.683	0.005
35	105	5.758	5.774	0.016
50	106	5.656	5.687	0.031
50	107	5.560	5.602	0.042
50	108	5.451	5.515	0.064
50	109	5.608	5.659	0.051
50	110	5.628	5.641	0.013
50	111	5.510	5.510	0.000
55	113	5.675	5.682	0.007
55	114	5.611	5.600	-0.011
55	115	5.662	5.684	0.022
55	116	5.619	5.627	0.008
55	126	5.741	5.731	-0.010
55	128	5.668	5.609	-0.059
100	129	5.669	5.747	0.078
100	131	5.550	5.496	-0.054
100	132	5.672	5.665	-0.007
100	133	5.670	5.714	0.044
100	134	5.559	5.604	0.045
100	135	5.598	5.586	-0.012
100	138	5.614	5.680	0.066
100	139	5.476	5.544	0.068
100	140	5.587	5.633	0.046
100	141	5.500	5.521	0.021
100	142	5.650	5.723	0.073
100	143	5.625	5.693	0.068
100	144	5.556	5.598	0.042
100	220	5.664	5.677	0.013
100	221	5.540	5.556	0.016
100	224	5.578	5.634	0.056
100	225	5.453	5.480	0.027
100	226	5.736	5.787	0.051
100	228	5.544	5.661	0.117
100	232	5.603	5.692	0.089
100	233	5.611	5.634	0.023
100	236	5.469	5.498	0.029
100	238	5.659	5.721	0.062
100	243	5.520	5.529	0.009
105	244	5.594	5.608	0.014
105	246	5.494	5.589	0.095
105	248	5.642	5.682	0.040
105	374	5.614	5.704	0.090
105	376	5.532	5.572	0.040
105	Max	5.834	5.854	0.020
105	Average	5.611	5.629	0.018
105	Min	5.451	5.442	-0.009
105	Std Dev	0.094	0.095	0.034



5.24 IDD\_ACT\_500K\_NOLOAD\_4V

Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	8	8
Min Limit		

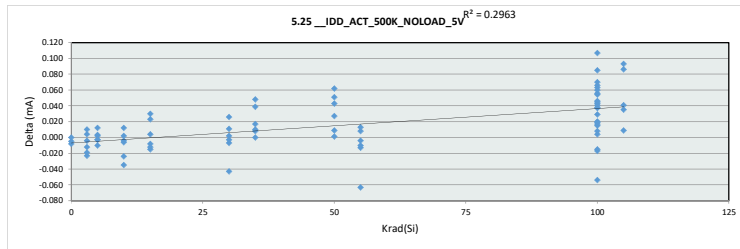
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	5.521	5.442	5.488	5.456	5.473	5.459	5.493	5.510	5.600	5.480	5.572
Average	5.599	5.583	5.670	5.581	5.675	5.590	5.689	5.602	5.656	5.628	5.631
Max	5.650	5.681	5.844	5.662	5.854	5.691	5.830	5.687	5.731	5.787	5.704
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000



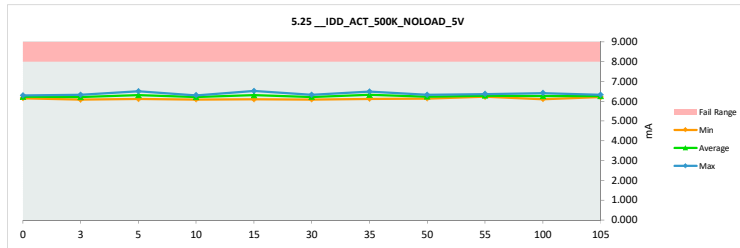
LDR TID Report  
TPS7H5001-SP

5.25 IDD_ACT_500K_NOLOAD_5V	
Test Site	
Tester	
Test Number	
Unit	mA mA
Max Limit	8 8
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.164	6.159	-0.005
0	118	6.264	6.264	0.000
0	122	6.300	6.292	-0.008
3	61	6.297	6.274	-0.023
3	62	6.329	6.325	-0.004
3	63	6.238	6.242	0.004
3	64	6.295	6.283	-0.012
3	65	6.098	6.079	-0.019
3	66	6.117	6.127	0.010
5	67	6.196	6.199	0.003
5	68	6.344	6.341	-0.003
5	69	6.117	6.119	0.002
5	70	6.492	6.504	0.012
5	71	6.316	6.306	-0.010
5	72	6.394	6.391	-0.003
10	73	6.297	6.262	-0.035
10	74	6.329	6.305	-0.024
10	75	6.238	6.250	0.012
10	76	6.295	6.289	-0.006
10	77	6.098	6.094	-0.004
10	78	6.117	6.119	0.002
15	79	6.196	6.226	0.030
15	80	6.344	6.336	-0.008
15	81	6.117	6.102	-0.015
15	82	6.492	6.515	0.023
15	83	6.316	6.304	-0.012
15	84	6.394	6.398	0.004
30	86	6.297	6.308	0.011
30	89	6.329	6.331	0.002
30	91	6.238	6.264	0.026
30	92	6.295	6.252	-0.043
30	94	6.098	6.095	-0.003
30	95	6.117	6.110	-0.007
35	100	6.196	6.244	0.048
35	101	6.344	6.383	0.039
35	102	6.117	6.127	0.010
35	103	6.492	6.492	0.000
35	104	6.316	6.324	0.008
35	105	6.394	6.411	0.017
50	106	6.299	6.326	0.027
50	107	6.201	6.244	0.043
50	108	6.088	6.150	0.062
50	109	6.248	6.299	0.051
50	110	6.266	6.275	0.009
50	111	6.146	6.147	0.001
55	113	6.312	6.308	-0.004
55	114	6.244	6.231	-0.013
55	115	6.296	6.309	0.013
55	116	6.256	6.264	0.008
55	126	6.382	6.372	-0.010
55	128	6.312	6.249	-0.063
100	129	6.307	6.377	0.070
100	131	6.184	6.130	-0.054
100	132	6.312	6.297	-0.015
100	133	6.309	6.350	0.041
100	134	6.198	6.241	0.043
100	135	6.239	6.222	-0.017
100	138	6.249	6.315	0.066
100	139	6.107	6.167	0.060
100	140	6.221	6.266	0.045
100	141	6.138	6.153	0.015
100	142	6.285	6.341	0.056
100	143	6.260	6.323	0.063
100	144	6.190	6.227	0.037
100	220	6.301	6.309	0.008
100	221	6.177	6.194	0.017
100	224	6.213	6.259	0.046
100	225	6.093	6.112	0.019
100	226	6.382	6.420	0.038
100	228	6.179	6.286	0.107
100	232	6.242	6.327	0.085
100	233	6.267	6.287	0.020
100	236	6.105	6.134	0.029
100	238	6.299	6.353	0.054
100	243	6.154	6.158	0.004
105	244	6.229	6.238	0.009
105	246	6.127	6.220	0.093
105	248	6.279	6.314	0.035
105	374	6.248	6.334	0.086
105	376	6.172	6.213	0.041
105	Max	6.492	6.515	0.107
105	Average	6.249	6.265	0.016
105	Min	6.088	6.079	-0.063
105	Std Dev	0.097	0.097	0.032



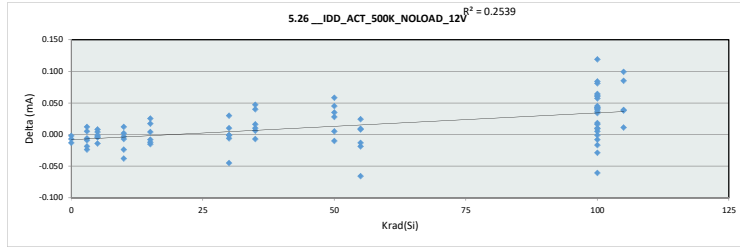
5.25 IDD_ACT_5	
Test Site	
Tester	
Test Number	
Max Limit	8 mA
Min Limit	
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	6.159 6.079 6.119 6.094 6.102 6.095 6.127 6.147 6.231 6.112 6.213
Average	6.238 6.222 6.310 6.220 6.314 6.227 6.330 6.240 6.289 6.260 6.264
Max	6.292 6.325 6.504 6.305 6.515 6.331 6.492 6.326 6.372 6.420 6.334
UL	8.000 8.000 8.000 8.000 8.000 8.000 8.000 8.000 8.000 8.000 8.000



LDR TID Report  
TPS7H5001-SP

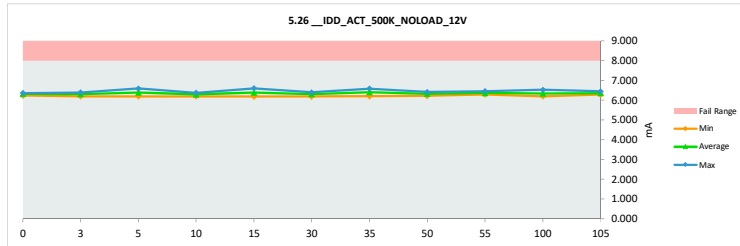
5.26 IDD_ACT_500K_NOLOAD_12V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	8
Min Limit	8

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.237	6.230	-0.007
0	118	6.359	6.357	-0.002
0	122	6.374	6.361	-0.013
3	61	6.383	6.359	-0.024
3	62	6.393	6.387	-0.006
3	63	6.298	6.303	0.005
3	64	6.376	6.367	-0.009
3	65	6.206	6.187	-0.019
3	66	6.188	6.200	0.012
5	67	6.270	6.269	-0.001
5	68	6.415	6.411	-0.004
5	69	6.185	6.189	0.004
5	70	6.582	6.590	0.008
5	71	6.394	6.380	-0.014
5	72	6.469	6.465	-0.004
10	73	6.383	6.345	-0.038
10	74	6.393	6.369	-0.024
10	75	6.298	6.310	0.012
10	76	6.376	6.369	-0.007
10	77	6.206	6.203	-0.003
10	78	6.188	6.190	0.002
15	79	6.270	6.295	0.025
15	80	6.415	6.407	-0.008
15	81	6.185	6.173	-0.012
15	82	6.582	6.599	0.017
15	83	6.394	6.379	-0.015
15	84	6.469	6.473	0.004
30	86	6.383	6.393	0.010
30	89	6.393	6.392	-0.001
30	91	6.398	6.328	-0.070
30	92	6.376	6.331	-0.045
30	94	6.206	6.205	-0.001
30	95	6.188	6.182	-0.006
35	100	6.270	6.317	0.047
35	101	6.415	6.455	0.040
35	102	6.185	6.195	0.010
35	103	6.582	6.575	-0.007
35	104	6.394	6.400	0.006
35	105	6.469	6.485	0.016
50	106	6.381	6.409	0.028
50	107	6.290	6.325	0.035
50	108	6.179	6.237	0.058
50	109	6.309	6.354	0.045
50	110	6.361	6.366	0.005
50	111	6.222	6.212	-0.010
55	113	6.408	6.416	0.008
55	114	6.308	6.289	-0.019
55	115	6.357	6.381	0.024
55	116	6.323	6.332	0.009
55	126	6.462	6.449	-0.013
55	128	6.367	6.301	-0.066
100	129	6.404	6.488	0.084
100	131	6.255	6.194	-0.061
100	132	6.384	6.367	-0.017
100	133	6.411	6.448	0.037
100	134	6.274	6.314	0.040
100	135	6.308	6.279	-0.029
100	138	6.326	6.383	0.057
100	139	6.186	6.246	0.060
100	140	6.305	6.347	0.042
100	141	6.207	6.216	0.009
100	142	6.360	6.422	0.062
100	143	6.332	6.396	0.064
100	144	6.266	6.300	0.034
100	220	6.376	6.375	-0.001
100	221	6.246	6.251	0.005
100	224	6.294	6.339	0.045
100	225	6.205	6.215	0.010
100	226	6.481	6.524	0.043
100	228	6.263	6.382	0.119
100	232	6.324	6.405	0.081
100	233	6.358	6.374	0.016
100	236	6.192	6.210	0.018
100	238	6.367	6.409	0.042
100	243	6.241	6.233	-0.008
105	244	6.313	6.324	0.011
105	246	6.193	6.278	0.085
105	248	6.370	6.408	0.038
105	374	6.345	6.444	0.099
105	376	6.245	6.287	0.039
	Max	6.582	6.590	0.119
	Average	6.328	6.342	0.014
	Min	6.179	6.173	-0.066
	Std Dev	0.097	0.099	0.034



5.26 IDD_ACT_500K_NOLOAD_12V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	8
Min Limit	8

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.230	6.187	6.189	6.190	6.173	6.182	6.195	6.212	6.289	6.194	6.278
Average	6.316	6.301	6.384	6.298	6.388	6.305	6.405	6.317	6.361	6.338	6.348
Max	6.361	6.387	6.590	6.369	6.599	6.393	6.575	6.409	6.449	6.524	6.444
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

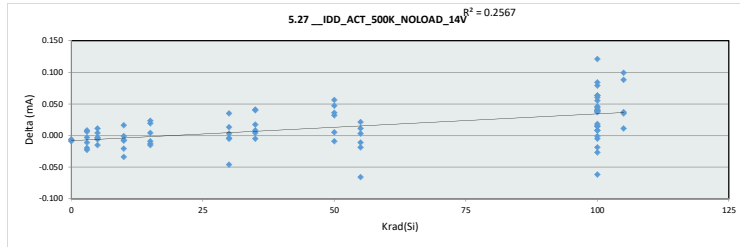


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5.27 IDD\_ACT\_500K\_NOLOAD\_14V

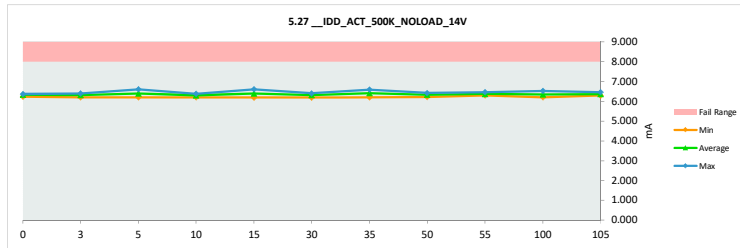
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	8	8
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.241	6.234	-0.007
0	118	6.365	6.359	-0.006
0	122	6.377	6.368	-0.009
3	61	6.385	6.362	-0.023
3	62	6.395	6.392	-0.003
3	63	6.301	6.307	0.006
3	64	6.381	6.370	-0.011
3	65	6.212	6.192	-0.020
3	66	6.194	6.202	0.008
5	67	6.276	6.272	-0.004
5	68	6.420	6.414	-0.006
5	69	6.191	6.195	0.004
5	70	6.585	6.596	0.011
5	71	6.396	6.381	-0.015
5	72	6.472	6.469	-0.003
10	73	6.385	6.351	-0.034
10	74	6.395	6.374	-0.021
10	75	6.301	6.317	0.016
10	76	6.381	6.373	-0.008
10	77	6.212	6.205	-0.007
10	78	6.194	6.193	-0.001
15	79	6.276	6.299	0.023
15	80	6.420	6.411	-0.009
15	81	6.191	6.178	-0.013
15	82	6.585	6.604	0.019
15	83	6.396	6.381	-0.015
15	84	6.472	6.476	0.004
30	86	6.385	6.398	0.013
30	89	6.395	6.398	0.003
30	91	6.301	6.336	0.035
30	92	6.381	6.335	-0.046
30	94	6.212	6.208	-0.004
30	95	6.194	6.189	-0.005
35	100	6.276	6.316	0.040
35	101	6.420	6.461	0.041
35	102	6.191	6.199	0.008
35	103	6.585	6.580	-0.005
35	104	6.396	6.400	0.004
35	105	6.472	6.489	0.017
50	106	6.384	6.416	0.032
50	107	6.294	6.330	0.036
50	108	6.186	6.242	0.056
50	109	6.313	6.360	0.047
50	110	6.365	6.370	0.005
50	111	6.226	6.217	-0.009
55	113	6.416	6.419	0.003
55	114	6.312	6.293	-0.019
55	115	6.364	6.385	0.021
55	116	6.326	6.337	0.011
55	126	6.466	6.455	-0.011
55	128	6.373	6.307	-0.066
100	129	6.408	6.492	0.084
100	131	6.257	6.195	-0.062
100	132	6.389	6.370	-0.019
100	133	6.413	6.452	0.039
100	134	6.279	6.317	0.038
100	135	6.310	6.283	-0.027
100	138	6.330	6.390	0.060
100	139	6.194	6.249	0.055
100	140	6.309	6.349	0.040
100	141	6.211	6.219	0.008
100	142	6.366	6.429	0.063
100	143	6.337	6.400	0.063
100	144	6.267	6.304	0.037
100	220	6.380	6.379	-0.001
100	221	6.246	6.254	0.008
100	224	6.298	6.342	0.044
100	225	6.207	6.221	0.014
100	226	6.485	6.526	0.041
100	228	6.264	6.385	0.121
100	232	6.329	6.408	0.079
100	233	6.363	6.378	0.015
100	236	6.196	6.214	0.018
100	238	6.370	6.416	0.046
100	243	6.243	6.238	-0.005
105	244	6.318	6.329	0.011
105	246	6.196	6.284	0.088
105	248	6.374	6.409	0.035
105	374	6.350	6.449	0.099
105	376	6.253	6.290	0.037
	Max	6.585	6.604	0.019
	Average	6.332	6.346	0.014
	Min	6.186	6.178	-0.008
	Std Dev	0.097	0.099	0.034



Test Site		
Tester		
Test Number		
Max Limit	8	mA
Min Limit		

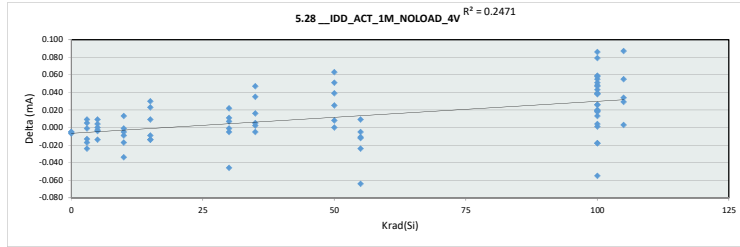
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.234	6.192	6.195	6.193	6.178	6.189	6.199	6.217	6.293	6.195	6.284
Average	6.320	6.304	6.388	6.302	6.392	6.311	6.408	6.323	6.366	6.342	6.352
Max	6.368	6.392	6.596	6.374	6.604	6.398	6.580	6.416	6.455	6.526	6.449
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000



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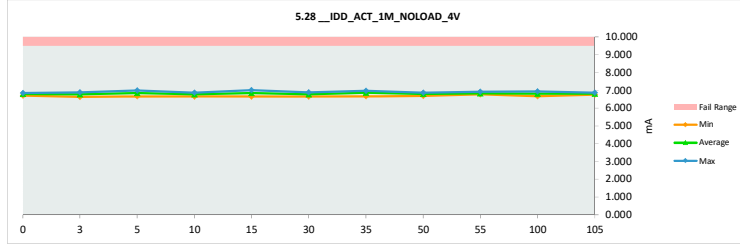
5.28 IDD_ACT_1M_NOLOAD_4V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.713	6.706	-0.007
0	118	6.814	6.809	-0.005
0	122	6.853	6.848	-0.005
3	61	6.844	6.820	-0.024
3	62	6.880	6.879	-0.001
3	63	6.780	6.785	0.005
3	64	6.842	6.829	-0.013
3	65	6.650	6.633	-0.017
3	66	6.674	6.683	0.009
5	67	6.738	6.738	0.000
5	68	6.898	6.894	-0.004
5	69	6.662	6.666	0.004
5	70	6.985	6.994	0.009
5	71	6.861	6.847	-0.014
5	72	6.940	6.937	-0.003
10	73	6.844	6.810	-0.034
10	74	6.880	6.863	-0.017
10	75	6.780	6.793	0.013
10	76	6.842	6.833	-0.009
10	77	6.650	6.645	-0.005
10	78	6.674	6.673	-0.001
15	79	6.738	6.768	0.030
15	80	6.898	6.889	-0.009
15	81	6.662	6.648	-0.014
15	82	6.985	7.008	0.023
15	83	6.861	6.847	-0.014
15	84	6.940	6.949	0.009
30	86	6.844	6.855	0.011
30	89	6.880	6.887	0.007
30	91	6.780	6.802	0.022
30	92	6.842	6.796	-0.046
30	94	6.650	6.649	-0.001
30	95	6.674	6.669	-0.005
35	100	6.738	6.785	0.047
35	101	6.898	6.933	0.035
35	102	6.662	6.667	0.005
35	103	6.985	6.980	-0.005
35	104	6.861	6.863	0.002
35	105	6.940	6.956	0.016
50	106	6.842	6.867	0.025
50	107	6.747	6.786	0.039
50	108	6.636	6.699	0.063
50	109	6.796	6.847	0.051
50	110	6.814	6.822	0.008
50	111	6.692	6.692	0.000
55	113	6.858	6.834	-0.024
55	114	6.784	6.772	-0.012
55	115	6.842	6.837	-0.005
55	116	6.799	6.808	0.009
55	126	6.925	6.914	-0.011
55	128	6.864	6.800	-0.064
100	129	6.857	6.908	0.051
100	131	6.741	6.686	-0.055
100	132	6.865	6.847	-0.018
100	133	6.864	6.902	0.038
100	134	6.753	6.792	0.039
100	135	6.786	6.768	-0.018
100	138	6.801	6.860	0.059
100	139	6.662	6.717	0.055
100	140	6.771	6.810	0.039
100	141	6.685	6.698	0.013
100	142	6.840	6.887	0.047
100	143	6.803	6.861	0.058
100	144	6.747	6.773	0.026
100	220	6.847	6.848	0.001
100	221	6.720	6.738	0.018
100	224	6.757	6.800	0.043
100	225	6.649	6.668	0.019
100	226	6.923	6.942	0.019
100	228	6.730	6.816	0.086
100	232	6.791	6.870	0.079
100	233	6.795	6.815	0.020
100	236	6.646	6.672	0.026
100	238	6.851	6.899	0.048
100	243	6.700	6.704	0.004
105	244	6.779	6.782	0.003
105	246	6.681	6.768	0.087
105	248	6.831	6.860	0.029
105	374	6.789	6.844	0.055
105	376	6.720	6.754	0.034
	Max	6.985	7.008	0.027
	Average	6.795	6.808	0.012
	Min	6.636	6.633	-0.064
	Std Dev	0.091	0.091	0.030



5.28 IDD_ACT	
Test Site	
Tester	
Test Number	
Max Limit	9.5 mA
Min Limit	mA

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.706	6.633	6.666	6.645	6.648	6.649	6.667	6.692	6.772	6.668	6.754
Average	6.788	6.772	6.846	6.770	6.852	6.776	6.864	6.786	6.828	6.803	6.802
Max	6.848	6.879	6.994	6.863	7.008	6.887	6.980	6.867	6.914	6.942	6.860
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

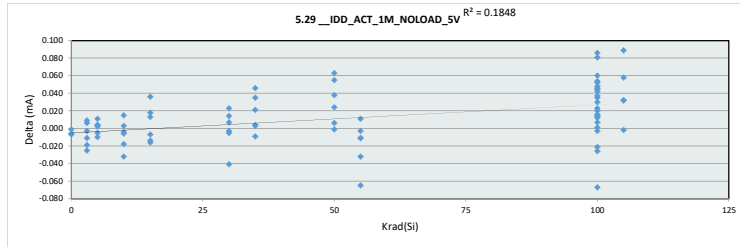




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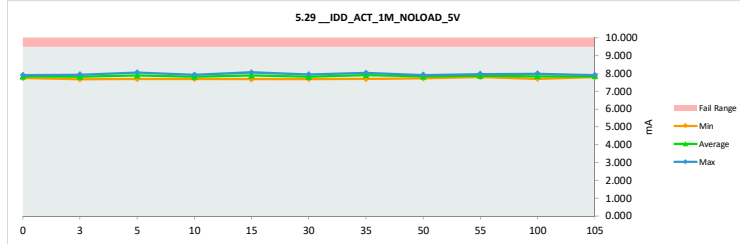
5.29 IDD_ACT_1M_NOLOAD_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.759	7.752	-0.007
0	118	7.857	7.856	-0.001
0	122	7.910	7.904	-0.006
3	61	7.901	7.876	-0.025
3	62	7.943	7.940	-0.003
3	63	7.821	7.827	0.006
3	64	7.894	7.883	-0.011
3	65	7.698	7.679	-0.019
3	66	7.724	7.733	0.009
5	67	7.783	7.787	0.004
5	68	7.945	7.940	-0.005
5	69	7.699	7.703	0.004
5	70	8.045	8.056	0.011
5	71	7.908	7.898	-0.010
5	72	7.986	7.898	-0.088
10	73	7.901	7.869	-0.032
10	74	7.943	7.925	-0.018
10	75	7.821	7.836	0.015
10	76	7.894	7.888	-0.006
10	77	7.698	7.694	-0.004
10	78	7.724	7.727	0.003
15	79	7.783	7.819	0.036
15	80	7.945	7.938	-0.007
15	81	7.699	7.683	-0.016
15	82	8.045	8.063	0.018
15	83	7.908	7.894	-0.014
15	84	7.986	7.999	0.013
30	86	7.901	7.915	0.014
30	89	7.943	7.950	0.007
30	91	7.821	7.844	0.023
30	92	7.894	7.853	-0.041
30	94	7.698	7.695	-0.003
30	95	7.724	7.719	-0.005
35	100	7.783	7.829	0.046
35	101	7.945	7.980	0.035
35	102	7.699	7.702	0.003
35	103	8.045	8.036	-0.009
35	104	7.908	7.912	0.004
35	105	7.986	8.007	0.021
50	106	7.899	7.923	0.024
50	107	7.804	7.842	0.038
50	108	7.681	7.744	0.063
50	109	7.846	7.901	0.055
50	110	7.865	7.871	0.006
50	111	7.739	7.738	-0.001
55	113	7.910	7.878	-0.032
55	114	7.822	7.811	-0.011
55	115	7.887	7.884	-0.003
55	116	7.845	7.856	0.011
55	126	7.986	7.975	-0.011
55	128	7.922	7.857	-0.065
100	129	7.906	7.954	0.048
100	131	7.791	7.724	-0.067
100	132	7.915	7.894	-0.021
100	133	7.915	7.945	0.030
100	134	7.806	7.843	0.037
100	135	7.839	7.813	-0.026
100	138	7.849	7.909	0.060
100	139	7.701	7.753	0.052
100	140	7.813	7.848	0.035
100	141	7.736	7.743	0.007
100	142	7.890	7.936	0.046
100	143	7.848	7.902	0.054
100	144	7.797	7.820	0.023
100	220	7.895	7.892	-0.003
100	221	7.772	7.784	0.012
100	224	7.796	7.838	0.042
100	225	7.699	7.713	0.014
100	226	7.981	7.995	0.014
100	228	7.775	7.861	0.086
100	232	7.840	7.921	0.081
100	233	7.864	7.880	0.016
100	236	7.688	7.708	0.020
100	238	7.906	7.950	0.044
100	243	7.744	7.745	0.001
105	244	7.824	7.822	-0.002
105	246	7.724	7.813	0.089
105	248	7.882	7.914	0.032
105	374	7.831	7.889	0.058
105	376	7.772	7.894	0.032
105	Max	8.045	8.063	0.089
105	Average	7.845	7.856	0.011
105	Min	7.681	7.679	-0.067
105	Std Dev	0.095	0.094	0.030



5.29 IDD_ACT	
Test Site	
Tester	
Test Number	
Max Limit	9.5 mA
Min Limit	9.5 mA

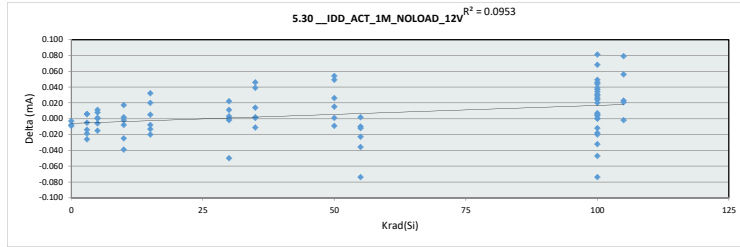
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.752	7.679	7.703	7.694	7.683	7.695	7.702	7.738	7.811	7.708	7.804
Average	7.837	7.823	7.895	7.823	7.899	7.829	7.911	7.837	7.877	7.849	7.848
Max	7.904	7.940	8.056	7.925	8.063	7.950	8.036	7.923	7.975	7.995	7.914
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



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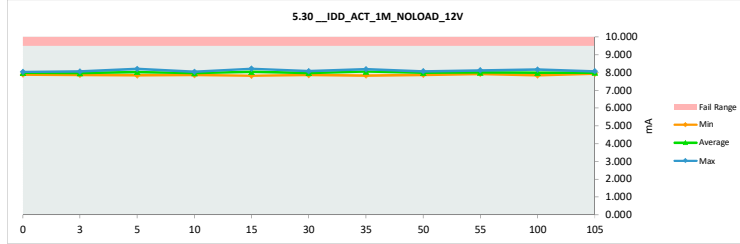
5.30 IDD_ACT_1M_NOLOAD_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	9.5	9.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.895	7.886	-0.009
0	118	8.028	8.025	-0.003
0	122	8.047	8.039	-0.008
3	61	8.061	8.035	-0.026
3	62	8.065	8.060	-0.005
3	63	7.930	7.936	0.006
3	64	8.045	8.031	-0.014
3	65	7.890	7.871	-0.019
3	66	7.860	7.866	0.006
5	67	7.915	7.915	0.000
5	68	8.078	8.072	-0.006
5	69	7.829	7.837	0.008
5	70	8.193	8.204	0.011
5	71	8.050	8.035	-0.015
5	72	8.125	8.126	0.001
10	73	8.061	8.022	-0.039
10	74	8.065	8.040	-0.025
10	75	7.930	7.947	0.017
10	76	8.045	8.037	-0.008
10	77	7.890	7.888	-0.002
10	78	7.860	7.862	0.002
15	79	7.915	7.947	0.032
15	80	8.078	8.070	-0.008
15	81	7.829	7.816	-0.013
15	82	8.193	8.213	0.020
15	83	8.050	8.030	-0.020
15	84	8.125	8.130	0.005
30	86	8.061	8.072	0.011
30	89	8.065	8.068	0.003
30	91	7.930	7.952	0.022
30	92	8.045	7.995	-0.050
30	94	7.890	7.890	0.000
30	95	7.860	7.858	-0.002
35	100	7.915	7.961	0.046
35	101	8.078	8.117	0.039
35	102	7.829	7.831	0.002
35	103	8.193	8.182	-0.011
35	104	8.050	8.051	0.001
35	105	8.125	8.139	0.014
50	106	8.051	8.066	0.015
50	107	7.965	7.991	0.026
50	108	7.846	7.900	0.054
50	109	7.961	8.010	0.049
50	110	8.039	8.040	0.001
50	111	7.876	7.867	-0.009
55	113	8.083	8.047	-0.036
55	114	7.943	7.920	-0.023
55	115	8.003	7.993	-0.010
55	116	7.974	7.976	0.002
55	126	8.130	8.118	-0.012
55	128	8.026	7.952	-0.074
100	129	8.083	8.122	0.039
100	131	7.918	7.844	-0.074
100	132	8.048	8.016	-0.032
100	133	8.100	8.124	0.024
100	134	7.943	7.974	0.031
100	135	7.967	7.920	-0.047
100	138	7.989	8.038	0.049
100	139	7.851	7.895	0.044
100	140	7.965	7.994	0.029
100	141	7.869	7.857	-0.012
100	142	8.029	8.066	0.037
100	143	7.986	8.032	0.046
100	144	7.929	7.949	0.020
100	220	8.031	8.011	-0.020
100	221	7.896	7.896	0.000
100	224	7.944	7.978	0.034
100	225	7.897	7.902	0.005
100	226	8.156	8.160	0.004
100	228	7.930	8.011	0.081
100	232	7.990	8.058	0.068
100	233	8.027	8.034	0.007
100	236	7.848	7.848	0.000
100	238	8.031	8.057	0.026
100	243	7.901	7.883	-0.018
105	244	7.976	7.974	-0.002
105	246	7.846	7.925	0.079
105	248	8.047	8.068	0.021
105	374	8.005	8.061	0.056
105	376	7.918	7.941	0.023
105	Max	8.193	8.213	0.020
105	Average	7.989	7.995	0.006
105	Min	7.829	7.816	-0.013
105	Std Dev	0.095	0.095	0.030



5.30 IDD_ACT		
Test Site		
Tester		
Test Number		
Max Limit	9.5	mA
Min Limit		mA
Krad(Si)	0	3

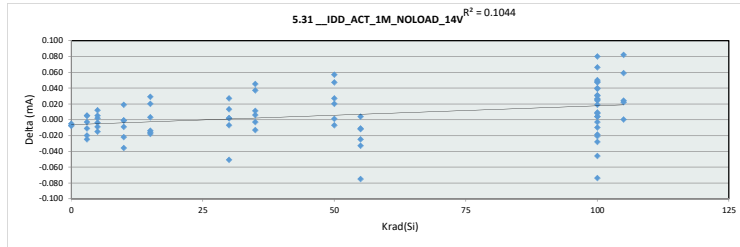
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.886	7.866	7.837	7.862	7.816	7.858	7.831	7.867	7.920	7.844	7.925
Average	7.983	7.967	8.032	7.966	8.034	7.973	8.047	7.979	8.001	7.986	7.994
Max	8.039	8.060	8.204	8.040	8.213	8.072	8.182	8.066	8.118	8.160	8.068
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



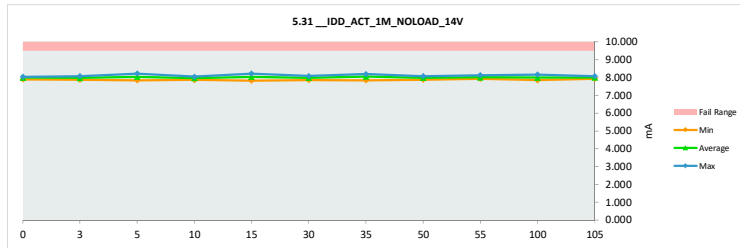
LDR TID Report  
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5.31 IDD_ACT_1M_NOLOAD_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.899	7.892	-0.007
0	118	8.031	8.026	-0.005
0	122	8.050	8.042	-0.008
3	61	8.064	8.039	-0.025
3	62	8.068	8.065	-0.003
3	63	7.933	7.938	0.005
3	64	8.048	8.037	-0.011
3	65	7.892	7.872	-0.020
3	66	7.867	7.872	0.005
5	67	7.922	7.918	-0.004
5	68	8.084	8.075	-0.009
5	69	7.833	7.838	0.005
5	70	8.198	8.210	0.012
5	71	8.054	8.039	-0.015
5	72	8.130	8.132	0.002
10	73	8.064	8.028	-0.036
10	74	8.068	8.046	-0.022
10	75	7.933	7.952	0.019
10	76	8.048	8.039	-0.009
10	77	7.892	7.891	-0.001
10	78	7.867	7.866	-0.001
15	79	7.922	7.951	0.029
15	80	8.084	8.070	-0.014
15	81	7.833	7.817	-0.016
15	82	8.198	8.218	0.020
15	83	8.054	8.036	-0.018
15	84	8.130	8.133	0.003
30	86	8.064	8.077	0.013
30	89	8.068	8.070	0.002
30	91	7.933	7.960	0.027
30	92	8.048	7.997	-0.051
30	94	7.892	7.894	0.002
30	95	7.867	7.860	-0.007
35	100	7.922	7.967	0.045
35	101	8.084	8.121	0.037
35	102	7.833	7.839	0.006
35	103	8.198	8.185	-0.013
35	104	8.054	8.051	-0.003
35	105	8.130	8.141	0.011
50	106	8.052	8.072	0.020
50	107	7.968	7.995	0.027
50	108	7.846	7.903	0.057
50	109	7.959	8.016	0.047
50	110	8.044	8.045	0.001
50	111	7.879	7.872	-0.007
55	113	8.085	8.052	-0.033
55	114	7.950	7.925	-0.025
55	115	8.008	7.996	-0.012
55	116	7.979	7.983	0.004
55	126	8.134	8.123	-0.011
55	128	8.031	7.956	-0.075
100	129	8.085	8.124	0.039
100	131	7.921	7.847	-0.074
100	132	8.050	8.022	-0.028
100	133	8.103	8.129	0.026
100	134	7.951	7.977	0.026
100	135	7.971	7.925	-0.046
100	138	7.993	8.043	0.050
100	139	7.853	7.900	0.047
100	140	7.966	7.996	0.030
100	141	7.870	7.860	-0.010
100	142	8.030	8.070	0.040
100	143	7.990	8.038	0.048
100	144	7.934	7.953	0.019
100	220	8.033	8.014	-0.019
100	221	7.901	7.898	-0.003
100	224	7.949	7.980	0.031
100	225	7.900	7.908	0.008
100	226	8.159	8.163	0.004
100	228	7.933	8.013	0.080
100	232	7.996	8.062	0.066
100	233	8.030	8.039	0.009
100	236	7.852	7.856	0.004
100	238	8.036	8.060	0.024
100	243	7.908	7.887	-0.021
105	244	7.978	7.978	0.000
105	246	7.848	7.930	0.082
105	248	8.050	8.072	0.022
105	374	8.006	8.065	0.059
105	376	7.921	7.945	0.024
105	Max	8.198	8.218	0.020
105	Average	7.993	7.999	0.006
105	Min	7.833	7.817	-0.016
105	Std Dev	0.095	0.095	0.030



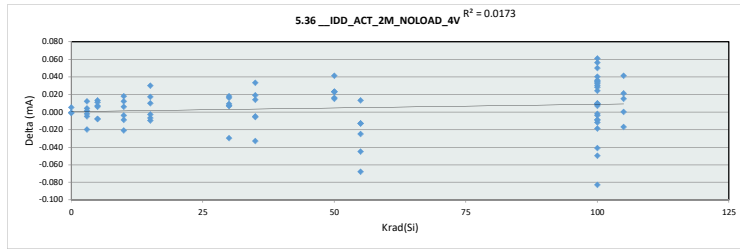
5.31 IDD_ACT											
Test Site											
Tester											
Test Number											
Max Limit	9.5 mA										
Min Limit	9.5 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.892	7.872	7.838	7.866	7.817	7.860	7.839	7.872	7.925	7.847	7.930
Average	7.987	7.971	8.035	7.970	8.038	7.976	8.051	7.984	8.006	7.990	7.998
Max	8.042	8.065	8.210	8.046	8.218	8.077	8.185	8.072	8.123	8.163	8.072
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



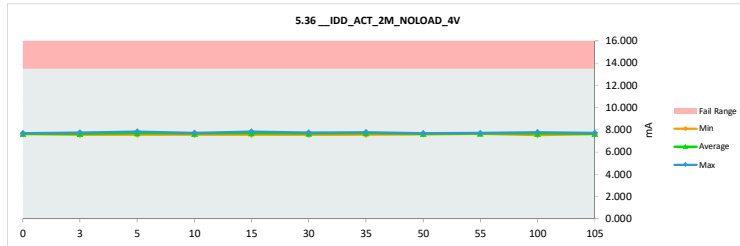
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5.36 IDD_ACT_2M_NOLOAD_4V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	13.5	13.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.611	7.610	-0.001
0	118	7.639	7.638	-0.001
0	122	7.699	7.704	0.005
3	61	7.675	7.655	-0.020
3	62	7.741	7.745	0.004
3	63	7.672	7.684	0.012
3	64	7.717	7.715	-0.002
3	65	7.547	7.542	-0.005
3	66	7.570	7.571	0.001
5	67	7.560	7.566	0.006
5	68	7.738	7.730	-0.008
5	69	7.561	7.568	0.007
5	70	7.820	7.833	0.013
5	71	7.711	7.703	-0.008
5	72	7.761	7.772	0.011
10	73	7.675	7.654	-0.021
10	74	7.741	7.732	-0.009
10	75	7.672	7.690	0.018
10	76	7.717	7.713	-0.004
10	77	7.547	7.553	0.006
10	78	7.570	7.582	0.012
15	79	7.560	7.590	0.030
15	80	7.738	7.735	-0.003
15	81	7.561	7.551	-0.010
15	82	7.820	7.837	0.017
15	83	7.711	7.704	-0.007
15	84	7.761	7.771	0.010
30	86	7.675	7.691	0.016
30	89	7.741	7.748	0.007
30	91	7.672	7.690	0.018
30	92	7.717	7.687	-0.030
30	94	7.547	7.556	0.009
30	95	7.570	7.577	0.007
35	100	7.560	7.593	0.033
35	101	7.738	7.757	0.019
35	102	7.561	7.556	-0.005
35	103	7.820	7.787	-0.033
35	104	7.711	7.705	-0.006
35	105	7.761	7.775	0.014
50	106	7.674	7.697	0.023
50	107	7.574	7.597	0.023
50	108	7.599	7.614	0.015
50	109	7.612	7.653	0.041
50	110	7.628	7.651	0.023
50	111	7.608	7.624	0.016
55	113	7.748	7.703	-0.045
55	114	7.650	7.637	-0.013
55	115	7.663	7.650	-0.013
55	116	7.620	7.633	0.013
55	126	7.735	7.710	-0.025
55	128	7.701	7.633	-0.068
100	129	7.675	7.715	0.040
100	131	7.618	7.568	-0.050
100	132	7.725	7.684	-0.041
100	133	7.688	7.718	0.030
100	134	7.594	7.628	0.034
100	135	7.779	7.696	-0.083
100	138	7.630	7.665	0.035
100	139	7.573	7.605	0.032
100	140	7.622	7.672	0.050
100	141	7.571	7.562	-0.009
100	142	7.680	7.704	0.024
100	143	7.644	7.680	0.036
100	144	7.602	7.609	0.007
100	220	7.705	7.701	-0.004
100	221	7.583	7.581	-0.002
100	224	7.609	7.637	0.028
100	225	7.594	7.585	-0.009
100	226	7.764	7.773	0.009
100	228	7.589	7.650	0.061
100	232	7.657	7.713	0.056
100	233	7.687	7.697	0.010
100	236	7.581	7.569	-0.012
100	238	7.749	7.758	0.009
100	243	7.549	7.530	-0.019
105	244	7.622	7.605	-0.017
105	246	7.567	7.608	0.041
105	248	7.712	7.712	0.000
105	374	7.640	7.655	0.015
105	376	7.606	7.627	0.021
105	Max	7.820	7.837	0.061
105	Average	7.657	7.662	0.005
105	Min	7.547	7.530	-0.083
105	Std Dev	0.075	0.072	0.025



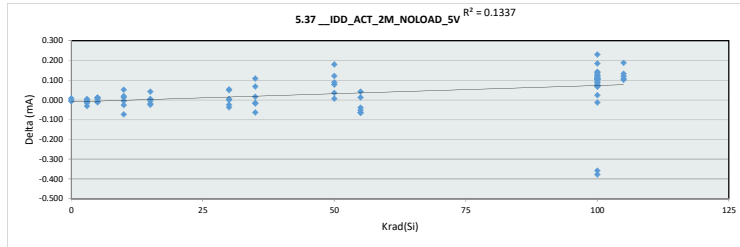
5.36 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.610	7.542	7.566	7.553	7.551	7.556	7.556	7.597	7.633	7.530	7.605
Average	7.651	7.652	7.695	7.654	7.698	7.658	7.696	7.639	7.661	7.654	7.641
Max	7.704	7.745	7.833	7.732	7.837	7.748	7.787	7.697	7.710	7.773	7.712
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



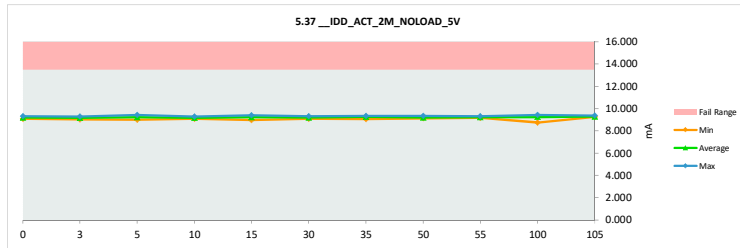
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TPS7H5001-SP

5.37 IDD_ACT_2M_NOLOAD_5V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	13.5	13.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.103	9.096	-0.007
0	118	9.228	9.226	-0.002
0	122	9.320	9.328	0.008
3	61	9.283	9.251	-0.032
3	62	9.313	9.303	-0.010
3	63	9.154	9.159	0.005
3	64	9.265	9.260	-0.005
3	65	9.044	9.029	-0.015
3	66	9.104	9.104	0.000
5	67	9.101	9.112	0.011
5	68	9.314	9.303	-0.011
5	69	8.992	9.003	0.011
5	70	9.426	9.428	0.002
5	71	9.278	9.266	-0.012
5	72	9.372	9.383	0.011
10	73	9.283	9.209	-0.074
10	74	9.313	9.286	-0.027
10	75	9.154	9.173	0.019
10	76	9.265	9.260	-0.005
10	77	9.044	9.096	0.052
10	78	9.104	9.116	0.012
15	79	9.101	9.143	0.042
15	80	9.314	9.316	0.002
15	81	8.992	8.987	-0.005
15	82	9.426	9.406	-0.020
15	83	9.278	9.254	-0.024
15	84	9.372	9.377	0.005
30	86	9.283	9.258	-0.025
30	89	9.313	9.313	0.000
30	91	9.154	9.204	0.050
30	92	9.265	9.228	-0.037
30	94	9.044	9.097	0.053
30	95	9.104	9.109	0.005
35	100	9.101	9.209	0.108
35	101	9.314	9.329	0.015
35	102	8.992	9.060	0.068
35	103	9.426	9.363	-0.063
35	104	9.278	9.263	-0.015
35	105	9.372	9.353	-0.019
50	106	9.228	9.349	0.121
50	107	9.108	9.143	0.035
50	108	8.983	9.163	0.180
50	109	9.204	9.210	0.006
50	110	9.218	9.296	0.078
50	111	9.049	9.137	0.088
55	113	9.302	9.315	0.013
55	114	9.183	9.224	0.041
55	115	9.229	9.177	-0.052
55	116	9.227	9.188	-0.039
55	126	9.318	9.252	-0.066
55	128	9.292	9.227	-0.065
100	129	9.300	9.405	0.105
100	131	9.204	9.191	-0.013
100	132	9.325	9.349	0.024
100	133	9.296	9.419	0.123
100	134	9.216	9.323	0.107
100	135	9.137	8.759	-0.378
100	138	9.220	9.362	0.142
100	139	9.058	9.179	0.121
100	140	9.188	9.292	0.104
100	141	9.084	9.154	0.070
100	142	9.282	8.924	-0.358
100	143	9.187	9.325	0.138
100	144	9.160	9.272	0.112
100	220	9.268	9.335	0.067
100	221	9.121	9.212	0.091
100	224	9.143	9.230	0.087
100	225	9.060	9.162	0.102
100	226	9.310	9.441	0.131
100	228	9.128	9.358	0.230
100	232	9.228	9.412	0.184
100	233	9.251	9.318	0.067
100	236	8.978	9.083	0.105
100	238	9.312	9.423	0.111
100	243	9.090	9.169	0.079
105	244	9.160	9.261	0.101
105	246	9.090	9.277	0.187
105	248	9.259	9.378	0.119
105	374	9.192	9.325	0.133
105	376	9.151	9.258	0.107
105	Max	9.426	9.441	0.015
105	Average	9.204	9.237	0.033
105	Min	8.978	8.759	-0.378
105	Std Dev	0.113	0.124	0.093



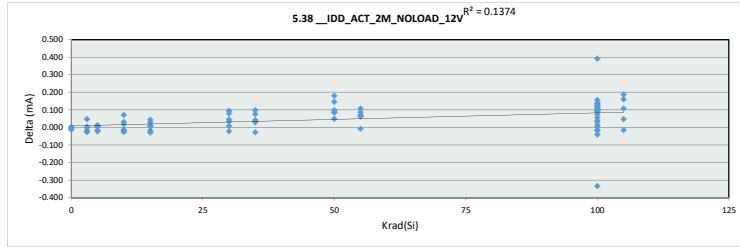
5.37 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.096	9.029	9.003	9.096	8.987	9.097	9.060	9.137	9.177	8.759	9.258
Average	9.217	9.184	9.249	9.190	9.247	9.202	9.263	9.216	9.231	9.254	9.300
Max	9.328	9.303	9.428	9.286	9.406	9.313	9.363	9.349	9.315	9.441	9.378
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



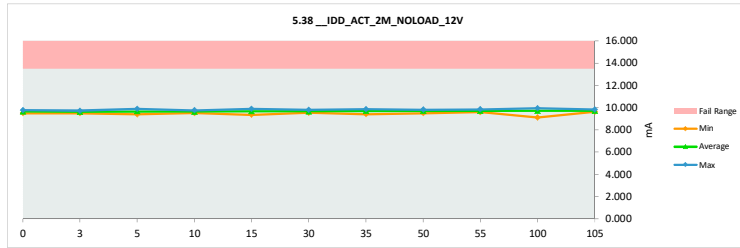
LDR TID Report  
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5.38 IDD_ACT_2M_NOLOAD_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	13.5	13.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.515	9.503	-0.012
0	118	9.709	9.704	-0.005
0	122	9.765	9.769	0.004
3	61	9.753	9.728	-0.025
3	62	9.727	9.727	0.000
3	63	9.504	9.508	0.004
3	64	9.692	9.674	-0.018
3	65	9.573	9.549	-0.024
3	66	9.489	9.536	0.047
5	67	9.553	9.559	0.006
5	68	9.746	9.724	-0.022
5	69	9.388	9.394	0.006
5	70	9.885	9.897	0.012
5	71	9.701	9.686	-0.015
5	72	9.781	9.791	0.010
10	73	9.753	9.728	-0.025
10	74	9.727	9.709	-0.018
10	75	9.504	9.522	0.018
10	76	9.692	9.763	0.071
10	77	9.573	9.560	-0.013
10	78	9.489	9.519	0.030
15	79	9.553	9.596	0.043
15	80	9.746	9.729	-0.017
15	81	9.388	9.359	-0.029
15	82	9.885	9.893	0.008
15	83	9.701	9.730	0.029
15	84	9.781	9.792	0.011
30	86	9.753	9.798	0.045
30	89	9.727	9.736	0.009
30	91	9.504	9.598	0.094
30	92	9.692	9.727	0.035
30	94	9.573	9.551	-0.022
30	95	9.489	9.569	0.080
35	100	9.553	9.652	0.099
35	101	9.746	9.777	0.031
35	102	9.388	9.420	0.032
35	103	9.885	9.857	-0.028
35	104	9.701	9.743	0.042
35	105	9.781	9.857	0.076
50	106	9.731	9.819	0.088
50	107	9.633	9.731	0.098
50	108	9.450	9.631	0.181
50	109	9.565	9.708	0.145
50	110	9.731	9.816	0.085
50	111	9.456	9.505	0.049
55	113	9.775	9.837	0.062
55	114	9.544	9.611	0.067
55	115	9.600	9.671	0.071
55	116	9.617	9.724	0.107
55	126	9.760	9.848	0.088
55	128	9.632	9.624	-0.008
100	129	9.794	9.908	0.114
100	131	9.603	9.588	-0.015
100	132	9.745	9.758	0.013
100	133	9.836	9.937	0.101
100	134	9.644	9.743	0.099
100	135	9.521	9.913	0.392
100	138	9.626	9.782	0.156
100	139	9.493	9.620	0.127
100	140	9.631	9.746	0.115
100	141	9.469	9.508	0.039
100	142	9.691	9.828	0.137
100	143	9.655	9.730	0.075
100	144	9.620	9.677	0.057
100	220	9.752	9.711	-0.041
100	221	9.561	9.563	0.002
100	224	9.580	9.677	0.097
100	225	9.610	9.699	0.089
100	226	9.873	9.903	0.030
100	228	9.655	9.796	0.141
100	232	9.744	9.842	0.098
100	233	9.676	9.777	0.101
100	236	9.431	9.098	-0.333
100	238	9.677	9.801	0.124
100	243	9.604	9.585	-0.019
105	244	9.654	9.702	0.048
105	246	9.456	9.643	0.187
105	248	9.723	9.831	0.108
105	374	9.665	9.826	0.161
105	376	9.638	9.624	-0.014
105	Max	9.885	9.937	0.392
105	Average	9.644	9.691	0.047
105	Min	9.388	9.098	-0.333
105	Std Dev	0.123	0.144	0.081



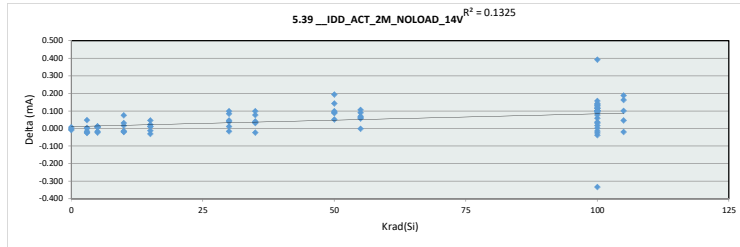
5.38 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.503	9.508	9.394	9.519	9.359	9.551	9.420	9.505	9.611	9.098	9.624
Average	9.659	9.620	9.675	9.634	9.683	9.663	9.718	9.702	9.719	9.716	9.725
Max	9.769	9.728	9.897	9.763	9.893	9.798	9.857	9.819	9.848	9.937	9.831
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



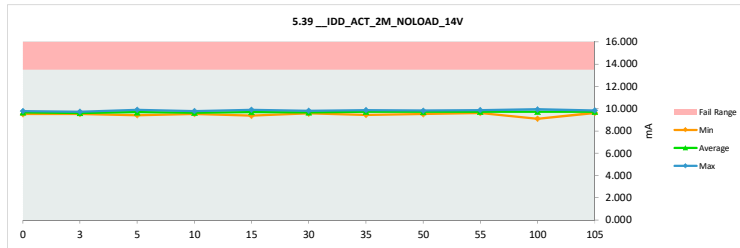
LDR TID Report  
TPS7H5001-SP

5.39 IDD_ACT_2M_NOLOAD_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	13.5
Min Limit	13.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.518	9.509	-0.009
0	118	9.710	9.706	-0.004
0	122	9.766	9.774	0.008
3	61	9.754	9.731	-0.023
3	62	9.728	9.728	0.000
3	63	9.508	9.511	0.003
3	64	9.696	9.680	-0.016
3	65	9.578	9.552	-0.026
3	66	9.496	9.543	0.047
5	67	9.557	9.566	0.009
5	68	9.747	9.724	-0.023
5	69	9.393	9.399	0.006
5	70	9.885	9.897	0.012
5	71	9.708	9.691	-0.017
5	72	9.785	9.794	0.009
10	73	9.754	9.733	-0.021
10	74	9.728	9.710	-0.018
10	75	9.508	9.524	0.016
10	76	9.696	9.771	0.075
10	77	9.578	9.564	-0.014
10	78	9.496	9.526	0.030
15	79	9.557	9.602	0.045
15	80	9.747	9.734	-0.013
15	81	9.393	9.362	-0.031
15	82	9.885	9.895	0.010
15	83	9.708	9.730	0.022
15	84	9.785	9.795	0.010
30	86	9.754	9.799	0.045
30	89	9.728	9.738	0.010
30	91	9.508	9.607	0.099
30	92	9.696	9.735	0.039
30	94	9.578	9.561	-0.017
30	95	9.496	9.579	0.083
35	100	9.557	9.656	0.099
35	101	9.747	9.782	0.035
35	102	9.393	9.424	0.031
35	103	9.885	9.861	-0.024
35	104	9.708	9.748	0.040
35	105	9.785	9.861	0.076
50	106	9.732	9.823	0.091
50	107	9.638	9.737	0.099
50	108	9.451	9.644	0.193
50	109	9.569	9.711	0.142
50	110	9.734	9.821	0.087
50	111	9.456	9.507	0.051
55	113	9.783	9.840	0.057
55	114	9.550	9.612	0.062
55	115	9.606	9.675	0.069
55	116	9.624	9.729	0.105
55	126	9.766	9.855	0.089
55	128	9.635	9.633	-0.002
100	129	9.797	9.911	0.114
100	131	9.604	9.590	-0.014
100	132	9.747	9.764	0.017
100	133	9.838	9.939	0.101
100	134	9.645	9.746	0.101
100	135	9.528	9.919	0.391
100	138	9.629	9.786	0.157
100	139	9.493	9.624	0.131
100	140	9.634	9.748	0.114
100	141	9.476	9.512	0.036
100	142	9.693	9.827	0.134
100	143	9.658	9.735	0.077
100	144	9.621	9.680	0.059
100	220	9.751	9.713	-0.038
100	221	9.564	9.565	0.001
100	224	9.583	9.682	0.099
100	225	9.612	9.702	0.090
100	226	9.875	9.905	0.030
100	228	9.658	9.800	0.142
100	232	9.745	9.845	0.100
100	233	9.680	9.780	0.100
100	236	9.434	9.100	-0.334
100	238	9.681	9.803	0.122
100	243	9.614	9.589	-0.025
105	244	9.659	9.705	0.046
105	246	9.461	9.648	0.187
105	248	9.732	9.832	0.100
105	374	9.668	9.830	0.162
105	376	9.643	9.623	-0.020
105		9.885	9.939	0.391
	Max	9.647	9.695	0.048
	Average	9.647	9.695	0.048
	Min	9.393	9.100	-0.334
	Std Dev	0.122	0.143	0.081



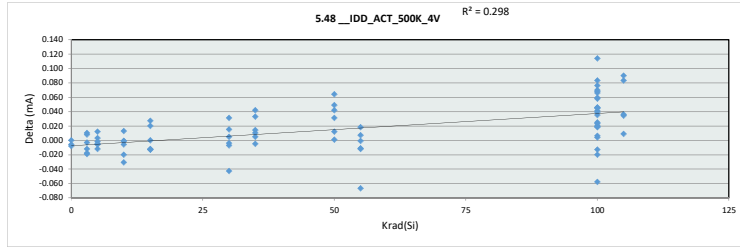
5.39 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	13.5 mA										
Min Limit	13.5 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.509	9.511	9.399	9.524	9.362	9.561	9.424	9.507	9.612	9.100	9.623
Average	9.663	9.624	9.679	9.638	9.686	9.670	9.722	9.707	9.724	9.719	9.728
Max	9.774	9.731	9.897	9.771	9.895	9.799	9.861	9.823	9.855	9.939	9.832
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



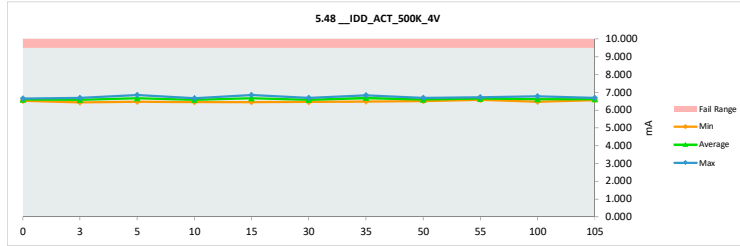
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5.48 IDD_ACT_500K_4V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.519	6.513	-0.006
0	118	6.617	6.617	0.000
0	122	6.649	6.641	-0.008
3	61	6.643	6.624	-0.019
3	62	6.681	6.678	-0.003
3	63	6.584	6.592	0.008
3	64	6.646	6.634	-0.012
3	65	6.455	6.437	-0.018
3	66	6.472	6.482	0.010
5	67	6.550	6.548	-0.002
5	68	6.696	6.691	-0.005
5	69	6.467	6.470	0.003
5	70	6.831	6.843	0.012
5	71	6.666	6.654	-0.012
5	72	6.739	6.733	-0.006
10	73	6.643	6.612	-0.031
10	74	6.681	6.661	-0.020
10	75	6.584	6.597	0.013
10	76	6.646	6.640	-0.006
10	77	6.455	6.454	-0.001
10	78	6.472	6.471	-0.001
15	79	6.550	6.577	0.027
15	80	6.696	6.683	-0.013
15	81	6.467	6.455	-0.012
15	82	6.831	6.851	0.020
15	83	6.666	6.653	-0.013
15	84	6.739	6.739	0.000
30	86	6.643	6.658	0.015
30	89	6.681	6.686	0.005
30	91	6.584	6.615	0.031
30	92	6.646	6.603	-0.043
30	94	6.455	6.451	-0.004
30	95	6.472	6.465	-0.007
35	100	6.550	6.592	0.042
35	101	6.696	6.729	0.033
35	102	6.467	6.477	0.010
35	103	6.831	6.826	-0.005
35	104	6.666	6.671	0.005
35	105	6.739	6.753	0.014
50	106	6.650	6.681	0.031
50	107	6.550	6.592	0.042
50	108	6.447	6.511	0.064
50	109	6.606	6.655	0.049
50	110	6.614	6.626	0.012
50	111	6.495	6.496	0.001
55	113	6.656	6.655	-0.001
55	114	6.590	6.579	-0.011
55	115	6.647	6.665	0.018
55	116	6.594	6.601	0.007
55	126	6.737	6.725	-0.012
55	128	6.662	6.595	-0.067
100	129	6.653	6.729	0.076
100	131	6.543	6.485	-0.058
100	132	6.670	6.657	-0.013
100	133	6.654	6.695	0.041
100	134	6.549	6.594	0.045
100	135	6.588	6.568	-0.020
100	138	6.598	6.664	0.066
100	139	6.464	6.522	0.058
100	140	6.577	6.616	0.039
100	141	6.491	6.509	0.018
100	142	6.639	6.709	0.070
100	143	6.605	6.673	0.068
100	144	6.547	6.582	0.035
100	220	6.654	6.658	0.004
100	221	6.530	6.549	0.019
100	224	6.567	6.613	0.046
100	225	6.449	6.469	0.020
100	226	6.731	6.776	0.045
100	228	6.529	6.643	0.114
100	232	6.589	6.672	0.083
100	233	6.595	6.618	0.023
100	236	6.455	6.480	0.025
100	238	6.651	6.710	0.059
100	243	6.505	6.511	0.006
105	244	6.574	6.583	0.009
105	246	6.479	6.569	0.090
105	248	6.634	6.668	0.034
105	374	6.593	6.676	0.083
105	376	6.527	6.558	0.036
105	Max	6.831	6.851	0.020
105	Average	6.599	6.615	0.016
105	Min	6.447	6.437	-0.010
105	Std Dev	0.094	0.095	0.033



5.48 IDD_ACT_5											
Test Site											
Tester											
Test Number											
Max Limit	9.5 mA										
Min Limit	mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.513	6.437	6.470	6.454	6.455	6.451	6.477	6.496	6.579	6.469	6.558
Average	6.590	6.575	6.657	6.573	6.660	6.580	6.675	6.594	6.637	6.613	6.611
Max	6.641	6.678	6.843	6.661	6.851	6.686	6.826	6.681	6.725	6.776	6.676
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

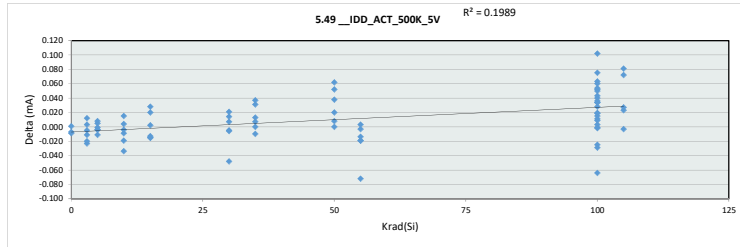




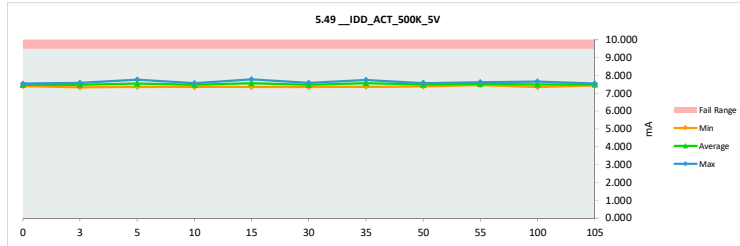
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5.49 IDD_ACT_500K_5V	
Test Site	
Tester	
Test Number	
Unit	mA      mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.420	7.411	-0.009
0	118	7.507	7.508	0.001
0	122	7.548	7.541	-0.007
3	61	7.548	7.525	-0.023
3	62	7.586	7.581	-0.005
3	63	7.479	7.482	0.003
3	64	7.553	7.542	-0.011
3	65	7.359	7.339	-0.020
3	66	7.368	7.380	0.012
5	67	7.450	7.449	-0.001
5	68	7.586	7.582	-0.004
5	69	7.366	7.371	0.005
5	70	7.760	7.768	0.008
5	71	7.568	7.557	-0.011
5	72	7.640	7.636	-0.004
10	73	7.548	7.514	-0.034
10	74	7.586	7.567	-0.019
10	75	7.479	7.494	0.015
10	76	7.553	7.544	-0.009
10	77	7.359	7.355	-0.004
10	78	7.368	7.372	0.004
15	79	7.450	7.478	0.028
15	80	7.586	7.573	-0.013
15	81	7.366	7.351	-0.015
15	82	7.760	7.780	0.020
15	83	7.568	7.553	-0.015
15	84	7.640	7.642	0.002
30	86	7.548	7.562	0.014
30	89	7.586	7.593	0.007
30	91	7.479	7.500	0.021
30	92	7.553	7.505	-0.048
30	94	7.359	7.354	-0.005
30	95	7.368	7.362	-0.006
35	100	7.450	7.487	0.037
35	101	7.586	7.617	0.031
35	102	7.366	7.373	0.007
35	103	7.760	7.750	-0.010
35	104	7.568	7.568	0.000
35	105	7.640	7.653	0.013
50	106	7.559	7.579	0.020
50	107	7.461	7.499	0.038
50	108	7.345	7.407	0.062
50	109	7.500	7.552	0.052
50	110	7.518	7.526	0.008
50	111	7.395	7.395	0.000
55	113	7.559	7.540	-0.019
55	114	7.486	7.467	-0.019
55	115	7.552	7.549	-0.003
55	116	7.484	7.487	0.003
55	126	7.636	7.622	-0.014
55	128	7.567	7.495	-0.072
100	129	7.556	7.619	0.063
100	131	7.437	7.373	-0.064
100	132	7.578	7.553	-0.025
100	133	7.552	7.586	0.034
100	134	7.462	7.498	0.036
100	135	7.491	7.462	-0.029
100	138	7.502	7.562	0.060
100	139	7.352	7.402	0.050
100	140	7.472	7.506	0.034
100	141	7.396	7.399	0.003
100	142	7.537	7.589	0.052
100	143	7.497	7.551	0.054
100	144	7.445	7.464	0.019
100	220	7.549	7.548	-0.001
100	221	7.428	7.437	0.009
100	224	7.462	7.502	0.040
100	225	7.355	7.366	0.011
100	226	7.633	7.661	0.028
100	228	7.420	7.522	0.102
100	232	7.497	7.572	0.075
100	233	7.510	7.525	0.015
100	236	7.342	7.361	0.019
100	238	7.551	7.594	0.043
100	243	7.397	7.395	-0.002
105	244	7.468	7.465	-0.003
105	246	7.372	7.453	0.081
105	248	7.534	7.557	0.023
105	374	7.477	7.549	0.072
105	376	7.428	7.455	0.027
	Max	7.760	7.780	0.102
	Average	7.500	7.511	0.010
	Min	7.342	7.339	-0.072
	Std Dev	0.098	0.097	0.031



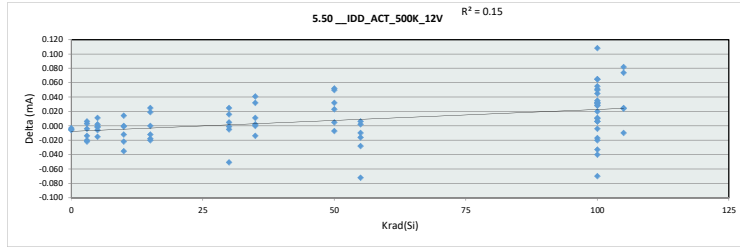
5.49 IDD_ACT_5											
Test Site											
Tester											
Test Number											
Max Limit	9.5 mA										
Min Limit	mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.411	7.339	7.371	7.355	7.351	7.354	7.373	7.395	7.467	7.361	7.453
Average	7.487	7.475	7.561	7.474	7.563	7.479	7.575	7.493	7.527	7.502	7.496
Max	7.541	7.581	7.768	7.567	7.780	7.593	7.750	7.579	7.622	7.661	7.557
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



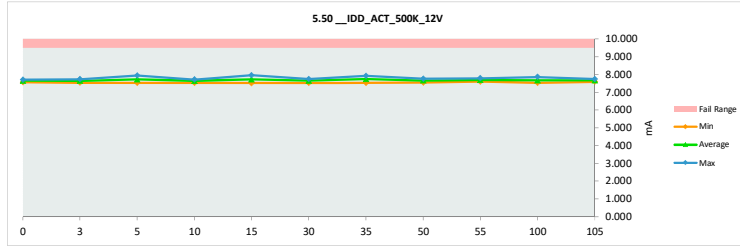
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5.50 IDD_ACT_500K_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	9.5	9.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.579	7.574	-0.005
0	118	7.698	7.695	-0.003
0	122	7.709	7.703	-0.006
3	61	7.730	7.708	-0.022
3	62	7.742	7.738	-0.004
3	63	7.620	7.623	0.003
3	64	7.732	7.718	-0.014
3	65	7.572	7.552	-0.020
3	66	7.535	7.541	0.006
5	67	7.611	7.610	-0.001
5	68	7.745	7.739	-0.006
5	69	7.535	7.537	0.002
5	70	7.941	7.952	0.011
5	71	7.739	7.724	-0.015
5	72	7.802	7.800	-0.002
10	73	7.730	7.695	-0.035
10	74	7.742	7.720	-0.022
10	75	7.620	7.634	0.014
10	76	7.732	7.720	-0.012
10	77	7.572	7.571	-0.001
10	78	7.535	7.535	0.000
15	79	7.611	7.636	0.025
15	80	7.745	7.733	-0.012
15	81	7.535	7.515	-0.020
15	82	7.941	7.960	0.019
15	83	7.739	7.721	-0.018
15	84	7.802	7.802	0.000
30	86	7.730	7.746	0.016
30	89	7.742	7.747	0.005
30	91	7.620	7.645	0.025
30	92	7.732	7.681	-0.051
30	94	7.572	7.571	-0.001
30	95	7.535	7.530	-0.005
35	100	7.611	7.652	0.041
35	101	7.745	7.777	0.032
35	102	7.535	7.537	0.002
35	103	7.941	7.927	-0.014
35	104	7.739	7.739	0.000
35	105	7.802	7.813	0.011
50	106	7.742	7.765	0.023
50	107	7.645	7.677	0.032
50	108	7.532	7.584	0.052
50	109	7.643	7.693	0.050
50	110	7.722	7.727	0.005
50	111	7.561	7.554	-0.007
55	113	7.757	7.747	-0.010
55	114	7.638	7.610	-0.028
55	115	7.695	7.701	0.006
55	116	7.639	7.641	0.002
55	126	7.809	7.793	-0.016
55	128	7.709	7.637	-0.072
100	129	7.764	7.829	0.065
100	131	7.599	7.529	-0.070
100	132	7.743	7.710	-0.033
100	133	7.765	7.785	0.020
100	134	7.629	7.661	0.032
100	135	7.643	7.603	-0.040
100	138	7.668	7.723	0.055
100	139	7.520	7.565	0.045
100	140	7.648	7.683	0.035
100	141	7.557	7.553	-0.004
100	142	7.704	7.754	0.050
100	143	7.661	7.712	0.051
100	144	7.610	7.638	0.028
100	220	7.722	7.705	-0.017
100	221	7.577	7.583	0.006
100	224	7.636	7.667	0.031
100	225	7.569	7.575	0.006
100	226	7.835	7.864	0.029
100	228	7.601	7.709	0.108
100	232	7.674	7.739	0.065
100	233	7.694	7.705	0.011
100	236	7.516	7.526	0.010
100	238	7.706	7.739	0.033
100	243	7.580	7.560	-0.020
105	244	7.646	7.636	-0.010
105	246	7.519	7.593	0.074
105	248	7.722	7.746	0.024
105	374	7.670	7.752	0.082
105	376	7.607	7.632	0.025
105	Max	7.941	7.960	0.108
105	Average	7.672	7.680	0.008
105	Min	7.516	7.515	-0.072
105	Std Dev	0.099	0.100	0.032



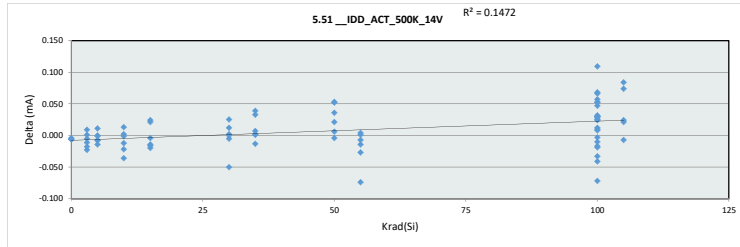
5.50 IDD_ACT_5		
Test Site		
Tester		
Test Number		
Min Limit	9.5	mA
Max Limit		
Krad(Si)	0	3
LL		
Min	7.574	7.541
Average	7.657	7.647
Max	7.703	7.738
UL	9.500	9.500



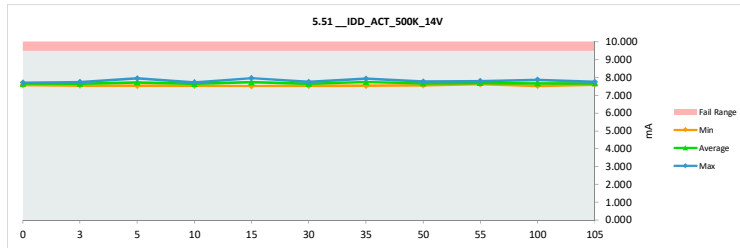
LDR TID Report  
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5.51 IDD_ACT_500K_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.582	7.576	-0.006
0	118	7.703	7.699	-0.004
0	122	7.714	7.708	-0.006
3	61	7.737	7.714	-0.023
3	62	7.748	7.743	-0.005
3	63	7.625	7.626	0.001
3	64	7.734	7.723	-0.011
3	65	7.576	7.558	-0.018
3	66	7.540	7.549	0.009
5	67	7.614	7.614	0.000
5	68	7.749	7.742	-0.007
5	69	7.540	7.539	-0.001
5	70	7.944	7.955	0.011
5	71	7.742	7.728	-0.014
5	72	7.810	7.802	-0.008
10	73	7.737	7.701	-0.036
10	74	7.748	7.726	-0.022
10	75	7.625	7.638	0.013
10	76	7.734	7.722	-0.012
10	77	7.576	7.578	0.002
10	78	7.540	7.539	-0.001
15	79	7.614	7.638	0.024
15	80	7.749	7.735	-0.014
15	81	7.540	7.520	-0.020
15	82	7.944	7.965	0.021
15	83	7.742	7.726	-0.016
15	84	7.810	7.806	-0.004
30	86	7.737	7.749	0.012
30	89	7.748	7.750	0.002
30	91	7.625	7.650	0.025
30	92	7.734	7.684	-0.050
30	94	7.576	7.576	0.000
30	95	7.540	7.535	-0.005
35	100	7.614	7.653	0.039
35	101	7.749	7.782	0.033
35	102	7.540	7.542	0.002
35	103	7.944	7.931	-0.013
35	104	7.742	7.743	0.001
35	105	7.810	7.817	0.007
50	106	7.747	7.768	0.021
50	107	7.648	7.684	0.036
50	108	7.536	7.588	0.052
50	109	7.647	7.700	0.053
50	110	7.726	7.732	0.006
50	111	7.566	7.562	-0.004
55	113	7.762	7.755	-0.007
55	114	7.645	7.618	-0.027
55	115	7.698	7.702	0.004
55	116	7.646	7.647	0.001
55	126	7.812	7.798	-0.014
55	128	7.715	7.641	-0.074
100	129	7.766	7.834	0.068
100	131	7.606	7.534	-0.072
100	132	7.748	7.715	-0.033
100	133	7.768	7.792	0.024
100	134	7.636	7.666	0.030
100	135	7.647	7.606	-0.041
100	138	7.673	7.730	0.057
100	139	7.526	7.573	0.047
100	140	7.654	7.684	0.030
100	141	7.565	7.555	-0.010
100	142	7.708	7.760	0.052
100	143	7.665	7.718	0.053
100	144	7.614	7.639	0.025
100	220	7.725	7.708	-0.017
100	221	7.586	7.583	-0.003
100	224	7.641	7.671	0.030
100	225	7.573	7.583	0.010
100	226	7.840	7.868	0.028
100	228	7.607	7.716	0.109
100	232	7.677	7.743	0.066
100	233	7.698	7.710	0.012
100	236	7.520	7.528	0.008
100	238	7.713	7.745	0.032
100	243	7.583	7.564	-0.019
105	244	7.649	7.642	-0.007
105	246	7.523	7.597	0.074
105	248	7.727	7.751	0.024
105	374	7.674	7.758	0.084
105	376	7.611	7.632	0.021
105	Max	7.944	7.965	0.109
105	Average	7.677	7.685	0.008
105	Min	7.520	7.520	-0.074
105	Std Dev	0.098	0.100	0.032



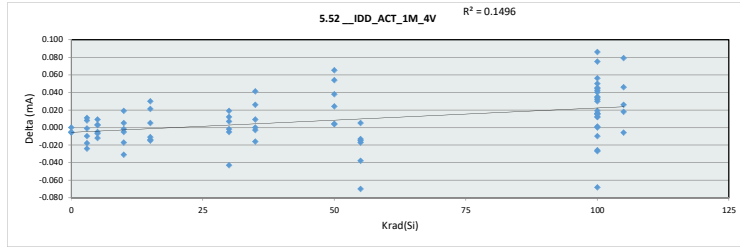
5.51 IDD_ACT_5											
Test Site											
Tester											
Test Number											
Max Limit	9.5 mA										
Min Limit	9.5 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.576	7.549	7.539	7.539	7.520	7.535	7.542	7.562	7.618	7.528	7.597
Average	7.661	7.652	7.730	7.651	7.732	7.657	7.745	7.672	7.694	7.676	7.676
Max	7.708	7.743	7.955	7.726	7.965	7.750	7.931	7.768	7.798	7.868	7.758
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



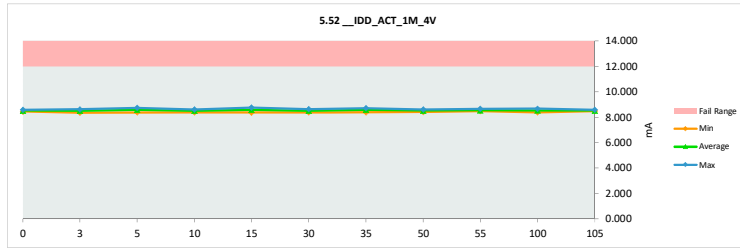
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5.52 IDD_ACT_1M_4V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	12
Min Limit	12

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.436	8.431	-0.005
0	118	8.532	8.532	0.000
0	122	8.576	8.570	-0.006
3	61	8.566	8.542	-0.024
3	62	8.614	8.613	-0.001
3	63	8.486	8.494	0.008
3	64	8.566	8.556	-0.010
3	65	8.374	8.356	-0.018
3	66	8.399	8.410	0.011
5	67	8.455	8.458	0.003
5	68	8.617	8.610	-0.007
5	69	8.370	8.373	0.003
5	70	8.720	8.729	0.009
5	71	8.579	8.567	-0.012
5	72	8.654	8.649	-0.005
10	73	8.566	8.535	-0.031
10	74	8.614	8.597	-0.017
10	75	8.486	8.505	0.019
10	76	8.566	8.561	-0.005
10	77	8.374	8.372	-0.002
10	78	8.399	8.404	0.005
15	79	8.455	8.485	0.030
15	80	8.617	8.606	-0.011
15	81	8.370	8.356	-0.014
15	82	8.720	8.741	0.021
15	83	8.579	8.564	-0.015
15	84	8.654	8.659	0.005
30	86	8.566	8.578	0.012
30	89	8.614	8.621	0.007
30	91	8.486	8.505	0.019
30	92	8.566	8.523	-0.043
30	94	8.374	8.372	-0.002
30	95	8.399	8.394	-0.005
35	100	8.455	8.496	0.041
35	101	8.617	8.643	0.026
35	102	8.370	8.370	0.000
35	103	8.720	8.704	-0.016
35	104	8.579	8.576	-0.003
35	105	8.654	8.663	0.009
50	106	8.567	8.591	0.024
50	107	8.467	8.505	0.038
50	108	8.359	8.424	0.065
50	109	8.525	8.579	0.054
50	110	8.539	8.543	0.004
50	111	8.405	8.409	0.004
55	113	8.572	8.534	-0.038
55	114	8.488	8.471	-0.017
55	115	8.561	8.548	-0.013
55	116	8.502	8.507	0.005
55	126	8.655	8.640	-0.015
55	128	8.588	8.518	-0.070
100	129	8.575	8.619	0.044
100	131	8.470	8.402	-0.068
100	132	8.597	8.571	-0.026
100	133	8.579	8.609	0.030
100	134	8.476	8.510	0.034
100	135	8.504	8.477	-0.027
100	138	8.522	8.578	0.056
100	139	8.381	8.426	0.045
100	140	8.489	8.521	0.032
100	141	8.408	8.409	0.001
100	142	8.562	8.604	0.042
100	143	8.512	8.562	0.050
100	144	8.475	8.487	0.012
100	220	8.569	8.559	-0.010
100	221	8.442	8.457	0.015
100	224	8.473	8.508	0.035
100	225	8.376	8.388	0.012
100	226	8.643	8.659	0.016
100	228	8.447	8.533	0.086
100	232	8.508	8.583	0.075
100	233	8.512	8.531	0.019
100	236	8.355	8.371	0.016
100	238	8.576	8.616	0.040
100	243	8.411	8.411	0.000
105	244	8.484	8.478	-0.006
105	246	8.398	8.477	0.079
105	248	8.558	8.576	0.018
105	374	8.493	8.539	0.046
105	376	8.447	8.473	0.026
105	Max	8.720	8.741	0.086
105	Average	8.515	8.524	0.009
105	Min	8.355	8.356	-0.070
105	Std Dev	0.094	0.092	0.029



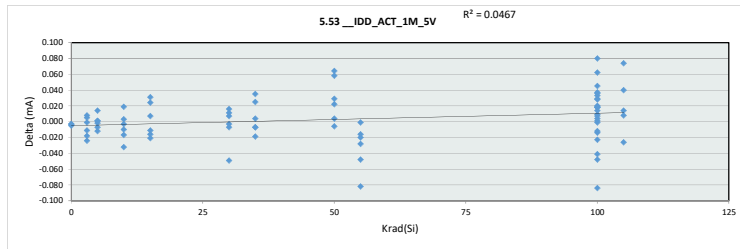
5.52 IDD_ACT	
Test Site	
Tester	
Test Number	
Max Limit	12 mA
Min Limit	12 mA
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	8.431 8.356 8.373 8.372 8.356 8.372 8.370 8.409 8.471 8.371 8.473
Average	8.511 8.495 8.564 8.496 8.569 8.499 8.575 8.509 8.536 8.516 8.509
Max	8.570 8.613 8.729 8.597 8.741 8.621 8.704 8.591 8.640 8.659 8.576
UL	12.000 12.000 12.000 12.000 12.000 12.000 12.000 12.000 12.000 12.000 12.000



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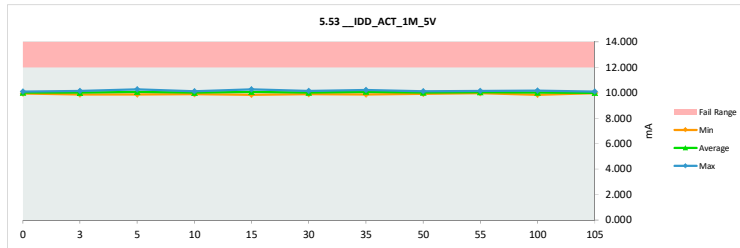
5.53 IDD_ACT_1M_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	12
Min Limit	12

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.941	9.936	-0.005
0	118	10.029	10.026	-0.003
0	122	10.090	10.087	-0.003
3	61	10.084	10.060	-0.024
3	62	10.134	10.133	-0.001
3	63	9.981	9.986	0.005
3	64	10.084	10.073	-0.011
3	65	9.885	9.867	-0.018
3	66	9.908	9.916	0.008
5	67	9.967	9.968	0.001
5	68	10.116	10.109	-0.007
5	69	9.875	9.876	0.001
5	70	10.248	10.262	0.014
5	71	10.089	10.077	-0.012
5	72	10.170	10.168	-0.002
10	73	10.084	10.052	-0.032
10	74	10.134	10.117	-0.017
10	75	9.981	10.000	0.019
10	76	10.084	10.074	-0.010
10	77	9.885	9.882	-0.003
10	78	9.908	9.911	0.003
15	79	9.967	9.998	0.031
15	80	10.116	10.105	-0.011
15	81	9.875	9.854	-0.021
15	82	10.248	10.272	0.024
15	83	10.089	10.073	-0.016
15	84	10.170	10.177	0.007
30	86	10.084	10.095	0.011
30	89	10.134	10.141	0.007
30	91	9.981	9.997	0.016
30	92	10.084	10.035	-0.049
30	94	9.885	9.882	-0.003
30	95	9.908	9.901	-0.007
35	100	9.967	10.002	0.035
35	101	10.116	10.141	0.025
35	102	9.875	9.868	-0.007
35	103	10.248	10.229	-0.019
35	104	10.089	10.082	-0.007
35	105	10.170	10.174	0.004
50	106	10.090	10.112	0.022
50	107	10.006	10.035	0.029
50	108	9.861	9.925	0.064
50	109	10.032	10.090	0.058
50	110	10.059	10.053	-0.006
50	111	9.915	9.919	0.004
55	113	10.083	10.035	-0.048
55	114	9.988	9.960	-0.028
55	115	10.074	10.054	-0.020
55	116	9.998	9.997	-0.001
55	126	10.169	10.153	-0.016
55	128	10.111	10.029	-0.082
100	129	10.091	10.120	0.029
100	131	9.976	9.892	-0.084
100	132	10.126	10.085	-0.041
100	133	10.087	10.107	0.020
100	134	10.017	10.035	0.018
100	135	10.016	9.968	-0.048
100	138	10.036	10.081	0.045
100	139	9.875	9.911	0.036
100	140	9.997	10.016	0.019
100	141	9.923	9.909	-0.014
100	142	10.076	10.113	0.037
100	143	10.012	10.045	0.033
100	144	9.983	9.987	0.004
100	220	10.072	10.049	-0.023
100	221	9.950	9.951	0.001
100	224	9.980	9.998	0.018
100	225	9.891	9.890	-0.001
100	226	10.154	10.168	0.014
100	228	9.948	10.028	0.080
100	232	10.031	10.093	0.062
100	233	10.035	10.042	0.007
100	236	9.838	9.847	0.009
100	238	10.086	10.114	0.028
100	243	9.907	9.895	-0.012
105	244	9.986	9.960	-0.026
105	246	9.894	9.968	0.074
105	248	10.071	10.085	0.014
105	374	9.980	10.020	0.040
105	376	9.967	9.975	0.008
105	Max	10.248	10.272	0.024
105	Average	10.026	10.029	0.003
105	Min	9.838	9.847	-0.009
105	Std Dev	0.099	0.098	0.030



5.53 IDD_ACT	
Test Site	
Tester	
Test Number	
Max Limit	12 mA
Min Limit	12 mA

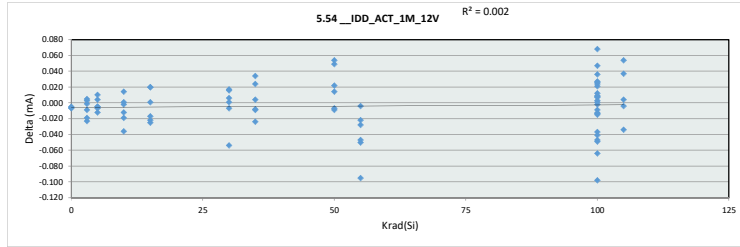
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.936	9.867	9.876	9.882	9.854	9.882	9.868	9.919	9.960	9.847	9.960
Average	10.016	10.006	10.077	10.006	10.080	10.009	10.083	10.022	10.038	10.014	10.002
Max	10.087	10.133	10.262	10.117	10.272	10.141	10.229	10.112	10.153	10.168	10.085
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000



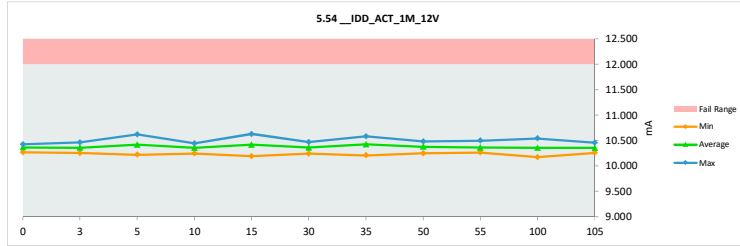
LDR TID Report  
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5.54 IDD_ACT_1M_12V			
Test Site			
Tester			
Test Number			
Unit	mA	mA	
Max Limit	12	12	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.275	10.268	-0.007
0	118	10.413	10.407	-0.006
0	122	10.428	10.423	-0.005
3	61	10.454	10.431	-0.023
3	62	10.463	10.462	-0.001
3	63	10.279	10.282	0.003
3	64	10.449	10.440	-0.009
3	65	10.311	10.292	-0.019
3	66	10.249	10.254	0.005
5	67	10.305	10.300	-0.005
5	68	10.447	10.440	-0.007
5	69	10.217	10.221	0.004
5	70	10.610	10.620	0.010
5	71	10.439	10.427	-0.012
5	72	10.509	10.504	-0.005
10	73	10.454	10.418	-0.036
10	74	10.463	10.444	-0.019
10	75	10.279	10.293	0.014
10	76	10.449	10.437	-0.012
10	77	10.311	10.312	0.001
10	78	10.249	10.247	-0.002
15	79	10.305	10.325	0.020
15	80	10.447	10.430	-0.017
15	81	10.217	10.195	-0.022
15	82	10.610	10.630	0.020
15	83	10.439	10.414	-0.025
15	84	10.509	10.510	0.001
30	86	10.454	10.471	0.017
30	89	10.463	10.469	0.006
30	91	10.279	10.295	0.016
30	92	10.449	10.395	-0.054
30	94	10.311	10.312	0.001
30	95	10.249	10.242	-0.007
35	100	10.305	10.339	0.034
35	101	10.447	10.471	0.024
35	102	10.217	10.209	-0.008
35	103	10.610	10.586	-0.024
35	104	10.439	10.430	-0.009
35	105	10.509	10.513	0.004
50	106	10.470	10.484	0.014
50	107	10.380	10.402	0.022
50	108	10.235	10.289	0.054
50	109	10.339	10.388	0.049
50	110	10.472	10.463	-0.009
50	111	10.256	10.249	-0.007
55	113	10.484	10.434	-0.050
55	114	10.312	10.265	-0.047
55	115	10.381	10.353	-0.028
55	116	10.322	10.318	-0.004
55	126	10.521	10.499	-0.022
55	128	10.414	10.319	-0.095
100	129	10.505	10.529	0.024
100	131	10.316	10.218	-0.098
100	132	10.461	10.412	-0.049
100	133	10.513	10.514	0.001
100	134	10.369	10.378	0.009
100	135	10.327	10.263	-0.064
100	138	10.383	10.419	0.036
100	139	10.223	10.244	0.021
100	140	10.363	10.375	0.012
100	141	10.262	10.225	-0.037
100	142	10.422	10.448	0.026
100	143	10.349	10.376	0.027
100	144	10.334	10.337	0.003
100	220	10.421	10.374	-0.047
100	221	10.265	10.252	-0.013
100	224	10.337	10.345	0.008
100	225	10.323	10.310	-0.013
100	226	10.552	10.543	-0.009
100	228	10.314	10.382	0.068
100	232	10.394	10.441	0.047
100	233	10.410	10.408	-0.002
100	236	10.190	10.175	-0.015
100	238	10.410	10.417	0.007
100	243	10.276	10.235	-0.041
105	244	10.347	10.313	-0.034
105	246	10.205	10.259	0.054
105	248	10.455	10.459	0.004
105	374	10.365	10.402	0.037
105	376	10.338	10.334	-0.004
105	Max	10.610	10.630	0.068
105	Average	10.379	10.375	-0.004
105	Min	10.190	10.175	-0.098
105	Std Dev	0.102	0.103	0.030



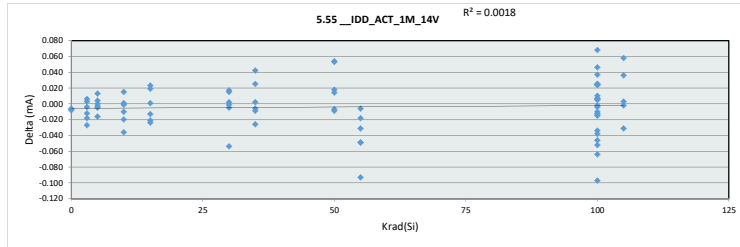
5.54 IDD_ACT											
Test Site											
Tester											
Test Number											
Max Limit	12	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.268	10.254	10.221	10.247	10.195	10.242	10.209	10.249	10.265	10.175	10.259
Average	10.366	10.360	10.419	10.359	10.417	10.364	10.425	10.379	10.365	10.359	10.353
Max	10.423	10.462	10.620	10.444	10.630	10.471	10.586	10.484	10.499	10.543	10.459
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000



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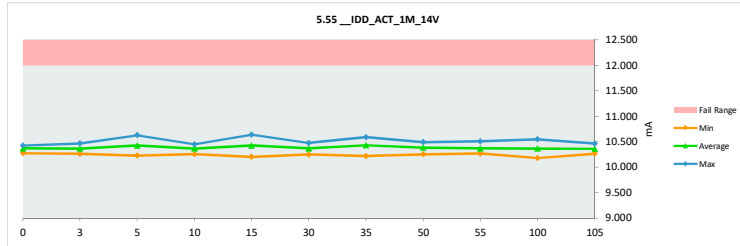
5.55 IDD_ACT_1M_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	12
Min Limit	12

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.279	10.272	-0.007
0	118	10.417	10.411	-0.006
0	122	10.433	10.425	-0.008
3	61	10.460	10.433	-0.027
3	62	10.469	10.465	-0.004
3	63	10.283	10.286	0.003
3	64	10.454	10.442	-0.012
3	65	10.316	10.298	-0.018
3	66	10.254	10.260	0.006
5	67	10.308	10.305	-0.003
5	68	10.451	10.446	-0.005
5	69	10.223	10.227	0.004
5	70	10.615	10.628	0.013
5	71	10.442	10.426	-0.016
5	72	10.513	10.513	0.000
10	73	10.460	10.424	-0.036
10	74	10.469	10.449	-0.020
10	75	10.283	10.298	0.015
10	76	10.454	10.444	-0.010
10	77	10.316	10.317	0.001
10	78	10.254	10.253	-0.001
15	79	10.308	10.331	0.023
15	80	10.451	10.438	-0.013
15	81	10.223	10.202	-0.021
15	82	10.615	10.634	0.019
15	83	10.442	10.418	-0.024
15	84	10.513	10.514	0.001
30	86	10.460	10.475	0.015
30	89	10.469	10.471	0.002
30	91	10.283	10.300	0.017
30	92	10.454	10.400	-0.054
30	94	10.316	10.315	-0.001
30	95	10.254	10.249	-0.005
35	100	10.308	10.350	0.042
35	101	10.451	10.476	0.025
35	102	10.223	10.217	-0.006
35	103	10.615	10.589	-0.026
35	104	10.442	10.433	-0.009
35	105	10.513	10.515	0.002
50	106	10.476	10.490	0.014
50	107	10.385	10.403	0.018
50	108	10.239	10.293	0.054
50	109	10.342	10.395	0.053
50	110	10.476	10.467	-0.009
50	111	10.259	10.252	-0.007
55	113	10.488	10.439	-0.049
55	114	10.317	10.268	-0.049
55	115	10.387	10.356	-0.031
55	116	10.327	10.321	-0.006
55	126	10.523	10.505	-0.018
55	128	10.419	10.326	-0.093
100	129	10.509	10.533	0.024
100	131	10.321	10.224	-0.097
100	132	10.468	10.416	-0.052
100	133	10.519	10.517	-0.002
100	134	10.375	10.381	0.006
100	135	10.332	10.268	-0.064
100	138	10.386	10.423	0.037
100	139	10.227	10.251	0.024
100	140	10.367	10.377	0.010
100	141	10.266	10.232	-0.034
100	142	10.427	10.452	0.025
100	143	10.353	10.378	0.025
100	144	10.338	10.343	0.005
100	220	10.425	10.379	-0.046
100	221	10.269	10.257	-0.012
100	224	10.342	10.349	0.007
100	225	10.327	10.313	-0.014
100	226	10.556	10.546	-0.010
100	228	10.318	10.386	0.068
100	232	10.398	10.444	0.046
100	233	10.413	10.409	-0.004
100	236	10.195	10.180	-0.015
100	238	10.414	10.420	0.006
100	243	10.278	10.240	-0.038
105	244	10.349	10.318	-0.031
105	246	10.207	10.265	0.058
105	248	10.459	10.462	0.003
105	374	10.368	10.404	0.036
105	376	10.341	10.339	-0.002
105	Max	10.615	10.634	0.068
105	Average	10.383	10.380	-0.004
105	Min	10.195	10.180	-0.097
105	Std Dev	0.102	0.102	0.030



5.55 IDD_ACT	
Test Site	
Tester	
Test Number	
Max Limit	12 mA
Min Limit	12 mA

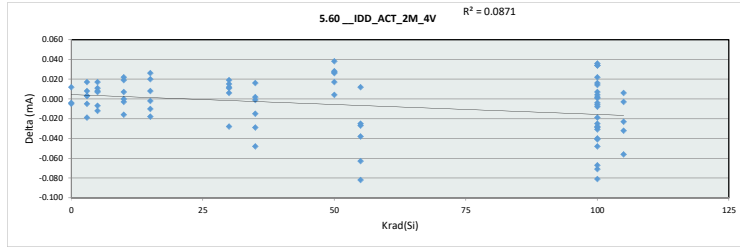
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.272	10.260	10.227	10.253	10.202	10.249	10.217	10.252	10.268	10.180	10.265
Average	10.369	10.364	10.424	10.364	10.423	10.368	10.430	10.383	10.369	10.363	10.358
Max	10.425	10.465	10.628	10.449	10.634	10.475	10.589	10.490	10.505	10.546	10.462
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000



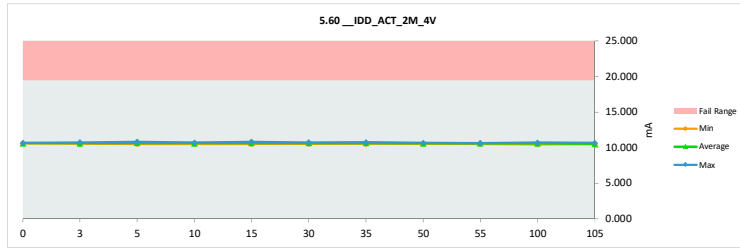
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5.60 IDD_ACT_2M_4V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	19.5	19.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.578	10.574	-0.004
0	118	10.578	10.573	-0.005
0	122	10.638	10.650	0.012
3	61	10.601	10.582	-0.019
3	62	10.692	10.700	0.008
3	63	10.614	10.631	0.017
3	64	10.679	10.682	0.003
3	65	10.512	10.507	-0.005
3	66	10.530	10.533	0.003
5	67	10.495	10.503	0.008
5	68	10.673	10.661	-0.012
5	69	10.515	10.522	0.007
5	70	10.777	10.794	0.017
5	71	10.649	10.642	-0.007
5	72	10.690	10.701	0.011
10	73	10.691	10.585	-0.106
10	74	10.692	10.689	-0.003
10	75	10.614	10.636	0.022
10	76	10.679	10.679	0.000
10	77	10.512	10.519	0.007
10	78	10.530	10.549	0.019
15	79	10.495	10.521	0.026
15	80	10.673	10.671	-0.002
15	81	10.515	10.497	-0.018
15	82	10.777	10.797	0.020
15	83	10.649	10.639	-0.010
15	84	10.690	10.698	0.008
30	86	10.601	10.620	0.019
30	89	10.692	10.703	0.011
30	91	10.614	10.626	0.012
30	92	10.679	10.651	-0.028
30	94	10.512	10.518	0.006
30	95	10.530	10.545	0.015
35	100	10.495	10.511	0.016
35	101	10.673	10.675	0.002
35	102	10.515	10.486	-0.029
35	103	10.777	10.729	-0.048
35	104	10.649	10.634	-0.015
35	105	10.690	10.689	-0.001
50	106	10.615	10.641	0.026
50	107	10.504	10.521	0.017
50	108	10.583	10.587	0.004
50	109	10.561	10.599	0.038
50	110	10.561	10.589	0.028
50	111	10.558	10.585	0.027
55	113	10.688	10.625	-0.063
55	114	10.576	10.549	-0.027
55	115	10.587	10.562	-0.025
55	116	10.523	10.535	0.012
55	126	10.673	10.635	-0.038
55	128	10.639	10.557	-0.082
100	129	10.605	10.627	0.022
100	131	10.576	10.509	-0.067
100	132	10.703	10.632	-0.071
100	133	10.617	10.624	0.007
100	134	10.543	10.559	0.016
100	135	10.750	10.669	-0.081
100	138	10.560	10.574	0.014
100	139	10.548	10.540	-0.008
100	140	10.569	10.603	0.034
100	141	10.521	10.481	-0.040
100	142	10.621	10.622	0.001
100	143	10.565	10.567	0.002
100	144	10.562	10.531	-0.031
100	220	10.657	10.629	-0.028
100	221	10.531	10.512	-0.019
100	224	10.355	10.359	0.004
100	225	10.571	10.542	-0.029
100	226	10.696	10.690	-0.006
100	228	10.529	10.565	0.036
100	232	10.606	10.640	0.034
100	233	10.646	10.642	-0.004
100	236	10.535	10.494	-0.041
100	238	10.711	10.686	-0.025
100	243	10.481	10.433	-0.048
105	244	10.538	10.482	-0.056
105	246	10.515	10.521	0.006
105	248	10.673	10.641	-0.032
105	374	10.556	10.533	-0.023
105	376	10.564	10.561	-0.003
105	Max	10.777	10.797	0.038
	Average	10.603	10.597	-0.006
	Min	10.481	10.433	-0.082
	Std Dev	0.075	0.075	0.027



5.60 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.573	10.507	10.503	10.519	10.497	10.518	10.486	10.521	10.535	10.433	10.482
Average	10.599	10.606	10.637	10.610	10.637	10.611	10.621	10.587	10.577	10.580	10.548
Max	10.650	10.700	10.794	10.689	10.797	10.703	10.729	10.641	10.635	10.690	10.641
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

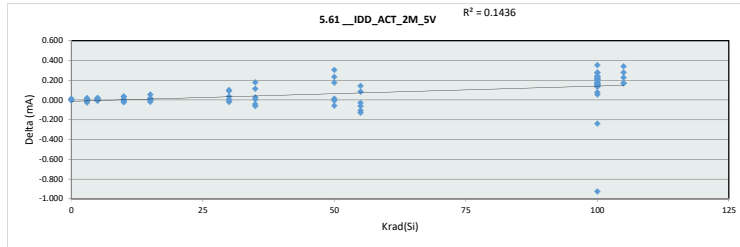




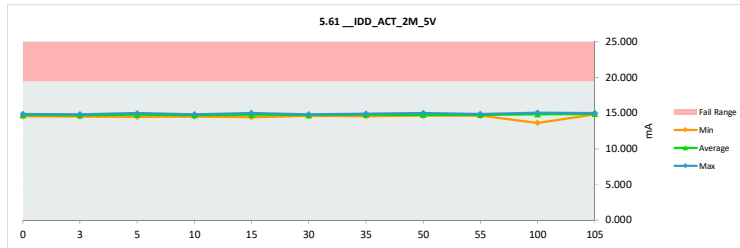
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5.61 IDD_ACT_2M_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	19.5
Min Limit	19.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	14.561	14.557	-0.004
0	118	14.725	14.727	0.002
0	122	14.863	14.875	0.012
3	61	14.778	14.750	-0.028
3	62	14.838	14.845	0.007
3	63	14.592	14.606	0.014
3	64	14.787	14.783	-0.004
3	65	14.525	14.509	-0.016
3	66	14.591	14.605	0.014
5	67	14.628	14.648	0.020
5	68	14.824	14.818	-0.006
5	69	14.447	14.463	0.016
5	70	14.975	14.992	0.017
5	71	14.768	14.759	-0.009
5	72	14.905	14.909	0.004
10	73	14.778	14.753	-0.025
10	74	14.838	14.819	-0.019
10	75	14.592	14.626	0.034
10	76	14.787	14.792	0.005
10	77	14.525	14.522	-0.003
10	78	14.591	14.621	0.030
15	79	14.628	14.683	0.055
15	80	14.824	14.825	0.001
15	81	14.447	14.425	-0.022
15	82	14.975	14.995	0.020
15	83	14.768	14.770	0.002
15	84	14.905	14.900	-0.005
30	86	14.778	14.782	0.004
30	89	14.838	14.824	-0.014
30	91	14.592	14.691	0.099
30	92	14.787	14.766	-0.021
30	94	14.525	14.616	0.091
30	95	14.591	14.627	0.036
35	100	14.628	14.805	0.177
35	101	14.824	14.830	0.006
35	102	14.447	14.560	0.113
35	103	14.975	14.911	-0.064
35	104	14.768	14.794	0.026
35	105	14.905	14.862	-0.043
50	106	14.766	14.999	0.233
50	107	14.657	14.670	0.013
50	108	14.447	14.749	0.302
50	109	14.712	14.706	-0.006
50	110	14.750	14.691	-0.059
50	111	14.509	14.683	0.174
55	113	14.798	14.882	0.084
55	114	14.633	14.775	0.142
55	115	14.749	14.688	-0.061
55	116	14.703	14.674	-0.029
55	126	14.839	14.731	-0.108
55	128	14.799	14.667	-0.132
100	129	14.832	14.886	0.054
100	131	14.721	14.798	0.077
100	132	14.904	15.037	0.133
100	133	14.818	15.051	0.233
100	134	14.816	14.993	0.177
100	135	14.569	13.641	-0.928
100	138	14.725	14.886	0.161
100	139	14.525	14.740	0.215
100	140	14.699	14.909	0.210
100	141	14.583	14.730	0.147
100	142	14.795	14.556	-0.239
100	143	14.678	14.912	0.234
100	144	14.707	14.921	0.214
100	220	14.794	14.934	0.140
100	221	14.623	14.819	0.196
100	224	14.641	14.834	0.193
100	225	14.534	14.740	0.206
100	226	14.819	15.060	0.241
100	228	14.637	14.988	0.351
100	232	14.793	15.069	0.276
100	233	14.749	14.906	0.157
100	236	14.381	14.548	0.167
100	238	14.779	15.053	0.274
100	243	14.574	14.746	0.172
105	244	14.662	14.833	0.171
105	246	14.539	14.878	0.339
105	248	14.776	15.001	0.225
105	374	14.618	14.896	0.278
105	376	14.678	14.846	0.168
105	375	14.975	15.069	0.351
Max		14.706	14.772	0.066
Min		14.381	13.641	-0.928
Std Dev		0.137	0.196	0.163



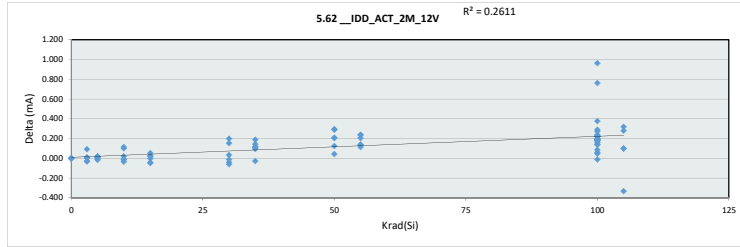
5.61 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5 mA										
Min Limit	19.5 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	14.557	14.509	14.463	14.522	14.425	14.616	14.560	14.670	14.667	13.641	14.833
Average	14.720	14.683	14.765	14.689	14.766	14.718	14.794	14.750	14.736	14.823	14.891
Max	14.875	14.845	14.992	14.819	14.995	14.824	14.911	14.999	14.882	15.069	15.001
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500



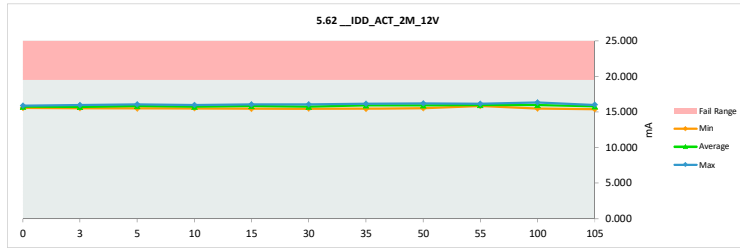
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TPS7H5001-SP

5.62 IDD_ACT_2M_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	19.5	19.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	15.575	15.568	-0.007
0	118	15.846	15.845	-0.001
0	122	15.906	15.909	0.003
3	61	15.887	15.979	0.092
3	62	15.851	15.858	0.007
3	63	15.516	15.520	0.004
3	64	15.837	15.805	-0.032
3	65	15.735	15.705	-0.030
3	66	15.553	15.561	0.008
5	67	15.829	15.841	0.012
5	68	15.844	15.837	-0.007
5	69	15.487	15.493	0.006
5	70	16.069	16.087	0.018
5	71	15.834	15.819	-0.015
5	72	16.042	16.063	0.021
10	73	15.887	16.000	0.113
10	74	15.851	15.952	0.101
10	75	15.516	15.537	0.021
10	76	15.837	15.800	-0.037
10	77	15.735	15.718	-0.017
10	78	15.553	15.543	-0.010
15	79	15.829	15.881	0.052
15	80	15.844	15.840	-0.004
15	81	15.487	15.437	-0.050
15	82	16.069	16.091	0.022
15	83	15.834	15.791	-0.043
15	84	16.042	16.063	0.021
30	86	15.887	16.085	0.198
30	89	15.851	16.004	0.153
30	91	15.516	15.503	-0.013
30	92	15.837	15.777	-0.060
30	94	15.735	15.694	-0.041
30	95	15.553	15.585	0.032
35	100	15.829	15.940	0.111
35	101	15.844	15.987	0.143
35	102	15.487	15.459	-0.028
35	103	16.069	16.185	0.116
35	104	15.834	16.022	0.188
35	105	16.042	16.135	0.093
50	106	15.903	16.192	0.289
50	107	15.772	16.067	0.295
50	108	15.517	15.725	0.208
50	109	15.778	15.982	0.204
50	110	16.086	16.210	0.124
50	111	15.515	15.557	0.042
55	113	15.920	16.155	0.235
55	114	15.612	15.850	0.238
55	115	15.711	15.914	0.203
55	116	15.808	15.942	0.134
55	126	15.984	16.123	0.139
55	128	15.742	15.854	0.112
100	129	16.064	16.255	0.191
100	131	15.770	15.829	0.059
100	132	15.962	16.103	0.141
100	133	16.079	16.240	0.161
100	134	15.909	16.096	0.187
100	135	15.512	16.274	0.762
100	138	15.762	16.049	0.287
100	139	15.571	15.750	0.179
100	140	15.812	16.035	0.223
100	141	15.505	15.493	-0.012
100	142	15.850	16.123	0.273
100	143	15.674	15.893	0.219
100	144	15.763	15.956	0.193
100	220	15.839	15.883	0.044
100	221	15.512	15.691	0.179
100	224	15.716	15.936	0.220
100	225	15.771	15.856	0.085
100	226	15.939	16.139	0.200
100	228	15.663	16.040	0.377
100	232	15.928	16.165	0.237
100	233	15.770	15.952	0.182
100	236	15.384	16.347	0.963
100	238	15.774	16.000	0.226
100	243	15.593	15.730	0.137
105	244	15.704	15.800	0.096
105	246	15.493	15.810	0.317
105	248	15.861	15.962	0.101
105	374	15.737	16.014	0.277
105	376	15.728	15.395	-0.333
105	Max	16.086	16.347	0.963
105	Average	15.772	15.891	0.119
105	Min	15.384	15.395	-0.333
105	Std Dev	0.176	0.223	0.168



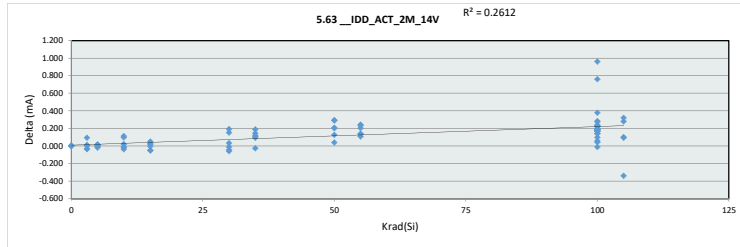
5.62 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	15.568	15.520	15.493	15.537	15.437	15.503	15.459	15.557	15.850	15.493	15.395
Average	15.774	15.738	15.857	15.758	15.851	15.775	15.955	15.956	15.973	15.993	15.796
Max	15.909	15.979	16.087	16.000	16.091	16.085	16.185	16.210	16.155	16.347	16.014
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500



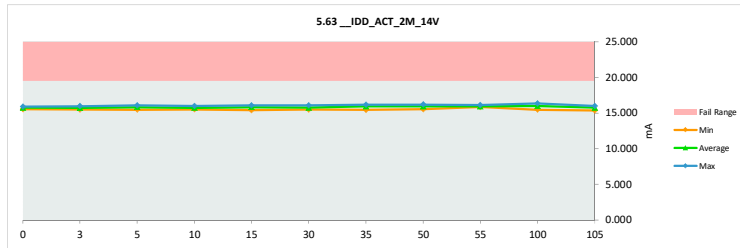
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5.63 IDD_ACT_2M_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	19.5
Min Limit	19.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	15.580	15.574	-0.006
0	118	15.854	15.856	0.002
0	122	15.908	15.916	0.008
3	61	15.893	15.986	0.093
3	62	15.857	15.861	0.004
3	63	15.521	15.527	0.006
3	64	15.843	15.809	-0.034
3	65	15.742	15.710	-0.032
3	66	15.560	15.570	0.010
5	67	15.836	15.845	0.009
5	68	15.848	15.842	-0.006
5	69	15.491	15.498	0.007
5	70	16.076	16.090	0.014
5	71	15.841	15.822	-0.019
5	72	16.047	16.065	0.018
10	73	15.893	16.007	0.114
10	74	15.857	15.955	0.098
10	75	15.521	15.542	0.021
10	76	15.843	15.806	-0.037
10	77	15.742	15.725	-0.017
10	78	15.560	15.549	-0.011
15	79	15.836	15.887	0.051
15	80	15.848	15.844	-0.004
15	81	15.491	15.441	-0.050
15	82	16.076	16.102	0.026
15	83	15.841	15.794	-0.047
15	84	16.047	16.069	0.022
30	86	15.893	16.086	0.193
30	89	15.857	16.010	0.153
30	91	15.521	15.510	-0.011
30	92	15.843	15.784	-0.059
30	94	15.742	15.699	-0.043
30	95	15.560	15.592	0.032
35	100	15.836	15.947	0.111
35	101	15.848	15.992	0.144
35	102	15.491	15.465	-0.026
35	103	16.076	16.191	0.115
35	104	15.841	16.030	0.189
35	105	16.047	16.141	0.094
50	106	15.909	16.200	0.291
50	107	15.778	16.072	0.294
50	108	15.535	15.729	0.204
50	109	15.783	15.990	0.207
50	110	16.092	16.219	0.127
50	111	15.521	15.559	0.038
55	113	15.926	16.160	0.234
55	114	15.616	15.858	0.242
55	115	15.717	15.920	0.203
55	116	15.815	15.951	0.136
55	126	15.991	16.128	0.137
55	128	15.748	15.858	0.110
100	129	16.072	16.259	0.187
100	131	15.776	15.834	0.058
100	132	15.968	16.110	0.142
100	133	16.082	16.246	0.164
100	134	15.915	16.105	0.190
100	135	15.520	16.282	0.762
100	138	15.768	16.052	0.284
100	139	15.577	15.752	0.175
100	140	15.818	16.042	0.224
100	141	15.512	15.500	-0.012
100	142	15.855	16.131	0.276
100	143	15.677	15.898	0.221
100	144	15.772	15.965	0.193
100	220	15.845	15.889	0.044
100	221	15.517	15.692	0.175
100	224	15.722	15.942	0.220
100	225	15.777	15.874	0.097
100	226	15.945	16.144	0.199
100	228	15.671	16.048	0.377
100	232	15.930	16.171	0.241
100	233	15.779	15.948	0.169
100	236	15.391	16.352	0.961
100	238	15.776	16.005	0.229
100	243	15.597	15.740	0.143
105	244	15.712	15.807	0.095
105	246	15.496	15.815	0.319
105	248	15.868	15.971	0.103
105	374	15.739	16.020	0.281
105	376	15.733	15.392	-0.341
105	Max	16.092	16.352	0.260
105	Average	15.778	15.897	0.119
105	Min	15.391	15.392	-0.001
105	Std Dev	0.176	0.224	0.169



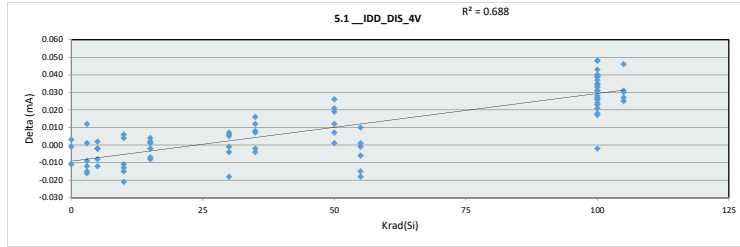
5.63 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5 mA										
Min Limit	19.5 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	15.574	15.527	15.498	15.542	15.441	15.510	15.465	15.559	15.858	15.500	15.392
Average	15.782	15.744	15.860	15.764	15.856	15.780	15.961	15.962	15.979	15.999	15.801
Max	15.916	15.986	16.090	16.007	16.102	16.086	16.191	16.219	16.160	16.352	16.020
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500



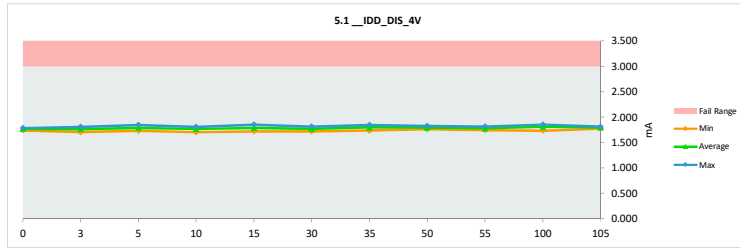
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5.1 IDD_DIS_4V	
Test Site	
Tester	
Test Number	
Unit	mA      mA
Max Limit	3      3
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.750	1.739	-0.011
0	118	1.782	1.781	-0.001
0	122	1.784	1.787	0.003
3	61	1.776	1.761	-0.015
3	62	1.819	1.807	-0.012
3	63	1.769	1.770	0.001
3	64	1.818	1.802	-0.016
3	65	1.714	1.705	-0.009
3	66	1.733	1.745	0.012
5	67	1.763	1.761	-0.002
5	68	1.822	1.820	-0.002
5	69	1.728	1.730	0.002
5	70	1.849	1.847	-0.002
5	71	1.791	1.783	-0.008
5	72	1.802	1.790	-0.012
10	73	1.776	1.755	-0.021
10	74	1.819	1.804	-0.015
10	75	1.769	1.773	0.004
10	76	1.818	1.807	-0.011
10	77	1.714	1.701	-0.013
10	78	1.733	1.739	0.006
15	79	1.763	1.767	0.004
15	80	1.822	1.820	-0.002
15	81	1.728	1.720	-0.008
15	82	1.849	1.851	0.002
15	83	1.791	1.792	0.001
15	84	1.802	1.795	-0.007
30	86	1.776	1.775	-0.001
30	89	1.819	1.815	-0.004
30	91	1.769	1.776	0.007
30	92	1.818	1.800	-0.018
30	94	1.714	1.720	0.006
30	95	1.733	1.738	0.005
35	100	1.763	1.775	0.012
35	101	1.822	1.838	0.016
35	102	1.728	1.736	0.008
35	103	1.849	1.847	-0.002
35	104	1.791	1.798	0.007
35	105	1.802	1.798	-0.004
50	106	1.816	1.823	0.007
50	107	1.800	1.826	0.026
50	108	1.750	1.771	0.021
50	109	1.796	1.815	0.019
50	110	1.784	1.785	0.001
50	111	1.748	1.760	0.012
55	113	1.788	1.773	-0.015
55	114	1.773	1.772	-0.001
55	115	1.765	1.775	0.010
55	116	1.790	1.791	0.001
55	126	1.822	1.816	-0.006
55	128	1.764	1.746	-0.018
100	129	1.795	1.834	0.039
100	131	1.762	1.760	-0.002
100	132	1.807	1.831	0.024
100	133	1.794	1.825	0.031
100	134	1.761	1.796	0.035
100	135	1.777	1.794	0.017
100	138	1.781	1.829	0.048
100	139	1.758	1.791	0.033
100	140	1.786	1.823	0.037
100	141	1.746	1.767	0.021
100	142	1.782	1.810	0.028
100	143	1.792	1.840	0.048
100	144	1.777	1.811	0.034
100	220	1.772	1.815	0.043
100	221	1.782	1.805	0.023
100	224	1.807	1.846	0.039
100	225	1.708	1.734	0.026
100	226	1.790	1.829	0.039
100	228	1.767	1.806	0.039
100	232	1.812	1.852	0.040
100	233	1.816	1.843	0.027
100	236	1.737	1.763	0.026
100	238	1.788	1.819	0.031
100	243	1.737	1.755	0.018
105	244	1.746	1.776	0.030
105	246	1.728	1.774	0.046
105	248	1.785	1.810	0.025
105	374	1.774	1.805	0.031
105	376	1.784	1.811	0.027
105	Max	1.849	1.852	0.048
105	Average	1.779	1.790	0.011
105	Min	1.708	1.701	-0.021
105	Std Dev	0.033	0.036	0.019



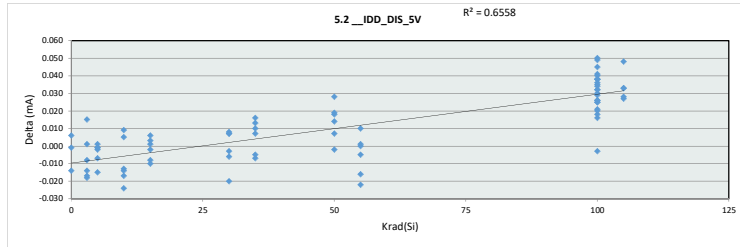
5.1 IDD DIS 4V	
Test Site	
Tester	
Test Number	
Max Limit	3      mA
Min Limit	
Krad(Si)	0      3      5      10      15      30      35      50      55      100      105
LL	
Min	1.739   1.705   1.730   1.701   1.720   1.720   1.736   1.760   1.746   1.734   1.774
Average	1.769   1.765   1.789   1.763   1.791   1.771   1.799   1.797   1.779   1.807   1.795
Max	1.787   1.807   1.847   1.807   1.851   1.815   1.847   1.826   1.816   1.852   1.811
UL	3.000   3.000   3.000   3.000   3.000   3.000   3.000   3.000   3.000   3.000   3.000



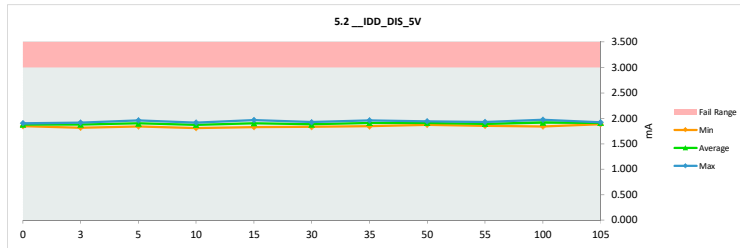
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5.2 IDD_DIS_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	3
Min Limit	3

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.858	1.844	-0.014
0	118	1.889	1.888	-0.001
0	122	1.892	1.898	0.006
3	61	1.886	1.869	-0.017
3	62	1.928	1.914	-0.014
3	63	1.876	1.877	0.001
3	64	1.928	1.910	-0.018
3	65	1.821	1.813	-0.008
3	66	1.838	1.853	0.015
5	67	1.870	1.869	-0.001
5	68	1.929	1.928	-0.001
5	69	1.834	1.835	0.001
5	70	1.961	1.959	-0.002
5	71	1.898	1.891	-0.007
5	72	1.912	1.897	-0.015
10	73	1.886	1.862	-0.024
10	74	1.928	1.911	-0.017
10	75	1.876	1.881	0.005
10	76	1.928	1.915	-0.013
10	77	1.821	1.807	-0.014
10	78	1.838	1.847	0.009
15	79	1.870	1.876	0.006
15	80	1.929	1.927	-0.002
15	81	1.834	1.826	-0.008
15	82	1.961	1.962	0.001
15	83	1.898	1.901	0.003
15	84	1.912	1.902	-0.010
30	86	1.886	1.883	-0.003
30	89	1.928	1.922	-0.006
30	91	1.876	1.883	0.007
30	92	1.928	1.908	-0.020
30	94	1.821	1.828	0.007
30	95	1.838	1.846	0.008
35	100	1.870	1.883	0.013
35	101	1.929	1.945	0.016
35	102	1.834	1.841	0.007
35	103	1.961	1.956	-0.005
35	104	1.898	1.908	0.010
35	105	1.912	1.905	-0.007
50	106	1.924	1.931	0.007
50	107	1.909	1.937	0.028
50	108	1.859	1.878	0.019
50	109	1.907	1.925	0.018
50	110	1.894	1.892	-0.002
50	111	1.854	1.868	0.014
55	113	1.897	1.881	-0.016
55	114	1.879	1.879	0.000
55	115	1.871	1.881	0.010
55	116	1.898	1.899	0.001
55	126	1.931	1.926	-0.005
55	128	1.873	1.851	-0.022
100	129	1.902	1.940	0.038
100	131	1.871	1.868	-0.003
100	132	1.915	1.941	0.026
100	133	1.901	1.933	0.032
100	134	1.868	1.902	0.034
100	135	1.886	1.902	0.016
100	138	1.887	1.936	0.049
100	139	1.866	1.896	0.030
100	140	1.896	1.932	0.036
100	141	1.852	1.872	0.020
100	142	1.889	1.918	0.029
100	143	1.900	1.950	0.050
100	144	1.885	1.920	0.035
100	220	1.879	1.924	0.045
100	221	1.892	1.913	0.021
100	224	1.916	1.954	0.038
100	225	1.813	1.838	0.025
100	226	1.899	1.939	0.040
100	228	1.873	1.914	0.041
100	232	1.923	1.961	0.038
100	233	1.945	1.970	0.025
100	236	1.846	1.872	0.026
100	238	1.896	1.928	0.032
100	243	1.844	1.862	0.018
105	244	1.854	1.887	0.033
105	246	1.834	1.882	0.048
105	248	1.892	1.920	0.028
105	374	1.882	1.915	0.033
105	376	1.894	1.921	0.027
	Max	1.961	1.970	0.050
	Average	1.887	1.898	0.011
	Min	1.813	1.807	-0.024
	Std Dev	0.035	0.037	0.019



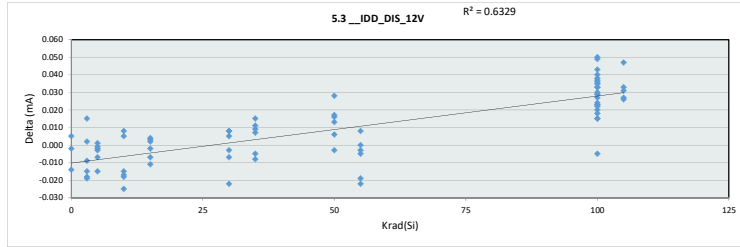
5.2 IDD DIS 5V											
Test Site											
Tester											
Test Number											
Max Limit	3 mA										
Min Limit	3 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.844	1.813	1.835	1.807	1.826	1.828	1.841	1.868	1.851	1.838	1.882
Average	1.877	1.873	1.897	1.871	1.899	1.878	1.906	1.905	1.886	1.916	1.905
Max	1.898	1.914	1.959	1.915	1.962	1.922	1.956	1.937	1.926	1.970	1.921
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000



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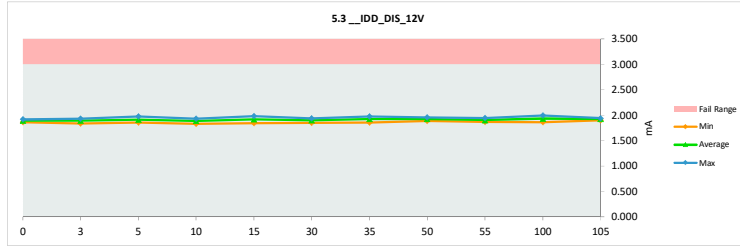
5.3 IDD_DIS_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	3	3
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.874	1.860	-0.014
0	118	1.910	1.908	-0.002
0	122	1.909	1.914	0.005
3	61	1.906	1.888	-0.018
3	62	1.943	1.928	-0.015
3	63	1.889	1.891	0.002
3	64	1.947	1.928	-0.019
3	65	1.845	1.836	-0.009
3	66	1.855	1.870	0.015
5	67	1.888	1.885	-0.003
5	68	1.945	1.944	-0.001
5	69	1.850	1.851	0.001
5	70	1.978	1.976	-0.002
5	71	1.915	1.908	-0.007
5	72	1.928	1.913	-0.015
10	73	1.906	1.881	-0.025
10	74	1.943	1.925	-0.018
10	75	1.889	1.894	0.005
10	76	1.947	1.932	-0.015
10	77	1.845	1.828	-0.017
10	78	1.855	1.863	0.008
15	79	1.888	1.892	0.004
15	80	1.945	1.943	-0.002
15	81	1.850	1.843	-0.007
15	82	1.978	1.980	0.002
15	83	1.915	1.918	0.003
15	84	1.928	1.917	-0.011
30	86	1.906	1.903	-0.003
30	89	1.943	1.936	-0.007
30	91	1.889	1.897	0.008
30	92	1.947	1.925	-0.022
30	94	1.845	1.850	0.005
30	95	1.855	1.863	0.008
35	100	1.888	1.899	0.011
35	101	1.945	1.960	0.015
35	102	1.850	1.857	0.007
35	103	1.978	1.973	-0.005
35	104	1.915	1.924	0.009
35	105	1.928	1.920	-0.008
50	106	1.943	1.949	0.006
50	107	1.928	1.956	0.028
50	108	1.879	1.896	0.017
50	109	1.922	1.938	0.016
50	110	1.914	1.911	-0.003
50	111	1.871	1.884	0.013
55	113	1.919	1.900	-0.019
55	114	1.895	1.892	-0.003
55	115	1.886	1.894	0.008
55	116	1.914	1.914	0.000
55	126	1.948	1.943	-0.005
55	128	1.886	1.864	-0.022
100	129	1.923	1.959	0.036
100	131	1.887	1.882	-0.005
100	132	1.932	1.955	0.023
100	133	1.923	1.952	0.029
100	134	1.885	1.918	0.033
100	135	1.901	1.916	0.015
100	138	1.904	1.953	0.049
100	139	1.883	1.913	0.030
100	140	1.915	1.950	0.035
100	141	1.869	1.887	0.018
100	142	1.907	1.934	0.027
100	143	1.917	1.967	0.050
100	144	1.902	1.935	0.033
100	220	1.896	1.939	0.043
100	221	1.907	1.927	0.020
100	224	1.935	1.972	0.037
100	225	1.837	1.859	0.022
100	226	1.920	1.958	0.038
100	228	1.892	1.932	0.040
100	232	1.942	1.977	0.035
100	233	1.967	1.990	0.023
100	236	1.866	1.890	0.024
100	238	1.912	1.941	0.029
100	243	1.863	1.878	0.015
105	244	1.873	1.904	0.031
105	246	1.849	1.896	0.047
105	248	1.912	1.938	0.026
105	374	1.902	1.935	0.033
105	376	1.912	1.939	0.027
105	Max	1.978	1.990	0.050
105	Average	1.905	1.914	0.010
105	Min	1.837	1.828	-0.025
105	Std Dev	0.034	0.037	0.019



5.3 IDD DIS 12		
Test Site		
Tester		
Test Number		
Max Limit	3	mA
Min Limit		mA
Krad(Si)	0	3

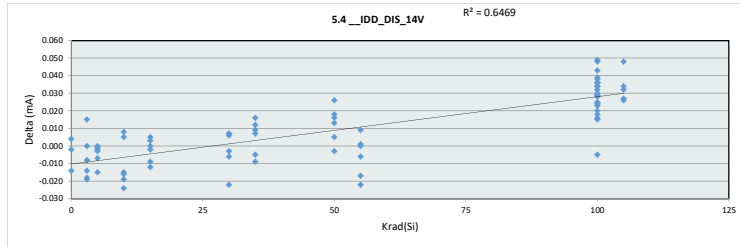
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.860	1.836	1.851	1.828	1.843	1.850	1.857	1.884	1.864	1.859	1.896
Average	1.894	1.890	1.913	1.887	1.916	1.896	1.922	1.922	1.901	1.933	1.922
Max	1.914	1.928	1.976	1.932	1.980	1.936	1.973	1.956	1.943	1.990	1.939
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000



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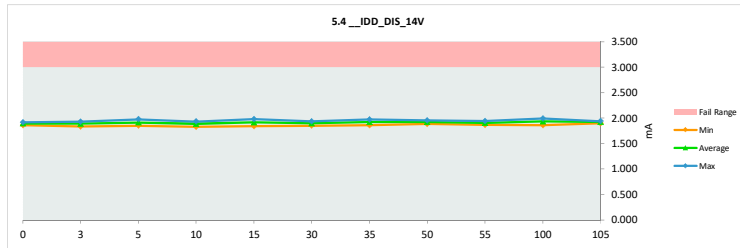
5.4 IDD_DIS_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	3
Min Limit	3

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.879	1.865	-0.014
0	118	1.914	1.912	-0.002
0	122	1.914	1.918	0.004
3	61	1.910	1.892	-0.018
3	62	1.947	1.933	-0.014
3	63	1.894	1.894	0.000
3	64	1.951	1.932	-0.019
3	65	1.848	1.840	-0.008
3	66	1.859	1.874	0.015
5	67	1.891	1.889	-0.002
5	68	1.949	1.948	-0.001
5	69	1.855	1.855	0.000
5	70	1.983	1.980	-0.003
5	71	1.919	1.912	-0.007
5	72	1.933	1.918	-0.015
10	73	1.910	1.886	-0.024
10	74	1.947	1.928	-0.019
10	75	1.894	1.899	0.005
10	76	1.951	1.936	-0.015
10	77	1.848	1.832	-0.016
10	78	1.859	1.867	0.008
15	79	1.891	1.896	0.005
15	80	1.949	1.947	-0.002
15	81	1.855	1.846	-0.009
15	82	1.983	1.983	0.000
15	83	1.919	1.922	0.003
15	84	1.933	1.921	-0.012
30	86	1.910	1.907	-0.003
30	89	1.947	1.941	-0.006
30	91	1.894	1.901	0.007
30	92	1.951	1.929	-0.022
30	94	1.848	1.854	0.006
30	95	1.859	1.866	0.007
35	100	1.891	1.903	0.012
35	101	1.949	1.965	0.016
35	102	1.855	1.862	0.007
35	103	1.983	1.978	-0.005
35	104	1.919	1.928	0.009
35	105	1.933	1.924	-0.009
50	106	1.948	1.953	0.005
50	107	1.933	1.959	0.026
50	108	1.883	1.901	0.018
50	109	1.926	1.942	0.016
50	110	1.919	1.916	-0.003
50	111	1.875	1.888	0.013
55	113	1.922	1.905	-0.017
55	114	1.898	1.898	0.000
55	115	1.889	1.898	0.009
55	116	1.918	1.919	0.001
55	126	1.953	1.947	-0.006
55	128	1.890	1.868	-0.022
100	129	1.927	1.963	0.036
100	131	1.891	1.886	-0.005
100	132	1.936	1.961	0.025
100	133	1.927	1.957	0.030
100	134	1.889	1.921	0.032
100	135	1.905	1.920	0.015
100	138	1.909	1.957	0.048
100	139	1.888	1.917	0.029
100	140	1.919	1.953	0.034
100	141	1.872	1.890	0.018
100	142	1.910	1.938	0.028
100	143	1.921	1.970	0.049
100	144	1.906	1.940	0.034
100	220	1.900	1.943	0.043
100	221	1.912	1.932	0.020
100	224	1.940	1.976	0.036
100	225	1.841	1.864	0.023
100	226	1.924	1.962	0.038
100	228	1.897	1.936	0.039
100	232	1.946	1.982	0.036
100	233	1.971	1.995	0.024
100	236	1.870	1.894	0.024
100	238	1.916	1.945	0.029
100	243	1.867	1.883	0.016
105	244	1.877	1.909	0.032
105	246	1.853	1.901	0.048
105	248	1.916	1.943	0.027
105	374	1.907	1.941	0.034
105	376	1.916	1.942	0.026
105	Max	1.983	1.995	0.049
105	Average	1.909	1.919	0.010
105	Min	1.841	1.832	-0.024
105	Std Dev	0.034	0.037	0.019



5.4 IDD DIS 14	
Test Site	
Tester	
Test Number	
Max Limit	3 mA
Min Limit	3 mA

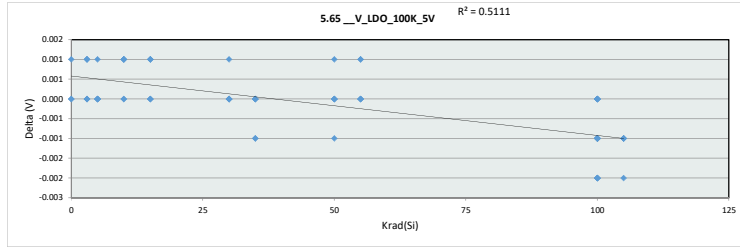
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.865	1.840	1.855	1.832	1.846	1.854	1.862	1.888	1.868	1.864	1.901
Average	1.898	1.894	1.917	1.891	1.919	1.900	1.927	1.927	1.906	1.937	1.927
Max	1.918	1.933	1.980	1.936	1.983	1.941	1.978	1.959	1.947	1.995	1.943
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000



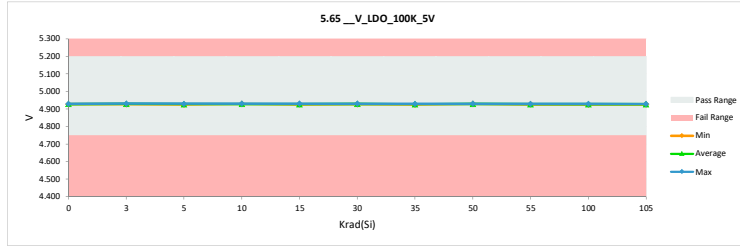
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5.65 V LDO 100K 5V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	5.2 5.2
Min Limit	4.75 4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.929	4.929	0.000
0	118	4.927	4.927	0.000
0	122	4.927	4.928	0.001
3	61	4.927	4.927	0.000
3	62	4.927	4.928	0.001
3	63	4.927	4.928	0.001
3	64	4.928	4.928	0.000
3	65	4.930	4.931	0.001
3	66	4.930	4.930	0.000
5	67	4.929	4.929	0.000
5	68	4.926	4.926	0.000
5	69	4.929	4.929	0.000
5	70	4.925	4.925	0.000
5	71	4.926	4.927	0.001
5	72	4.926	4.926	0.000
10	73	4.927	4.928	0.001
10	74	4.927	4.928	0.001
10	75	4.927	4.928	0.001
10	76	4.928	4.928	0.000
10	77	4.930	4.930	0.000
10	78	4.930	4.931	0.001
15	79	4.929	4.929	0.000
15	80	4.926	4.926	0.000
15	81	4.929	4.930	0.001
15	82	4.925	4.926	0.001
15	83	4.926	4.927	0.001
15	84	4.926	4.926	0.000
30	86	4.927	4.927	0.000
30	89	4.927	4.927	0.000
30	91	4.927	4.927	0.000
30	92	4.928	4.928	0.000
30	94	4.930	4.930	0.000
30	95	4.930	4.931	0.001
35	100	4.929	4.928	-0.001
35	101	4.926	4.925	-0.001
35	102	4.929	4.929	0.000
35	103	4.925	4.925	0.000
35	104	4.926	4.926	0.000
35	105	4.926	4.926	0.000
50	106	4.927	4.928	0.001
50	107	4.929	4.929	0.000
50	108	4.930	4.929	-0.001
50	109	4.928	4.928	0.000
50	110	4.928	4.928	0.000
50	111	4.930	4.930	0.000
55	113	4.927	4.927	0.000
55	114	4.928	4.928	0.000
55	115	4.927	4.927	0.000
55	116	4.927	4.928	0.001
55	126	4.926	4.926	0.000
55	128	4.927	4.928	0.001
100	129	4.927	4.926	-0.001
100	131	4.929	4.929	0.000
100	132	4.927	4.927	0.000
100	133	4.927	4.926	-0.001
100	134	4.929	4.928	-0.001
100	135	4.929	4.929	0.000
100	138	4.928	4.927	-0.001
100	139	4.930	4.929	-0.001
100	140	4.928	4.927	-0.001
100	141	4.930	4.929	-0.001
100	142	4.927	4.926	-0.001
100	143	4.928	4.926	-0.002
100	144	4.929	4.928	-0.001
100	220	4.927	4.927	0.000
100	221	4.929	4.929	0.000
100	224	4.929	4.927	-0.002
100	225	4.931	4.929	-0.002
100	226	4.926	4.925	-0.001
100	228	4.929	4.927	-0.002
100	232	4.929	4.927	-0.002
100	233	4.928	4.927	-0.001
100	236	4.930	4.929	-0.001
100	238	4.927	4.926	-0.001
100	243	4.929	4.929	0.000
105	244	4.928	4.927	-0.001
105	246	4.930	4.928	-0.002
105	248	4.928	4.927	-0.001
105	374	4.927	4.926	-0.001
105	376	4.929	4.928	-0.001
105	Max	4.931	4.931	0.001
105	Average	4.928	4.928	0.000
105	Min	4.925	4.925	-0.002
105	Std Dev	0.001	0.001	0.001



5.65 V LDO 10										
Test Site										
Tester										
Test Number										
Max Limit	5.2 V									
Min Limit	4.75 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.927	4.927	4.925	4.928	4.926	4.927	4.925	4.928	4.926	4.925
Average	4.928	4.929	4.927	4.929	4.927	4.928	4.927	4.929	4.927	4.927
Max	4.929	4.931	4.929	4.931	4.930	4.931	4.929	4.930	4.928	4.928
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

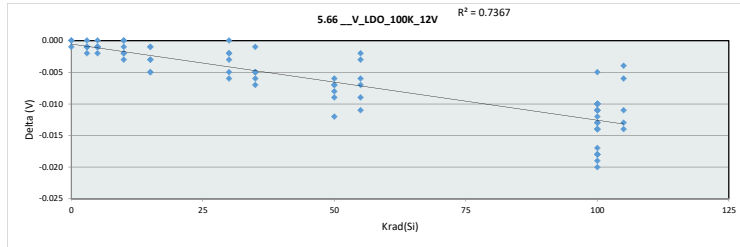




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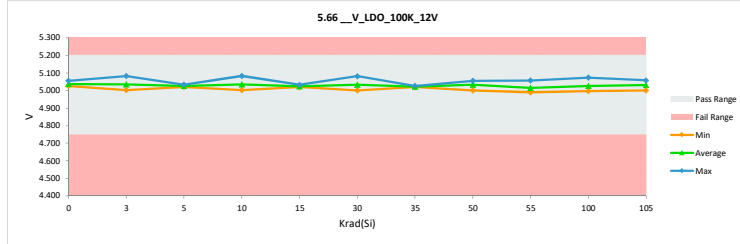
5.66 V_LDO_100K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.029	5.028	-0.001
0	118	5.055	5.055	0.000
0	122	5.026	5.026	0.000
3	61	5.047	5.046	-0.001
3	62	5.010	5.009	-0.001
3	63	5.003	5.002	-0.001
3	64	5.040	5.040	0.000
3	65	5.083	5.082	-0.001
3	66	5.029	5.027	-0.002
5	67	5.024	5.022	-0.002
5	68	5.021	5.020	-0.001
5	69	5.026	5.025	-0.001
5	70	5.033	5.033	0.000
5	71	5.029	5.028	-0.001
5	72	5.026	5.025	-0.001
10	73	5.047	5.045	-0.002
10	74	5.010	5.007	-0.003
10	75	5.003	5.002	-0.001
10	76	5.040	5.040	0.000
10	77	5.083	5.083	0.000
10	78	5.029	5.027	-0.002
15	79	5.024	5.019	-0.005
15	80	5.021	5.020	-0.001
15	81	5.026	5.023	-0.003
15	82	5.033	5.032	-0.001
15	83	5.029	5.026	-0.003
15	84	5.026	5.021	-0.005
30	86	5.047	5.047	0.000
30	89	5.010	5.005	-0.005
30	91	5.003	5.000	-0.003
30	92	5.040	5.034	-0.006
30	94	5.083	5.081	-0.002
30	95	5.029	5.027	-0.002
35	100	5.024	5.019	-0.005
35	101	5.021	5.020	-0.001
35	102	5.026	5.021	-0.005
35	103	5.033	5.026	-0.007
35	104	5.029	5.024	-0.005
35	105	5.026	5.020	-0.006
50	106	5.038	5.031	-0.007
50	107	5.053	5.041	-0.012
50	108	5.056	5.047	-0.009
50	109	5.008	5.000	-0.008
50	110	5.061	5.055	-0.006
50	111	5.029	5.022	-0.007
55	113	5.058	5.056	-0.002
55	114	5.013	5.002	-0.011
55	115	5.005	4.998	-0.007
55	116	5.019	5.013	-0.006
55	126	5.033	5.030	-0.003
55	128	4.999	4.990	-0.009
100	129	5.062	5.051	-0.011
100	131	5.021	5.011	-0.010
100	132	5.025	5.012	-0.013
100	133	5.074	5.061	-0.013
100	134	5.032	5.022	-0.010
100	135	5.019	5.000	-0.019
100	138	5.032	5.019	-0.013
100	139	5.040	5.030	-0.010
100	140	5.041	5.031	-0.010
100	141	5.025	5.007	-0.018
100	142	5.028	5.018	-0.010
100	143	5.029	5.018	-0.011
100	144	5.023	5.018	-0.005
100	220	5.025	5.008	-0.017
100	221	5.016	5.002	-0.014
100	224	5.039	5.027	-0.012
100	225	5.087	5.073	-0.014
100	226	5.059	5.045	-0.014
100	228	5.047	5.036	-0.011
100	232	5.043	5.025	-0.018
100	233	5.051	5.040	-0.011
100	236	5.051	5.033	-0.018
100	238	5.014	4.996	-0.018
100	243	5.048	5.028	-0.020
105	244	5.042	5.036	-0.006
105	246	5.014	5.000	-0.014
105	248	5.051	5.040	-0.011
105	374	5.061	5.057	-0.004
105	376	5.036	5.023	-0.013
	Max	5.087	5.083	0.000
	Average	5.034	5.027	-0.007
	Min	4.999	4.990	-0.020
	Std Dev	0.019	0.020	0.006



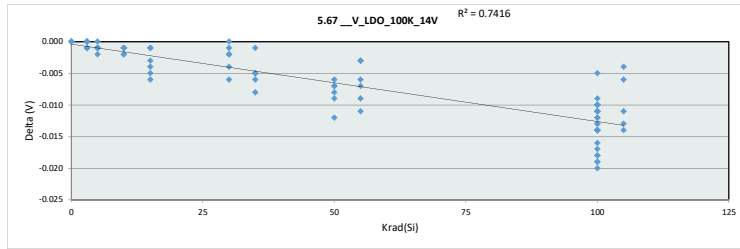
5.66 V_LDO_100	
Test Site	
Tester	
Test Number	
Max Limit	5.2
Min Limit	4.75

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.026	5.002	5.020	5.002	5.019	5.000	5.019	5.000	4.990	4.996	5.000
Average	5.036	5.034	5.026	5.034	5.024	5.032	5.022	5.033	5.015	5.025	5.031
Max	5.055	5.082	5.033	5.083	5.032	5.081	5.026	5.055	5.056	5.073	5.057
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

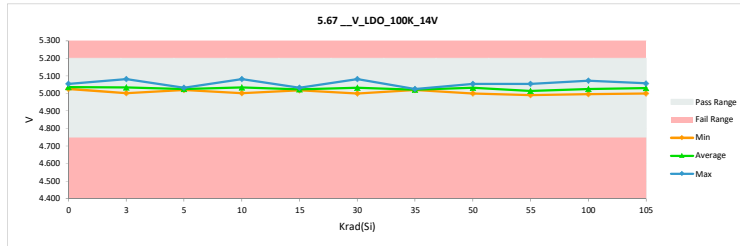


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5.67 V_LDO_100K_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.028	5.028	0.000
0	118	5.055	5.055	0.000
0	122	5.026	5.026	0.000
3	61	5.047	5.046	-0.001
3	62	5.009	5.009	0.000
3	63	5.002	5.002	0.000
3	64	5.040	5.040	0.000
3	65	5.083	5.082	-0.001
3	66	5.028	5.027	-0.001
5	67	5.024	5.022	-0.002
5	68	5.021	5.020	-0.001
5	69	5.026	5.025	-0.001
5	70	5.033	5.033	0.000
5	71	5.029	5.028	-0.001
5	72	5.026	5.025	-0.001
10	73	5.047	5.045	-0.002
10	74	5.009	5.007	-0.002
10	75	5.002	5.001	-0.001
10	76	5.040	5.039	-0.001
10	77	5.083	5.082	-0.001
10	78	5.028	5.027	-0.001
15	79	5.024	5.018	-0.006
15	80	5.021	5.020	-0.001
15	81	5.026	5.023	-0.003
15	82	5.033	5.032	-0.001
15	83	5.029	5.025	-0.004
15	84	5.026	5.021	-0.005
30	86	5.047	5.046	-0.001
30	89	5.009	5.005	-0.004
30	91	5.026	5.020	-0.006
30	92	5.040	5.034	-0.006
30	94	5.083	5.081	-0.002
30	95	5.028	5.028	0.000
35	100	5.024	5.019	-0.005
35	101	5.021	5.020	-0.001
35	102	5.026	5.021	-0.005
35	103	5.033	5.025	-0.008
35	104	5.029	5.024	-0.005
35	105	5.026	5.020	-0.006
50	106	5.038	5.031	-0.007
50	107	5.053	5.041	-0.012
50	108	5.056	5.047	-0.009
50	109	5.008	5.000	-0.008
50	110	5.061	5.055	-0.006
50	111	5.029	5.022	-0.007
55	113	5.058	5.055	-0.003
55	114	5.013	5.002	-0.011
55	115	5.005	4.998	-0.007
55	116	5.019	5.013	-0.006
55	126	5.033	5.030	-0.003
55	128	4.999	4.990	-0.009
100	129	5.062	5.051	-0.011
100	131	5.021	5.010	-0.011
100	132	5.024	5.012	-0.012
100	133	5.075	5.061	-0.014
100	134	5.032	5.022	-0.010
100	135	5.019	5.000	-0.019
100	138	5.032	5.019	-0.013
100	139	5.040	5.030	-0.010
100	140	5.041	5.032	-0.009
100	141	5.025	5.007	-0.018
100	142	5.028	5.018	-0.010
100	143	5.029	5.018	-0.011
100	144	5.023	5.018	-0.005
100	220	5.024	5.008	-0.016
100	221	5.016	5.002	-0.014
100	224	5.040	5.027	-0.013
100	225	5.087	5.073	-0.014
100	226	5.059	5.045	-0.014
100	228	5.047	5.036	-0.011
100	232	5.042	5.025	-0.017
100	233	5.051	5.039	-0.012
100	236	5.051	5.032	-0.019
100	238	5.014	4.996	-0.018
100	243	5.048	5.028	-0.020
105	244	5.042	5.036	-0.006
105	246	5.014	5.000	-0.014
105	248	5.051	5.040	-0.011
105	374	5.061	5.057	-0.004
105	376	5.036	5.023	-0.013
105	Max	5.087	5.082	0.000
105	Average	5.034	5.027	-0.007
105	Min	4.999	4.990	-0.020
105	Std Dev	0.020	0.020	0.006

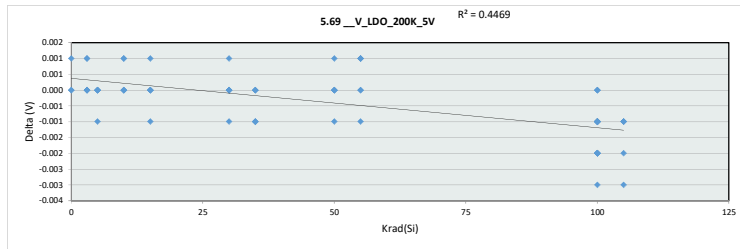


5.67 V_LDO_100											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.026	5.002	5.020	5.001	5.018	5.000	5.019	5.000	4.990	4.996	5.000
Average	5.036	5.034	5.026	5.034	5.023	5.032	5.022	5.033	5.015	5.025	5.031
Max	5.055	5.082	5.033	5.082	5.032	5.081	5.025	5.055	5.055	5.073	5.057
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

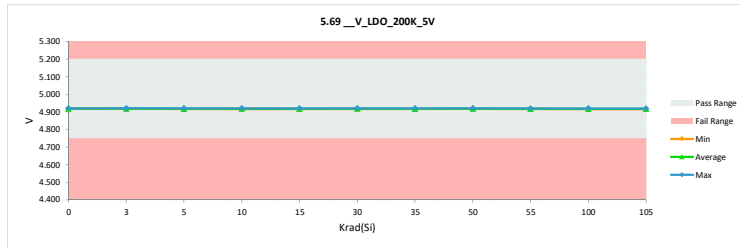


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5.69_V_LDO_200K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.921	4.921	0.000
0	118	4.919	4.919	0.000
0	122	4.918	4.919	0.001
3	61	4.918	4.919	0.001
3	62	4.918	4.919	0.001
3	63	4.919	4.919	0.000
3	64	4.920	4.920	0.000
3	65	4.922	4.922	0.000
3	66	4.922	4.922	0.000
5	67	4.921	4.920	-0.001
5	68	4.917	4.917	0.000
5	69	4.921	4.921	0.000
5	70	4.916	4.916	0.000
5	71	4.918	4.918	0.000
5	72	4.917	4.917	0.000
10	73	4.918	4.919	0.001
10	74	4.918	4.919	0.001
10	75	4.919	4.919	0.000
10	76	4.920	4.920	0.000
10	77	4.922	4.922	0.000
10	78	4.922	4.922	0.000
15	79	4.921	4.920	-0.001
15	80	4.917	4.918	0.001
15	81	4.921	4.921	0.000
15	82	4.916	4.916	0.000
15	83	4.918	4.918	0.000
15	84	4.917	4.917	0.000
30	86	4.918	4.918	0.000
30	89	4.918	4.919	0.001
30	91	4.919	4.918	-0.001
30	92	4.920	4.920	0.000
30	94	4.922	4.922	0.000
30	95	4.922	4.922	0.000
35	100	4.921	4.920	-0.001
35	101	4.917	4.916	-0.001
35	102	4.921	4.921	0.000
35	103	4.916	4.916	0.000
35	104	4.918	4.917	-0.001
35	105	4.917	4.917	0.000
50	106	4.918	4.918	0.000
50	107	4.921	4.920	-0.001
50	108	4.921	4.921	0.000
50	109	4.920	4.920	0.000
50	110	4.919	4.919	0.000
50	111	4.921	4.922	0.001
55	113	4.918	4.919	0.001
55	114	4.919	4.920	0.001
55	115	4.918	4.918	0.000
55	116	4.919	4.918	-0.001
55	126	4.918	4.918	0.000
55	128	4.918	4.919	0.001
100	129	4.919	4.917	-0.002
100	131	4.921	4.921	0.000
100	132	4.919	4.918	-0.001
100	133	4.919	4.918	-0.001
100	134	4.921	4.920	-0.001
100	135	4.920	4.920	0.000
100	138	4.920	4.918	-0.002
100	139	4.922	4.920	-0.002
100	140	4.920	4.918	-0.002
100	141	4.921	4.921	0.000
100	142	4.918	4.917	-0.001
100	143	4.919	4.917	-0.002
100	144	4.921	4.920	-0.001
100	220	4.919	4.918	-0.001
100	221	4.921	4.920	-0.001
100	224	4.921	4.919	-0.002
100	225	4.922	4.921	-0.001
100	226	4.917	4.916	-0.001
100	228	4.920	4.918	-0.002
100	232	4.921	4.918	-0.003
100	233	4.920	4.919	-0.001
100	236	4.922	4.921	-0.001
100	238	4.918	4.917	-0.001
100	243	4.921	4.920	-0.001
105	244	4.919	4.918	-0.001
105	246	4.922	4.919	-0.003
105	248	4.919	4.918	-0.001
105	374	4.919	4.917	-0.002
105	376	4.921	4.920	-0.001
	Max	4.922	4.922	0.001
	Average	4.919	4.919	0.000
	Min	4.916	4.916	-0.003
	Std Dev	0.002	0.002	0.001



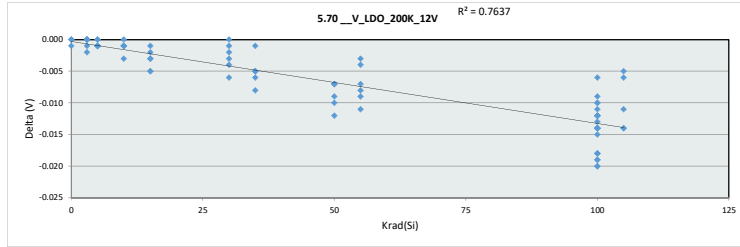
5.69_V_LDO_200											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.919	4.919	4.916	4.919	4.916	4.918	4.916	4.918	4.918	4.916	4.917
Average	4.920	4.920	4.918	4.920	4.918	4.920	4.918	4.920	4.919	4.919	4.918
Max	4.921	4.922	4.921	4.922	4.921	4.922	4.921	4.922	4.920	4.921	4.920
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



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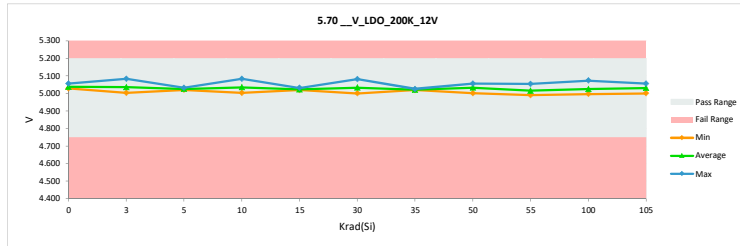
5.70_V_LDO_200K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.030	5.030	0.000
0	118	5.056	5.056	0.000
0	122	5.029	5.028	-0.001
3	61	5.048	5.047	-0.001
3	62	5.011	5.011	0.000
3	63	5.004	5.004	0.000
3	64	5.042	5.042	0.000
3	65	5.084	5.084	0.000
3	66	5.030	5.028	-0.002
5	67	5.025	5.024	-0.001
5	68	5.022	5.021	-0.001
5	69	5.027	5.026	-0.001
5	70	5.034	5.033	-0.001
5	71	5.030	5.030	0.000
5	72	5.026	5.026	0.000
10	73	5.048	5.047	-0.001
10	74	5.011	5.008	-0.003
10	75	5.004	5.003	-0.001
10	76	5.042	5.041	-0.001
10	77	5.084	5.084	0.000
10	78	5.030	5.029	-0.001
15	79	5.025	5.020	-0.005
15	80	5.022	5.021	-0.001
15	81	5.027	5.024	-0.003
15	82	5.034	5.032	-0.002
15	83	5.030	5.027	-0.003
15	84	5.026	5.021	-0.005
30	86	5.048	5.048	0.000
30	89	5.011	5.007	-0.004
30	91	5.001	5.001	0.000
30	92	5.042	5.036	-0.006
30	94	5.084	5.082	-0.002
30	95	5.030	5.029	-0.001
35	100	5.025	5.020	-0.005
35	101	5.022	5.021	-0.001
35	102	5.027	5.022	-0.005
35	103	5.034	5.026	-0.008
35	104	5.030	5.025	-0.005
35	105	5.026	5.020	-0.006
50	106	5.040	5.031	-0.009
50	107	5.055	5.043	-0.012
50	108	5.057	5.047	-0.010
50	109	5.009	5.002	-0.007
50	110	5.063	5.056	-0.007
50	111	5.030	5.023	-0.007
55	113	5.059	5.055	-0.004
55	114	5.014	5.003	-0.011
55	115	5.006	4.998	-0.008
55	116	5.021	5.014	-0.007
55	126	5.034	5.031	-0.003
55	128	5.000	4.991	-0.009
100	129	5.063	5.051	-0.012
100	131	5.023	5.011	-0.012
100	132	5.026	5.012	-0.014
100	133	5.076	5.062	-0.014
100	134	5.033	5.023	-0.010
100	135	5.019	5.001	-0.018
100	138	5.033	5.019	-0.014
100	139	5.041	5.031	-0.010
100	140	5.042	5.033	-0.009
100	141	5.027	5.007	-0.020
100	142	5.029	5.018	-0.011
100	143	5.031	5.019	-0.012
100	144	5.024	5.018	-0.006
100	220	5.026	5.008	-0.018
100	221	5.017	5.002	-0.015
100	224	5.041	5.028	-0.013
100	225	5.088	5.074	-0.014
100	226	5.059	5.045	-0.014
100	228	5.048	5.036	-0.012
100	232	5.044	5.026	-0.018
100	233	5.052	5.040	-0.012
100	236	5.052	5.033	-0.019
100	238	5.016	4.997	-0.019
100	243	5.049	5.029	-0.020
105	244	5.043	5.037	-0.006
105	246	5.015	5.001	-0.014
105	248	5.052	5.041	-0.011
105	374	5.062	5.057	-0.005
105	376	5.038	5.024	-0.014
105	Max	5.088	5.084	0.000
105	Average	5.035	5.028	-0.007
105	Min	5.000	4.991	-0.020
105	Std Dev	0.019	0.020	0.006



5.70_V_LDO_200	
Test Site	
Tester	
Test Number	
Max Limit	5.2 V
Min Limit	4.75 V

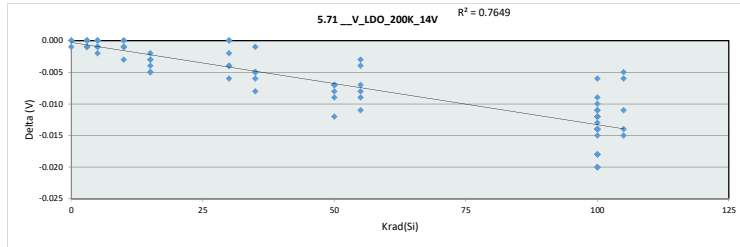
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.028	5.004	5.021	5.003	5.020	5.001	5.020	5.002	4.991	4.997	5.001
Average	5.038	5.036	5.027	5.035	5.024	5.034	5.022	5.034	5.015	5.026	5.032
Max	5.056	5.084	5.033	5.084	5.032	5.082	5.026	5.056	5.055	5.074	5.057
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



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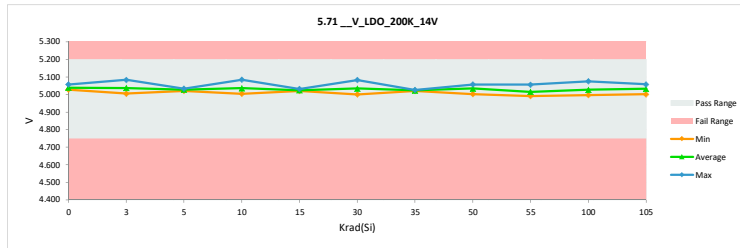
5.71 V_LDO_200K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.030	5.029	-0.001
0	118	5.056	5.056	0.000
0	122	5.028	5.028	0.000
3	61	5.048	5.047	-0.001
3	62	5.011	5.011	0.000
3	63	5.004	5.004	0.000
3	64	5.042	5.042	0.000
3	65	5.084	5.083	-0.001
3	66	5.029	5.028	-0.001
5	67	5.025	5.024	-0.001
5	68	5.022	5.020	-0.002
5	69	5.027	5.027	0.000
5	70	5.034	5.033	-0.001
5	71	5.030	5.030	0.000
5	72	5.026	5.026	0.000
10	73	5.048	5.047	-0.001
10	74	5.011	5.008	-0.003
10	75	5.004	5.003	-0.001
10	76	5.042	5.041	-0.001
10	77	5.084	5.084	0.000
10	78	5.029	5.029	0.000
15	79	5.025	5.020	-0.005
15	80	5.022	5.020	-0.002
15	81	5.027	5.024	-0.003
15	82	5.034	5.031	-0.003
15	83	5.030	5.026	-0.004
15	84	5.026	5.021	-0.005
30	86	5.048	5.048	0.000
30	89	5.011	5.007	-0.004
30	91	5.004	5.000	-0.004
30	92	5.042	5.036	-0.006
30	94	5.084	5.082	-0.002
30	95	5.029	5.029	0.000
35	100	5.025	5.020	-0.005
35	101	5.022	5.021	-0.001
35	102	5.027	5.022	-0.005
35	103	5.034	5.026	-0.008
35	104	5.030	5.025	-0.005
35	105	5.026	5.020	-0.006
50	106	5.039	5.031	-0.008
50	107	5.055	5.043	-0.012
50	108	5.057	5.048	-0.009
50	109	5.009	5.002	-0.007
50	110	5.063	5.056	-0.007
50	111	5.030	5.023	-0.007
55	113	5.059	5.055	-0.004
55	114	5.014	5.003	-0.011
55	115	5.006	4.998	-0.008
55	116	5.021	5.014	-0.007
55	126	5.034	5.031	-0.003
55	128	5.000	4.991	-0.009
100	129	5.063	5.051	-0.012
100	131	5.023	5.011	-0.012
100	132	5.026	5.012	-0.014
100	133	5.076	5.062	-0.014
100	134	5.033	5.023	-0.010
100	135	5.019	5.001	-0.018
100	138	5.033	5.019	-0.014
100	139	5.041	5.030	-0.011
100	140	5.042	5.033	-0.009
100	141	5.027	5.007	-0.020
100	142	5.029	5.018	-0.011
100	143	5.030	5.018	-0.012
100	144	5.024	5.018	-0.006
100	220	5.026	5.008	-0.018
100	221	5.017	5.002	-0.015
100	224	5.041	5.028	-0.013
100	225	5.088	5.074	-0.014
100	226	5.059	5.045	-0.014
100	228	5.048	5.036	-0.012
100	232	5.044	5.026	-0.018
100	233	5.052	5.040	-0.012
100	236	5.051	5.033	-0.018
100	238	5.016	4.996	-0.020
100	243	5.049	5.029	-0.020
105	244	5.043	5.037	-0.006
105	246	5.016	5.001	-0.015
105	248	5.052	5.041	-0.011
105	374	5.062	5.057	-0.005
105	376	5.038	5.024	-0.014
	Max	5.088	5.084	0.000
	Average	5.035	5.028	-0.007
	Min	5.000	4.991	-0.010
	Std Dev	0.019	0.020	0.006



5.71 V_LDO_200	
Test Site	
Tester	
Test Number	
Max Limit	5.2
Min Limit	4.75

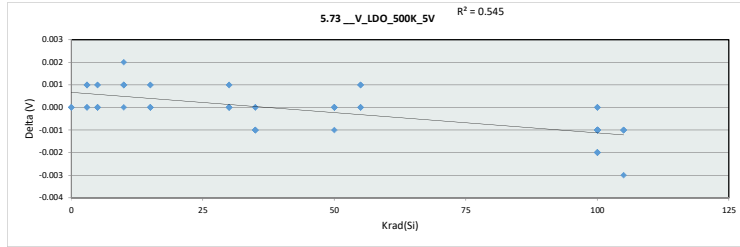
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.028	5.004	5.020	5.003	5.020	5.000	5.020	5.002	4.991	4.996	5.001
Average	5.038	5.036	5.027	5.035	5.024	5.034	5.022	5.034	5.015	5.026	5.032
Max	5.056	5.083	5.033	5.084	5.031	5.082	5.026	5.056	5.055	5.074	5.057
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



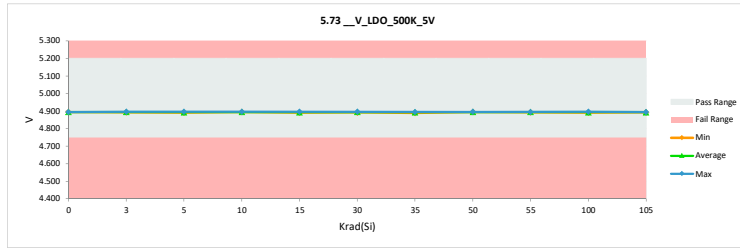
LDR TID Report  
TPS7H5001-SP

5.73 V_LDO_500K_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.894	4.894	0.000
0	118	4.893	4.893	0.000
0	122	4.893	4.893	0.000
3	61	4.891	4.892	0.001
3	62	4.892	4.893	0.001
3	63	4.893	4.893	0.000
3	64	4.893	4.894	0.001
3	65	4.895	4.896	0.001
3	66	4.895	4.895	0.000
5	67	4.894	4.895	0.001
5	68	4.891	4.891	0.000
5	69	4.895	4.895	0.000
5	70	4.890	4.890	0.000
5	71	4.891	4.892	0.001
5	72	4.892	4.892	0.000
10	73	4.891	4.893	0.002
10	74	4.892	4.893	0.001
10	75	4.893	4.893	0.000
10	76	4.893	4.894	0.001
10	77	4.895	4.896	0.001
10	78	4.895	4.896	0.001
15	79	4.894	4.894	0.000
15	80	4.891	4.892	0.001
15	81	4.895	4.895	0.000
15	82	4.890	4.890	0.000
15	83	4.891	4.892	0.001
15	84	4.892	4.892	0.000
30	86	4.891	4.891	0.000
30	89	4.892	4.893	0.001
30	91	4.893	4.893	0.000
30	92	4.893	4.894	0.001
30	94	4.895	4.895	0.000
30	95	4.895	4.895	0.000
35	100	4.894	4.894	0.000
35	101	4.891	4.890	-0.001
35	102	4.895	4.894	-0.001
35	103	4.890	4.889	-0.001
35	104	4.891	4.891	0.000
35	105	4.892	4.891	-0.001
50	106	4.893	4.893	0.000
50	107	4.895	4.894	-0.001
50	108	4.895	4.895	0.000
50	109	4.894	4.894	0.000
50	110	4.893	4.893	0.000
50	111	4.895	4.895	0.000
55	113	4.893	4.893	0.000
55	114	4.893	4.894	0.001
55	115	4.892	4.893	0.001
55	116	4.893	4.893	0.000
55	126	4.892	4.892	0.000
55	128	4.892	4.893	0.001
100	129	4.893	4.891	-0.002
100	131	4.894	4.894	0.000
100	132	4.893	4.892	-0.001
100	133	4.893	4.892	-0.001
100	134	4.894	4.893	-0.001
100	135	4.894	4.894	0.000
100	138	4.894	4.892	-0.002
100	139	4.895	4.894	-0.001
100	140	4.893	4.892	-0.001
100	141	4.895	4.894	-0.001
100	142	4.892	4.891	-0.001
100	143	4.893	4.891	-0.002
100	144	4.895	4.894	-0.001
100	220	4.893	4.892	-0.001
100	221	4.895	4.894	-0.001
100	224	4.895	4.893	-0.002
100	225	4.895	4.894	-0.001
100	226	4.891	4.890	-0.001
100	228	4.894	4.892	-0.002
100	232	4.894	4.892	-0.002
100	233	4.894	4.893	-0.001
100	236	4.895	4.895	0.000
100	238	4.893	4.891	-0.002
100	243	4.895	4.894	-0.001
105	244	4.893	4.892	-0.001
105	246	4.896	4.893	-0.003
105	248	4.893	4.892	-0.001
105	374	4.893	4.892	-0.001
105	376	4.895	4.894	-0.001
	Max	4.896	4.896	0.002
	Average	4.893	4.893	0.000
	Min	4.890	4.889	-0.003
	Std Dev	0.002	0.002	0.001



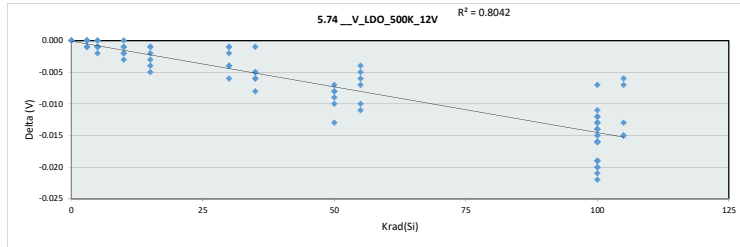
5.73 V_LDO_500K_5V										
Test Site										
Tester										
Test Number										
Max Limit	5.2 V									
Min Limit	4.75 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.893	4.894	4.890	4.893	4.890	4.891	4.889	4.893	4.892	4.890
Average	4.893	4.894	4.893	4.894	4.893	4.894	4.892	4.894	4.893	4.893
Max	4.894	4.896	4.895	4.896	4.895	4.895	4.894	4.895	4.894	4.895
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



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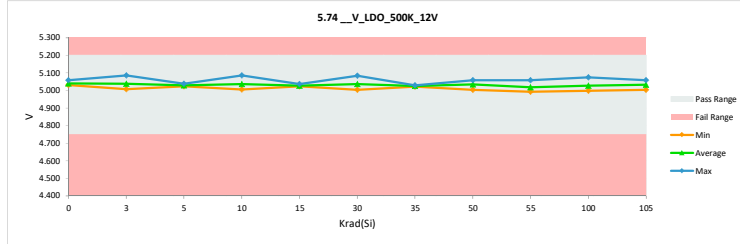
5.74_V_LDO_500K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.032	5.032	0.000
0	118	5.058	5.058	0.000
0	122	5.031	5.031	0.000
3	61	5.051	5.050	-0.001
3	62	5.013	5.013	0.000
3	63	5.007	5.007	0.000
3	64	5.044	5.044	0.000
3	65	5.086	5.085	-0.001
3	66	5.032	5.031	-0.001
5	67	5.028	5.026	-0.002
5	68	5.025	5.024	-0.001
5	69	5.029	5.029	0.000
5	70	5.038	5.038	0.000
5	71	5.033	5.032	-0.001
5	72	5.030	5.029	-0.001
10	73	5.051	5.049	-0.002
10	74	5.013	5.010	-0.003
10	75	5.007	5.005	-0.002
10	76	5.044	5.044	0.000
10	77	5.086	5.085	-0.001
10	78	5.032	5.031	-0.001
15	79	5.028	5.023	-0.005
15	80	5.025	5.024	-0.001
15	81	5.029	5.027	-0.002
15	82	5.038	5.037	-0.001
15	83	5.033	5.030	-0.003
15	84	5.030	5.026	-0.004
30	86	5.051	5.050	-0.001
30	89	5.013	5.009	-0.004
30	91	5.007	5.003	-0.004
30	92	5.044	5.038	-0.006
30	94	5.086	5.084	-0.002
30	95	5.032	5.031	-0.001
35	100	5.028	5.022	-0.006
35	101	5.025	5.024	-0.001
35	102	5.029	5.024	-0.005
35	103	5.038	5.030	-0.008
35	104	5.033	5.028	-0.005
35	105	5.030	5.024	-0.006
50	106	5.043	5.035	-0.008
50	107	5.058	5.045	-0.013
50	108	5.059	5.049	-0.010
50	109	5.011	5.003	-0.008
50	110	5.065	5.058	-0.007
50	111	5.032	5.023	-0.009
55	113	5.064	5.059	-0.005
55	114	5.016	5.005	-0.011
55	115	5.008	5.001	-0.007
55	116	5.023	5.017	-0.006
55	126	5.037	5.033	-0.004
55	128	5.003	4.993	-0.010
100	129	5.068	5.054	-0.014
100	131	5.025	5.012	-0.013
100	132	5.029	5.013	-0.016
100	133	5.079	5.063	-0.016
100	134	5.036	5.024	-0.012
100	135	5.021	5.001	-0.020
100	138	5.034	5.020	-0.014
100	139	5.044	5.032	-0.012
100	140	5.045	5.034	-0.011
100	141	5.030	5.009	-0.021
100	142	5.032	5.020	-0.012
100	143	5.033	5.020	-0.013
100	144	5.026	5.019	-0.007
100	220	5.029	5.010	-0.019
100	221	5.019	5.003	-0.016
100	224	5.044	5.029	-0.015
100	225	5.090	5.075	-0.015
100	226	5.063	5.047	-0.016
100	228	5.051	5.038	-0.013
100	232	5.047	5.028	-0.019
100	233	5.055	5.041	-0.014
100	236	5.053	5.034	-0.019
100	238	5.018	4.998	-0.020
100	243	5.052	5.030	-0.022
105	244	5.045	5.038	-0.007
105	246	5.018	5.003	-0.015
105	248	5.055	5.042	-0.013
105	374	5.065	5.059	-0.006
105	376	5.040	5.025	-0.015
105	Max	5.090	5.085	0.000
105	Average	5.038	5.030	-0.008
105	Min	5.003	4.993	-0.022
105	Std Dev	0.020	0.020	0.006



5.74_V_LDO_50	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

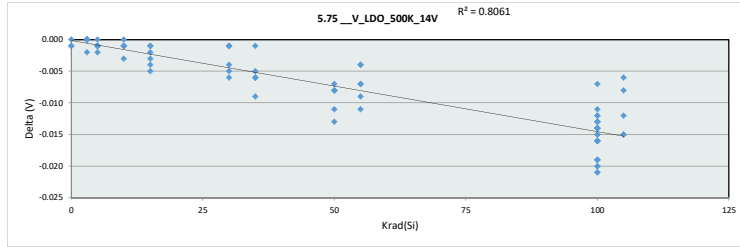
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.031	5.007	5.024	5.005	5.023	5.003	5.022	5.003	4.993	5.003
Average	5.040	5.038	5.030	5.037	5.028	5.036	5.025	5.036	5.018	5.027
Max	5.058	5.085	5.038	5.085	5.037	5.084	5.030	5.058	5.059	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



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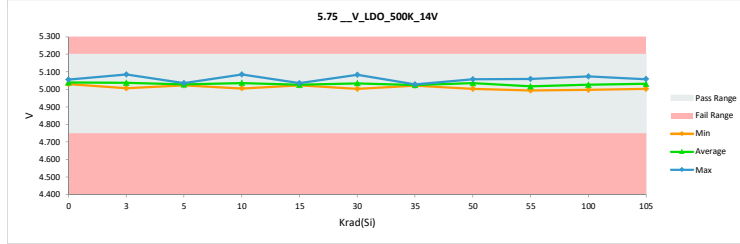
5.75 V_LDO_500K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.032	5.031	-0.001
0	118	5.058	5.057	-0.001
0	122	5.031	5.031	0.000
3	61	5.050	5.050	0.000
3	62	5.013	5.013	0.000
3	63	5.007	5.007	0.000
3	64	5.044	5.044	0.000
3	65	5.085	5.085	0.000
3	66	5.032	5.030	-0.002
5	67	5.028	5.026	-0.002
5	68	5.025	5.024	-0.001
5	69	5.029	5.029	0.000
5	70	5.038	5.037	-0.001
5	71	5.033	5.032	-0.001
5	72	5.030	5.029	-0.001
10	73	5.050	5.049	-0.001
10	74	5.013	5.010	-0.003
10	75	5.007	5.006	-0.001
10	76	5.044	5.043	-0.001
10	77	5.085	5.085	0.000
10	78	5.032	5.031	-0.001
15	79	5.028	5.023	-0.005
15	80	5.025	5.024	-0.001
15	81	5.029	5.027	-0.002
15	82	5.038	5.037	-0.001
15	83	5.033	5.030	-0.003
15	84	5.030	5.026	-0.004
30	86	5.050	5.049	-0.001
30	89	5.013	5.008	-0.005
30	91	5.007	5.003	-0.004
30	92	5.044	5.038	-0.006
30	94	5.085	5.084	-0.001
30	95	5.032	5.031	-0.001
35	100	5.028	5.022	-0.006
35	101	5.025	5.024	-0.001
35	102	5.029	5.023	-0.006
35	103	5.038	5.029	-0.009
35	104	5.033	5.028	-0.005
35	105	5.030	5.024	-0.006
50	106	5.043	5.035	-0.008
50	107	5.058	5.045	-0.013
50	108	5.060	5.049	-0.011
50	109	5.011	5.003	-0.008
50	110	5.065	5.058	-0.007
50	111	5.032	5.024	-0.008
55	113	5.064	5.060	-0.004
55	114	5.016	5.005	-0.011
55	115	5.008	5.001	-0.007
55	116	5.023	5.016	-0.007
55	126	5.037	5.033	-0.004
55	128	5.003	4.994	-0.009
100	129	5.068	5.053	-0.015
100	131	5.025	5.012	-0.013
100	132	5.029	5.013	-0.016
100	133	5.079	5.063	-0.016
100	134	5.036	5.024	-0.012
100	135	5.021	5.001	-0.020
100	138	5.034	5.020	-0.014
100	139	5.044	5.032	-0.012
100	140	5.045	5.034	-0.011
100	141	5.030	5.009	-0.021
100	142	5.032	5.019	-0.013
100	143	5.033	5.020	-0.013
100	144	5.026	5.019	-0.007
100	220	5.029	5.010	-0.019
100	221	5.019	5.003	-0.016
100	224	5.043	5.029	-0.014
100	225	5.090	5.075	-0.015
100	226	5.063	5.047	-0.016
100	228	5.051	5.037	-0.014
100	232	5.047	5.028	-0.019
100	233	5.055	5.041	-0.014
100	236	5.053	5.034	-0.019
100	238	5.018	4.998	-0.020
100	243	5.051	5.030	-0.021
105	244	5.045	5.037	-0.008
105	246	5.018	5.003	-0.015
105	248	5.054	5.042	-0.012
105	374	5.065	5.059	-0.006
105	376	5.040	5.025	-0.015
105	Max	5.090	5.085	0.000
105	Average	5.038	5.030	-0.008
105	Min	5.003	4.994	-0.021
105	Std Dev	0.019	0.020	0.006



5.75 V_LDO_500	
Test Site	
Tester	
Test Number	
Max Limit	5.2 V
Min Limit	4.75 V

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.031	5.007	5.024	5.006	5.023	5.003	5.022	5.003	4.994	4.998	5.003
Average	5.040	5.038	5.030	5.037	5.028	5.036	5.025	5.036	5.018	5.027	5.033
Max	5.057	5.085	5.037	5.085	5.037	5.084	5.029	5.058	5.060	5.075	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

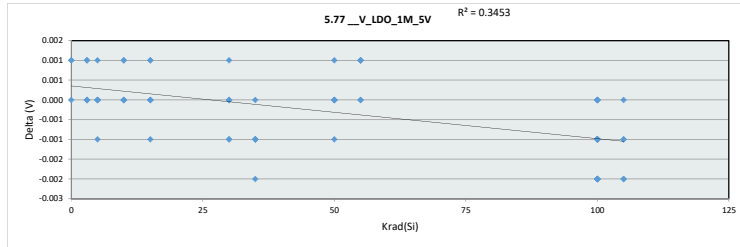




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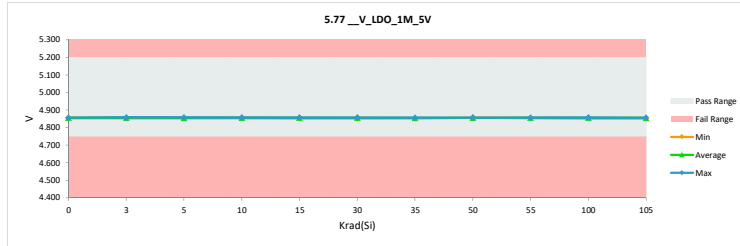
5.77_V_LDO_1M_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.856	4.857	0.001
0	118	4.855	4.856	0.001
0	122	4.855	4.856	0.001
3	61	4.854	4.854	0.000
3	62	4.855	4.855	0.000
3	63	4.856	4.856	0.000
3	64	4.856	4.857	0.001
3	65	4.858	4.859	0.001
3	66	4.858	4.858	0.000
5	67	4.858	4.857	-0.001
5	68	4.854	4.854	0.000
5	69	4.858	4.858	0.000
5	70	4.853	4.853	0.000
5	71	4.854	4.855	0.001
5	72	4.855	4.855	0.000
10	73	4.854	4.855	0.001
10	74	4.855	4.855	0.000
10	75	4.856	4.856	0.000
10	76	4.856	4.857	0.001
10	77	4.858	4.858	0.000
10	78	4.858	4.858	0.000
15	79	4.858	4.857	-0.001
15	80	4.854	4.855	0.001
15	81	4.858	4.858	0.000
15	82	4.853	4.853	0.000
15	83	4.854	4.855	0.001
15	84	4.855	4.855	0.000
30	86	4.854	4.853	-0.001
30	89	4.855	4.855	0.000
30	91	4.856	4.856	0.000
30	92	4.856	4.857	0.001
30	94	4.858	4.858	0.000
30	95	4.858	4.858	0.000
35	100	4.858	4.856	-0.002
35	101	4.854	4.853	-0.001
35	102	4.858	4.857	-0.001
35	103	4.853	4.852	-0.001
35	104	4.854	4.854	0.000
35	105	4.855	4.854	-0.001
50	106	4.856	4.856	0.000
50	107	4.857	4.857	0.000
50	108	4.858	4.857	-0.001
50	109	4.857	4.857	0.000
50	110	4.855	4.856	0.001
50	111	4.858	4.858	0.000
55	113	4.856	4.856	0.000
55	114	4.856	4.857	0.001
55	115	4.855	4.856	0.001
55	116	4.856	4.856	0.000
55	126	4.854	4.855	0.001
55	128	4.855	4.856	0.001
100	129	4.856	4.855	-0.001
100	131	4.857	4.857	0.000
100	132	4.855	4.855	0.000
100	133	4.855	4.854	-0.001
100	134	4.857	4.856	-0.001
100	135	4.857	4.857	0.000
100	138	4.857	4.855	-0.002
100	139	4.858	4.857	-0.001
100	140	4.856	4.855	-0.001
100	141	4.858	4.857	-0.001
100	142	4.855	4.854	-0.001
100	143	4.856	4.854	-0.002
100	144	4.858	4.856	-0.002
100	220	4.855	4.855	0.000
100	221	4.857	4.857	0.000
100	224	4.857	4.856	-0.001
100	225	4.858	4.857	-0.001
100	226	4.854	4.853	-0.001
100	228	4.857	4.855	-0.002
100	232	4.857	4.855	-0.002
100	233	4.856	4.855	-0.001
100	236	4.859	4.858	-0.001
100	238	4.855	4.853	-0.002
100	243	4.858	4.857	-0.001
105	244	4.855	4.855	0.000
105	246	4.858	4.856	-0.002
105	248	4.856	4.854	-0.002
105	374	4.856	4.855	-0.001
105	376	4.858	4.857	-0.001
	Max	4.859	4.859	0.001
	Average	4.856	4.856	0.000
	Min	4.853	4.852	-0.002
	Std Dev	0.002	0.002	0.001



5.77_V_LDO_1M	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

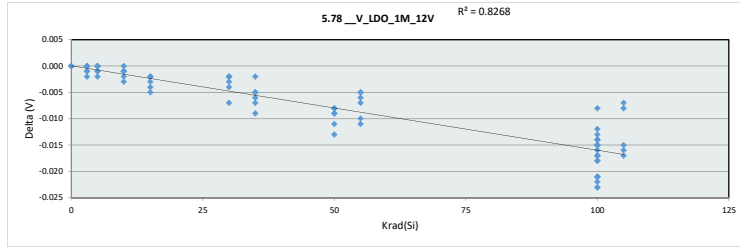
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.855	4.854	4.853	4.855	4.853	4.853	4.852	4.856	4.855	4.853	4.854
Average	4.856	4.857	4.855	4.857	4.856	4.856	4.854	4.857	4.856	4.856	4.855
Max	4.857	4.859	4.858	4.858	4.858	4.858	4.857	4.858	4.857	4.858	4.857
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



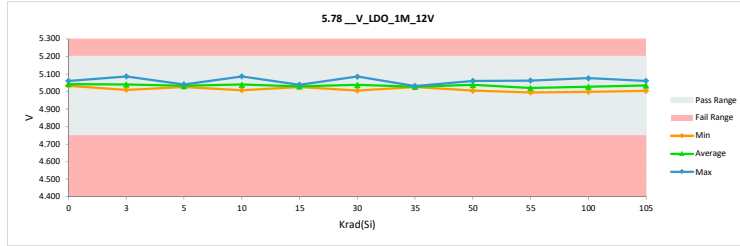
LDR TID Report  
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		5.78 V_LDO_1M_12V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	5.2	5.2	
Min Limit	4.75	4.75	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.033	5.033	0.000
0	118	5.059	5.059	0.000
0	122	5.033	5.033	0.000
3	61	5.052	5.051	-0.001
3	62	5.014	5.014	0.000
3	63	5.008	5.008	0.000
3	64	5.046	5.046	0.000
3	65	5.086	5.085	-0.001
3	66	5.034	5.032	-0.002
5	67	5.030	5.029	-0.001
5	68	5.027	5.025	-0.002
5	69	5.030	5.030	0.000
5	70	5.040	5.040	0.000
5	71	5.034	5.034	0.000
5	72	5.031	5.030	-0.001
10	73	5.052	5.050	-0.002
10	74	5.014	5.011	-0.003
10	75	5.008	5.007	-0.001
10	76	5.046	5.045	-0.001
10	77	5.086	5.086	0.000
10	78	5.034	5.033	-0.001
15	79	5.030	5.025	-0.005
15	80	5.027	5.025	-0.002
15	81	5.030	5.028	-0.002
15	82	5.040	5.038	-0.002
15	83	5.034	5.031	-0.003
15	84	5.031	5.027	-0.004
30	86	5.052	5.050	-0.002
30	89	5.014	5.010	-0.004
30	91	5.008	5.005	-0.003
30	92	5.046	5.039	-0.007
30	94	5.086	5.084	-0.002
30	95	5.034	5.032	-0.002
35	100	5.030	5.024	-0.006
35	101	5.027	5.025	-0.002
35	102	5.030	5.025	-0.005
35	103	5.040	5.031	-0.009
35	104	5.034	5.029	-0.005
35	105	5.031	5.024	-0.007
50	106	5.045	5.037	-0.008
50	107	5.060	5.047	-0.013
50	108	5.061	5.050	-0.011
50	109	5.013	5.004	-0.009
50	110	5.067	5.059	-0.008
50	111	5.032	5.023	-0.009
55	113	5.066	5.061	-0.005
55	114	5.018	5.007	-0.011
55	115	5.009	5.002	-0.007
55	116	5.024	5.018	-0.006
55	126	5.040	5.035	-0.005
55	128	5.004	4.994	-0.010
100	129	5.070	5.054	-0.016
100	131	5.027	5.012	-0.015
100	132	5.030	5.013	-0.017
100	133	5.082	5.064	-0.018
100	134	5.038	5.024	-0.014
100	135	5.022	5.001	-0.021
100	138	5.035	5.018	-0.017
100	139	5.046	5.032	-0.014
100	140	5.047	5.035	-0.012
100	141	5.032	5.009	-0.023
100	142	5.032	5.019	-0.013
100	143	5.034	5.020	-0.014
100	144	5.027	5.019	-0.008
100	220	5.031	5.010	-0.021
100	221	5.021	5.003	-0.018
100	224	5.045	5.030	-0.015
100	225	5.092	5.075	-0.017
100	226	5.064	5.047	-0.017
100	228	5.052	5.037	-0.015
100	232	5.050	5.029	-0.021
100	233	5.057	5.042	-0.015
100	236	5.054	5.033	-0.021
100	238	5.020	4.998	-0.022
100	243	5.053	5.030	-0.023
105	244	5.046	5.038	-0.008
105	246	5.020	5.003	-0.017
105	248	5.057	5.042	-0.015
105	374	5.066	5.059	-0.007
105	376	5.042	5.026	-0.016
105	Max	5.092	5.086	0.000
105	Average	5.039	5.031	-0.008
105	Min	5.004	4.994	-0.023
105	Std Dev	0.020	0.020	0.007



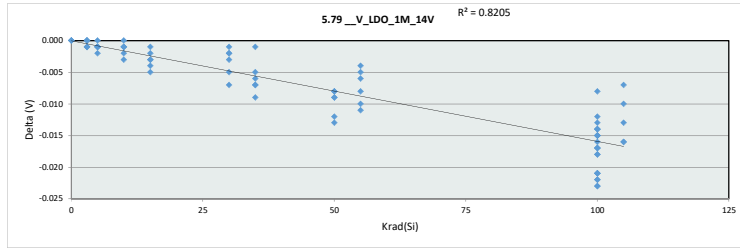
		5.78 V_LDO_1M								
Test Site										
Tester										
Test Number										
Max Limit	5.2	V								
Min Limit	4.75	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.033	5.008	5.025	5.007	5.025	5.005	5.024	5.004	4.994	5.003
Average	5.042	5.039	5.031	5.039	5.029	5.037	5.026	5.037	5.020	5.034
Max	5.059	5.085	5.040	5.086	5.038	5.084	5.031	5.059	5.061	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



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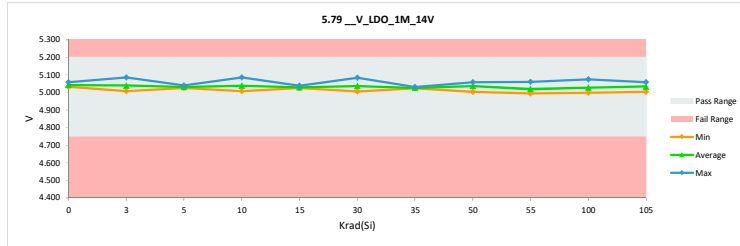
5.79_V_LDO_1M_14V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	5.2 5.2
Min Limit	4.75 4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.033	5.033	0.000
0	118	5.059	5.059	0.000
0	122	5.033	5.033	0.000
3	61	5.052	5.051	-0.001
3	62	5.014	5.014	0.000
3	63	5.008	5.008	0.000
3	64	5.046	5.046	0.000
3	65	5.086	5.085	-0.001
3	66	5.033	5.032	-0.001
5	67	5.030	5.028	-0.002
5	68	5.026	5.025	-0.001
5	69	5.031	5.030	-0.001
5	70	5.040	5.040	0.000
5	71	5.034	5.033	-0.001
5	72	5.031	5.030	-0.001
10	73	5.052	5.050	-0.002
10	74	5.014	5.011	-0.003
10	75	5.008	5.007	-0.001
10	76	5.046	5.045	-0.001
10	77	5.086	5.086	0.000
10	78	5.033	5.032	-0.001
15	79	5.030	5.025	-0.005
15	80	5.026	5.025	-0.001
15	81	5.031	5.028	-0.003
15	82	5.040	5.038	-0.002
15	83	5.034	5.031	-0.003
15	84	5.031	5.027	-0.004
30	86	5.052	5.050	-0.002
30	89	5.014	5.009	-0.005
30	91	5.008	5.005	-0.003
30	92	5.046	5.039	-0.007
30	94	5.086	5.084	-0.002
30	95	5.033	5.032	-0.001
35	100	5.030	5.024	-0.006
35	101	5.026	5.025	-0.001
35	102	5.031	5.024	-0.007
35	103	5.040	5.031	-0.009
35	104	5.034	5.029	-0.005
35	105	5.031	5.024	-0.007
50	106	5.045	5.037	-0.008
50	107	5.060	5.047	-0.013
50	108	5.061	5.049	-0.012
50	109	5.013	5.004	-0.009
50	110	5.067	5.059	-0.008
50	111	5.032	5.023	-0.009
55	113	5.066	5.061	-0.005
55	114	5.018	5.007	-0.011
55	115	5.009	5.001	-0.008
55	116	5.024	5.018	-0.006
55	126	5.039	5.035	-0.004
55	128	5.004	4.994	-0.010
100	129	5.070	5.054	-0.016
100	131	5.027	5.012	-0.015
100	132	5.030	5.013	-0.017
100	133	5.081	5.064	-0.017
100	134	5.038	5.024	-0.014
100	135	5.022	5.000	-0.022
100	138	5.034	5.019	-0.015
100	139	5.046	5.033	-0.013
100	140	5.047	5.035	-0.012
100	141	5.032	5.009	-0.023
100	142	5.032	5.018	-0.014
100	143	5.034	5.020	-0.014
100	144	5.027	5.019	-0.008
100	220	5.031	5.010	-0.021
100	221	5.021	5.003	-0.018
100	224	5.045	5.030	-0.015
100	225	5.092	5.075	-0.017
100	226	5.064	5.046	-0.018
100	228	5.052	5.037	-0.015
100	232	5.050	5.029	-0.021
100	233	5.057	5.042	-0.015
100	236	5.054	5.033	-0.021
100	238	5.020	4.998	-0.022
100	243	5.053	5.030	-0.023
105	244	5.047	5.037	-0.010
105	246	5.020	5.004	-0.016
105	248	5.056	5.043	-0.013
105	374	5.066	5.059	-0.007
105	376	5.042	5.026	-0.016
105	Max	5.092	5.086	0.000
105	Average	5.039	5.031	-0.008
105	Min	5.004	4.994	-0.023
105	Std Dev	0.020	0.020	0.007



5.79_V_LDO_1M	
Test Site	
Tester	
Test Number	
Max Limit	5.2 V
Min Limit	4.75 V

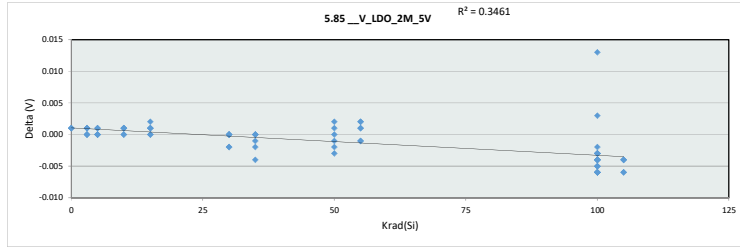
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.033	5.008	5.025	5.007	5.025	5.005	5.024	5.004	4.994	4.998	5.004
Average	5.042	5.039	5.031	5.039	5.029	5.037	5.026	5.037	5.019	5.027	5.034
Max	5.059	5.085	5.040	5.086	5.038	5.084	5.031	5.059	5.061	5.075	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



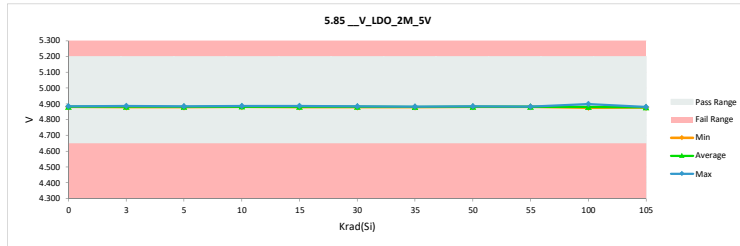
LDR TID Report  
TPS7H5001-SP

		5.85 V_LDO_2M_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	5.2	5.2	
Min Limit	4.65	4.65	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.884	4.885	0.001
0	118	4.882	4.883	0.001
0	122	4.880	4.881	0.001
3	61	4.880	4.880	0.000
3	62	4.882	4.883	0.001
3	63	4.884	4.884	0.000
3	64	4.884	4.885	0.001
3	65	4.886	4.887	0.001
3	66	4.885	4.885	0.000
5	67	4.885	4.885	0.000
5	68	4.880	4.880	0.000
5	69	4.886	4.886	0.000
5	70	4.880	4.880	0.000
5	71	4.880	4.881	0.001
5	72	4.881	4.882	0.001
10	73	4.880	4.881	0.001
10	74	4.882	4.883	0.001
10	75	4.884	4.884	0.000
10	76	4.884	4.885	0.001
10	77	4.886	4.887	0.001
10	78	4.885	4.885	0.000
15	79	4.885	4.885	0.000
15	80	4.880	4.881	0.001
15	81	4.886	4.887	0.001
15	82	4.880	4.880	0.000
15	83	4.880	4.881	0.001
15	84	4.881	4.883	0.002
30	86	4.880	4.880	0.000
30	89	4.882	4.882	0.000
30	91	4.884	4.884	0.000
30	92	4.884	4.884	0.000
30	94	4.886	4.884	-0.002
30	95	4.885	4.885	0.000
35	100	4.885	4.881	-0.004
35	101	4.880	4.880	0.000
35	102	4.886	4.884	-0.002
35	103	4.880	4.879	-0.001
35	104	4.880	4.880	0.000
35	105	4.881	4.881	0.000
50	106	4.883	4.881	-0.002
50	107	4.885	4.886	0.001
50	108	4.886	4.883	-0.003
50	109	4.885	4.885	0.000
50	110	4.882	4.884	0.002
50	111	4.886	4.885	-0.001
55	113	4.882	4.881	-0.001
55	114	4.883	4.882	-0.001
55	115	4.882	4.884	0.002
55	116	4.882	4.883	0.001
55	126	4.882	4.883	0.001
55	128	4.882	4.884	0.002
100	129	4.883	4.881	-0.002
100	131	4.883	4.880	-0.003
100	132	4.881	4.878	-0.003
100	133	4.882	4.877	-0.005
100	134	4.883	4.879	-0.004
100	135	4.886	4.899	0.013
100	138	4.884	4.880	-0.004
100	139	4.886	4.881	-0.005
100	140	4.882	4.878	-0.004
100	141	4.885	4.882	-0.003
100	142	4.881	4.884	0.003
100	143	4.883	4.877	-0.006
100	144	4.885	4.879	-0.006
100	220	4.882	4.879	-0.003
100	221	4.885	4.881	-0.004
100	224	4.886	4.880	-0.006
100	225	4.886	4.881	-0.005
100	226	4.881	4.877	-0.004
100	228	4.883	4.877	-0.006
100	232	4.884	4.878	-0.006
100	233	4.883	4.879	-0.004
100	236	4.888	4.885	-0.003
100	238	4.882	4.876	-0.006
100	243	4.885	4.882	-0.003
105	244	4.881	4.877	-0.004
105	246	4.886	4.880	-0.006
105	248	4.882	4.878	-0.004
105	374	4.884	4.878	-0.006
105	376	4.885	4.881	-0.004
	Max	4.888	4.899	0.013
	Average	4.883	4.882	-0.001
	Min	4.880	4.876	-0.006
	Std Dev	0.002	0.003	0.003

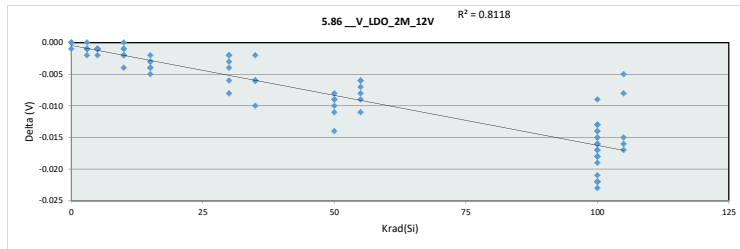


		5.85 V_LDO_2M								
Test Site										
Tester										
Test Number										
Max Limit	5.2	V								
Min Limit	4.65	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	4.881	4.880	4.880	4.881	4.880	4.880	4.879	4.881	4.881	4.876
Average	4.883	4.884	4.882	4.884	4.883	4.883	4.881	4.884	4.883	4.880
Max	4.885	4.887	4.886	4.887	4.887	4.885	4.884	4.886	4.884	4.899
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

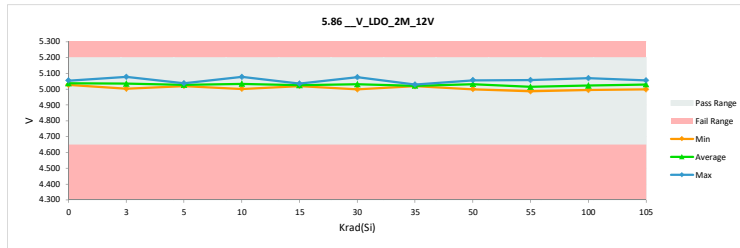


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5.86_V_LDO_2M_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.65	4.65		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.029	5.028	-0.001
0	118	5.055	5.055	0.000
0	122	5.030	5.030	0.000
3	61	5.048	5.047	-0.001
3	62	5.013	5.012	-0.001
3	63	5.004	5.004	0.000
3	64	5.043	5.042	-0.001
3	65	5.079	5.078	-0.001
3	66	5.032	5.030	-0.002
5	67	5.028	5.027	-0.001
5	68	5.023	5.021	-0.002
5	69	5.026	5.025	-0.001
5	70	5.039	5.038	-0.001
5	71	5.031	5.030	-0.001
5	72	5.027	5.026	-0.001
10	73	5.048	5.046	-0.002
10	74	5.013	5.009	-0.004
10	75	5.004	5.003	-0.001
10	76	5.043	5.042	-0.001
10	77	5.079	5.079	0.000
10	78	5.032	5.030	-0.002
15	79	5.028	5.023	-0.005
15	80	5.023	5.021	-0.002
15	81	5.026	5.023	-0.003
15	82	5.039	5.036	-0.003
15	83	5.031	5.027	-0.004
15	84	5.027	5.023	-0.004
30	86	5.048	5.046	-0.002
30	89	5.013	5.007	-0.006
30	91	5.004	5.000	-0.004
30	92	5.043	5.035	-0.008
30	94	5.079	5.077	-0.002
30	95	5.032	5.029	-0.003
35	100	5.028	5.022	-0.006
35	101	5.023	5.021	-0.002
35	102	5.026	5.020	-0.006
35	103	5.039	5.029	-0.010
35	104	5.031	5.025	-0.006
35	105	5.027	5.021	-0.006
50	106	5.044	5.035	-0.009
50	107	5.059	5.045	-0.014
50	108	5.025	5.043	-0.011
50	109	5.010	5.001	-0.009
50	110	5.065	5.057	-0.008
50	111	5.028	5.018	-0.010
55	113	5.064	5.058	-0.006
55	114	5.014	5.003	-0.011
55	115	5.006	4.998	-0.008
55	116	5.021	5.014	-0.007
55	126	5.037	5.031	-0.006
55	128	4.997	4.988	-0.009
100	129	5.069	5.051	-0.018
100	131	5.023	5.008	-0.015
100	132	5.025	5.008	-0.017
100	133	5.080	5.063	-0.017
100	134	5.036	5.021	-0.015
100	135	5.017	4.995	-0.022
100	138	5.030	5.014	-0.016
100	139	5.042	5.028	-0.014
100	140	5.045	5.032	-0.013
100	141	5.029	5.006	-0.023
100	142	5.026	5.013	-0.013
100	143	5.030	5.017	-0.013
100	144	5.024	5.015	-0.009
100	220	5.030	5.008	-0.022
100	221	5.018	4.999	-0.019
100	224	5.043	5.027	-0.016
100	225	5.088	5.070	-0.018
100	226	5.062	5.044	-0.018
100	228	5.046	5.032	-0.014
100	232	5.050	5.028	-0.022
100	233	5.055	5.039	-0.016
100	236	5.047	5.026	-0.021
100	238	5.017	4.995	-0.022
100	243	5.048	5.026	-0.022
105	244	5.042	5.034	-0.008
105	246	5.016	5.000	-0.016
105	248	5.055	5.040	-0.015
105	374	5.061	5.056	-0.005
105	376	5.041	5.024	-0.017
	Max	5.088	5.079	0.009
	Average	5.036	5.027	-0.009
	Min	4.997	4.988	-0.023
	Std Dev	0.019	0.020	0.007

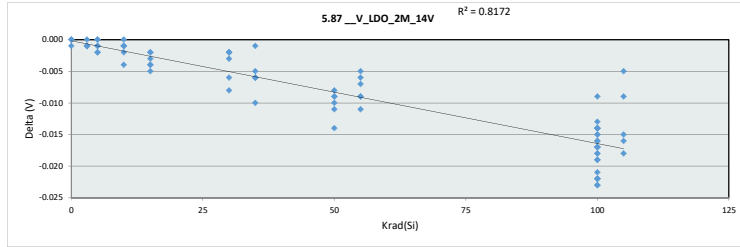


5.86_V_LDO_2M											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.65	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	5.028	5.004	5.021	5.003	5.021	5.000	5.020	5.001	4.988	4.995	5.000
Average	5.038	5.036	5.028	5.035	5.026	5.032	5.023	5.033	5.015	5.024	5.031
Max	5.055	5.078	5.038	5.079	5.036	5.077	5.029	5.057	5.058	5.070	5.056
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

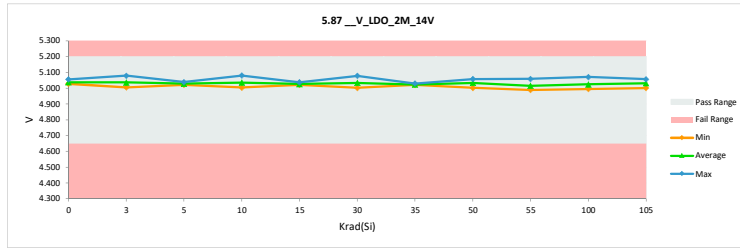


LDR TID Report  
TPS7H5001-SP

5.87_V_LDO_2M_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.65	4.65		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.029	5.028	-0.001
0	118	5.055	5.055	0.000
0	122	5.030	5.030	0.000
3	61	5.048	5.047	-0.001
3	62	5.013	5.012	-0.001
3	63	5.004	5.004	0.000
3	64	5.043	5.042	-0.001
3	65	5.079	5.078	-0.001
3	66	5.031	5.030	-0.001
5	67	5.028	5.026	-0.002
5	68	5.023	5.021	-0.002
5	69	5.026	5.026	0.000
5	70	5.039	5.038	-0.001
5	71	5.030	5.030	0.000
5	72	5.027	5.026	-0.001
10	73	5.048	5.046	-0.002
10	74	5.013	5.009	-0.004
10	75	5.004	5.003	-0.001
10	76	5.043	5.042	-0.001
10	77	5.079	5.079	0.000
10	78	5.031	5.030	-0.001
15	79	5.028	5.023	-0.005
15	80	5.023	5.021	-0.002
15	81	5.026	5.022	-0.004
15	82	5.039	5.037	-0.002
15	83	5.030	5.027	-0.003
15	84	5.027	5.023	-0.004
30	86	5.048	5.046	-0.002
30	89	5.013	5.007	-0.006
30	91	5.004	5.001	-0.003
30	92	5.043	5.035	-0.008
30	94	5.079	5.077	-0.002
30	95	5.031	5.029	-0.002
35	100	5.028	5.022	-0.006
35	101	5.023	5.022	-0.001
35	102	5.026	5.020	-0.006
35	103	5.039	5.029	-0.010
35	104	5.030	5.025	-0.005
35	105	5.027	5.021	-0.006
50	106	5.044	5.035	-0.009
50	107	5.059	5.045	-0.014
50	108	5.054	5.043	-0.011
50	109	5.010	5.001	-0.009
50	110	5.065	5.057	-0.008
50	111	5.028	5.018	-0.010
55	113	5.063	5.058	-0.005
55	114	5.014	5.003	-0.011
55	115	5.007	4.998	-0.009
55	116	5.021	5.014	-0.007
55	126	5.037	5.031	-0.006
55	128	4.997	4.988	-0.009
100	129	5.069	5.052	-0.017
100	131	5.023	5.008	-0.015
100	132	5.025	5.007	-0.018
100	133	5.080	5.063	-0.017
100	134	5.036	5.020	-0.016
100	135	5.016	4.994	-0.022
100	138	5.030	5.013	-0.017
100	139	5.042	5.028	-0.014
100	140	5.045	5.032	-0.013
100	141	5.029	5.006	-0.023
100	142	5.027	5.013	-0.014
100	143	5.030	5.016	-0.014
100	144	5.024	5.015	-0.009
100	220	5.030	5.008	-0.022
100	221	5.018	4.999	-0.019
100	224	5.042	5.027	-0.015
100	225	5.088	5.070	-0.018
100	226	5.062	5.043	-0.019
100	228	5.046	5.032	-0.014
100	232	5.050	5.028	-0.022
100	233	5.055	5.039	-0.016
100	236	5.047	5.026	-0.021
100	238	5.017	4.995	-0.022
100	243	5.048	5.025	-0.023
105	244	5.042	5.033	-0.009
105	246	5.016	5.000	-0.016
105	248	5.055	5.040	-0.015
105	374	5.061	5.056	-0.005
105	376	5.041	5.023	-0.018
105	Max	5.088	5.079	0.000
105	Average	5.036	5.027	-0.009
105	Min	4.997	4.988	-0.023
105	Std Dev	0.019	0.020	0.007



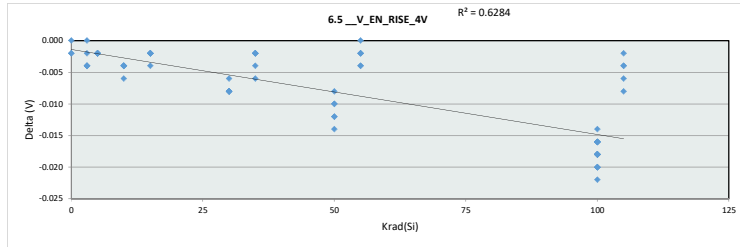
5.87_V_LDO_2M											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.65	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	5.028	5.004	5.021	5.003	5.021	5.001	5.020	5.001	4.988	4.994	5.000
Average	5.038	5.036	5.028	5.035	5.026	5.033	5.023	5.033	5.015	5.023	5.030
Max	5.055	5.078	5.038	5.079	5.037	5.077	5.029	5.057	5.058	5.070	5.056
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



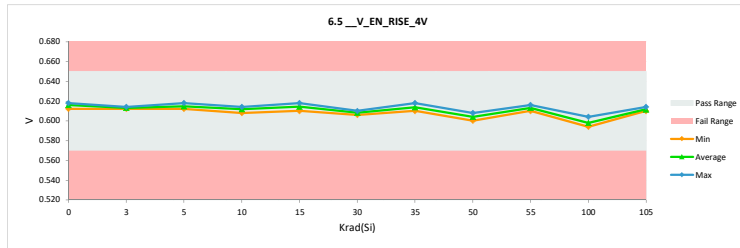
LDR TID Report  
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6.5_V_EN_RISE_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.618	0.618	0.000
0	118	0.614	0.612	-0.002
0	122	0.620	0.618	-0.002
3	61	0.618	0.614	-0.004
3	62	0.616	0.612	-0.004
3	63	0.612	0.612	0.000
3	64	0.618	0.614	-0.004
3	65	0.616	0.614	-0.002
3	66	0.616	0.612	-0.004
5	67	0.620	0.618	-0.002
5	68	0.616	0.614	-0.002
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.612	-0.006
10	77	0.616	0.612	-0.004
10	78	0.616	0.612	-0.004
15	79	0.620	0.618	-0.002
15	80	0.616	0.614	-0.002
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.610	-0.004
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.608	-0.008
30	95	0.616	0.608	-0.008
35	100	0.620	0.618	-0.002
35	101	0.616	0.614	-0.002
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.612	-0.002
35	105	0.614	0.610	-0.004
50	106	0.614	0.606	-0.008
50	107	0.614	0.602	-0.012
50	108	0.612	0.600	-0.012
50	109	0.616	0.606	-0.010
50	110	0.618	0.608	-0.010
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.612	0.612	0.000
55	115	0.618	0.616	-0.002
55	116	0.616	0.612	-0.004
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.620	0.600	-0.020
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.604	-0.014
100	139	0.616	0.600	-0.016
100	140	0.612	0.594	-0.018
100	141	0.618	0.602	-0.016
100	142	0.612	0.594	-0.018
100	143	0.616	0.598	-0.018
100	144	0.622	0.600	-0.022
100	220	0.616	0.598	-0.018
100	221	0.616	0.598	-0.018
100	224	0.616	0.598	-0.018
100	225	0.614	0.596	-0.018
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.596	-0.020
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.620	0.612	-0.008
105	376	0.616	0.610	-0.006
	Max	0.622	0.618	0.000
	Average	0.616	0.608	-0.008
	Min	0.612	0.594	-0.022
	Std Dev	0.002	0.007	0.007



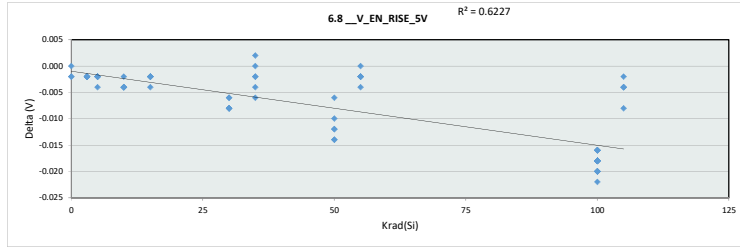
6.5_V_EN_RISE											
Test Site											
Tester											
Test Number											
Max Limit	0.65 V										
Min Limit	0.57 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.612	0.612	0.608	0.610	0.606	0.610	0.600	0.610	0.594	0.610
Average	0.616	0.613	0.615	0.612	0.614	0.608	0.614	0.604	0.613	0.598	0.612
Max	0.618	0.614	0.618	0.614	0.618	0.610	0.618	0.608	0.616	0.604	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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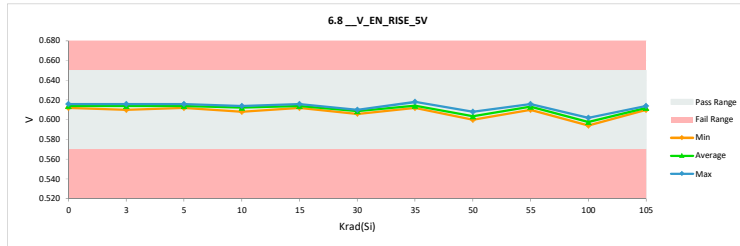
6.8_V_EN_RISE_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.616	0.616	0.000
0	118	0.614	0.612	-0.002
0	122	0.616	0.614	-0.002
3	61	0.618	0.616	-0.002
3	62	0.616	0.614	-0.002
3	63	0.612	0.610	-0.002
3	64	0.618	0.616	-0.002
3	65	0.616	0.614	-0.002
3	66	0.616	0.614	-0.002
5	67	0.616	0.614	-0.002
5	68	0.618	0.614	-0.004
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.614	-0.004
10	77	0.616	0.614	-0.002
10	78	0.616	0.612	-0.004
15	79	0.616	0.614	-0.002
15	80	0.618	0.614	-0.004
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.612	-0.002
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.610	-0.006
30	95	0.616	0.608	-0.008
35	100	0.616	0.618	0.002
35	101	0.618	0.614	-0.004
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.614	0.000
35	105	0.614	0.612	-0.002
50	106	0.614	0.602	-0.012
50	107	0.616	0.602	-0.014
50	108	0.614	0.600	-0.012
50	109	0.618	0.608	-0.010
50	110	0.614	0.608	-0.006
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.614	0.612	-0.002
55	115	0.616	0.616	0.000
55	116	0.616	0.614	-0.002
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.618	0.600	-0.018
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.600	-0.018
100	139	0.616	0.594	-0.022
100	140	0.612	0.594	-0.018
100	141	0.618	0.600	-0.018
100	142	0.614	0.594	-0.020
100	143	0.616	0.598	-0.018
100	144	0.622	0.602	-0.020
100	220	0.616	0.600	-0.016
100	221	0.618	0.600	-0.018
100	224	0.614	0.596	-0.018
100	225	0.616	0.596	-0.020
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.598	-0.018
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.620	0.612	-0.008
105	376	0.616	0.612	-0.004
105	Max	0.622	0.618	0.002
105	Average	0.616	0.608	-0.008
105	Min	0.612	0.594	-0.022
105	Std Dev	0.002	0.007	0.007



6.8_V_EN_RISE	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.610	0.612	0.608	0.612	0.606	0.612	0.600	0.610	0.594	0.610
Average	0.614	0.614	0.614	0.612	0.614	0.609	0.614	0.604	0.613	0.598	0.612
Max	0.616	0.616	0.616	0.614	0.616	0.610	0.618	0.608	0.616	0.602	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

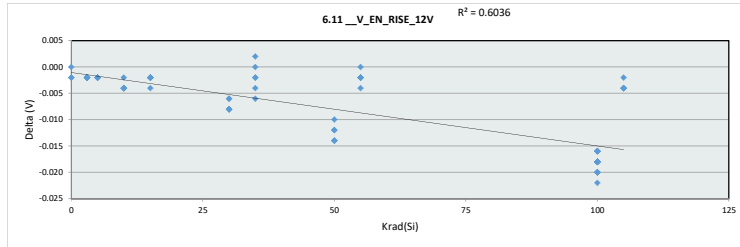




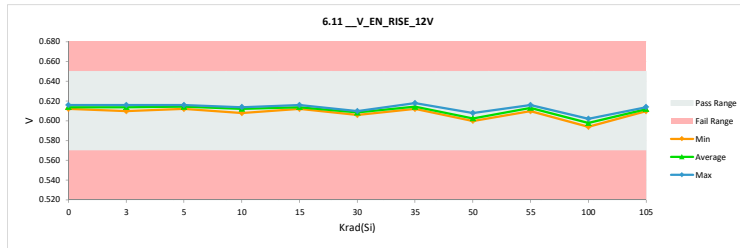
LDR TID Report  
TPS7H5001-SP

6.11 V_EN_RISE_12V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	0.65	0.65
Min Limit	0.57	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.616	0.616	0.000
0	118	0.614	0.612	-0.002
0	122	0.616	0.614	-0.002
3	61	0.618	0.616	-0.002
3	62	0.616	0.614	-0.002
3	63	0.612	0.610	-0.002
3	64	0.618	0.616	-0.002
3	65	0.616	0.614	-0.002
3	66	0.616	0.614	-0.002
5	67	0.616	0.614	-0.002
5	68	0.618	0.616	-0.002
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.614	-0.004
10	77	0.616	0.614	-0.002
10	78	0.616	0.612	-0.004
15	79	0.616	0.614	-0.002
15	80	0.618	0.614	-0.004
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.612	-0.002
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.610	-0.006
30	95	0.616	0.608	-0.008
35	100	0.616	0.618	0.002
35	101	0.618	0.614	-0.004
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.614	0.000
35	105	0.614	0.612	-0.002
50	106	0.614	0.602	-0.012
50	107	0.616	0.602	-0.014
50	108	0.612	0.600	-0.012
50	109	0.618	0.608	-0.010
50	110	0.616	0.602	-0.014
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.614	0.612	-0.002
55	115	0.616	0.616	0.000
55	116	0.616	0.614	-0.002
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.618	0.600	-0.018
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.600	-0.018
100	139	0.616	0.596	-0.020
100	140	0.612	0.594	-0.018
100	141	0.618	0.600	-0.018
100	142	0.614	0.594	-0.020
100	143	0.616	0.598	-0.018
100	144	0.624	0.602	-0.022
100	220	0.616	0.600	-0.016
100	221	0.618	0.600	-0.018
100	224	0.614	0.596	-0.018
100	225	0.616	0.596	-0.020
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.598	-0.018
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.616	0.612	-0.004
105	376	0.616	0.612	-0.004
	Max	0.624	0.618	0.002
	Average	0.616	0.608	-0.008
	Min	0.612	0.594	-0.022
	Std Dev	0.002	0.007	0.007



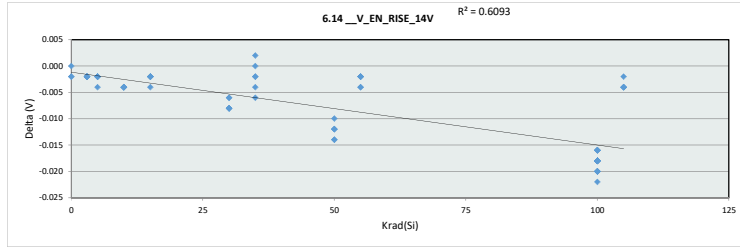
6.11 V_EN_RISE										
Test Site										
Tester										
Test Number										
Max Limit	0.65	V								
Min Limit	0.57	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.610	0.612	0.608	0.612	0.606	0.612	0.600	0.610	0.594
Average	0.614	0.614	0.614	0.612	0.614	0.609	0.614	0.603	0.613	0.598
Max	0.616	0.616	0.616	0.614	0.616	0.610	0.618	0.608	0.616	0.602
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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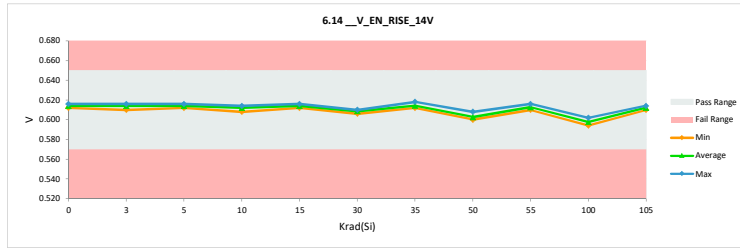
6.14 V_EN_RISE_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.616	0.616	0.000
0	118	0.614	0.612	-0.002
0	122	0.616	0.614	-0.002
3	61	0.618	0.616	-0.002
3	62	0.616	0.614	-0.002
3	63	0.612	0.610	-0.002
3	64	0.618	0.616	-0.002
3	65	0.616	0.614	-0.002
3	66	0.616	0.614	-0.002
5	67	0.616	0.614	-0.002
5	68	0.618	0.614	-0.004
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.614	-0.004
10	77	0.616	0.612	-0.004
10	78	0.616	0.612	-0.004
15	79	0.616	0.614	-0.002
15	80	0.618	0.614	-0.004
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.612	-0.002
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.610	-0.006
30	95	0.616	0.608	-0.008
35	100	0.616	0.618	0.002
35	101	0.618	0.614	-0.004
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.614	0.000
35	105	0.614	0.612	-0.002
50	106	0.614	0.602	-0.012
50	107	0.616	0.602	-0.014
50	108	0.614	0.600	-0.012
50	109	0.618	0.608	-0.010
50	110	0.614	0.602	-0.012
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.614	0.612	-0.002
55	115	0.616	0.612	-0.004
55	116	0.616	0.614	-0.002
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.618	0.600	-0.018
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.600	-0.018
100	139	0.616	0.594	-0.022
100	140	0.612	0.594	-0.018
100	141	0.618	0.600	-0.018
100	142	0.614	0.594	-0.020
100	143	0.616	0.598	-0.018
100	144	0.622	0.602	-0.020
100	220	0.616	0.600	-0.016
100	221	0.618	0.600	-0.018
100	224	0.614	0.596	-0.018
100	225	0.616	0.596	-0.020
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.598	-0.018
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.616	0.612	-0.004
105	376	0.616	0.612	-0.004
105	Max	0.622	0.618	0.002
105	Average	0.616	0.608	-0.008
105	Min	0.612	0.594	-0.022
105	Std Dev	0.002	0.007	0.007



6.14 V_EN_RISE	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

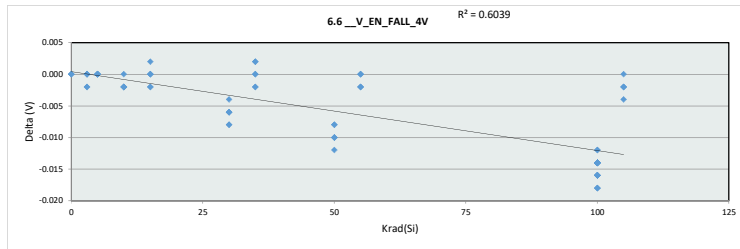
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.610	0.612	0.608	0.612	0.606	0.612	0.600	0.610	0.594	0.610
Average	0.614	0.614	0.614	0.612	0.614	0.609	0.614	0.603	0.613	0.598	0.612
Max	0.616	0.616	0.616	0.614	0.616	0.610	0.618	0.608	0.616	0.602	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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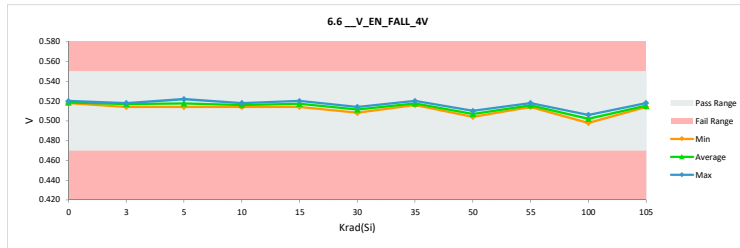
6.6_V_EN_FALL_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.518	0.518	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.516	-0.002
3	63	0.516	0.514	-0.002
3	64	0.518	0.518	0.000
3	65	0.518	0.518	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.516	0.516	0.000
5	69	0.520	0.520	0.000
5	70	0.518	0.518	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	-0.002
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.518	0.518	0.000
10	77	0.518	0.516	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.520	-0.002
15	80	0.516	0.516	0.000
15	81	0.520	0.518	-0.002
15	82	0.518	0.520	0.002
15	83	0.516	0.516	0.000
15	84	0.514	0.514	0.000
30	86	0.518	0.512	-0.006
30	89	0.518	0.510	-0.008
30	91	0.518	0.508	-0.010
30	92	0.518	0.512	-0.006
30	94	0.518	0.514	-0.004
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.516	0.518	0.002
35	102	0.520	0.520	0.000
35	103	0.518	0.516	-0.002
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.516	0.508	-0.008
50	107	0.516	0.504	-0.012
50	108	0.514	0.504	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.510	-0.008
50	111	0.516	0.506	-0.010
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.518	0.000
55	128	0.516	0.514	-0.002
100	129	0.516	0.498	-0.018
100	131	0.516	0.500	-0.016
100	132	0.520	0.506	-0.014
100	133	0.516	0.500	-0.016
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.516	0.502	-0.014
100	140	0.512	0.498	-0.014
100	141	0.520	0.506	-0.014
100	142	0.512	0.498	-0.014
100	143	0.516	0.502	-0.014
100	144	0.524	0.506	-0.018
100	220	0.518	0.502	-0.016
100	221	0.516	0.502	-0.014
100	224	0.516	0.500	-0.016
100	225	0.516	0.502	-0.014
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.516	0.500	-0.016
100	243	0.516	0.504	-0.012
105	244	0.516	0.514	-0.002
105	246	0.516	0.514	-0.002
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.520	0.514	-0.002
	Max	0.524	0.522	0.002
	Average	0.517	0.511	-0.006
	Min	0.512	0.498	-0.018
	Std Dev	0.002	0.007	0.006



6.6_V_EN_FALL	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

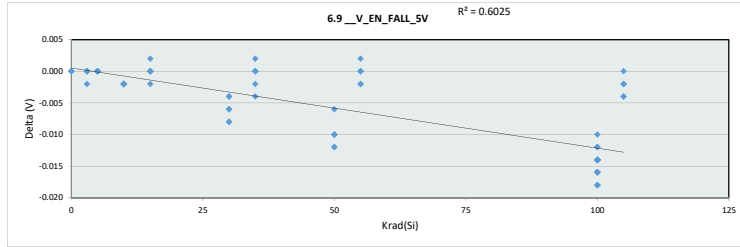
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.514	0.514	0.514	0.514	0.508	0.516	0.504	0.514	0.498	0.514
Average	0.519	0.517	0.518	0.516	0.517	0.511	0.518	0.507	0.516	0.502	0.515
Max	0.520	0.518	0.522	0.518	0.520	0.514	0.520	0.510	0.518	0.506	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



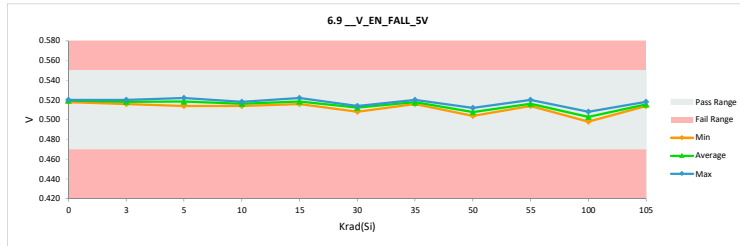
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6.9_V_EN_FALL_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.518	0.000
3	63	0.516	0.516	0.000
3	64	0.520	0.518	-0.002
3	65	0.520	0.520	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.518	0.518	0.000
5	69	0.520	0.520	0.000
5	70	0.520	0.520	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	-0.002
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.520	0.518	-0.002
10	77	0.520	0.518	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.522	0.000
15	80	0.518	0.516	-0.002
15	81	0.520	0.520	0.000
15	82	0.520	0.520	0.000
15	83	0.516	0.516	0.000
15	84	0.514	0.516	0.002
30	86	0.518	0.514	-0.004
30	89	0.518	0.514	-0.004
30	91	0.516	0.508	-0.008
30	92	0.520	0.512	-0.008
30	94	0.520	0.514	-0.006
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.518	0.518	0.000
35	102	0.520	0.520	0.000
35	103	0.520	0.516	-0.004
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.518	0.508	-0.010
50	107	0.516	0.504	-0.012
50	108	0.516	0.506	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.512	-0.006
50	111	0.518	0.506	-0.012
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.520	0.002
55	128	0.516	0.516	0.000
100	129	0.516	0.498	-0.018
100	131	0.516	0.502	-0.014
100	132	0.522	0.506	-0.016
100	133	0.516	0.504	-0.012
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.518	0.504	-0.014
100	140	0.514	0.498	-0.016
100	141	0.520	0.506	-0.014
100	142	0.516	0.500	-0.016
100	143	0.518	0.502	-0.016
100	144	0.524	0.508	-0.016
100	220	0.518	0.504	-0.014
100	221	0.518	0.502	-0.016
100	224	0.516	0.502	-0.014
100	225	0.518	0.502	-0.016
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.518	0.500	-0.018
100	243	0.516	0.506	-0.010
105	244	0.518	0.516	-0.002
105	246	0.518	0.514	-0.004
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.516	0.514	-0.002
105	Max	0.524	0.522	0.002
105	Average	0.518	0.512	-0.006
105	Min	0.514	0.498	-0.018
105	Std Dev	0.002	0.007	0.007



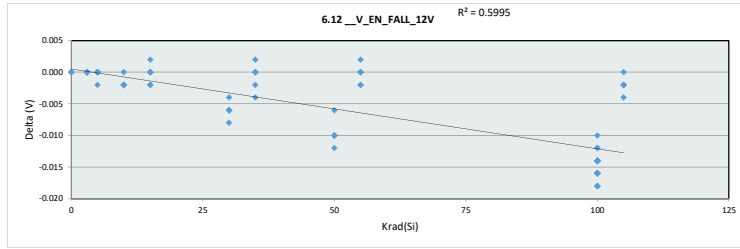
6.9_V_EN_FALL		0	5	10	15	30	35	50	55	100	105
LL		0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min		0.518	0.516	0.514	0.514	0.516	0.508	0.516	0.504	0.514	0.498
Average		0.519	0.518	0.518	0.516	0.518	0.512	0.518	0.508	0.516	0.503
Max		0.520	0.520	0.522	0.518	0.522	0.514	0.520	0.512	0.520	0.508
UL		0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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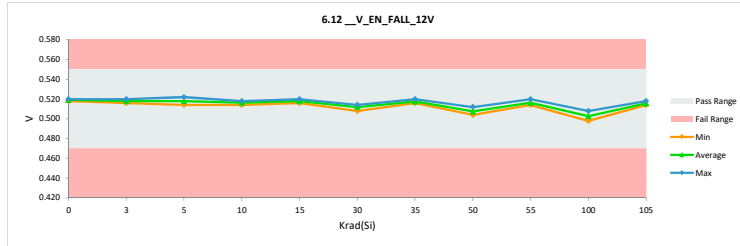
6.12 V_EN_FALL_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.518	0.000
3	63	0.516	0.516	0.000
3	64	0.518	0.518	0.000
3	65	0.520	0.520	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.518	0.516	-0.002
5	69	0.520	0.520	0.000
5	70	0.520	0.520	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	-0.002
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.518	0.518	0.000
10	77	0.520	0.518	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.520	-0.002
15	80	0.518	0.516	-0.002
15	81	0.520	0.520	0.000
15	82	0.520	0.520	0.000
15	83	0.516	0.516	0.000
15	84	0.514	0.516	0.002
30	86	0.518	0.512	-0.006
30	89	0.518	0.514	-0.004
30	91	0.516	0.508	-0.008
30	92	0.518	0.512	-0.006
30	94	0.520	0.514	-0.006
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.518	0.518	0.000
35	102	0.520	0.520	0.000
35	103	0.520	0.516	-0.004
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.518	0.508	-0.010
50	107	0.516	0.504	-0.012
50	108	0.516	0.506	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.512	-0.006
50	111	0.516	0.506	-0.010
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.520	0.002
55	128	0.516	0.516	0.000
100	129	0.516	0.498	-0.018
100	131	0.516	0.502	-0.014
100	132	0.522	0.506	-0.016
100	133	0.516	0.504	-0.012
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.518	0.504	-0.014
100	140	0.514	0.498	-0.016
100	141	0.520	0.506	-0.014
100	142	0.516	0.500	-0.016
100	143	0.518	0.502	-0.016
100	144	0.524	0.508	-0.016
100	220	0.518	0.504	-0.014
100	221	0.518	0.502	-0.016
100	224	0.518	0.502	-0.016
100	225	0.518	0.502	-0.016
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.518	0.500	-0.018
100	243	0.516	0.506	-0.010
105	244	0.518	0.516	-0.002
105	246	0.516	0.514	-0.002
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.516	0.514	-0.002
	Max	0.524	0.522	0.002
	Average	0.518	0.512	-0.006
	Min	0.514	0.498	-0.018
	Std Dev	0.002	0.007	0.007



6.12 V_EN_FALL	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

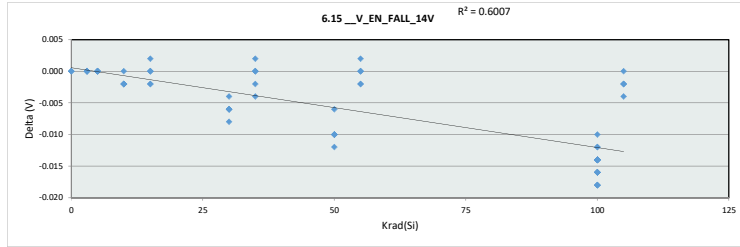
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.514	0.516	0.508	0.516	0.504	0.514	0.498	0.514
Average	0.519	0.518	0.518	0.518	0.518	0.512	0.518	0.508	0.516	0.503	0.516
Max	0.520	0.520	0.522	0.518	0.520	0.514	0.520	0.512	0.520	0.508	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



LDR TID Report  
TPS7H5001-SP

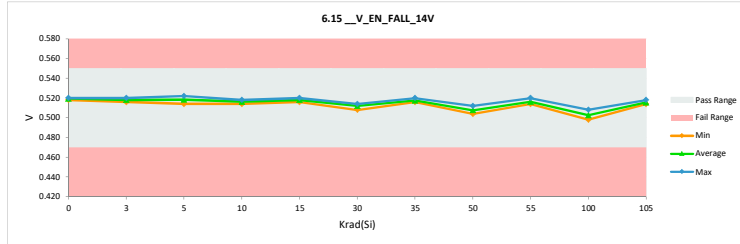
6.15_V_EN_FALL_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.518	0.000
3	63	0.516	0.516	0.000
3	64	0.518	0.518	0.000
3	65	0.520	0.520	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.518	0.518	0.000
5	69	0.520	0.520	0.000
5	70	0.520	0.520	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	-0.000
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.518	0.518	0.000
10	77	0.520	0.518	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.520	-0.002
15	80	0.518	0.516	-0.002
15	81	0.520	0.520	0.000
15	82	0.520	0.520	0.000
15	83	0.516	0.516	0.000
15	84	0.514	0.516	0.002
30	86	0.518	0.512	-0.006
30	89	0.518	0.514	-0.004
30	91	0.516	0.508	-0.008
30	92	0.518	0.512	-0.006
30	94	0.520	0.514	-0.006
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.518	0.518	0.000
35	102	0.520	0.520	0.000
35	103	0.520	0.516	-0.004
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.518	0.508	-0.010
50	107	0.516	0.504	-0.012
50	108	0.516	0.506	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.512	-0.006
50	111	0.516	0.506	-0.010
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.520	0.002
55	128	0.516	0.516	0.000
100	129	0.516	0.498	-0.018
100	131	0.516	0.502	-0.014
100	132	0.522	0.506	-0.016
100	133	0.516	0.504	-0.012
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.518	0.504	-0.014
100	140	0.512	0.498	-0.014
100	141	0.520	0.506	-0.014
100	142	0.516	0.498	-0.018
100	143	0.518	0.502	-0.016
100	144	0.524	0.508	-0.016
100	220	0.518	0.504	-0.014
100	221	0.518	0.502	-0.016
100	224	0.516	0.502	-0.014
100	225	0.518	0.502	-0.016
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.518	0.500	-0.018
100	243	0.516	0.506	-0.010
105	244	0.518	0.516	-0.002
105	246	0.516	0.514	-0.002
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.516	0.514	-0.002
105	Max	0.524	0.522	0.002
105	Average	0.518	0.512	-0.006
105	Min	0.512	0.498	-0.018
105	Std Dev	0.002	0.007	0.007



6.15_V_EN_FALL	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

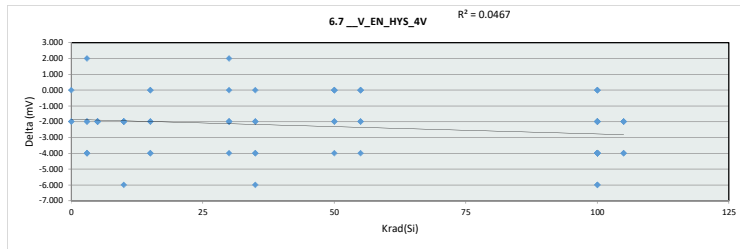
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.514	0.516	0.508	0.516	0.504	0.514	0.498	0.514
Average	0.519	0.518	0.518	0.518	0.518	0.512	0.518	0.508	0.516	0.503	0.516
Max	0.520	0.520	0.522	0.518	0.520	0.514	0.520	0.512	0.520	0.508	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



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TPS7H5001-SP

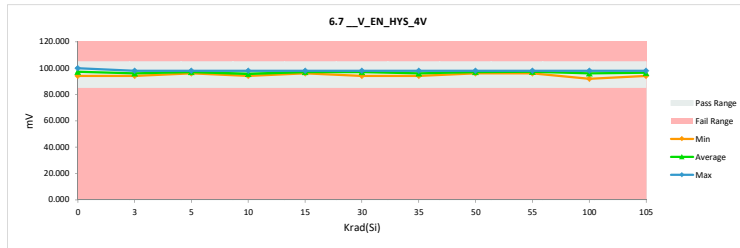
6.7_V_EN_HYS_4V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	100.000	100.000	0.000
0	118	96.000	94.000	-2.000
0	122	100.000	98.000	-2.000
3	61	100.000	96.000	-4.000
3	62	98.000	96.000	-2.000
3	63	96.000	98.000	2.000
3	64	100.000	96.000	-4.000
3	65	98.000	96.000	-2.000
3	66	98.000	94.000	-4.000
5	67	98.000	96.000	-2.000
5	68	100.000	98.000	-2.000
5	69	98.000	96.000	-2.000
5	70	100.000	98.000	-2.000
5	71	98.000	96.000	-2.000
5	72	100.000	98.000	-2.000
10	73	100.000	98.000	-2.000
10	74	98.000	96.000	-2.000
10	75	96.000	94.000	-2.000
10	76	100.000	94.000	-6.000
10	77	98.000	96.000	-2.000
10	78	98.000	96.000	-2.000
15	79	98.000	98.000	0.000
15	80	100.000	98.000	-2.000
15	81	98.000	98.000	0.000
15	82	100.000	96.000	-4.000
15	83	98.000	96.000	-2.000
15	84	100.000	96.000	-4.000
30	86	100.000	98.000	-2.000
30	89	98.000	98.000	0.000
30	91	96.000	98.000	2.000
30	92	100.000	98.000	-2.000
30	94	98.000	94.000	-4.000
30	95	98.000	96.000	-2.000
35	100	98.000	98.000	0.000
35	101	100.000	96.000	-4.000
35	102	98.000	96.000	-2.000
35	103	100.000	96.000	-4.000
35	104	98.000	96.000	-2.000
35	105	100.000	94.000	-6.000
50	106	98.000	98.000	0.000
50	107	98.000	98.000	0.000
50	108	98.000	96.000	-2.000
50	109	96.000	96.000	0.000
50	110	100.000	98.000	-2.000
50	111	100.000	96.000	-4.000
55	113	98.000	98.000	0.000
55	114	98.000	98.000	0.000
55	115	98.000	98.000	0.000
55	116	100.000	96.000	-4.000
55	126	100.000	98.000	-2.000
55	128	98.000	96.000	-2.000
100	129	100.000	98.000	-2.000
100	131	100.000	96.000	-4.000
100	132	100.000	94.000	-6.000
100	133	100.000	98.000	-2.000
100	134	98.000	94.000	-4.000
100	135	98.000	98.000	0.000
100	138	98.000	98.000	0.000
100	139	100.000	98.000	-2.000
100	140	100.000	96.000	-4.000
100	141	98.000	96.000	-2.000
100	142	100.000	96.000	-4.000
100	143	100.000	96.000	-4.000
100	144	98.000	94.000	-4.000
100	220	98.000	96.000	-2.000
100	221	100.000	96.000	-4.000
100	224	100.000	98.000	-2.000
100	225	98.000	94.000	-4.000
100	226	98.000	94.000	-4.000
100	228	100.000	98.000	-2.000
100	232	98.000	96.000	-2.000
100	233	98.000	92.000	-6.000
100	236	100.000	96.000	-4.000
100	238	98.000	98.000	0.000
100	243	98.000	94.000	-4.000
105	244	100.000	98.000	-2.000
105	246	98.000	96.000	-2.000
105	248	100.000	98.000	-2.000
105	374	98.000	94.000	-4.000
105	375	100.000	96.000	-4.000
105	Max	100.000	100.000	2.000
105	Average	98.775	96.450	-2.325
105	Min	96.000	92.000	-6.000
105	Std Dev	1.211	1.558	1.727



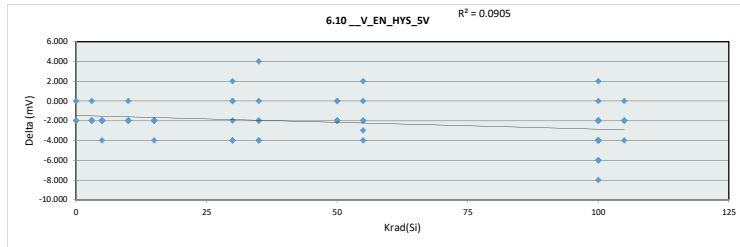
6.7_V_EN_HYS	
Test Site	
Tester	
Test Number	
Max Limit	105 mV
Min Limit	85 mV

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	96.000	94.000	96.000	94.000	94.000	96.000	96.000	92.000	94.000
Average	97.333	96.000	97.000	95.667	97.000	97.000	96.000	97.000	97.333	96.000	96.400
Max	100.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

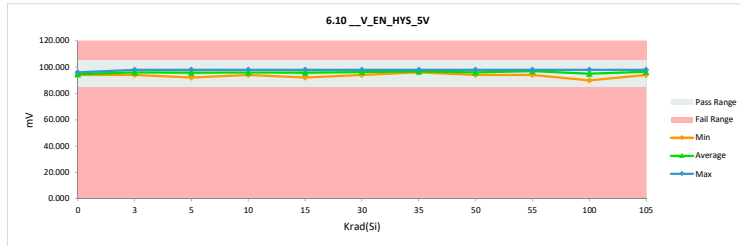


LDR TID Report  
TPS7H5001-SP

6.10_V_EN_HYS_SV				
Test Site	Tester	Test Number	Unit	Delta
			mV	mV
Max Limit	105	105		
Min Limit	85	85		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	96.000	96.000	0.000
0	118	96.000	94.000	-2.000
0	122	96.000	94.000	-2.000
3	61	100.000	98.000	-2.000
3	62	98.000	96.000	-2.000
3	63	96.000	94.000	-2.000
3	64	98.000	98.000	0.000
3	65	96.000	94.000	-2.000
3	66	98.000	96.000	-2.000
5	67	94.000	92.000	-2.000
5	68	100.000	96.000	-4.000
5	69	98.000	96.000	-2.000
5	70	98.000	96.000	-2.000
5	71	98.000	96.000	-2.000
5	72	100.000	98.000	-2.000
10	73	100.000	98.000	-2.000
10	74	98.000	96.000	-2.000
10	75	96.000	94.000	-2.000
10	76	98.000	96.000	-2.000
10	77	96.000	96.000	0.000
10	78	98.000	96.000	-2.000
15	79	94.000	92.000	-2.000
15	80	100.000	98.000	-2.000
15	81	98.000	96.000	-2.000
15	82	98.000	96.000	-2.000
15	83	98.000	96.000	-2.000
15	84	100.000	96.000	-4.000
30	86	100.000	96.000	-4.000
30	89	98.000	94.000	-4.000
30	91	96.000	98.000	2.000
30	92	98.000	98.000	0.000
30	94	96.000	96.000	0.000
30	95	98.000	96.000	-2.000
35	100	94.000	98.000	4.000
35	101	100.000	96.000	-4.000
35	102	98.000	96.000	-2.000
35	103	98.000	96.000	-2.000
35	104	98.000	98.000	0.000
35	105	100.000	96.000	-4.000
50	106	96.000	94.000	-2.000
50	107	100.000	98.000	-2.000
50	108	96.000	94.000	-2.000
50	109	98.000	98.000	0.000
50	110	96.000	96.000	0.000
50	111	98.000	96.000	-2.000
55	113	98.000	98.000	0.000
55	114	100.000	98.000	-2.000
55	115	96.000	98.000	2.000
55	116	100.000	98.000	-2.000
55	126	100.000	96.000	-4.000
55	128	97.000	94.000	-3.000
100	129	100.000	98.000	-2.000
100	131	100.000	94.000	-6.000
100	132	96.000	94.000	-2.000
100	133	100.000	94.000	-6.000
100	134	98.000	94.000	-4.000
100	135	98.000	98.000	0.000
100	138	98.000	94.000	-4.000
100	139	98.000	90.000	-8.000
100	140	98.000	96.000	-2.000
100	141	98.000	94.000	-4.000
100	142	98.000	94.000	-4.000
100	143	98.000	96.000	-2.000
100	144	98.000	94.000	-4.000
100	220	98.000	96.000	-2.000
100	221	100.000	98.000	-2.000
100	224	98.000	94.000	-4.000
100	225	98.000	94.000	-4.000
100	226	98.000	94.000	-4.000
100	228	100.000	98.000	-2.000
100	232	98.000	96.000	-2.000
100	233	98.000	94.000	-4.000
100	236	100.000	96.000	-4.000
100	238	96.000	98.000	2.000
100	243	98.000	92.000	-6.000
105	244	98.000	96.000	-2.000
105	246	96.000	96.000	0.000
105	248	100.000	98.000	-2.000
105	374	98.000	94.000	-4.000
105	376	100.000	98.000	-2.000
Max		100.000	98.000	4.000
Average		97.963	95.775	-2.188
Min		94.000	90.000	-8.000
Std Dev		1.579	1.800	1.890



6.10_V_EN_HYS											
Test Site	Tester	Test Number	Unit	Delta							
			mV	mV							
Max Limit	105	105									
Min Limit	85	85									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	92.000	94.000	92.000	94.000	96.000	94.000	94.000	90.000	94.000
Average	94.667	96.000	95.667	96.000	95.667	96.333	96.667	96.000	97.000	95.000	96.400
Max	96.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

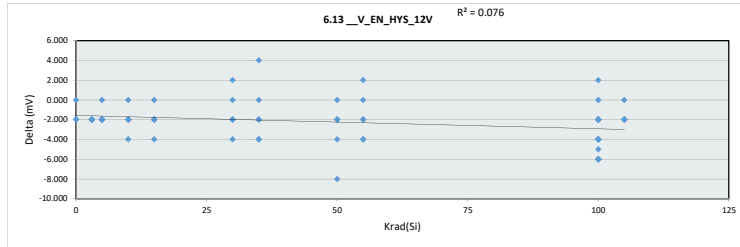




LDR TID Report  
TPS7H5001-SP

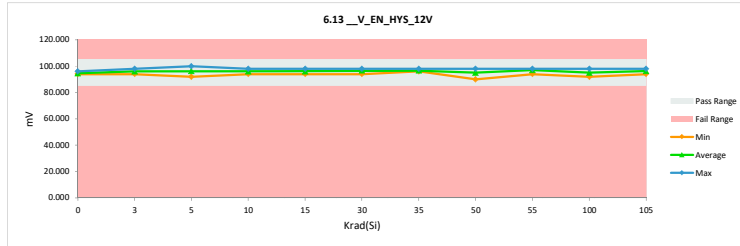
6.13 V_EN_HYS_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	96.000	96.000	0.000
0	118	96.000	94.000	-2.000
0	122	96.000	94.000	-2.000
3	61	100.000	98.000	-2.000
3	62	98.000	96.000	-2.000
3	63	96.000	94.000	-2.000
3	64	100.000	98.000	-2.000
3	65	96.000	94.000	-2.000
3	66	98.000	96.000	-2.000
5	67	94.000	92.000	-2.000
5	68	100.000	100.000	0.000
5	69	98.000	96.000	-2.000
5	70	98.000	96.000	-2.000
5	71	98.000	96.000	-2.000
5	72	100.000	98.000	-2.000
10	73	100.000	98.000	-2.000
10	74	98.000	96.000	-2.000
10	75	96.000	94.000	-2.000
10	76	100.000	96.000	-4.000
10	77	96.000	96.000	0.000
10	78	98.000	96.000	-2.000
15	79	94.000	94.000	0.000
15	80	100.000	98.000	-2.000
15	81	98.000	96.000	-2.000
15	82	98.000	96.000	-2.000
15	83	98.000	96.000	-2.000
15	84	100.000	96.000	-4.000
30	86	100.000	98.000	-2.000
30	89	98.000	94.000	-4.000
30	91	96.000	94.000	-2.000
30	92	100.000	98.000	-2.000
30	94	96.000	96.000	0.000
30	95	98.000	96.000	-2.000
35	100	94.000	98.000	4.000
35	101	100.000	96.000	-4.000
35	102	98.000	96.000	-2.000
35	103	98.000	96.000	-2.000
35	104	98.000	98.000	0.000
35	105	100.000	96.000	-4.000
50	106	96.000	94.000	-2.000
50	107	100.000	98.000	-2.000
50	108	96.000	94.000	-2.000
50	109	98.000	98.000	0.000
50	110	98.000	90.000	-8.000
50	111	100.000	96.000	-4.000
55	113	98.000	98.000	0.000
55	114	100.000	98.000	-2.000
55	115	96.000	98.000	2.000
55	116	100.000	98.000	-2.000
55	126	100.000	96.000	-4.000
55	128	98.000	94.000	-4.000
100	129	100.000	98.000	-2.000
100	131	100.000	94.000	-6.000
100	132	96.000	94.000	-2.000
100	133	100.000	94.000	-6.000
100	134	98.000	94.000	-4.000
100	135	98.000	98.000	0.000
100	138	98.000	94.000	-4.000
100	139	98.000	92.000	-6.000
100	140	98.000	96.000	-2.000
100	141	98.000	93.000	-5.000
100	142	98.000	94.000	-4.000
100	143	98.000	96.000	-2.000
100	144	100.000	94.000	-6.000
100	220	98.000	96.000	-2.000
100	221	100.000	98.000	-2.000
100	224	96.000	94.000	-2.000
100	225	98.000	94.000	-4.000
100	226	98.000	94.000	-4.000
100	228	100.000	98.000	-2.000
100	232	98.000	96.000	-2.000
100	233	98.000	94.000	-4.000
100	236	100.000	96.000	-4.000
100	238	96.000	98.000	2.000
100	243	98.000	92.000	-6.000
105	244	98.000	96.000	-2.000
105	246	98.000	96.000	-2.000
105	248	100.000	98.000	-2.000
105	374	94.000	94.000	0.000
105	376	100.000	98.000	-2.000
105	Max	100.000	100.000	4.000
105	Average	98.075	95.813	-2.263
105	Min	94.000	90.000	-8.000
105	Std Dev	1.697	1.903	1.960



6.13 V_EN_HYS	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

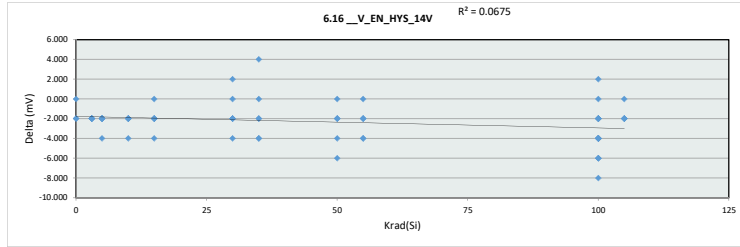
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	92.000	94.000	94.000	94.000	96.000	90.000	94.000	92.000	94.000
Average	94.667	96.000	96.333	96.000	96.000	96.667	96.667	95.000	97.000	95.042	96.400
Max	96.000	98.000	100.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000



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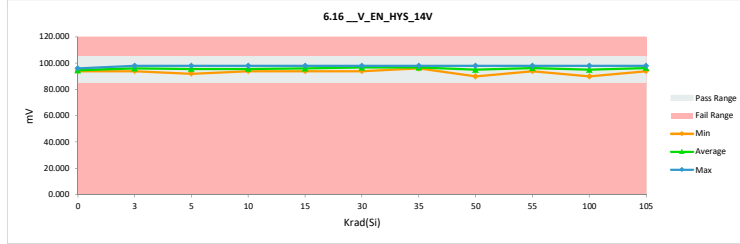
6.16_V_EN_HYS_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	96.000	96.000	0.000
0	118	96.000	94.000	-2.000
0	122	96.000	94.000	-2.000
3	61	100.000	98.000	-2.000
3	62	98.000	96.000	-2.000
3	63	96.000	94.000	-2.000
3	64	100.000	98.000	-2.000
3	65	96.000	94.000	-2.000
3	66	98.000	96.000	-2.000
5	67	94.000	92.000	-2.000
5	68	100.000	96.000	-4.000
5	69	98.000	96.000	-2.000
5	70	98.000	96.000	-2.000
5	71	98.000	96.000	-2.000
5	72	100.000	98.000	-2.000
10	73	100.000	98.000	-2.000
10	74	98.000	96.000	-2.000
10	75	96.000	94.000	-2.000
10	76	100.000	96.000	-4.000
10	77	96.000	94.000	-2.000
10	78	98.000	96.000	-2.000
15	79	94.000	94.000	0.000
15	80	100.000	98.000	-2.000
15	81	98.000	96.000	-2.000
15	82	98.000	96.000	-2.000
15	83	98.000	96.000	-2.000
15	84	100.000	96.000	-4.000
30	86	100.000	98.000	-2.000
30	89	98.000	94.000	-4.000
30	91	96.000	94.000	-2.000
30	92	100.000	98.000	-2.000
30	94	96.000	96.000	0.000
30	95	98.000	96.000	-2.000
35	100	94.000	98.000	4.000
35	101	100.000	96.000	-4.000
35	102	98.000	96.000	-2.000
35	103	98.000	96.000	-2.000
35	104	98.000	98.000	0.000
35	105	100.000	96.000	-4.000
50	106	96.000	94.000	-2.000
50	107	100.000	98.000	-2.000
50	108	96.000	94.000	-2.000
50	109	98.000	98.000	0.000
50	110	96.000	90.000	-6.000
50	111	100.000	96.000	-4.000
55	113	98.000	98.000	0.000
55	114	100.000	98.000	-2.000
55	115	96.000	94.000	-2.000
55	116	100.000	98.000	-2.000
55	126	100.000	96.000	-4.000
55	128	98.000	94.000	-4.000
100	129	100.000	98.000	-2.000
100	131	100.000	94.000	-6.000
100	132	96.000	94.000	-2.000
100	133	100.000	94.000	-6.000
100	134	98.000	94.000	-4.000
100	135	98.000	98.000	0.000
100	138	98.000	94.000	-4.000
100	139	98.000	90.000	-8.000
100	140	100.000	96.000	-4.000
100	141	98.000	94.000	-4.000
100	142	98.000	96.000	-2.000
100	143	98.000	96.000	-2.000
100	144	98.000	94.000	-4.000
100	220	98.000	96.000	-2.000
100	221	100.000	98.000	-2.000
100	224	98.000	94.000	-4.000
100	225	98.000	94.000	-4.000
100	226	98.000	94.000	-4.000
100	228	100.000	98.000	-2.000
100	232	98.000	96.000	-2.000
100	233	98.000	94.000	-4.000
100	236	100.000	96.000	-4.000
100	238	96.000	98.000	2.000
100	243	98.000	92.000	-6.000
105	244	98.000	96.000	-2.000
105	246	98.000	96.000	-2.000
105	248	100.000	98.000	-2.000
105	374	94.000	94.000	0.000
105	376	100.000	98.000	-2.000
105	Max	100.000	98.000	4.000
105	Average	98.075	95.700	-2.375
105	Min	94.000	90.000	-8.000
105	Std Dev	1.697	1.885	1.831



6.16_V_EN_HYS	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

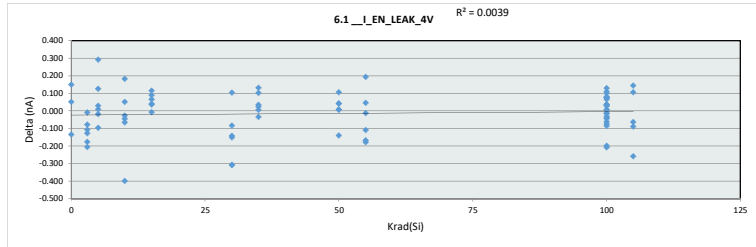
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	92.000	94.000	94.000	94.000	96.000	90.000	94.000	90.000	94.000
Average	94.667	96.000	95.667	95.667	96.000	96.667	96.667	95.000	96.333	95.083	96.400
Max	96.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000



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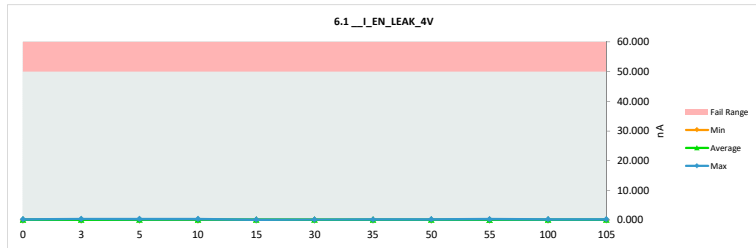
6.1_I_EN_LEAK_4V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50 50
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.095	0.244	0.149
0	118	0.017	0.068	0.051
0	122	0.203	0.068	-0.135
3	61	0.029	0.020	-0.009
3	62	0.156	0.029	-0.127
3	63	0.400	0.322	-0.078
3	64	0.361	0.156	-0.205
3	65	0.166	0.059	-0.107
3	66	0.264	0.088	-0.176
5	67	0.039	0.020	-0.019
5	68	0.020	0.312	0.292
5	69	0.039	0.049	0.010
5	70	0.039	0.068	0.029
5	71	0.117	0.020	-0.097
5	72	0.059	0.185	0.126
10	73	0.029	0.212	0.183
10	74	0.156	0.207	0.051
10	75	0.400	0.002	-0.398
10	76	0.361	0.295	-0.066
10	77	0.166	0.120	-0.046
10	78	0.264	0.237	-0.027
15	79	0.039	0.076	0.037
15	80	0.020	0.134	0.114
15	81	0.039	0.105	0.066
15	82	0.039	0.129	0.090
15	83	0.117	0.110	-0.007
15	84	0.059	0.100	0.041
30	86	0.029	0.132	0.103
30	89	0.156	0.073	-0.083
30	91	0.400	0.093	-0.307
30	92	0.361	0.054	-0.307
30	94	0.166	0.024	-0.142
30	95	0.264	0.112	-0.152
35	100	0.039	0.171	0.132
35	101	0.020	0.122	0.102
35	102	0.039	0.063	0.024
35	103	0.039	0.073	0.034
35	104	0.117	0.122	0.005
35	105	0.059	0.024	-0.035
50	106	0.146	0.005	-0.141
50	107	0.095	0.102	0.007
50	108	0.041	0.083	0.042
50	109	0.041	0.083	0.042
50	110	0.027	0.034	0.007
50	111	0.134	0.239	0.105
55	113	0.203	0.024	-0.179
55	114	0.222	0.054	-0.168
55	115	0.066	0.112	0.046
55	116	0.124	0.015	-0.109
55	126	0.066	0.259	0.193
55	128	0.017	0.005	-0.012
100	129	0.105	0.142	0.037
100	131	0.124	0.083	-0.041
100	132	0.183	0.210	0.027
100	133	0.154	0.112	-0.042
100	134	0.129	0.054	-0.075
100	135	0.002	0.132	0.130
100	138	0.056	0.024	-0.032
100	139	0.173	0.112	-0.061
100	140	0.027	0.102	0.075
100	141	0.203	0.005	-0.198
100	142	0.022	0.122	0.100
100	143	0.115	0.102	-0.013
100	144	0.066	0.073	0.007
100	220	0.037	0.073	0.036
100	221	0.271	0.063	-0.208
100	224	0.090	0.073	-0.017
100	225	0.090	0.093	0.003
100	226	0.105	0.181	0.076
100	228	0.012	0.122	0.110
100	232	0.149	0.063	-0.086
100	233	0.115	0.181	0.066
100	236	0.061	0.093	0.032
100	238	0.056	0.132	0.076
100	243	0.051	0.132	0.081
105	244	0.085	0.190	0.105
105	246	0.134	0.044	-0.090
105	248	0.027	0.171	0.144
105	374	0.176	0.112	-0.064
105	376	0.264	0.005	-0.259
105	Max	0.400	0.322	0.292
105	Average	0.121	0.106	-0.014
105	Min	0.002	0.002	-0.398
105	Std Dev	0.102	0.074	0.123



6.1_I_EN_LEAK	
Test Site	
Tester	
Test Number	
Max Limit	50 nA
Min Limit	nA
Krad(Si)	0 3

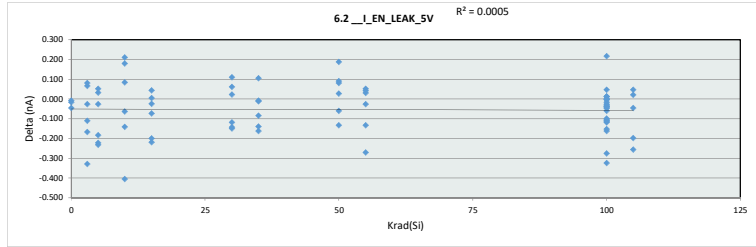
LL	0	3	5	10	15	30	35	50	55	100	105
Min	0.068	0.020	0.020	0.002	0.076	0.024	0.024	0.005	0.005	0.005	0.005
Average	0.127	0.112	0.109	0.179	0.109	0.081	0.096	0.091	0.078	0.103	0.104
Max	0.244	0.322	0.312	0.295	0.134	0.132	0.171	0.239	0.259	0.210	0.190
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



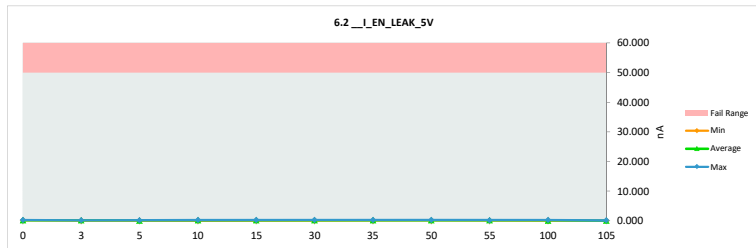
LDR TID Report  
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6.2_I_EN_LEAK_5V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50 50
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.098	0.090	-0.008
0	118	0.127	0.110	-0.017
0	122	0.234	0.188	-0.046
3	61	0.049	0.115	0.066
3	62	0.088	0.061	-0.027
3	63	0.185	0.017	-0.168
3	64	0.039	0.120	0.081
3	65	0.195	0.085	-0.110
3	66	0.429	0.100	-0.329
5	67	0.020	0.071	0.051
5	68	0.156	0.129	-0.027
5	69	0.224	0.002	-0.222
5	70	0.127	0.159	0.032
5	71	0.273	0.090	-0.183
5	72	0.234	0.002	-0.232
10	73	0.049	0.229	0.180
10	74	0.088	0.024	-0.064
10	75	0.185	0.268	0.083
10	76	0.039	0.249	0.210
10	77	0.195	0.054	-0.141
10	78	0.429	0.024	-0.405
15	79	0.020	0.063	0.043
15	80	0.156	0.083	-0.073
15	81	0.224	0.229	0.005
15	82	0.127	0.102	-0.025
15	83	0.273	0.073	-0.200
15	84	0.234	0.015	-0.219
30	86	0.049	0.159	0.110
30	89	0.088	0.110	0.022
30	91	0.185	0.066	-0.119
30	92	0.039	0.100	0.061
30	94	0.195	0.046	-0.149
30	95	0.429	0.286	-0.143
35	100	0.020	0.012	-0.008
35	101	0.156	0.144	-0.012
35	102	0.224	0.061	-0.163
35	103	0.127	0.232	0.105
35	104	0.273	0.134	-0.139
35	105	0.234	0.149	-0.085
50	106	0.312	0.178	-0.134
50	107	0.059	0.149	0.090
50	108	0.020	0.207	0.187
50	109	0.117	0.198	0.081
50	110	0.068	0.095	0.027
50	111	0.127	0.066	-0.061
55	113	0.020	0.071	0.051
55	114	0.273	0.139	-0.134
55	115	0.137	0.168	0.031
55	116	0.156	0.129	-0.027
55	126	0.381	0.110	-0.271
55	128	0.078	0.120	0.042
100	129	0.176	0.139	-0.037
100	131	0.224	0.115	-0.109
100	132	0.049	0.051	0.002
100	133	0.322	0.046	-0.276
100	134	0.088	0.027	-0.061
100	135	0.224	0.105	-0.119
100	138	0.137	0.100	-0.037
100	139	0.117	0.129	0.012
100	140	0.185	0.032	-0.153
100	141	0.049	0.017	-0.032
100	142	0.224	0.061	-0.163
100	143	0.010	0.056	0.046
100	144	0.068	0.022	-0.046
100	220	0.029	0.002	-0.027
100	221	0.195	0.090	-0.105
100	224	0.020	0.237	0.217
100	225	0.078	0.032	-0.046
100	226	0.078	0.061	-0.017
100	228	0.215	0.212	-0.003
100	232	0.098	0.110	0.012
100	233	0.117	0.017	-0.100
100	236	0.146	0.032	-0.114
100	238	0.049	0.046	-0.003
100	243	0.400	0.076	-0.324
105	244	0.127	0.081	-0.046
105	246	0.322	0.066	-0.256
105	248	0.029	0.076	0.047
105	374	0.020	0.041	0.021
105	376	0.254	0.056	-0.198
105	Max	0.429	0.286	0.217
105	Average	0.154	0.100	-0.054
105	Min	0.010	0.002	-0.405
105	Std Dev	0.108	0.068	0.124



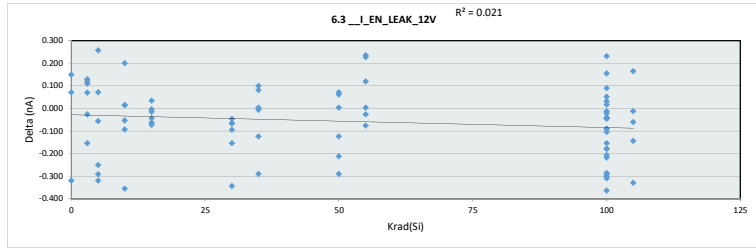
6.2_I_EN_LEAK	
Test Site	
Tester	
Test Number	
Max Limit	50 nA
Min Limit	nA
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105



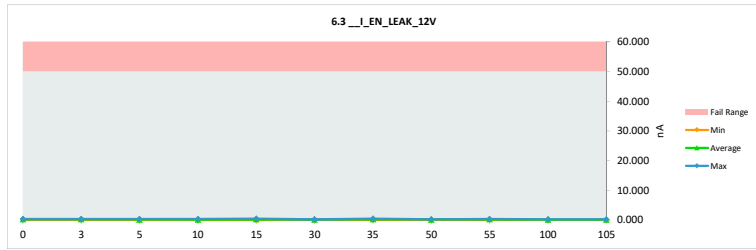
LDR TID Report  
TPS7H5001-SP

6.3 I_EN_LEAK_12V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50 50
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.205	0.276	0.071
0	118	0.664	0.344	-0.320
0	122	0.039	0.188	0.149
3	61	0.371	0.344	-0.027
3	62	0.166	0.295	0.129
3	63	0.127	0.246	0.119
3	64	0.137	0.207	0.070
3	65	0.342	0.188	-0.154
3	66	0.176	0.286	0.110
5	67	0.117	0.373	0.256
5	68	0.107	0.178	0.071
5	69	0.117	0.061	-0.056
5	70	0.429	0.178	-0.251
5	71	0.332	0.041	-0.291
5	72	0.449	0.129	-0.320
10	73	0.371	0.015	-0.356
10	74	0.166	0.366	0.200
10	75	0.127	0.142	0.015
10	76	0.137	0.151	0.014
10	77	0.342	0.249	-0.093
10	78	0.176	0.122	-0.054
15	79	0.117	0.044	-0.073
15	80	0.107	0.044	-0.063
15	81	0.117	0.073	-0.044
15	82	0.429	0.425	-0.004
15	83	0.332	0.366	0.034
15	84	0.449	0.434	-0.015
30	86	0.371	0.027	-0.344
30	89	0.166	0.100	-0.066
30	91	0.127	0.081	-0.046
30	92	0.137	0.071	-0.066
30	94	0.342	0.188	-0.154
30	95	0.176	0.081	-0.095
35	100	0.117	0.110	-0.007
35	101	0.107	0.207	0.100
35	102	0.117	0.198	0.081
35	103	0.429	0.432	0.003
35	104	0.332	0.207	-0.125
35	105	0.449	0.159	-0.290
50	106	0.273	0.061	-0.212
50	107	0.166	0.237	0.071
50	108	0.410	0.120	-0.290
50	109	0.234	0.110	-0.124
50	110	0.107	0.110	0.003
50	111	0.088	0.149	0.061
55	113	0.059	0.178	0.119
55	114	0.254	0.178	-0.076
55	115	0.254	0.227	-0.027
55	116	0.351	0.354	0.003
55	126	0.107	0.334	0.227
55	128	0.059	0.295	0.236
100	129	0.303	0.012	-0.291
100	131	0.303	0.124	-0.179
100	132	0.390	0.183	-0.207
100	133	0.244	0.232	-0.012
100	134	0.244	0.222	-0.022
100	135	0.166	0.076	-0.090
100	138	0.010	0.164	0.154
100	139	0.273	0.095	-0.178
100	140	0.107	0.124	0.017
100	141	0.215	0.173	-0.042
100	142	0.351	0.051	-0.300
100	143	0.469	0.251	-0.218
100	144	0.361	0.051	-0.310
100	220	0.459	0.095	-0.364
100	221	0.156	0.115	-0.041
100	224	0.117	0.027	-0.090
100	225	0.205	0.051	-0.154
100	226	0.224	0.120	-0.104
100	228	0.059	0.290	0.231
100	232	0.176	0.129	-0.047
100	233	0.371	0.085	-0.286
100	236	0.000	0.090	0.090
100	238	0.137	0.188	0.051
100	243	0.098	0.129	0.031
105	244	0.273	0.129	-0.144
105	246	0.127	0.066	-0.061
105	248	0.254	0.242	-0.012
105	374	0.342	0.012	-0.330
105	376	0.078	0.242	0.164
105	375	0.664	0.434	0.256
Max		0.230	0.172	-0.058
Average		0.230	0.172	-0.058
Min		0.000	0.012	-0.364
Std Dev		0.134	0.107	0.157



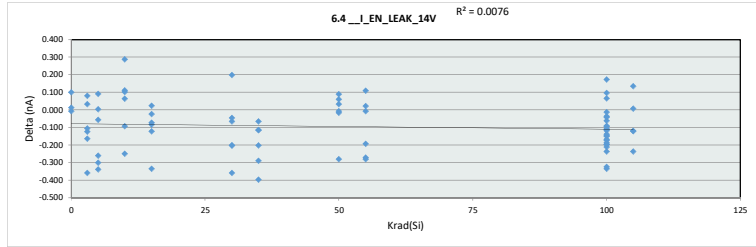
6.3 I_EN_LEAK											
Test Site											
Tester											
Test Number											
Max Limit	50	nA									
Min Limit		nA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.188	0.188	0.041	0.015	0.044	0.027	0.110	0.061	0.178	0.012	0.012
Average	0.269	0.261	0.160	0.174	0.231	0.091	0.219	0.131	0.261	0.128	0.138
Max	0.344	0.344	0.373	0.366	0.434	0.188	0.432	0.237	0.354	0.290	0.242
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



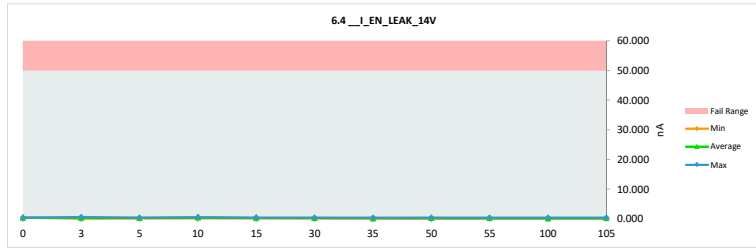
LDR TID Report  
TPS7H5001-SP

6.4 I_EN_LEAK_14V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50 50
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.371	0.383	0.012
0	118	0.234	0.334	0.100
0	122	0.381	0.373	-0.008
3	61	0.127	0.159	0.032
3	62	0.400	0.237	-0.163
3	63	0.449	0.530	0.081
3	64	0.459	0.100	-0.359
3	65	0.361	0.237	-0.124
3	66	0.420	0.315	-0.105
5	67	0.429	0.168	-0.261
5	68	0.595	0.256	-0.339
5	69	0.381	0.081	-0.300
5	70	0.234	0.237	0.003
5	71	0.390	0.334	-0.056
5	72	0.224	0.091	-0.133
10	73	0.127	0.415	0.288
10	74	0.400	0.151	-0.249
10	75	0.449	0.356	-0.093
10	76	0.459	0.522	0.063
10	77	0.361	0.473	0.112
10	78	0.420	0.522	0.102
15	79	0.429	0.405	-0.024
15	80	0.595	0.259	-0.336
15	81	0.381	0.405	0.024
15	82	0.234	0.151	-0.083
15	83	0.390	0.268	-0.122
15	84	0.224	0.151	-0.073
30	86	0.127	0.325	0.198
30	89	0.400	0.198	-0.202
30	91	0.400	0.449	-0.359
30	92	0.459	0.393	-0.066
30	94	0.361	0.315	-0.046
30	95	0.420	0.217	-0.203
35	100	0.429	0.139	-0.290
35	101	0.595	0.198	-0.397
35	102	0.381	0.315	-0.066
35	103	0.234	0.032	-0.202
35	104	0.390	0.276	-0.114
35	105	0.224	0.110	-0.114
50	106	0.215	0.276	0.061
50	107	0.322	0.354	0.032
50	108	0.185	0.168	-0.017
50	109	0.244	0.334	0.090
50	110	0.351	0.071	-0.280
50	111	0.342	0.334	-0.008
55	113	0.303	0.325	0.022
55	114	0.332	0.139	-0.193
55	115	0.361	0.090	-0.271
55	116	0.137	0.246	0.109
55	126	0.410	0.129	-0.281
55	128	0.303	0.295	-0.008
100	129	0.137	0.203	0.066
100	131	0.303	0.290	-0.013
100	132	0.371	0.203	-0.168
100	133	0.107	0.203	0.096
100	134	0.156	0.095	-0.061
100	135	0.351	0.203	-0.148
100	138	0.410	0.300	-0.110
100	139	0.322	0.212	-0.110
100	140	0.342	0.007	-0.335
100	141	0.517	0.320	-0.197
100	142	0.234	0.022	-0.212
100	143	0.117	0.290	0.173
100	144	0.185	0.012	-0.173
100	220	0.176	0.134	-0.042
100	221	0.322	0.183	-0.139
100	224	0.439	0.203	-0.236
100	225	0.361	0.164	-0.197
100	226	0.205	0.110	-0.095
100	228	0.410	0.222	-0.188
100	232	0.098	0.061	-0.037
100	233	0.185	0.066	-0.119
100	236	0.342	0.251	-0.091
100	238	0.439	0.290	-0.149
100	243	0.429	0.105	-0.324
105	244	0.254	0.134	-0.120
105	246	0.166	0.300	0.134
105	248	0.293	0.173	-0.120
105	374	0.254	0.017	-0.237
105	376	0.390	0.398	0.008
105	Max	0.595	0.530	0.288
105	Average	0.328	0.233	-0.094
105	Min	0.098	0.007	-0.397
105	Std Dev	0.119	0.124	0.148



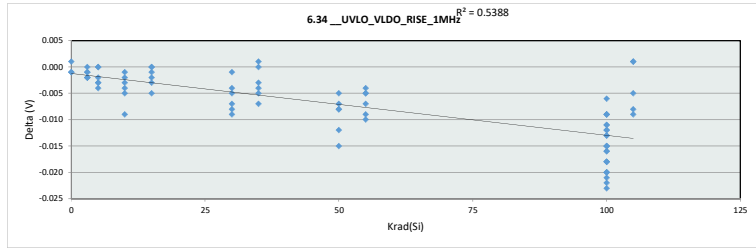
6.4 I EN LEAK	
Test Site	
Tester	
Test Number	
Max Limit	50 nA
Min Limit	nA
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	0.334 0.100 0.081 0.151 0.151 0.090 0.032 0.071 0.090 0.007 0.017
Average	0.363 0.263 0.232 0.407 0.273 0.256 0.178 0.256 0.204 0.173 0.204
Max	0.383 0.530 0.334 0.522 0.405 0.393 0.315 0.354 0.325 0.320 0.398
UL	50.000 50.000 50.000 50.000 50.000 50.000 50.000 50.000 50.000 50.000 50.000



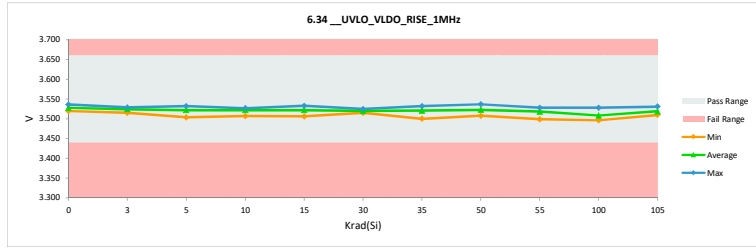
LDR TID Report  
TPS7H5001-SP

6.34 UVLO_VLDO_RISE_1MHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.519	3.520	0.001
0	118	3.528	3.527	-0.001
0	122	3.537	3.536	-0.001
3	61	3.525	3.523	-0.002
3	62	3.528	3.526	-0.002
3	63	3.516	3.515	-0.001
3	64	3.529	3.528	-0.001
3	65	3.528	3.526	-0.002
3	66	3.529	3.529	0.000
5	67	3.535	3.532	-0.003
5	68	3.507	3.504	-0.003
5	69	3.532	3.528	-0.004
5	70	3.524	3.524	0.000
5	71	3.523	3.523	0.000
5	72	3.523	3.521	-0.002
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.516	3.507	-0.009
10	76	3.529	3.525	-0.004
10	77	3.528	3.527	-0.001
10	78	3.529	3.527	-0.002
15	79	3.535	3.533	-0.002
15	80	3.507	3.506	-0.001
15	81	3.532	3.529	-0.003
15	82	3.524	3.524	0.000
15	83	3.523	3.518	-0.005
15	84	3.523	3.523	0.000
30	86	3.525	3.517	-0.008
30	89	3.528	3.521	-0.007
30	91	3.516	3.515	-0.001
30	92	3.529	3.520	-0.009
30	94	3.528	3.523	-0.005
30	95	3.529	3.525	-0.004
35	100	3.535	3.532	-0.003
35	101	3.507	3.500	-0.007
35	102	3.532	3.528	-0.004
35	103	3.524	3.525	0.001
35	104	3.523	3.518	-0.005
35	105	3.523	3.523	0.000
50	106	3.528	3.523	-0.005
50	107	3.534	3.519	-0.015
50	108	3.520	3.508	-0.012
50	109	3.539	3.531	-0.008
50	110	3.545	3.537	-0.008
50	111	3.525	3.518	-0.007
55	113	3.529	3.520	-0.009
55	114	3.504	3.499	-0.005
55	115	3.531	3.527	-0.004
55	116	3.526	3.516	-0.010
55	126	3.535	3.528	-0.007
55	128	3.524	3.519	-0.005
100	129	3.535	3.524	-0.011
100	131	3.518	3.509	-0.009
100	132	3.530	3.512	-0.018
100	133	3.521	3.509	-0.012
100	134	3.535	3.515	-0.020
100	135	3.520	3.502	-0.018
100	138	3.528	3.508	-0.020
100	139	3.527	3.512	-0.015
100	140	3.520	3.511	-0.009
100	141	3.528	3.506	-0.022
100	142	3.506	3.497	-0.009
100	143	3.526	3.511	-0.015
100	144	3.526	3.515	-0.011
100	220	3.529	3.506	-0.023
100	221	3.541	3.523	-0.018
100	224	3.513	3.497	-0.016
100	225	3.512	3.496	-0.016
100	226	3.541	3.528	-0.013
100	228	3.524	3.509	-0.015
100	232	3.525	3.504	-0.021
100	233	3.523	3.503	-0.020
100	236	3.516	3.503	-0.013
100	238	3.516	3.504	-0.012
100	243	3.513	3.507	-0.006
105	244	3.509	3.510	0.001
105	246	3.530	3.525	-0.005
105	248	3.527	3.519	-0.008
105	374	3.530	3.531	0.001
105	376	3.520	3.511	-0.009
105	Max	3.545	3.537	0.001
105	Average	3.525	3.518	-0.007
105	Min	3.504	3.496	-0.023
105	Std Dev	0.008	0.010	0.006



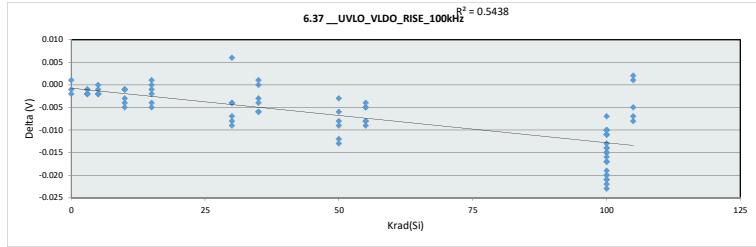
6.34 UVLO_VLD		0	5	10	15	30	35	50	55	100	105
LL		3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min		3.520	3.515	3.504	3.507	3.506	3.515	3.500	3.508	3.499	3.496
Average		3.528	3.525	3.522	3.522	3.522	3.520	3.521	3.523	3.518	3.509
Max		3.536	3.529	3.532	3.527	3.533	3.525	3.532	3.537	3.528	3.528
UL		3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



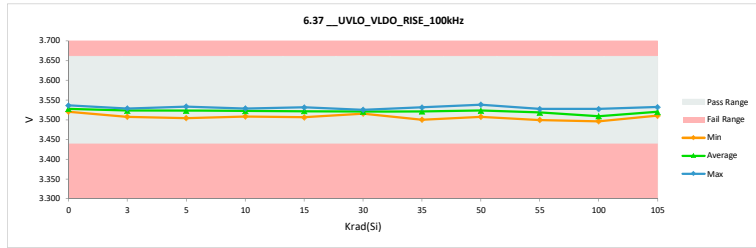
LDR TID Report  
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6.37 UVLO_VLDO_RISE_100kHz			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	3.66	3.66	
Min Limit	3.44	3.44	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.519	3.520	0.001
0	118	3.527	3.526	-0.001
0	122	3.538	3.536	-0.002
3	61	3.525	3.523	-0.002
3	62	3.528	3.526	-0.002
3	63	3.509	3.507	-0.002
3	64	3.529	3.528	-0.001
3	65	3.528	3.526	-0.002
3	66	3.529	3.528	-0.001
5	67	3.535	3.533	-0.002
5	68	3.506	3.504	-0.002
5	69	3.531	3.529	-0.002
5	70	3.525	3.525	0.000
5	71	3.524	3.522	-0.002
5	72	3.522	3.521	-0.001
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.509	3.508	-0.001
10	76	3.529	3.525	-0.004
10	77	3.528	3.527	-0.001
10	78	3.529	3.528	-0.001
15	79	3.535	3.531	-0.004
15	80	3.506	3.506	0.000
15	81	3.531	3.529	-0.002
15	82	3.525	3.524	-0.001
15	83	3.524	3.519	-0.005
15	84	3.522	3.523	0.001
30	86	3.525	3.517	-0.008
30	89	3.528	3.521	-0.007
30	91	3.509	3.515	0.006
30	92	3.529	3.520	-0.009
30	94	3.528	3.524	-0.004
30	95	3.529	3.525	-0.004
35	100	3.535	3.531	-0.004
35	101	3.506	3.500	-0.006
35	102	3.531	3.528	-0.003
35	103	3.525	3.525	0.000
35	104	3.524	3.518	-0.006
35	105	3.522	3.523	0.001
50	106	3.527	3.524	-0.003
50	107	3.533	3.520	-0.013
50	108	3.519	3.507	-0.012
50	109	3.540	3.531	-0.009
50	110	3.546	3.538	-0.008
50	111	3.524	3.518	-0.006
55	113	3.528	3.520	-0.008
55	114	3.504	3.499	-0.005
55	115	3.531	3.526	-0.005
55	116	3.525	3.516	-0.009
55	126	3.535	3.527	-0.008
55	128	3.524	3.520	-0.004
100	129	3.536	3.525	-0.011
100	131	3.519	3.509	-0.010
100	132	3.531	3.511	-0.020
100	133	3.520	3.509	-0.011
100	134	3.534	3.515	-0.019
100	135	3.520	3.503	-0.017
100	138	3.528	3.507	-0.021
100	139	3.527	3.512	-0.015
100	140	3.521	3.511	-0.010
100	141	3.528	3.505	-0.023
100	142	3.507	3.496	-0.011
100	143	3.526	3.512	-0.014
100	144	3.526	3.516	-0.010
100	220	3.529	3.507	-0.022
100	221	3.541	3.524	-0.017
100	224	3.513	3.497	-0.016
100	225	3.512	3.497	-0.015
100	226	3.542	3.527	-0.015
100	228	3.524	3.510	-0.014
100	232	3.525	3.504	-0.021
100	233	3.523	3.503	-0.020
100	236	3.516	3.503	-0.013
100	238	3.516	3.505	-0.011
100	243	3.514	3.507	-0.007
105	244	3.509	3.510	0.001
105	246	3.530	3.525	-0.005
105	248	3.527	3.519	-0.008
105	374	3.530	3.532	0.002
105	375	3.519	3.512	-0.007
105	Max	3.546	3.538	0.006
105	Average	3.525	3.518	-0.007
105	Min	3.504	3.496	-0.023
105	Std Dev	0.009	0.010	0.007



6.37 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.520	3.507	3.504	3.508	3.506	3.515	3.500	3.507	3.499	3.496	3.510
Average	3.527	3.523	3.522	3.522	3.522	3.520	3.521	3.523	3.518	3.509	3.520
Max	3.536	3.528	3.533	3.528	3.531	3.525	3.531	3.538	3.527	3.527	3.532
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

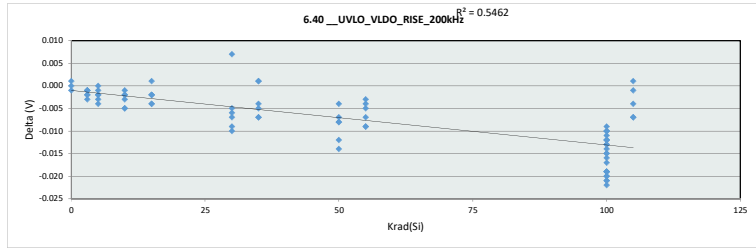




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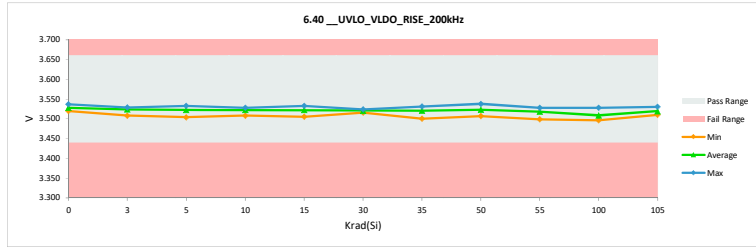
6.40 UVLO_VLDO_RISE_200kHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.519	3.520	0.001
0	118	3.527	3.526	-0.001
0	122	3.537	3.537	0.000
3	61	3.525	3.524	-0.001
3	62	3.528	3.526	-0.002
3	63	3.509	3.508	-0.001
3	64	3.530	3.527	-0.003
3	65	3.529	3.527	-0.002
3	66	3.530	3.529	-0.001
5	67	3.535	3.533	-0.002
5	68	3.507	3.504	-0.003
5	69	3.533	3.529	-0.004
5	70	3.525	3.525	0.000
5	71	3.524	3.522	-0.002
5	72	3.522	3.521	-0.001
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.509	3.508	-0.001
10	76	3.530	3.525	-0.005
10	77	3.529	3.527	-0.002
10	78	3.530	3.528	-0.002
15	79	3.535	3.533	-0.002
15	80	3.507	3.505	-0.002
15	81	3.533	3.529	-0.004
15	82	3.525	3.523	-0.002
15	83	3.524	3.520	-0.004
15	84	3.522	3.523	0.001
30	86	3.525	3.516	-0.009
30	89	3.528	3.521	-0.007
30	91	3.509	3.516	0.007
30	92	3.530	3.520	-0.010
30	94	3.529	3.524	-0.005
30	95	3.530	3.524	-0.006
35	100	3.535	3.531	-0.004
35	101	3.507	3.500	-0.007
35	102	3.533	3.528	-0.005
35	103	3.525	3.526	0.001
35	104	3.524	3.517	-0.007
35	105	3.522	3.523	0.001
50	106	3.528	3.524	-0.004
50	107	3.534	3.520	-0.014
50	108	3.519	3.507	-0.012
50	109	3.539	3.531	-0.008
50	110	3.546	3.538	-0.008
50	111	3.524	3.517	-0.007
55	113	3.529	3.520	-0.009
55	114	3.504	3.499	-0.005
55	115	3.531	3.527	-0.004
55	116	3.524	3.515	-0.009
55	126	3.535	3.528	-0.007
55	128	3.523	3.520	-0.003
100	129	3.536	3.524	-0.012
100	131	3.518	3.508	-0.010
100	132	3.530	3.511	-0.019
100	133	3.521	3.509	-0.012
100	134	3.534	3.515	-0.019
100	135	3.520	3.501	-0.019
100	138	3.528	3.508	-0.020
100	139	3.527	3.512	-0.015
100	140	3.521	3.512	-0.009
100	141	3.528	3.506	-0.022
100	142	3.506	3.496	-0.010
100	143	3.526	3.511	-0.015
100	144	3.526	3.516	-0.010
100	220	3.528	3.507	-0.021
100	221	3.542	3.523	-0.019
100	224	3.513	3.497	-0.016
100	225	3.513	3.496	-0.017
100	226	3.541	3.528	-0.013
100	228	3.524	3.510	-0.014
100	232	3.525	3.504	-0.021
100	233	3.523	3.503	-0.020
100	236	3.516	3.503	-0.013
100	238	3.516	3.504	-0.012
100	243	3.519	3.508	-0.011
105	244	3.509	3.510	0.001
105	246	3.530	3.526	-0.004
105	248	3.526	3.519	-0.007
105	374	3.531	3.530	-0.001
105	376	3.519	3.512	-0.007
	Max	3.546	3.538	0.007
	Average	3.525	3.518	-0.007
	Min	3.504	3.496	-0.022
	Std Dev	0.009	0.010	0.007



6.40 UVLO_VLD	
Test Site	
Tester	
Test Number	
Max Limit	3.66 V
Min Limit	3.44 V

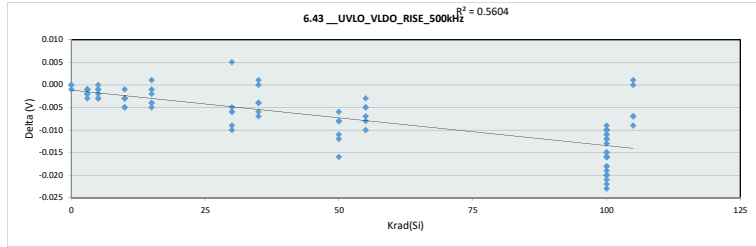
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.520	3.508	3.504	3.508	3.505	3.516	3.500	3.507	3.499	3.496	3.510
Average	3.528	3.524	3.522	3.522	3.522	3.520	3.521	3.523	3.518	3.509	3.519
Max	3.537	3.529	3.533	3.528	3.533	3.524	3.531	3.538	3.528	3.528	3.530
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



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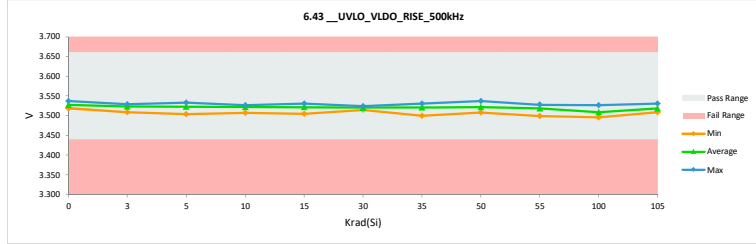
6.43 UVLO_VLDO_RISE_500kHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.519	3.519	0.000
0	118	3.528	3.527	-0.001
0	122	3.537	3.537	0.000
3	61	3.525	3.523	-0.002
3	62	3.528	3.525	-0.003
3	63	3.510	3.509	-0.001
3	64	3.530	3.529	-0.001
3	65	3.528	3.526	-0.002
3	66	3.530	3.529	-0.001
5	67	3.535	3.533	-0.002
5	68	3.507	3.504	-0.003
5	69	3.532	3.529	-0.003
5	70	3.525	3.525	0.000
5	71	3.524	3.523	-0.001
5	72	3.522	3.521	-0.001
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.510	3.507	-0.003
10	76	3.530	3.525	-0.005
10	77	3.528	3.527	-0.001
10	78	3.530	3.527	-0.003
15	79	3.535	3.531	-0.004
15	80	3.507	3.505	-0.002
15	81	3.532	3.528	-0.004
15	82	3.525	3.524	-0.001
15	83	3.524	3.519	-0.005
15	84	3.522	3.523	0.001
30	86	3.525	3.516	-0.009
30	89	3.528	3.522	-0.006
30	91	3.510	3.515	0.005
30	92	3.530	3.520	-0.010
30	94	3.528	3.523	-0.005
30	95	3.530	3.524	-0.006
35	100	3.535	3.531	-0.004
35	101	3.507	3.500	-0.007
35	102	3.532	3.528	-0.004
35	103	3.525	3.525	0.000
35	104	3.524	3.518	-0.006
35	105	3.522	3.523	0.001
50	106	3.528	3.516	-0.012
50	107	3.535	3.519	-0.016
50	108	3.519	3.508	-0.011
50	109	3.539	3.531	-0.008
50	110	3.545	3.537	-0.008
50	111	3.525	3.519	-0.006
55	113	3.528	3.520	-0.008
55	114	3.504	3.499	-0.005
55	115	3.530	3.527	-0.003
55	116	3.526	3.516	-0.010
55	126	3.535	3.528	-0.007
55	128	3.525	3.520	-0.005
100	129	3.536	3.524	-0.012
100	131	3.518	3.508	-0.010
100	132	3.531	3.512	-0.019
100	133	3.520	3.509	-0.011
100	134	3.535	3.515	-0.020
100	135	3.520	3.502	-0.018
100	138	3.528	3.507	-0.021
100	139	3.527	3.511	-0.016
100	140	3.521	3.512	-0.009
100	141	3.528	3.505	-0.023
100	142	3.506	3.496	-0.010
100	143	3.526	3.511	-0.015
100	144	3.527	3.516	-0.011
100	220	3.529	3.507	-0.022
100	221	3.541	3.523	-0.018
100	224	3.513	3.497	-0.016
100	225	3.512	3.496	-0.016
100	226	3.542	3.527	-0.015
100	228	3.525	3.509	-0.016
100	232	3.524	3.504	-0.020
100	233	3.524	3.504	-0.020
100	236	3.516	3.503	-0.013
100	238	3.517	3.505	-0.012
100	243	3.518	3.508	-0.010
105	244	3.509	3.509	0.000
105	246	3.532	3.525	-0.007
105	248	3.526	3.519	-0.007
105	374	3.530	3.531	0.001
105	376	3.520	3.511	-0.009
105	Max	3.545	3.537	0.005
105	Average	3.525	3.518	-0.008
105	Min	3.504	3.496	-0.023
105	Std Dev	0.009	0.010	0.007



6.43 UVLO_VLD	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

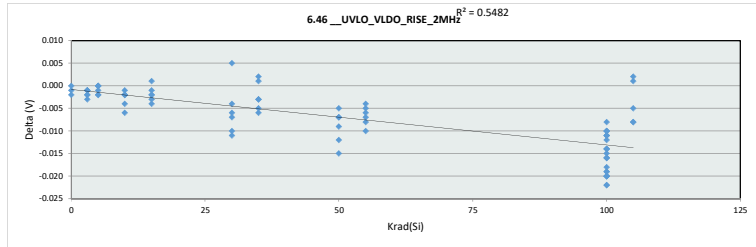
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.519	3.509	3.504	3.507	3.505	3.515	3.500	3.508	3.499	3.496	3.509
Average	3.528	3.524	3.523	3.522	3.522	3.520	3.521	3.522	3.518	3.509	3.519
Max	3.537	3.529	3.533	3.527	3.531	3.524	3.531	3.537	3.528	3.527	3.531
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



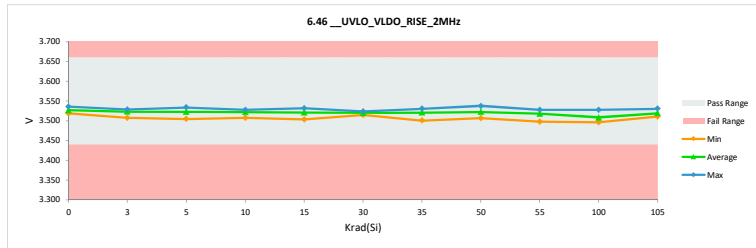
LDR TID Report  
TPS7H5001-SP

6.46 UVLO_VLDO_RISE_2MHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.520	3.519	-0.001
0	118	3.527	3.527	0.000
0	122	3.538	3.536	-0.002
3	61	3.526	3.523	-0.003
3	62	3.528	3.527	-0.001
3	63	3.510	3.508	-0.002
3	64	3.530	3.528	-0.002
3	65	3.528	3.527	-0.001
3	66	3.530	3.529	-0.001
5	67	3.534	3.534	0.000
5	68	3.507	3.505	-0.002
5	69	3.531	3.529	-0.002
5	70	3.524	3.524	0.000
5	71	3.523	3.522	-0.001
5	72	3.522	3.522	0.000
10	73	3.526	3.520	-0.006
10	74	3.528	3.526	-0.002
10	75	3.510	3.508	-0.002
10	76	3.530	3.526	-0.004
10	77	3.528	3.527	-0.001
10	78	3.530	3.528	-0.002
15	79	3.534	3.532	-0.002
15	80	3.507	3.504	-0.003
15	81	3.531	3.529	-0.002
15	82	3.524	3.523	-0.001
15	83	3.523	3.519	-0.004
15	84	3.522	3.523	0.001
30	86	3.526	3.516	-0.010
30	89	3.528	3.521	-0.007
30	91	3.510	3.515	0.005
30	92	3.530	3.519	-0.011
30	94	3.528	3.524	-0.004
30	95	3.530	3.524	-0.006
35	100	3.534	3.531	-0.003
35	101	3.507	3.501	-0.006
35	102	3.531	3.528	-0.003
35	103	3.524	3.525	0.001
35	104	3.523	3.518	-0.005
35	105	3.522	3.524	0.002
50	106	3.528	3.523	-0.005
50	107	3.534	3.519	-0.015
50	108	3.519	3.507	-0.012
50	109	3.540	3.531	-0.009
50	110	3.545	3.538	-0.007
50	111	3.526	3.519	-0.007
55	113	3.529	3.521	-0.008
55	114	3.504	3.498	-0.006
55	115	3.531	3.527	-0.004
55	116	3.526	3.516	-0.010
55	126	3.535	3.528	-0.007
55	128	3.524	3.519	-0.005
100	129	3.535	3.524	-0.011
100	131	3.517	3.509	-0.008
100	132	3.531	3.511	-0.020
100	133	3.521	3.510	-0.011
100	134	3.534	3.515	-0.019
100	135	3.520	3.502	-0.018
100	138	3.527	3.507	-0.020
100	139	3.526	3.512	-0.014
100	140	3.522	3.511	-0.011
100	141	3.528	3.506	-0.022
100	142	3.507	3.497	-0.010
100	143	3.526	3.510	-0.016
100	144	3.526	3.516	-0.010
100	220	3.529	3.507	-0.022
100	221	3.541	3.522	-0.019
100	224	3.514	3.498	-0.016
100	225	3.512	3.496	-0.016
100	226	3.542	3.528	-0.014
100	228	3.524	3.509	-0.015
100	232	3.525	3.505	-0.020
100	233	3.523	3.503	-0.020
100	236	3.516	3.502	-0.014
100	238	3.515	3.505	-0.010
100	243	3.520	3.508	-0.012
105	244	3.509	3.511	0.002
105	246	3.531	3.526	-0.005
105	248	3.527	3.519	-0.008
105	374	3.530	3.531	0.001
105	375	3.519	3.511	-0.008
105	Max	3.545	3.538	0.005
105	Average	3.525	3.518	-0.007
105	Min	3.504	3.496	-0.022
105	Std Dev	0.009	0.010	0.007



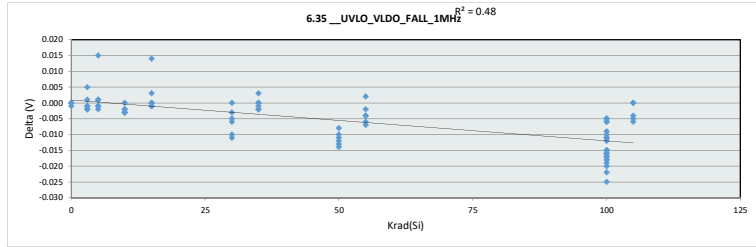
6.46 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.66 V										
Min Limit	3.44 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.519	3.508	3.505	3.508	3.504	3.515	3.501	3.507	3.498	3.496	3.511
Average	3.527	3.524	3.523	3.523	3.522	3.520	3.521	3.523	3.518	3.509	3.520
Max	3.536	3.529	3.534	3.528	3.532	3.524	3.531	3.538	3.528	3.528	3.531
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



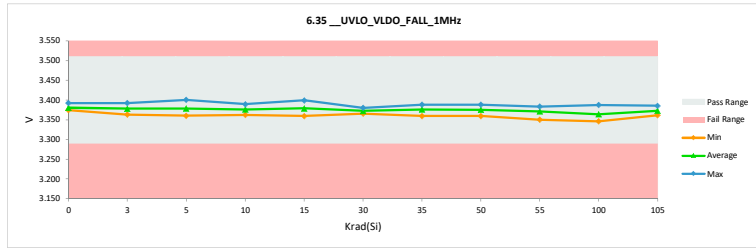
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6.35 UVLO_VLDO_FALL_1MHz			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	3.51	3.51	
Min Limit	3.29	3.29	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.374	0.000
0	118	3.375	3.375	0.000
0	122	3.393	3.392	-0.001
3	61	3.373	3.378	0.005
3	62	3.377	3.376	-0.001
3	63	3.365	3.363	-0.002
3	64	3.381	3.379	-0.002
3	65	3.391	3.392	0.001
3	66	3.380	3.379	-0.001
5	67	3.385	3.400	0.015
5	68	3.359	3.360	0.001
5	69	3.390	3.388	-0.002
5	70	3.375	3.376	0.001
5	71	3.378	3.377	-0.001
5	72	3.370	3.369	-0.001
10	73	3.373	3.373	0.000
10	74	3.377	3.374	-0.003
10	75	3.365	3.362	-0.003
10	76	3.381	3.378	-0.003
10	77	3.391	3.389	-0.002
10	78	3.380	3.378	-0.002
15	79	3.385	3.399	0.014
15	80	3.359	3.359	0.000
15	81	3.390	3.389	-0.001
15	82	3.375	3.375	0.000
15	83	3.378	3.377	-0.001
15	84	3.370	3.373	0.003
30	86	3.373	3.367	-0.006
30	89	3.377	3.372	-0.005
30	91	3.365	3.365	0.000
30	92	3.381	3.371	-0.010
30	94	3.391	3.380	-0.011
30	95	3.380	3.377	-0.003
35	100	3.385	3.383	-0.002
35	101	3.359	3.359	0.000
35	102	3.390	3.388	-0.002
35	103	3.375	3.375	0.000
35	104	3.378	3.377	-0.001
35	105	3.370	3.373	0.003
50	106	3.391	3.377	-0.014
50	107	3.389	3.379	-0.010
50	108	3.371	3.359	-0.012
50	109	3.390	3.379	-0.011
50	110	3.396	3.388	-0.008
50	111	3.382	3.369	-0.013
55	113	3.377	3.375	-0.002
55	114	3.354	3.350	-0.004
55	115	3.379	3.381	0.002
55	116	3.373	3.367	-0.006
55	126	3.390	3.383	-0.007
55	128	3.372	3.368	-0.004
100	129	3.383	3.374	-0.009
100	131	3.381	3.359	-0.022
100	132	3.384	3.367	-0.017
100	133	3.380	3.374	-0.006
100	134	3.381	3.364	-0.017
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.390	3.372	-0.018
100	140	3.370	3.365	-0.005
100	141	3.377	3.357	-0.020
100	142	3.371	3.346	-0.025
100	143	3.375	3.360	-0.015
100	144	3.374	3.368	-0.006
100	220	3.379	3.369	-0.010
100	221	3.389	3.373	-0.016
100	224	3.367	3.348	-0.019
100	225	3.363	3.352	-0.011
100	226	3.392	3.387	-0.005
100	228	3.383	3.367	-0.016
100	232	3.378	3.360	-0.018
100	233	3.373	3.362	-0.011
100	236	3.385	3.369	-0.016
100	238	3.370	3.358	-0.012
100	243	3.368	3.357	-0.011
105	244	3.361	3.361	0.000
105	246	3.380	3.376	-0.004
105	248	3.375	3.370	-0.005
105	374	3.385	3.385	0.000
105	376	3.376	3.370	-0.006
105	Max	3.396	3.400	0.015
105	Average	3.378	3.372	-0.006
105	Min	3.354	3.346	-0.025
105	Std Dev	0.009	0.011	0.008



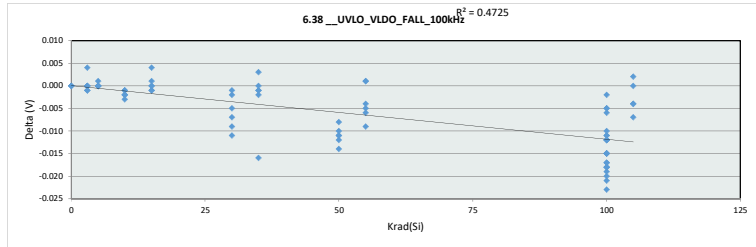
6.35 UVLO_VLD										
Test Site										
Tester										
Test Number										
Max Limit	3.51	V								
Min Limit	3.29	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.363	3.360	3.362	3.359	3.365	3.359	3.359	3.350	3.346
Average	3.380	3.378	3.378	3.376	3.379	3.372	3.376	3.375	3.371	3.364
Max	3.392	3.392	3.400	3.389	3.399	3.380	3.388	3.388	3.383	3.387
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



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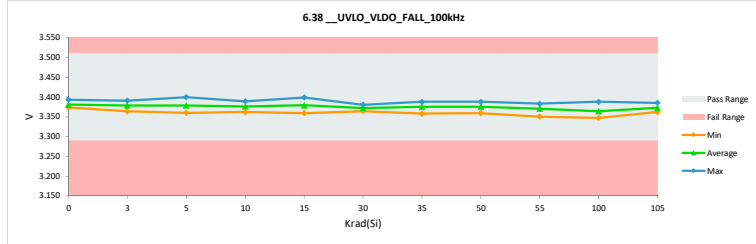
6.38 UVLO_VLDO_FALL_100kHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.51
Min Limit	3.29

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.374	0.000
0	118	3.376	3.376	0.000
0	122	3.393	3.393	0.000
3	61	3.374	3.378	0.004
3	62	3.377	3.377	0.000
3	63	3.365	3.364	-0.001
3	64	3.381	3.380	-0.001
3	65	3.391	3.391	0.000
3	66	3.380	3.380	0.000
5	67	3.400	3.400	0.000
5	68	3.359	3.360	0.001
5	69	3.389	3.389	0.000
5	70	3.375	3.375	0.000
5	71	3.378	3.378	0.000
5	72	3.370	3.370	0.000
10	73	3.374	3.373	-0.001
10	74	3.377	3.375	-0.002
10	75	3.365	3.362	-0.003
10	76	3.381	3.379	-0.002
10	77	3.391	3.389	-0.002
10	78	3.380	3.379	-0.001
15	79	3.400	3.399	-0.001
15	80	3.359	3.359	0.000
15	81	3.389	3.390	0.001
15	82	3.375	3.374	-0.001
15	83	3.378	3.378	0.000
15	84	3.370	3.374	0.004
30	86	3.374	3.367	-0.007
30	89	3.377	3.372	-0.005
30	91	3.365	3.364	-0.001
30	92	3.381	3.372	-0.009
30	94	3.391	3.380	-0.011
30	95	3.380	3.378	-0.002
35	100	3.400	3.384	-0.016
35	101	3.359	3.358	-0.001
35	102	3.389	3.388	-0.001
35	103	3.375	3.375	0.000
35	104	3.378	3.376	-0.002
35	105	3.370	3.373	0.003
50	106	3.391	3.377	-0.014
50	107	3.390	3.379	-0.011
50	108	3.370	3.359	-0.011
50	109	3.390	3.380	-0.010
50	110	3.396	3.388	-0.008
50	111	3.382	3.370	-0.012
55	113	3.376	3.377	0.001
55	114	3.355	3.350	-0.005
55	115	3.379	3.380	0.001
55	116	3.373	3.367	-0.006
55	126	3.392	3.383	-0.009
55	128	3.372	3.368	-0.004
100	129	3.384	3.373	-0.011
100	131	3.381	3.360	-0.021
100	132	3.385	3.368	-0.017
100	133	3.380	3.374	-0.006
100	134	3.382	3.364	-0.018
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.391	3.372	-0.019
100	140	3.369	3.367	-0.002
100	141	3.378	3.358	-0.020
100	142	3.370	3.347	-0.023
100	143	3.373	3.361	-0.012
100	144	3.373	3.368	-0.005
100	220	3.380	3.368	-0.012
100	221	3.389	3.374	-0.015
100	224	3.366	3.348	-0.018
100	225	3.364	3.352	-0.012
100	226	3.393	3.388	-0.005
100	228	3.384	3.367	-0.017
100	232	3.378	3.360	-0.018
100	233	3.373	3.363	-0.010
100	236	3.384	3.369	-0.015
100	238	3.370	3.358	-0.012
100	243	3.368	3.357	-0.011
105	244	3.360	3.362	0.002
105	246	3.380	3.376	-0.004
105	248	3.375	3.371	-0.004
105	374	3.385	3.385	0.000
105	376	3.376	3.369	-0.007
105	Max	3.400	3.400	0.004
105	Average	3.378	3.372	-0.006
105	Min	3.355	3.347	-0.023
105	Std Dev	0.010	0.011	0.007



6.38 UVLO_VLD	
Test Site	
Tester	
Test Number	
Max Limit	3.51
Min Limit	3.29

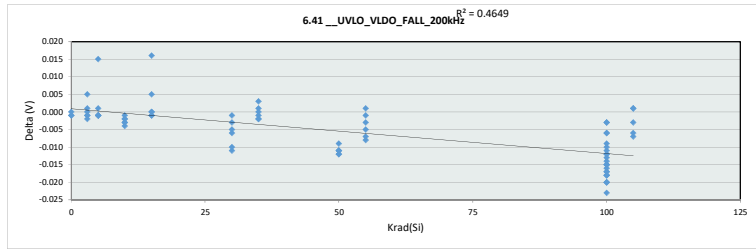
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.364	3.360	3.362	3.359	3.364	3.358	3.359	3.350	3.347
Average	3.381	3.378	3.379	3.376	3.379	3.372	3.376	3.376	3.371	3.364
Max	3.393	3.391	3.400	3.389	3.399	3.380	3.388	3.388	3.383	3.388
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



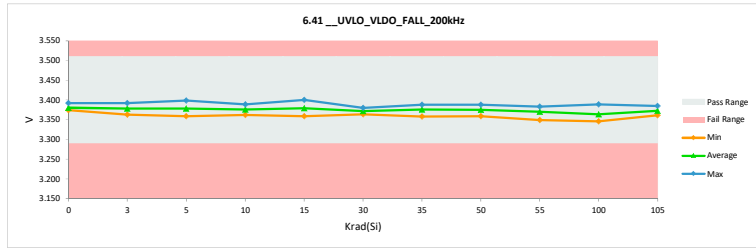
LDR TID Report  
TPS7H5001-SP

6.41 UVLO_VLDO_FALL_200kHz			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	3.51	3.51	
Min Limit	3.29	3.29	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.374	0.000
0	118	3.376	3.375	-0.001
0	122	3.393	3.392	-0.001
3	61	3.374	3.379	0.005
3	62	3.378	3.377	-0.001
3	63	3.365	3.363	-0.002
3	64	3.381	3.380	-0.001
3	65	3.391	3.392	0.001
3	66	3.380	3.380	0.000
5	67	3.384	3.399	0.015
5	68	3.360	3.359	-0.001
5	69	3.389	3.388	-0.001
5	70	3.375	3.374	-0.001
5	71	3.378	3.377	-0.001
5	72	3.370	3.371	0.001
10	73	3.374	3.372	-0.002
10	74	3.378	3.374	-0.004
10	75	3.365	3.362	-0.003
10	76	3.381	3.378	-0.003
10	77	3.391	3.389	-0.002
10	78	3.380	3.379	-0.001
15	79	3.384	3.400	0.016
15	80	3.360	3.359	-0.001
15	81	3.389	3.389	0.000
15	82	3.375	3.374	-0.001
15	83	3.378	3.378	0.000
15	84	3.370	3.375	0.005
30	86	3.374	3.368	-0.006
30	89	3.378	3.373	-0.005
30	91	3.365	3.364	-0.001
30	92	3.381	3.371	-0.010
30	94	3.391	3.380	-0.011
30	95	3.380	3.377	-0.003
35	100	3.384	3.384	0.000
35	101	3.360	3.358	-0.002
35	102	3.389	3.388	-0.001
35	103	3.375	3.376	0.001
35	104	3.378	3.376	-0.002
35	105	3.370	3.373	0.003
50	106	3.390	3.378	-0.012
50	107	3.390	3.379	-0.011
50	108	3.371	3.359	-0.012
50	109	3.390	3.379	-0.011
50	110	3.397	3.388	-0.009
50	111	3.381	3.370	-0.011
55	113	3.377	3.376	-0.001
55	114	3.354	3.349	-0.005
55	115	3.379	3.380	0.001
55	116	3.373	3.366	-0.007
55	126	3.391	3.383	-0.008
55	128	3.372	3.369	-0.003
100	129	3.384	3.375	-0.009
100	131	3.383	3.360	-0.023
100	132	3.385	3.367	-0.018
100	133	3.380	3.374	-0.006
100	134	3.381	3.364	-0.017
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.391	3.373	-0.018
100	140	3.369	3.366	-0.003
100	141	3.378	3.358	-0.020
100	142	3.366	3.346	-0.020
100	143	3.375	3.360	-0.015
100	144	3.374	3.368	-0.006
100	220	3.379	3.369	-0.010
100	221	3.387	3.373	-0.014
100	224	3.366	3.346	-0.020
100	225	3.364	3.351	-0.013
100	226	3.392	3.389	-0.003
100	228	3.384	3.367	-0.017
100	232	3.378	3.360	-0.018
100	233	3.374	3.362	-0.012
100	236	3.385	3.369	-0.016
100	238	3.369	3.357	-0.012
100	243	3.368	3.357	-0.011
105	244	3.360	3.361	0.001
105	246	3.380	3.377	-0.003
105	248	3.376	3.370	-0.006
105	374	3.384	3.385	0.001
105	376	3.376	3.369	-0.007
105	375	3.397	3.400	0.016
Average		3.378	3.372	-0.006
Min		3.354	3.346	-0.023
Std Dev		0.009	0.011	0.008



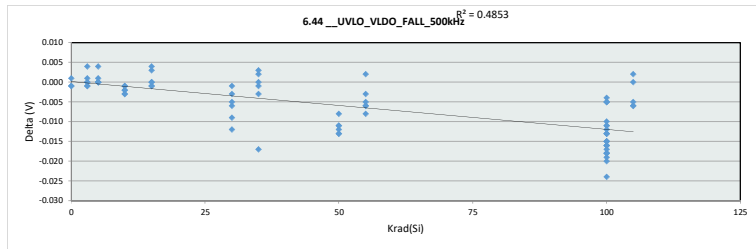
6.41 UVLO_VLD										
Test Site										
Tester										
Test Number										
Max Limit	3.51	V								
Min Limit	3.29	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.363	3.359	3.362	3.359	3.364	3.358	3.359	3.349	3.346
Average	3.380	3.379	3.378	3.376	3.379	3.372	3.376	3.376	3.371	3.364
Max	3.392	3.392	3.399	3.389	3.400	3.380	3.388	3.388	3.383	3.389
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



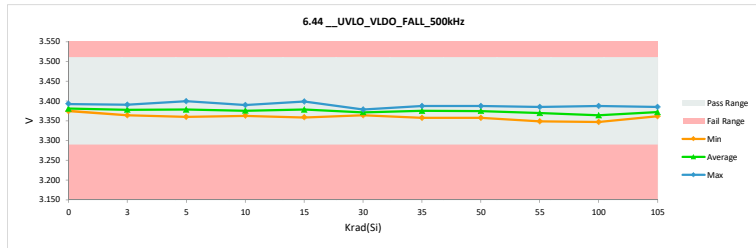
LDR TID Report  
TPS7H5001-SP

6.44 UVLO_VLDO_FALL_500kHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.51
Min Limit	3.29

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.375	0.001
0	118	3.376	3.375	-0.001
0	122	3.394	3.393	-0.001
3	61	3.374	3.378	0.004
3	62	3.377	3.378	0.001
3	63	3.365	3.364	-0.001
3	64	3.381	3.380	-0.001
3	65	3.391	3.391	0.000
3	66	3.380	3.380	0.000
5	67	3.400	3.400	0.000
5	68	3.359	3.360	0.001
5	69	3.385	3.389	0.004
5	70	3.375	3.375	0.000
5	71	3.378	3.378	0.000
5	72	3.371	3.371	0.000
10	73	3.374	3.373	-0.001
10	74	3.377	3.374	-0.003
10	75	3.365	3.363	-0.002
10	76	3.381	3.378	-0.003
10	77	3.391	3.390	-0.001
10	78	3.380	3.378	-0.002
15	79	3.400	3.399	-0.001
15	80	3.359	3.359	0.000
15	81	3.385	3.388	0.003
15	82	3.375	3.375	0.000
15	83	3.378	3.377	-0.001
15	84	3.371	3.375	0.004
30	86	3.374	3.368	-0.006
30	89	3.377	3.372	-0.005
30	91	3.365	3.364	-0.001
30	92	3.381	3.372	-0.009
30	94	3.391	3.379	-0.012
30	95	3.380	3.377	-0.003
35	100	3.400	3.383	-0.017
35	101	3.359	3.358	-0.001
35	102	3.385	3.388	0.003
35	103	3.375	3.375	0.000
35	104	3.378	3.375	-0.003
35	105	3.371	3.373	0.002
50	106	3.391	3.378	-0.013
50	107	3.390	3.379	-0.011
50	108	3.371	3.358	-0.013
50	109	3.390	3.379	-0.011
50	110	3.396	3.388	-0.008
50	111	3.381	3.369	-0.012
55	113	3.376	3.371	-0.005
55	114	3.355	3.349	-0.006
55	115	3.379	3.381	0.002
55	116	3.374	3.366	-0.008
55	126	3.391	3.385	-0.006
55	128	3.372	3.369	-0.003
100	129	3.384	3.373	-0.011
100	131	3.383	3.359	-0.024
100	132	3.384	3.367	-0.017
100	133	3.379	3.374	-0.005
100	134	3.381	3.365	-0.016
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.390	3.372	-0.018
100	140	3.369	3.365	-0.004
100	141	3.377	3.357	-0.020
100	142	3.365	3.347	-0.018
100	143	3.374	3.361	-0.013
100	144	3.374	3.369	-0.005
100	220	3.380	3.369	-0.011
100	221	3.389	3.373	-0.016
100	224	3.367	3.348	-0.019
100	225	3.364	3.351	-0.013
100	226	3.393	3.388	-0.005
100	228	3.383	3.367	-0.016
100	232	3.378	3.360	-0.018
100	233	3.374	3.361	-0.013
100	236	3.384	3.369	-0.015
100	238	3.370	3.358	-0.012
100	243	3.367	3.357	-0.010
105	244	3.360	3.362	0.002
105	246	3.381	3.376	-0.005
105	248	3.376	3.370	-0.006
105	374	3.385	3.385	0.000
105	375	3.376	3.370	-0.006
105	376	3.400	3.400	0.004
	Max	3.400	3.400	0.004
	Average	3.378	3.372	-0.006
	Min	3.355	3.347	-0.024
	Std Dev	0.010	0.011	0.007



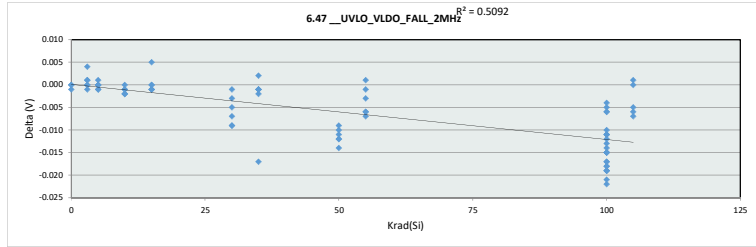
6.44 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.51 V										
Min Limit	3.29 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.375	3.364	3.360	3.363	3.359	3.364	3.358	3.358	3.349	3.347	3.362
Average	3.381	3.379	3.379	3.376	3.379	3.372	3.375	3.375	3.370	3.364	3.373
Max	3.393	3.391	3.400	3.390	3.399	3.379	3.388	3.388	3.385	3.388	3.385
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



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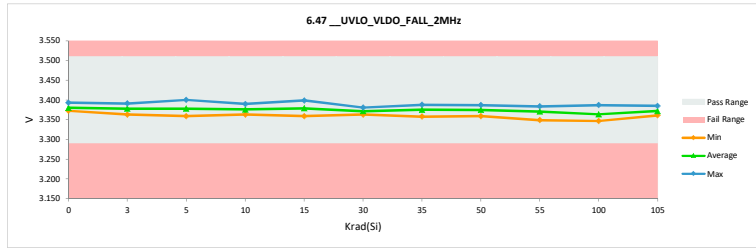
6.47 UVLO_VLDO_FALL_2MHz			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	3.51	3.51	
Min Limit	3.29	3.29	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.373	-0.001
0	118	3.375	3.375	0.000
0	122	3.393	3.393	0.000
3	61	3.374	3.378	0.004
3	62	3.376	3.377	0.001
3	63	3.364	3.363	-0.001
3	64	3.380	3.380	0.000
3	65	3.390	3.391	0.001
3	66	3.380	3.380	0.000
5	67	3.400	3.400	0.000
5	68	3.359	3.359	0.000
5	69	3.389	3.388	-0.001
5	70	3.376	3.375	-0.001
5	71	3.377	3.378	0.001
5	72	3.370	3.370	0.000
10	73	3.374	3.372	-0.002
10	74	3.376	3.375	-0.001
10	75	3.364	3.363	-0.001
10	76	3.380	3.378	-0.002
10	77	3.390	3.390	0.000
10	78	3.380	3.378	-0.002
15	79	3.400	3.399	-0.001
15	80	3.359	3.359	0.000
15	81	3.389	3.388	-0.001
15	82	3.376	3.375	-0.001
15	83	3.377	3.377	0.000
15	84	3.370	3.375	0.005
30	86	3.374	3.367	-0.007
30	89	3.376	3.371	-0.005
30	91	3.364	3.363	-0.001
30	92	3.380	3.371	-0.009
30	94	3.390	3.381	-0.009
30	95	3.380	3.377	-0.003
35	100	3.400	3.383	-0.017
35	101	3.359	3.358	-0.001
35	102	3.389	3.388	-0.001
35	103	3.376	3.374	-0.002
35	104	3.377	3.376	-0.001
35	105	3.370	3.372	0.002
50	106	3.391	3.377	-0.014
50	107	3.390	3.380	-0.010
50	108	3.371	3.359	-0.012
50	109	3.389	3.378	-0.011
50	110	3.396	3.387	-0.009
50	111	3.381	3.369	-0.012
55	113	3.377	3.376	-0.001
55	114	3.355	3.349	-0.006
55	115	3.379	3.380	0.001
55	116	3.373	3.367	-0.006
55	126	3.391	3.384	-0.007
55	128	3.371	3.368	-0.003
100	129	3.383	3.373	-0.010
100	131	3.382	3.360	-0.022
100	132	3.386	3.367	-0.019
100	133	3.380	3.374	-0.006
100	134	3.381	3.364	-0.017
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.390	3.371	-0.019
100	140	3.370	3.366	-0.004
100	141	3.378	3.357	-0.021
100	142	3.366	3.347	-0.019
100	143	3.374	3.360	-0.014
100	144	3.374	3.369	-0.005
100	220	3.379	3.368	-0.011
100	221	3.388	3.373	-0.015
100	224	3.365	3.347	-0.018
100	225	3.364	3.351	-0.013
100	226	3.393	3.387	-0.006
100	228	3.383	3.366	-0.017
100	232	3.378	3.360	-0.018
100	233	3.374	3.363	-0.011
100	236	3.385	3.370	-0.015
100	238	3.370	3.358	-0.012
100	243	3.368	3.357	-0.011
105	244	3.360	3.361	0.001
105	246	3.381	3.376	-0.005
105	248	3.376	3.370	-0.006
105	374	3.385	3.385	0.000
105	376	3.376	3.369	-0.007
105	Max	3.400	3.400	0.005
105	Average	3.378	3.372	-0.006
105	Min	3.355	3.347	-0.022
105	Std Dev	0.010	0.011	0.007



6.47 UVLO_VLD			
Test Site			
Tester			
Test Number			
Max Limit	3.51	V	
Min Limit	3.29	V	

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.373	3.363	3.359	3.363	3.359	3.363	3.358	3.359	3.349	3.347	3.361
Average	3.380	3.378	3.378	3.378	3.379	3.372	3.375	3.375	3.371	3.364	3.372
Max	3.393	3.391	3.400	3.390	3.399	3.381	3.388	3.387	3.384	3.387	3.385
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

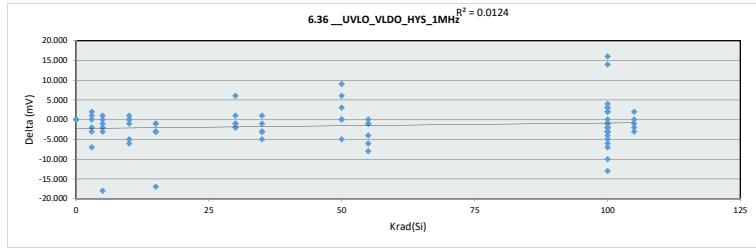




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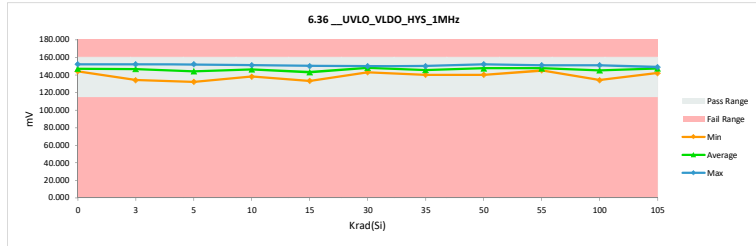
6.36 UVLO_VLDO_HYS_1MHz	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	160
Min Limit	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	145.000	145.000	0.000
0	118	152.000	152.000	0.000
0	122	144.000	144.000	0.000
3	61	152.000	145.000	-7.000
3	62	151.000	149.000	-2.000
3	63	151.000	152.000	1.000
3	64	148.000	150.000	2.000
3	65	137.000	134.000	-3.000
3	66	149.000	149.000	0.000
5	67	150.000	132.000	-18.000
5	68	147.000	145.000	-2.000
5	69	143.000	140.000	-3.000
5	70	149.000	149.000	0.000
5	71	145.000	146.000	1.000
5	72	153.000	152.000	-1.000
10	73	152.000	147.000	-5.000
10	74	151.000	151.000	0.000
10	75	151.000	145.000	-6.000
10	76	148.000	147.000	-1.000
10	77	137.000	138.000	1.000
10	78	149.000	149.000	0.000
15	79	150.000	133.000	-17.000
15	80	147.000	146.000	-1.000
15	81	143.000	140.000	-3.000
15	82	149.000	148.000	-1.000
15	83	145.000	142.000	-3.000
15	84	153.000	150.000	-3.000
30	86	152.000	150.000	-2.000
30	89	151.000	149.000	-2.000
30	91	151.000	150.000	-1.000
30	92	148.000	149.000	1.000
30	94	137.000	143.000	6.000
30	95	149.000	148.000	-1.000
35	100	150.000	149.000	-1.000
35	101	147.000	142.000	-5.000
35	102	143.000	140.000	-3.000
35	103	149.000	150.000	1.000
35	104	145.000	142.000	-3.000
35	105	153.000	150.000	-3.000
50	106	137.000	146.000	9.000
50	107	145.000	140.000	-5.000
50	108	149.000	149.000	0.000
50	109	149.000	152.000	3.000
50	110	149.000	149.000	0.000
50	111	143.000	149.000	6.000
55	113	153.000	145.000	-8.000
55	114	150.000	149.000	-1.000
55	115	152.000	146.000	-6.000
55	116	153.000	149.000	-4.000
55	126	145.000	145.000	0.000
55	128	152.000	151.000	-1.000
100	129	152.000	151.000	-1.000
100	131	136.000	150.000	14.000
100	132	147.000	145.000	-2.000
100	133	140.000	135.000	-5.000
100	134	154.000	151.000	-3.000
100	135	145.000	142.000	-3.000
100	138	149.000	145.000	-4.000
100	139	137.000	140.000	3.000
100	140	150.000	147.000	-3.000
100	141	151.000	149.000	-2.000
100	142	135.000	151.000	16.000
100	143	152.000	151.000	-1.000
100	144	153.000	147.000	-6.000
100	220	150.000	137.000	-13.000
100	221	152.000	150.000	-2.000
100	224	147.000	149.000	2.000
100	225	148.000	145.000	-3.000
100	226	149.000	142.000	-7.000
100	228	141.000	143.000	2.000
100	232	147.000	144.000	-3.000
100	233	150.000	140.000	-10.000
100	236	131.000	134.000	3.000
100	238	146.000	146.000	0.000
100	243	146.000	150.000	4.000
105	244	149.000	149.000	0.000
105	246	150.000	149.000	-1.000
105	248	152.000	149.000	-3.000
105	374	145.000	147.000	2.000
105	376	144.000	142.000	-2.000
105	Max	154.000	152.000	16.000
105	Average	147.388	145.900	-1.488
105	Min	131.000	132.000	-18.000
105	Std Dev	4.982	4.864	4.963



6.36 UVLO_VLD	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	160
Min Limit	115

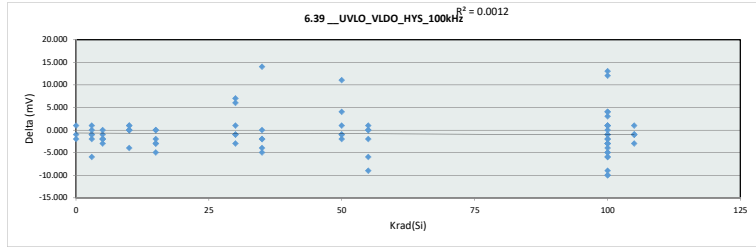
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	144.000	134.000	132.000	138.000	133.000	143.000	140.000	145.000	134.000	142.000	
Average	147.000	146.500	144.000	146.167	143.167	148.167	145.500	147.500	145.167	147.200	
Max	152.000	152.000	152.000	151.000	150.000	150.000	150.000	152.000	151.000	149.000	
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	



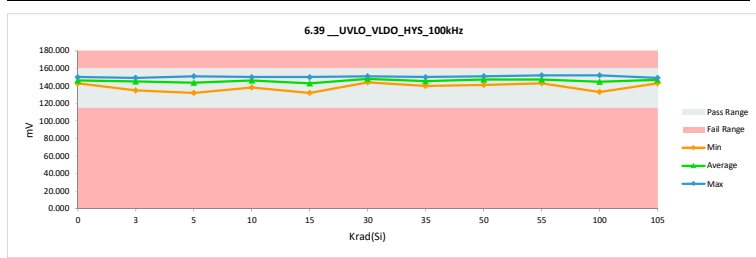
LDR TID Report  
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6.39 UVLO_VLDO_HYS_100kHz		
Test Site		
Tester		
Test Number		
Unit	mV	mV
Max Limit	160	160
Min Limit	115	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	145.000	146.000	1.000
0	118	151.000	150.000	-1.000
0	122	145.000	143.000	-2.000
3	61	151.000	145.000	-6.000
3	62	150.000	149.000	-1.000
3	63	145.000	144.000	-1.000
3	64	147.000	148.000	1.000
3	65	137.000	135.000	-2.000
3	66	149.000	149.000	0.000
5	67	134.000	132.000	-2.000
5	68	147.000	144.000	-3.000
5	69	142.000	140.000	-2.000
5	70	150.000	150.000	0.000
5	71	146.000	144.000	-2.000
5	72	152.000	151.000	-1.000
10	73	151.000	147.000	-4.000
10	74	150.000	150.000	0.000
10	75	145.000	146.000	1.000
10	76	147.000	147.000	0.000
10	77	137.000	138.000	1.000
10	78	149.000	149.000	0.000
15	79	134.000	132.000	-2.000
15	80	147.000	147.000	0.000
15	81	142.000	139.000	-3.000
15	82	150.000	150.000	0.000
15	83	146.000	141.000	-5.000
15	84	152.000	149.000	-3.000
30	86	151.000	150.000	-1.000
30	89	150.000	149.000	-1.000
30	91	145.000	151.000	6.000
30	92	147.000	148.000	1.000
30	94	137.000	144.000	7.000
30	95	149.000	146.000	-3.000
35	100	134.000	148.000	14.000
35	101	147.000	142.000	-5.000
35	102	142.000	140.000	-2.000
35	103	150.000	150.000	0.000
35	104	146.000	142.000	-4.000
35	105	152.000	150.000	-2.000
50	106	136.000	147.000	11.000
50	107	143.000	141.000	-2.000
50	108	149.000	148.000	-1.000
50	109	150.000	151.000	1.000
50	110	150.000	149.000	-1.000
50	111	143.000	147.000	4.000
55	113	152.000	143.000	-9.000
55	114	149.000	149.000	0.000
55	115	152.000	146.000	-6.000
55	116	152.000	150.000	-2.000
55	126	143.000	144.000	1.000
55	128	152.000	152.000	0.000
100	129	152.000	152.000	0.000
100	131	137.000	149.000	12.000
100	132	146.000	144.000	-2.000
100	133	140.000	135.000	-5.000
100	134	152.000	151.000	-1.000
100	135	145.000	142.000	-3.000
100	138	150.000	144.000	-6.000
100	139	136.000	140.000	4.000
100	140	151.000	145.000	-6.000
100	141	150.000	148.000	-2.000
100	142	136.000	149.000	13.000
100	143	153.000	150.000	-3.000
100	144	153.000	148.000	-5.000
100	220	149.000	139.000	-10.000
100	221	151.000	150.000	-1.000
100	224	148.000	149.000	1.000
100	225	149.000	145.000	-4.000
100	226	149.000	140.000	-9.000
100	228	140.000	143.000	3.000
100	232	147.000	144.000	-3.000
100	233	150.000	140.000	-10.000
100	236	132.000	133.000	1.000
100	238	146.000	147.000	1.000
100	243	146.000	150.000	4.000
105	244	149.000	148.000	-1.000
105	246	150.000	149.000	-1.000
105	248	151.000	148.000	-3.000
105	374	146.000	147.000	1.000
105	376	144.000	143.000	-1.000
105	Max	153.000	152.000	14.000
105	Average	146.375	145.550	-0.825
105	Min	132.000	132.000	-10.000
105	Std Dev	5.371	4.752	4.380



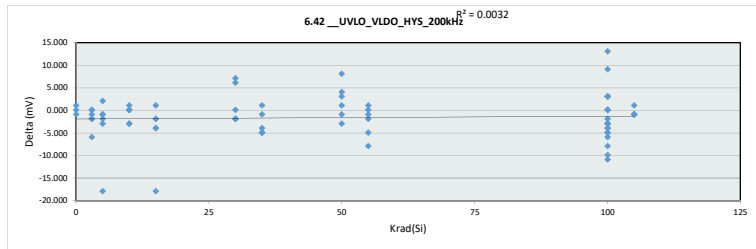
6.39 UVLO_VLD		
Test Site		
Tester		
Test Number		
Max Limit	160	mV
Min Limit	115	mV



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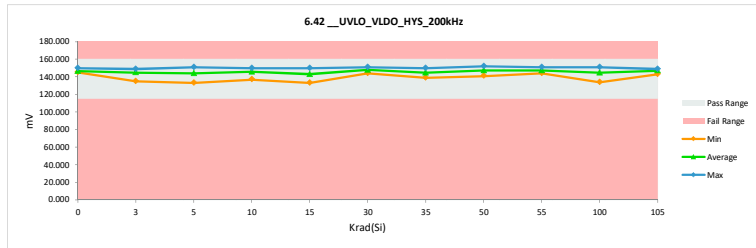
6.42 UVLO_VLDO_HYS_200kHz		
Test Site		
Tester		
Test Number		
Unit	mV	mV
Max Limit	160	160
Min Limit	115	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	145.000	145.000	0.000
0	118	151.000	150.000	-1.000
0	122	144.000	145.000	1.000
3	61	151.000	145.000	-6.000
3	62	150.000	149.000	-1.000
3	63	145.000	145.000	0.000
3	64	149.000	147.000	-2.000
3	65	137.000	135.000	-2.000
3	66	149.000	149.000	0.000
5	67	151.000	133.000	-18.000
5	68	147.000	145.000	-2.000
5	69	144.000	141.000	-3.000
5	70	149.000	151.000	2.000
5	71	146.000	145.000	-1.000
5	72	151.000	150.000	-1.000
10	73	151.000	148.000	-3.000
10	74	150.000	150.000	0.000
10	75	145.000	146.000	1.000
10	76	149.000	146.000	-3.000
10	77	137.000	137.000	0.000
10	78	149.000	149.000	0.000
15	79	151.000	133.000	-18.000
15	80	147.000	145.000	-2.000
15	81	144.000	140.000	-4.000
15	82	149.000	150.000	1.000
15	83	146.000	142.000	-4.000
15	84	151.000	149.000	-2.000
30	86	151.000	149.000	-2.000
30	89	150.000	148.000	-2.000
30	91	145.000	151.000	6.000
30	92	149.000	149.000	0.000
30	94	137.000	144.000	7.000
30	95	149.000	147.000	-2.000
35	100	151.000	147.000	-4.000
35	101	147.000	142.000	-5.000
35	102	144.000	139.000	-5.000
35	103	149.000	150.000	1.000
35	104	146.000	141.000	-5.000
35	105	151.000	150.000	-1.000
50	106	138.000	146.000	8.000
50	107	144.000	141.000	-3.000
50	108	149.000	148.000	-1.000
50	109	149.000	152.000	3.000
50	110	149.000	150.000	1.000
50	111	143.000	147.000	4.000
55	113	152.000	144.000	-8.000
55	114	150.000	149.000	-1.000
55	115	152.000	147.000	-5.000
55	116	151.000	149.000	-2.000
55	126	144.000	145.000	1.000
55	128	151.000	151.000	0.000
100	129	153.000	150.000	-3.000
100	131	135.000	148.000	13.000
100	132	146.000	144.000	-2.000
100	133	140.000	135.000	-5.000
100	134	153.000	150.000	-3.000
100	135	145.000	142.000	-3.000
100	138	150.000	145.000	-5.000
100	139	136.000	139.000	3.000
100	140	152.000	146.000	-6.000
100	141	151.000	148.000	-3.000
100	142	140.000	149.000	9.000
100	143	151.000	151.000	0.000
100	144	152.000	148.000	-4.000
100	220	149.000	138.000	-11.000
100	221	155.000	150.000	-5.000
100	224	148.000	151.000	3.000
100	225	149.000	145.000	-4.000
100	226	149.000	139.000	-10.000
100	228	140.000	143.000	3.000
100	232	147.000	144.000	-3.000
100	233	149.000	141.000	-8.000
100	236	131.000	134.000	3.000
100	238	147.000	147.000	0.000
100	243	151.000	151.000	0.000
105	244	148.000	149.000	1.000
105	246	150.000	149.000	-1.000
105	248	150.000	149.000	-1.000
105	374	147.000	146.000	-1.000
105	376	144.000	143.000	-1.000
105	Max	155.000	152.000	13.000
105	Average	147.213	145.625	-1.588
105	Min	131.000	133.000	-18.000
105	Std Dev	4.711	4.662	4.673



6.42 UVLO_VLD		
Test Site		
Tester		
Test Number		
Max Limit	160	mV
Min Limit	115	mV

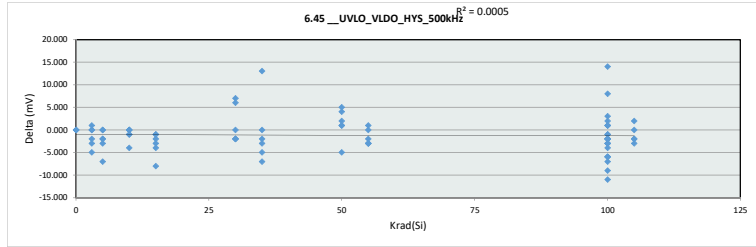
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	145.000	135.000	133.000	137.000	133.000	144.000	139.000	141.000	144.000	134.000	143.000
Average	146.667	145.000	144.167	146.000	143.167	148.000	144.833	147.333	147.500	144.917	147.200
Max	150.000	149.000	151.000	150.000	150.000	151.000	150.000	152.000	151.000	151.000	149.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000



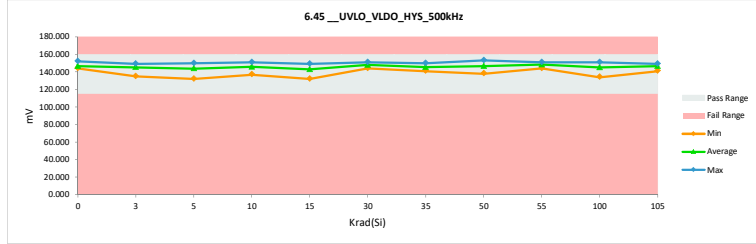
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6.45 UVLO_VLDO_HYS_500kHz	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	160
Min Limit	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	144.000	144.000	0.000
0	118	152.000	152.000	0.000
0	122	144.000	144.000	0.000
3	61	151.000	146.000	-5.000
3	62	151.000	148.000	-3.000
3	63	145.000	145.000	0.000
3	64	148.000	149.000	1.000
3	65	137.000	135.000	-2.000
3	66	149.000	149.000	0.000
5	67	135.000	132.000	-3.000
5	68	147.000	145.000	-2.000
5	69	148.000	141.000	-7.000
5	70	150.000	150.000	0.000
5	71	145.000	145.000	0.000
5	72	152.000	150.000	-2.000
10	73	151.000	147.000	-4.000
10	74	151.000	151.000	0.000
10	75	145.000	144.000	-1.000
10	76	148.000	147.000	-1.000
10	77	137.000	137.000	0.000
10	78	149.000	149.000	0.000
15	79	135.000	132.000	-3.000
15	80	147.000	146.000	-1.000
15	81	148.000	140.000	-8.000
15	82	150.000	149.000	-1.000
15	83	145.000	143.000	-2.000
15	84	152.000	148.000	-4.000
30	86	151.000	149.000	-2.000
30	89	151.000	149.000	-2.000
30	91	145.000	151.000	6.000
30	92	148.000	148.000	0.000
30	94	137.000	144.000	7.000
30	95	149.000	147.000	-2.000
35	100	135.000	148.000	13.000
35	101	147.000	142.000	-5.000
35	102	148.000	141.000	-7.000
35	103	150.000	150.000	0.000
35	104	145.000	142.000	-3.000
35	105	152.000	150.000	-2.000
50	106	137.000	138.000	1.000
50	107	145.000	140.000	-5.000
50	108	148.000	150.000	2.000
50	109	149.000	153.000	4.000
50	110	149.000	150.000	1.000
50	111	144.000	149.000	5.000
55	113	152.000	149.000	-3.000
55	114	150.000	150.000	0.000
55	115	150.000	147.000	-3.000
55	116	153.000	150.000	-3.000
55	126	143.000	144.000	1.000
55	128	153.000	151.000	-2.000
100	129	152.000	151.000	-1.000
100	131	135.000	149.000	14.000
100	132	147.000	145.000	-2.000
100	133	141.000	135.000	-6.000
100	134	153.000	150.000	-3.000
100	135	145.000	142.000	-3.000
100	138	150.000	144.000	-6.000
100	139	138.000	139.000	1.000
100	140	152.000	148.000	-4.000
100	141	151.000	149.000	-2.000
100	142	141.000	149.000	8.000
100	143	152.000	150.000	-2.000
100	144	153.000	147.000	-6.000
100	220	149.000	138.000	-11.000
100	221	152.000	150.000	-2.000
100	224	147.000	150.000	3.000
100	225	148.000	145.000	-3.000
100	226	149.000	140.000	-9.000
100	228	142.000	143.000	1.000
100	232	146.000	144.000	-2.000
100	233	150.000	143.000	-7.000
100	236	132.000	134.000	2.000
100	238	147.000	148.000	1.000
100	243	151.000	150.000	-1.000
105	244	149.000	147.000	-2.000
105	246	151.000	149.000	-2.000
105	248	149.000	149.000	0.000
105	374	145.000	147.000	2.000
105	376	144.000	141.000	-3.000
105	Max	153.000	153.000	14.000
105	Average	146.850	145.700	-1.150
105	Min	132.000	132.000	-11.000
105	Std Dev	5.139	4.853	4.041



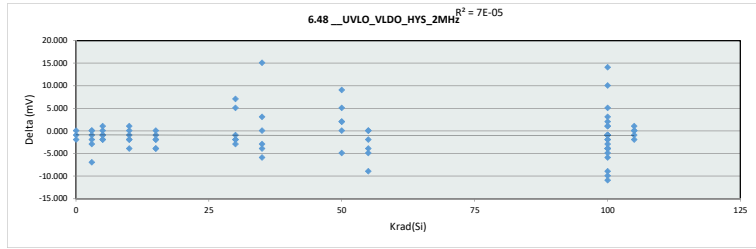
6.45 UVLO_VLD		0	3	5	10	15	30	35	50	55	100	105
LL		115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min		144.000	135.000	132.000	137.000	132.000	144.000	141.000	138.000	144.000	134.000	141.000
Average		146.667	145.333	143.833	145.833	143.000	148.000	145.500	146.667	148.500	145.125	146.600
Max		152.000	149.000	150.000	151.000	149.000	151.000	150.000	153.000	151.000	151.000	149.000
UL		160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000



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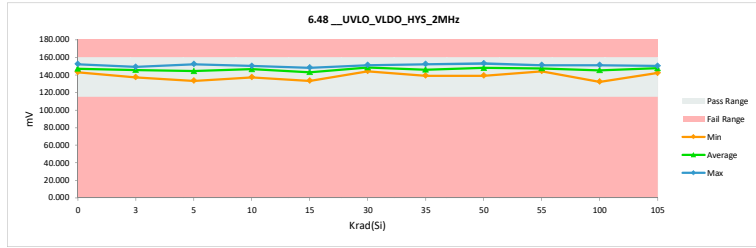
6.48 UVLO_VLDO_HYS_2MHz		
Test Site		
Tester		
Test Number		
Unit	mV	mV
Max Limit	160	160
Min Limit	115	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	146.000	146.000	0.000
0	118	153.000	152.000	-1.000
0	122	145.000	145.000	-2.000
3	61	152.000	145.000	-7.000
3	62	152.000	149.000	-3.000
3	63	146.000	145.000	-1.000
3	64	150.000	148.000	-2.000
3	65	137.000	137.000	0.000
3	66	149.000	149.000	0.000
5	67	134.000	133.000	-1.000
5	68	148.000	146.000	-2.000
5	69	142.000	141.000	-1.000
5	70	148.000	149.000	1.000
5	71	146.000	144.000	-2.000
5	72	152.000	152.000	0.000
10	73	152.000	148.000	-4.000
10	74	152.000	150.000	-2.000
10	75	146.000	145.000	-1.000
10	76	150.000	148.000	-2.000
10	77	137.000	137.000	0.000
10	78	149.000	150.000	1.000
15	79	134.000	133.000	-1.000
15	80	148.000	146.000	-2.000
15	81	142.000	140.000	-2.000
15	82	148.000	148.000	0.000
15	83	146.000	142.000	-4.000
15	84	152.000	148.000	-4.000
30	86	152.000	149.000	-3.000
30	89	152.000	150.000	-2.000
30	91	146.000	151.000	5.000
30	92	150.000	148.000	-2.000
30	94	137.000	144.000	7.000
30	95	149.000	148.000	-1.000
35	100	134.000	149.000	15.000
35	101	148.000	142.000	-6.000
35	102	142.000	139.000	-3.000
35	103	148.000	151.000	3.000
35	104	146.000	142.000	-4.000
35	105	152.000	152.000	0.000
50	106	137.000	146.000	9.000
50	107	144.000	139.000	-5.000
50	108	148.000	148.000	0.000
50	109	151.000	153.000	2.000
50	110	149.000	151.000	2.000
50	111	145.000	150.000	5.000
55	113	153.000	144.000	-9.000
55	114	149.000	149.000	0.000
55	115	152.000	147.000	-5.000
55	116	153.000	149.000	-4.000
55	126	144.000	144.000	0.000
55	128	153.000	151.000	-2.000
100	129	152.000	151.000	-1.000
100	131	135.000	149.000	14.000
100	132	145.000	144.000	-1.000
100	133	140.000	136.000	-4.000
100	134	152.000	151.000	-1.000
100	135	145.000	142.000	-3.000
100	138	148.000	144.000	-4.000
100	139	136.000	141.000	5.000
100	140	152.000	146.000	-6.000
100	141	150.000	149.000	-1.000
100	142	141.000	151.000	10.000
100	143	152.000	150.000	-2.000
100	144	152.000	147.000	-5.000
100	220	150.000	139.000	-11.000
100	221	153.000	149.000	-4.000
100	224	149.000	151.000	2.000
100	225	149.000	145.000	-4.000
100	226	149.000	140.000	-9.000
100	228	140.000	143.000	3.000
100	232	147.000	145.000	-2.000
100	233	150.000	140.000	-10.000
100	236	131.000	132.000	1.000
100	238	146.000	147.000	1.000
100	243	152.000	151.000	-1.000
105	244	149.000	150.000	1.000
105	246	150.000	150.000	0.000
105	248	151.000	149.000	-2.000
105	374	146.000	146.000	0.000
105	376	143.000	142.000	-1.000
105	Max	153.000	153.000	15.000
105	Average	146.813	145.875	-0.938
105	Min	131.000	132.000	-11.000
105	Std Dev	5.526	4.845	4.385



6.48 UVLO_VLD		
Test Site		
Tester		
Test Number		
Max Limit	160	mV
Min Limit	115	mV

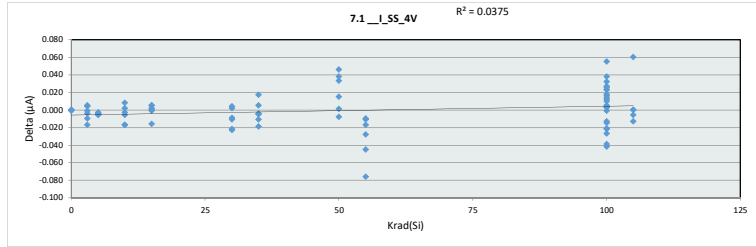
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	143.000	137.000	133.000	137.000	133.000	144.000	139.000	139.000	144.000	132.000	142.000
Average	147.000	145.500	144.167	146.333	142.833	148.333	145.833	147.833	147.333	145.125	147.400
Max	152.000	149.000	152.000	150.000	148.000	151.000	152.000	153.000	151.000	151.000	150.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000



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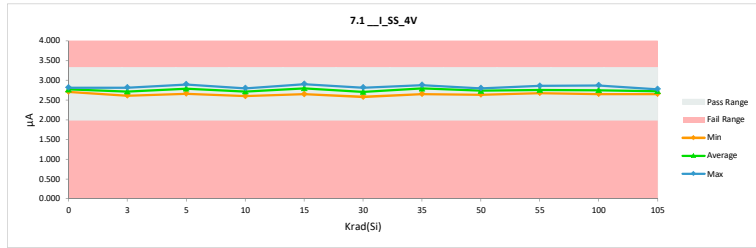
7.1_I_SS_4V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	3.32
Min Limit	1.98

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.702	2.702	0.000
0	118	2.786	2.785	-0.001
0	122	2.808	2.808	0.000
3	61	2.747	2.730	-0.017
3	62	2.811	2.810	-0.001
3	63	2.697	2.702	0.005
3	64	2.784	2.780	-0.004
3	65	2.661	2.651	-0.010
3	66	2.601	2.605	0.004
5	67	2.692	2.687	-0.005
5	68	2.837	2.833	-0.004
5	69	2.660	2.657	-0.003
5	70	2.897	2.893	-0.004
5	71	2.834	2.829	-0.005
5	72	2.832	2.826	-0.006
10	73	2.747	2.730	-0.017
10	74	2.811	2.794	-0.017
10	75	2.697	2.699	0.002
10	76	2.784	2.792	0.008
10	77	2.661	2.658	-0.003
10	78	2.601	2.595	-0.006
15	79	2.692	2.697	0.005
15	80	2.837	2.836	-0.001
15	81	2.660	2.644	-0.016
15	82	2.897	2.902	0.005
15	83	2.834	2.834	0.000
15	84	2.832	2.834	0.002
30	86	2.747	2.751	0.004
30	89	2.811	2.813	0.002
30	91	2.697	2.688	-0.009
30	92	2.784	2.761	-0.023
30	94	2.661	2.650	-0.011
30	95	2.601	2.579	-0.022
35	100	2.692	2.687	-0.005
35	101	2.837	2.854	0.017
35	102	2.660	2.649	-0.011
35	103	2.897	2.878	-0.019
35	104	2.834	2.839	0.005
35	105	2.832	2.828	-0.004
50	106	2.705	2.720	0.015
50	107	2.755	2.793	0.038
50	108	2.671	2.717	0.046
50	109	2.721	2.754	0.033
50	110	2.779	2.780	0.001
50	111	2.643	2.635	-0.008
55	113	2.790	2.745	-0.045
55	114	2.758	2.741	-0.017
55	115	2.694	2.683	-0.011
55	116	2.851	2.823	-0.028
55	126	2.864	2.854	-0.010
55	128	2.749	2.673	-0.076
100	129	2.794	2.821	0.027
100	131	2.745	2.703	-0.042
100	132	2.761	2.748	-0.013
100	133	2.843	2.857	0.014
100	134	2.741	2.753	0.012
100	135	2.699	2.660	-0.039
100	138	2.739	2.762	0.023
100	139	2.652	2.670	0.018
100	140	2.832	2.835	0.003
100	141	2.698	2.677	-0.021
100	142	2.821	2.837	0.016
100	143	2.760	2.765	0.005
100	144	2.712	2.716	0.004
100	220	2.772	2.757	-0.015
100	221	2.691	2.690	-0.001
100	224	2.734	2.760	0.026
100	225	2.636	2.646	0.010
100	226	2.829	2.867	0.038
100	228	2.721	2.776	0.055
100	232	2.751	2.783	0.032
100	233	2.780	2.753	-0.027
100	236	2.661	2.664	0.003
100	238	2.702	2.725	0.023
100	243	2.713	2.691	-0.022
105	244	2.767	2.761	-0.006
105	246	2.680	2.740	0.060
105	248	2.645	2.645	0.000
105	374	2.767	2.767	0.000
105	376	2.701	2.688	-0.013
105	Max	2.897	2.902	0.006
105	Average	2.747	2.747	-0.001
105	Min	2.601	2.579	-0.076
105	Std Dev	0.074	0.076	0.021



7.1_I_SS_4V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	3.32
Min Limit	1.98

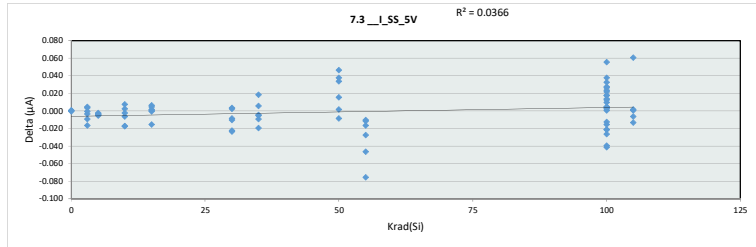
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.702	2.605	2.657	2.595	2.644	2.579	2.649	2.635	2.673	2.646	2.645
Average	2.765	2.713	2.788	2.711	2.791	2.707	2.789	2.733	2.753	2.747	2.720
Max	2.808	2.810	2.893	2.794	2.902	2.813	2.878	2.793	2.854	2.867	2.767
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320



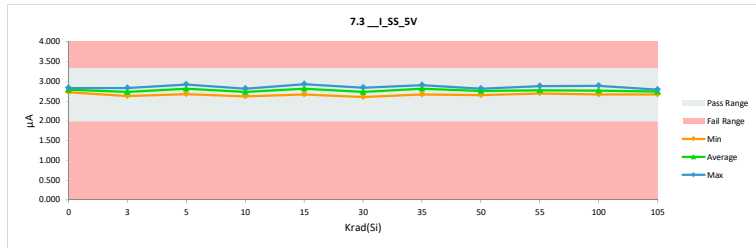
LDR TID Report  
TPS7H5001-SP

7.3_I_SS_5V		
Test Site		
Tester		
Test Number		
Unit	µA	µA
Max Limit	3.32	3.32
Min Limit	1.98	1.98

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.724	2.724	0.000
0	118	2.808	2.808	0.000
0	122	2.831	2.830	-0.001
3	61	2.770	2.753	-0.017
3	62	2.834	2.833	-0.001
3	63	2.719	2.723	0.004
3	64	2.807	2.803	-0.004
3	65	2.682	2.672	-0.010
3	66	2.623	2.626	0.003
5	67	2.715	2.710	-0.005
5	68	2.859	2.855	-0.004
5	69	2.681	2.678	-0.003
5	70	2.920	2.916	-0.004
5	71	2.857	2.852	-0.005
5	72	2.856	2.850	-0.006
10	73	2.770	2.752	-0.018
10	74	2.834	2.816	-0.018
10	75	2.980	2.721	-0.002
10	76	2.807	2.814	0.007
10	77	2.682	2.679	-0.003
10	78	2.623	2.616	-0.007
15	79	2.715	2.721	0.006
15	80	2.859	2.858	-0.001
15	81	2.681	2.665	-0.016
15	82	2.920	2.924	0.004
15	83	2.857	2.858	0.001
15	84	2.856	2.856	0.000
30	86	2.770	2.773	0.003
30	89	2.834	2.836	0.002
30	91	2.719	2.710	-0.009
30	92	2.807	2.783	-0.024
30	94	2.682	2.671	-0.011
30	95	2.623	2.600	-0.023
35	100	2.715	2.710	-0.005
35	101	2.859	2.877	0.018
35	102	2.681	2.671	-0.010
35	103	2.920	2.900	-0.020
35	104	2.857	2.862	0.005
35	105	2.856	2.850	-0.006
50	106	2.728	2.743	0.015
50	107	2.778	2.815	0.037
50	108	2.693	2.739	0.046
50	109	2.743	2.776	0.033
50	110	2.802	2.803	0.001
50	111	2.664	2.655	-0.009
55	113	2.813	2.766	-0.047
55	114	2.779	2.762	-0.017
55	115	2.716	2.704	-0.012
55	116	2.873	2.845	-0.028
55	126	2.887	2.876	-0.011
55	128	2.772	2.696	-0.076
100	129	2.816	2.843	0.027
100	131	2.768	2.726	-0.042
100	132	2.782	2.769	-0.013
100	133	2.866	2.879	0.013
100	134	2.763	2.775	0.012
100	135	2.722	2.682	-0.040
100	138	2.762	2.783	0.021
100	139	2.673	2.690	0.017
100	140	2.854	2.856	0.002
100	141	2.720	2.698	-0.022
100	142	2.844	2.861	0.017
100	143	2.781	2.785	0.004
100	144	2.734	2.737	0.003
100	220	2.794	2.778	-0.016
100	221	2.711	2.711	0.000
100	224	2.756	2.782	0.026
100	225	2.656	2.665	0.009
100	226	2.852	2.889	0.037
100	228	2.742	2.797	0.055
100	232	2.774	2.806	0.032
100	233	2.802	2.775	-0.027
100	236	2.681	2.685	0.004
100	238	2.723	2.746	0.023
100	243	2.734	2.712	-0.022
105	244	2.790	2.783	-0.007
105	246	2.701	2.761	0.060
105	248	2.665	2.665	0.000
105	374	2.788	2.789	0.001
105	376	2.725	2.711	-0.014
105		2.920	2.924	0.004
	Max			
	Average	2.769	2.768	-0.001
	Min	2.623	2.600	-0.076
	Std Dev	0.074	0.076	0.021

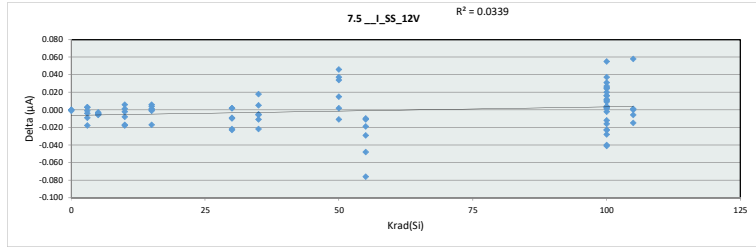


7.3_I_SS_5V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.724	2.626	2.678	2.616	2.665	2.600	2.671	2.655	2.696	2.665	2.665
Average	2.787	2.735	2.810	2.733	2.814	2.729	2.812	2.755	2.775	2.768	2.742
Max	2.830	2.833	2.916	2.816	2.924	2.836	2.900	2.815	2.876	2.889	2.789
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

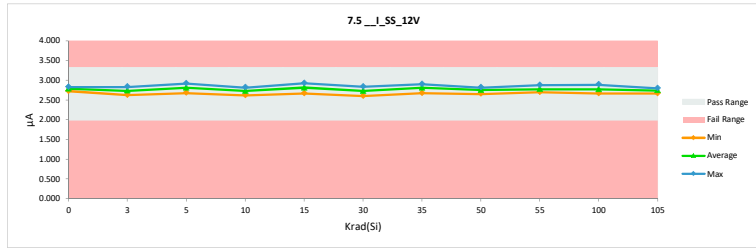


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7.5_I_SS_12V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	3.32	3.32		
Min Limit	1.98	1.98		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.725	2.725	0.000
0	118	2.811	2.810	-0.001
0	122	2.831	2.831	0.000
3	61	2.772	2.754	-0.018
3	62	2.834	2.833	-0.001
3	63	2.720	2.723	0.003
3	64	2.808	2.804	-0.004
3	65	2.683	2.674	-0.009
3	66	2.624	2.627	0.003
5	67	2.716	2.711	-0.005
5	68	2.859	2.855	-0.004
5	69	2.683	2.679	-0.004
5	70	2.921	2.918	-0.003
5	71	2.858	2.853	-0.005
5	72	2.856	2.850	-0.006
10	73	2.772	2.754	-0.018
10	74	2.834	2.817	-0.017
10	75	2.720	2.721	0.001
10	76	2.808	2.814	0.006
10	77	2.683	2.681	-0.002
10	78	2.624	2.616	-0.008
15	79	2.716	2.722	0.006
15	80	2.859	2.858	-0.001
15	81	2.683	2.666	-0.017
15	82	2.921	2.925	0.004
15	83	2.858	2.859	0.001
15	84	2.856	2.856	0.000
30	86	2.772	2.774	0.002
30	89	2.834	2.836	0.002
30	91	2.720	2.711	-0.009
30	92	2.808	2.785	-0.023
30	94	2.683	2.673	-0.010
30	95	2.624	2.602	-0.022
35	100	2.716	2.710	-0.006
35	101	2.859	2.877	0.018
35	102	2.683	2.672	-0.011
35	103	2.921	2.899	-0.022
35	104	2.858	2.863	0.005
35	105	2.856	2.851	-0.005
50	106	2.729	2.744	0.015
50	107	2.780	2.817	0.037
50	108	2.695	2.741	0.046
50	109	2.743	2.777	0.034
50	110	2.802	2.804	0.002
50	111	2.665	2.654	-0.011
55	113	2.815	2.767	-0.048
55	114	2.780	2.761	-0.019
55	115	2.716	2.705	-0.011
55	116	2.874	2.845	-0.029
55	126	2.888	2.878	-0.010
55	128	2.772	2.696	-0.076
100	129	2.818	2.845	0.027
100	131	2.769	2.728	-0.041
100	132	2.782	2.770	-0.012
100	133	2.868	2.880	0.012
100	134	2.764	2.775	0.011
100	135	2.722	2.682	-0.040
100	138	2.763	2.783	0.020
100	139	2.674	2.690	0.016
100	140	2.856	2.857	0.001
100	141	2.721	2.698	-0.023
100	142	2.845	2.861	0.016
100	143	2.781	2.785	0.004
100	144	2.734	2.737	0.003
100	220	2.795	2.779	-0.016
100	221	2.712	2.710	-0.002
100	224	2.757	2.782	0.025
100	225	2.658	2.667	0.009
100	226	2.853	2.890	0.037
100	228	2.743	2.798	0.055
100	232	2.775	2.806	0.031
100	233	2.803	2.775	-0.028
100	236	2.683	2.686	0.003
100	238	2.723	2.747	0.024
100	243	2.735	2.712	-0.023
105	244	2.791	2.785	-0.006
105	246	2.702	2.760	0.058
105	248	2.666	2.666	0.000
105	374	2.790	2.791	0.001
105	376	2.726	2.711	-0.015
	Max	2.921	2.925	0.058
	Average	2.770	2.769	-0.001
	Min	2.624	2.602	-0.076
	Std Dev	0.074	0.076	0.021



7.5_I_SS_12V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.725	2.627	2.679	2.616	2.666	2.602	2.672	2.654	2.696	2.667	2.666
Average	2.789	2.736	2.811	2.734	2.814	2.730	2.812	2.756	2.775	2.768	2.743
Max	2.831	2.833	2.918	2.817	2.925	2.836	2.899	2.817	2.878	2.890	2.791
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

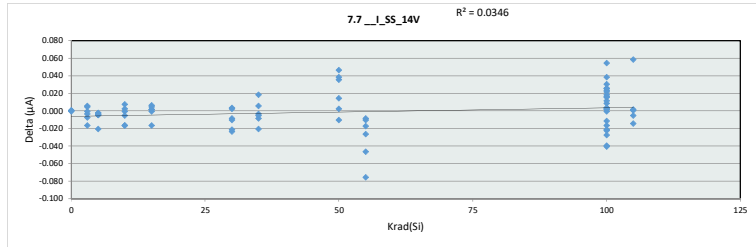




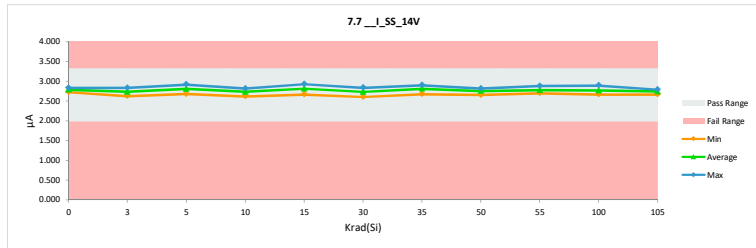
LDR TID Report  
TPS7H5001-SP

7.7_I_SS_14V		
Test Site		
Tester		
Test Number		
Unit	µA	µA
Max Limit	3.32	3.32
Min Limit	1.98	1.98

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.725	2.725	0.000
0	118	2.810	2.810	0.000
0	122	2.831	2.830	-0.001
3	61	2.771	2.754	-0.017
3	62	2.834	2.833	-0.001
3	63	2.719	2.724	0.005
3	64	2.808	2.804	-0.004
3	65	2.683	2.675	-0.008
3	66	2.623	2.627	0.004
5	67	2.716	2.711	-0.005
5	68	2.860	2.855	-0.005
5	69	2.682	2.679	-0.003
5	70	2.921	2.918	-0.003
5	71	2.858	2.853	-0.005
5	72	2.856	2.835	-0.021
10	73	2.771	2.754	-0.017
10	74	2.834	2.817	-0.017
10	75	2.719	2.721	0.002
10	76	2.808	2.815	0.007
10	77	2.683	2.682	-0.001
10	78	2.623	2.617	-0.006
15	79	2.716	2.722	0.006
15	80	2.860	2.859	-0.001
15	81	2.682	2.665	-0.017
15	82	2.921	2.925	0.004
15	83	2.858	2.859	0.001
15	84	2.856	2.857	0.001
30	86	2.771	2.774	0.003
30	89	2.834	2.836	0.002
30	91	2.719	2.710	-0.009
30	92	2.808	2.784	-0.024
30	94	2.683	2.672	-0.011
30	95	2.623	2.601	-0.022
35	100	2.716	2.710	-0.006
35	101	2.860	2.878	0.018
35	102	2.682	2.673	-0.009
35	103	2.921	2.900	-0.021
35	104	2.858	2.863	0.005
35	105	2.856	2.852	-0.004
50	106	2.729	2.743	0.014
50	107	2.779	2.817	0.038
50	108	2.695	2.741	0.046
50	109	2.743	2.778	0.035
50	110	2.803	2.805	0.002
50	111	2.665	2.654	-0.011
55	113	2.815	2.768	-0.047
55	114	2.781	2.763	-0.018
55	115	2.716	2.705	-0.011
55	116	2.874	2.847	-0.027
55	126	2.888	2.879	-0.009
55	128	2.772	2.696	-0.076
100	129	2.819	2.844	0.025
100	131	2.769	2.728	-0.041
100	132	2.782	2.770	-0.012
100	133	2.867	2.882	0.015
100	134	2.765	2.776	0.011
100	135	2.722	2.682	-0.040
100	138	2.762	2.783	0.021
100	139	2.674	2.690	0.016
100	140	2.856	2.857	0.001
100	141	2.721	2.699	-0.022
100	142	2.844	2.862	0.018
100	143	2.781	2.784	0.003
100	144	2.735	2.737	0.002
100	220	2.795	2.778	-0.017
100	221	2.712	2.711	-0.001
100	224	2.757	2.782	0.025
100	225	2.658	2.666	0.008
100	226	2.853	2.891	0.038
100	228	2.744	2.798	0.054
100	232	2.776	2.806	0.030
100	233	2.804	2.776	-0.028
100	236	2.682	2.685	0.003
100	238	2.723	2.746	0.023
100	243	2.735	2.712	-0.023
105	244	2.791	2.785	-0.006
105	246	2.702	2.760	0.058
105	248	2.666	2.667	0.001
105	374	2.790	2.790	0.000
105	376	2.726	2.711	-0.015
105	375	2.921	2.925	0.004
105	Average	2.770	2.769	-0.001
105	Min	2.623	2.601	-0.022
105	Std Dev	0.074	0.076	0.021



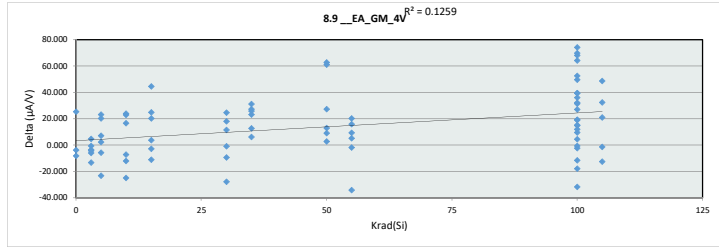
7.7_I_SS_14V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.725	2.627	2.679	2.617	2.665	2.601	2.673	2.654	2.696	2.666	2.667
Average	2.788	2.736	2.809	2.734	2.815	2.730	2.813	2.756	2.776	2.769	2.743
Max	2.830	2.833	2.918	2.817	2.925	2.836	2.900	2.817	2.879	2.891	2.790
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320



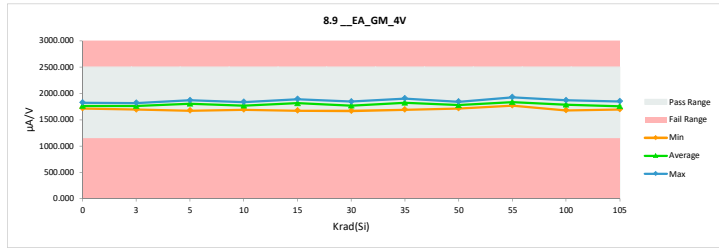
LDR TID Report  
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8.9_EA_GM_4V	
Test Site	
Tester	
Test Number	
Unit	µA/V
Max Limit	2500
Min Limit	1150

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1720.652	1712.302	-8.350
0	118	1747.872	1743.857	-4.015
0	122	1795.843	1821.097	25.254
3	61	1831.434	1818.008	-13.426
3	62	1812.910	1817.506	4.596
3	63	1728.389	1722.321	-6.068
3	64	1810.738	1806.294	-4.444
3	65	1695.071	1691.414	-3.657
3	66	1706.354	1705.678	-0.676
5	67	1781.172	1801.319	20.147
5	68	1787.111	1794.019	6.908
5	69	1666.754	1668.835	2.081
5	70	1872.927	1866.998	-5.929
5	71	1864.032	1840.727	-23.305
5	72	1820.267	1843.205	22.938
10	73	1831.434	1819.142	-12.292
10	74	1812.910	1787.958	-24.952
10	75	1728.389	1752.217	23.828
10	76	1810.738	1833.555	22.817
10	77	1695.071	1687.772	-7.299
10	78	1706.354	1722.772	16.418
15	79	1781.172	1801.329	20.157
15	80	1787.111	1775.978	-11.133
15	81	1666.754	1670.422	3.668
15	82	1872.927	1869.953	-2.974
15	83	1864.032	1888.805	24.773
15	84	1820.267	1864.570	44.303
30	86	1831.434	1842.891	11.457
30	89	1812.910	1811.922	-0.988
30	91	1728.389	1746.399	18.010
30	92	1810.738	1801.329	-9.409
30	94	1695.071	1667.077	-27.994
30	95	1706.354	1730.743	24.389
35	100	1781.172	1806.858	25.686
35	101	1787.111	1799.667	12.556
35	102	1666.754	1689.719	22.965
35	103	1872.927	1900.111	27.184
35	104	1864.032	1869.942	5.910
35	105	1820.267	1851.340	31.073
50	106	1707.344	1734.500	27.156
50	107	1768.294	1781.172	12.878
50	108	1751.906	1812.910	61.004
50	109	1778.020	1840.737	62.717
50	110	1759.900	1768.872	8.972
50	111	1710.809	1713.508	2.699
55	113	1770.860	1768.872	-1.988
55	114	1848.240	1864.021	15.781
55	115	1823.630	1832.714	9.084
55	116	1790.381	1810.553	20.172
55	126	1919.788	1924.942	5.154
55	128	1852.301	1818.008	-34.293
100	129	1774.203	1823.630	49.427
100	131	1748.392	1779.566	31.174
100	132	1746.390	1728.398	-17.992
100	133	1814.273	1833.085	18.812
100	134	1784.934	1852.833	67.899
100	135	1842.456	1810.553	-31.903
100	138	1763.572	1802.829	39.257
100	139	1722.321	1758.305	35.984
100	140	1768.872	1778.378	9.506
100	141	1795.286	1792.811	-2.475
100	142	1823.630	1822.927	-0.703
100	143	1792.562	1866.562	74.000
100	144	1770.197	1784.953	14.756
100	220	1825.525	1844.745	19.220
100	221	1702.415	1729.209	26.794
100	224	1704.595	1719.532	14.937
100	225	1774.193	1762.609	-11.584
100	226	1730.600	1782.989	52.389
100	228	1765.344	1834.999	69.655
100	232	1699.466	1763.591	64.125
100	233	1763.572	1802.974	39.402
100	236	1661.481	1673.388	11.907
100	238	1774.794	1806.848	32.054
100	243	1702.406	1706.684	4.278
105	244	1731.422	1763.572	32.150
105	246	1706.354	1754.976	48.622
105	248	1695.645	1716.462	20.817
105	374	1844.988	1843.599	-1.389
105	376	1705.218	1692.625	-12.593
105	Max	1919.788	1924.942	74.000
105	Average	1772.655	1786.881	14.226
105	Min	1661.481	1667.077	-34.293
105	Std Dev	59.035	60.061	24.002



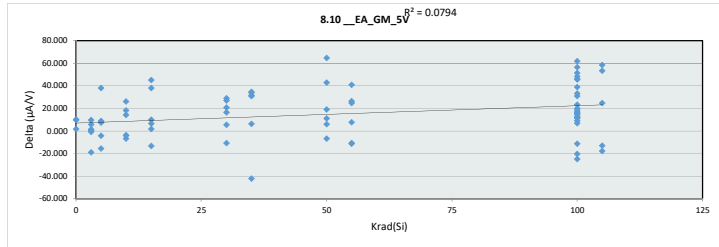
8.9_EA_GM_4V											
Test Site											
Tester											
Test Number											
Max Limit	2500 µA/V										
Min Limit	1150 µA/V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1712.302	1691.414	1668.835	1687.772	1670.422	1667.077	1689.719	1713.508	1768.872	1673.388	1692.625
Average	1759.085	1760.204	1802.517	1767.236	1811.843	1766.727	1819.606	1775.283	1836.518	1785.933	1754.247
Max	1821.097	1818.008	1866.998	1833.555	1888.805	1842.891	1900.111	1840.737	1924.942	1866.562	1843.599
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



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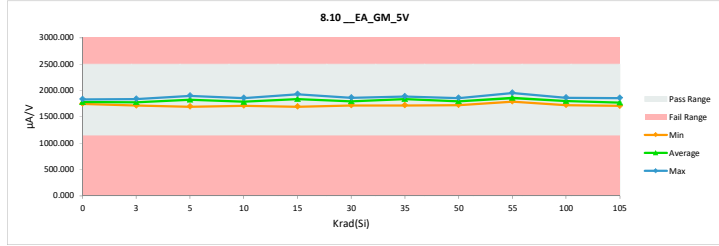
8.10_EA_GM_5V	
Test Site	
Tester	
Test Number	
Unit	µA/V
Max Limit	2500
Min Limit	1150

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1733.380	1742.698	9.318
0	118	1755.058	1765.019	9.961
0	122	1828.041	1829.297	1.256
3	61	1834.999	1815.765	-19.234
3	62	1820.514	1819.270	-1.244
3	63	1747.882	1753.078	5.196
3	64	1825.903	1835.311	9.408
3	65	1712.310	1712.258	-0.052
3	66	1726.744	1727.713	0.969
5	67	1795.065	1790.372	-4.693
5	68	1799.947	1807.773	7.826
5	69	1682.956	1691.490	8.534
5	70	1916.181	1900.111	-16.070
5	71	1881.898	1889.039	7.141
5	72	1845.425	1882.912	37.487
10	73	1834.999	1852.833	17.834
10	74	1820.514	1813.224	-7.290
10	75	1747.882	1761.600	13.718
10	76	1825.903	1851.729	25.826
10	77	1712.310	1708.039	-4.271
10	78	1726.744	1722.321	-4.423
15	79	1795.065	1801.329	6.264
15	80	1799.947	1801.329	1.382
15	81	1682.956	1692.625	9.669
15	82	1916.181	1902.556	-13.625
15	83	1881.898	1926.522	44.624
15	84	1845.425	1883.102	37.677
30	86	1834.999	1861.508	26.509
30	89	1820.514	1849.075	28.561
30	91	1747.882	1768.284	20.402
30	92	1825.903	1814.803	-11.100
30	94	1712.310	1717.297	4.987
30	95	1726.744	1742.707	15.963
35	100	1795.065	1825.913	30.848
35	101	1799.947	1834.287	34.340
35	102	1682.956	1716.304	33.348
35	103	1916.181	1873.513	-42.668
35	104	1881.898	1887.923	6.025
35	105	1845.425	1875.901	30.476
50	106	1729.984	1748.747	18.763
50	107	1790.630	1801.329	10.699
50	108	1767.632	1831.834	64.202
50	109	1813.537	1855.876	42.339
50	110	1786.749	1792.351	5.602
50	111	1728.897	1721.755	-7.142
55	113	1761.813	1787.939	26.126
55	114	1863.700	1887.934	24.234
55	115	1829.297	1869.755	40.458
55	116	1805.186	1812.411	7.225
55	126	1961.582	1950.442	-11.140
55	128	1852.311	1840.737	-11.574
100	129	1806.848	1852.311	45.463
100	131	1762.701	1777.935	15.234
100	132	1781.087	1755.692	-25.395
100	133	1834.999	1846.501	11.512
100	134	1801.329	1852.291	50.962
100	135	1828.061	1807.228	-20.833
100	138	1788.902	1777.106	-11.796
100	139	1737.563	1782.781	45.218
100	140	1806.848	1822.610	15.762
100	141	1807.968	1818.935	10.967
100	142	1804.691	1823.819	19.128
100	143	1834.999	1852.311	17.312
100	144	1791.041	1799.677	8.636
100	220	1846.867	1861.085	14.218
100	221	1704.196	1760.269	56.073
100	224	1726.104	1732.450	6.346
100	225	1779.557	1812.411	32.854
100	226	1760.787	1799.185	38.398
100	228	1773.612	1834.999	61.387
100	232	1733.022	1755.986	22.964
100	233	1808.708	1828.051	19.343
100	236	1674.557	1722.321	47.764
100	238	1777.775	1808.240	30.465
100	243	1716.190	1728.300	12.110
105	244	1722.321	1780.454	58.133
105	246	1717.305	1770.206	52.901
105	248	1709.998	1734.357	24.359
105	374	1869.671	1856.195	-13.476
105	376	1724.339	1706.094	-18.245
105	Max	1961.582	1950.442	64.202
105	Average	1790.116	1805.097	14.980
105	Min	1674.557	1691.490	-42.668
105	Std Dev	61.263	59.207	22.015



8.10_EA_GM_5V	
Test Site	
Tester	
Test Number	
Max Limit	2500 µA/V
Min Limit	1150 µA/V

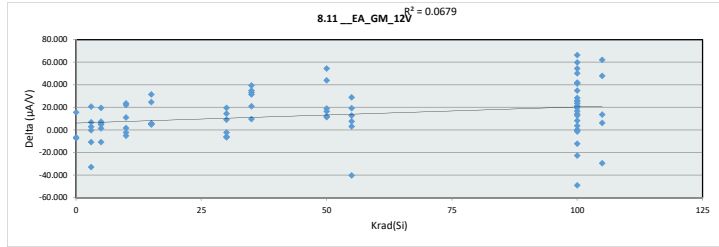
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1742.698	1712.258	1691.490	1708.039	1692.625	1717.297	1716.304	1721.755	1787.939	1722.321	1706.094
Average	1779.005	1777.233	1826.950	1784.958	1834.577	1792.279	1835.640	1791.982	1858.203	1800.521	1769.461
Max	1829.297	1835.311	1900.111	1852.833	1926.522	1861.508	1887.923	1855.876	1950.442	1861.085	1856.195
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



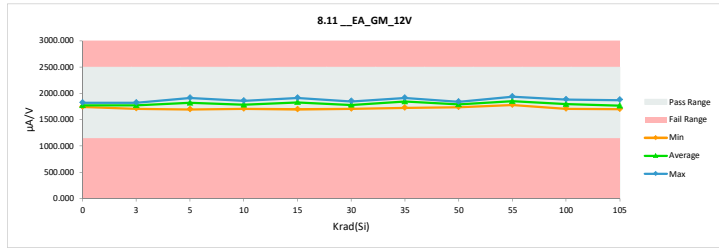
LDR TID Report  
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8.11 EA GM 12V	
Test Site	
Tester	
Test Number	
Unit	µA/V
Max Limit	2500
Min Limit	1150

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1749.951	1743.232	-6.719
0	118	1748.155	1763.572	15.417
0	122	1828.320	1821.008	-7.312
3	61	1856.482	1823.620	-32.862
3	62	1816.601	1823.244	6.643
3	63	1737.563	1758.305	20.742
3	64	1823.125	1812.196	-10.929
3	65	1707.344	1710.242	2.898
3	66	1728.389	1728.007	-0.382
5	67	1801.329	1790.372	-10.957
5	68	1807.101	1808.513	1.412
5	69	1692.625	1697.506	4.881
5	70	1906.881	1914.213	7.332
5	71	1862.552	1881.898	19.346
5	72	1842.931	1848.434	5.503
10	73	1856.482	1858.158	1.676
10	74	1816.601	1827.593	10.992
10	75	1737.563	1759.632	22.069
10	76	1823.125	1846.501	23.376
10	77	1707.344	1705.106	-2.238
10	78	1728.389	1723.330	-5.059
15	79	1801.329	1806.848	5.519
15	80	1807.101	1812.411	5.310
15	81	1692.625	1697.497	4.872
15	82	1906.881	1912.446	5.565
15	83	1862.552	1894.008	31.456
15	84	1842.931	1867.632	24.701
30	86	1856.482	1850.370	-6.112
30	89	1816.601	1830.924	14.323
30	91	1737.563	1746.462	8.899
30	92	1823.125	1816.483	-6.642
30	94	1707.344	1704.872	-2.472
30	95	1728.389	1747.872	19.483
35	100	1801.329	1822.214	20.885
35	101	1807.101	1846.501	39.400
35	102	1692.625	1727.366	34.741
35	103	1906.881	1916.411	9.530
35	104	1862.552	1893.997	31.445
35	105	1842.931	1875.901	32.970
50	106	1740.411	1752.601	12.190
50	107	1790.391	1806.858	16.467
50	108	1767.753	1822.214	54.461
50	109	1795.834	1839.587	43.753
50	110	1789.598	1800.836	11.238
50	111	1717.297	1736.089	18.792
55	113	1774.193	1781.863	7.670
55	114	1872.927	1875.912	2.985
55	115	1837.864	1866.707	28.843
55	116	1803.158	1822.234	19.076
55	126	1924.942	1937.614	12.672
55	128	1864.021	1823.630	-40.391
100	129	1806.848	1834.989	28.141
100	131	1764.750	1784.174	19.424
100	132	1794.739	1745.572	-49.167
100	133	1829.297	1850.370	21.073
100	134	1823.006	1846.501	23.495
100	135	1832.144	1809.450	-22.694
100	138	1801.329	1801.329	0.000
100	139	1729.637	1795.834	66.197
100	140	1801.319	1818.008	16.689
100	141	1808.084	1816.129	8.045
100	142	1813.998	1828.041	14.043
100	143	1825.515	1885.239	59.724
100	144	1806.858	1806.848	-0.010
100	220	1858.158	1861.798	3.640
100	221	1705.236	1740.131	34.895
100	224	1722.321	1720.811	-1.510
100	225	1767.548	1780.161	12.613
100	226	1755.471	1797.209	41.738
100	228	1780.756	1834.989	54.233
100	232	1727.873	1768.872	40.999
100	233	1800.642	1826.459	25.817
100	236	1681.876	1707.344	25.468
100	238	1784.943	1834.989	50.046
100	243	1729.556	1717.297	-12.259
105	244	1737.051	1784.953	47.902
105	246	1706.884	1768.872	61.988
105	248	1708.700	1714.804	6.104
105	374	1860.910	1874.560	13.650
105	376	1732.450	1703.029	-29.421
105	Max	1924.942	1937.614	66.197
105	Average	1790.644	1804.248	13.604
105	Min	1681.876	1697.497	-49.167
105	Std Dev	58.886	59.591	21.992



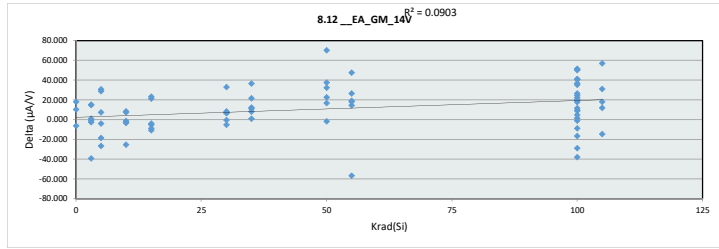
8.11 EA GM 12											
Test Site											
Tester											
Test Number											
Max Limit	2500 µA/V										
Min Limit	1150 µA/V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1743.232	1710.242	1697.506	1705.106	1697.497	1704.872	1727.366	1736.089	1781.863	1707.344	1703.029
Average	1775.937	1775.936	1823.489	1786.720	1831.807	1782.831	1847.065	1793.031	1851.327	1800.523	1769.244
Max	1821.008	1823.620	1914.213	1858.158	1912.446	1850.370	1916.411	1839.587	1937.614	1885.239	1874.560
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



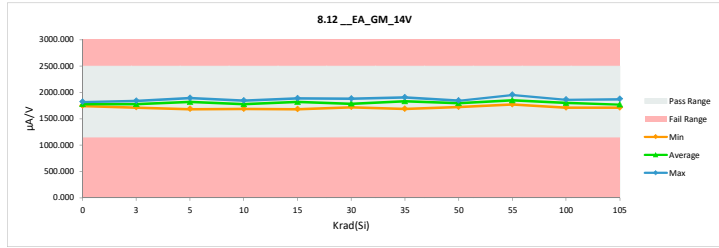
LDR TID Report  
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8.12 EA GM 14V	
Test Site	
Tester	
Test Number	
Unit	µA/V
Max Limit	2500
Min Limit	1150

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1727.366	1745.045	17.679
0	118	1758.959	1768.872	9.913
0	122	1824.711	1818.008	-6.703
3	61	1846.501	1806.848	-39.653
3	62	1829.297	1827.872	-1.425
3	63	1742.698	1757.549	14.851
3	64	1824.463	1838.821	14.358
3	65	1712.302	1712.870	0.568
3	66	1720.493	1717.297	-3.196
5	67	1812.411	1793.472	-18.939
5	68	1805.613	1812.411	6.798
5	69	1687.280	1682.956	-4.324
5	70	1897.049	1869.953	-27.096
5	71	1864.032	1892.342	28.310
5	72	1837.693	1867.964	30.271
10	73	1846.501	1842.648	-3.853
10	74	1829.297	1827.414	-1.883
10	75	1742.698	1750.590	7.892
10	76	1824.463	1831.424	6.961
10	77	1712.302	1686.576	-25.726
10	78	1720.493	1717.297	-3.196
15	79	1812.411	1801.319	-11.092
15	80	1805.613	1801.329	-4.284
15	81	1687.280	1681.522	-5.758
15	82	1897.049	1887.923	-9.126
15	83	1864.032	1886.723	22.691
15	84	1837.693	1858.426	20.733
30	86	1846.501	1878.895	32.394
30	89	1829.297	1828.579	-0.718
30	91	1742.698	1749.030	6.332
30	92	1824.463	1818.935	-5.528
30	94	1712.302	1718.669	6.367
30	95	1720.493	1728.389	7.896
35	100	1812.411	1822.966	10.555
35	101	1805.613	1826.857	21.244
35	102	1687.280	1687.781	0.501
35	103	1897.049	1904.586	7.537
35	104	1864.032	1875.912	11.880
35	105	1837.693	1873.921	36.228
50	106	1724.676	1740.989	16.313
50	107	1784.953	1822.026	37.073
50	108	1759.642	1829.297	69.655
50	109	1812.411	1844.218	31.807
50	110	1786.578	1808.708	22.130
50	111	1725.350	1723.321	-2.029
55	113	1758.305	1772.422	14.117
55	114	1868.464	1886.055	17.591
55	115	1816.139	1863.390	47.251
55	116	1810.563	1829.297	18.734
55	126	1924.257	1950.442	26.185
55	128	1864.032	1806.858	-57.174
100	129	1822.917	1834.047	11.130
100	131	1759.051	1778.020	18.969
100	132	1783.956	1745.527	-38.429
100	133	1836.717	1860.672	23.955
100	134	1827.175	1847.060	19.885
100	135	1823.620	1806.760	-16.860
100	138	1801.329	1802.325	0.996
100	139	1747.872	1788.559	40.687
100	140	1817.998	1816.611	-1.387
100	141	1783.871	1818.806	34.935
100	142	1801.319	1852.301	50.982
100	143	1818.008	1844.187	26.179
100	144	1819.615	1790.381	-29.234
100	220	1852.311	1861.085	8.774
100	221	1707.692	1747.872	40.180
100	224	1727.375	1731.824	4.449
100	225	1761.461	1778.887	17.426
100	226	1751.760	1801.329	49.569
100	228	1790.381	1840.727	50.346
100	232	1763.572	1785.551	21.979
100	233	1806.858	1815.441	8.583
100	236	1675.769	1712.310	36.541
100	238	1777.775	1817.998	40.223
100	243	1732.065	1722.825	-9.240
105	244	1739.284	1795.834	56.550
105	246	1717.297	1747.872	30.575
105	248	1706.441	1723.994	17.553
105	374	1859.002	1870.547	11.545
105	376	1727.375	1712.302	-15.073
105	Max	1924.257	1950.442	69.655
105	Average	1790.322	1801.608	11.286
105	Min	1675.769	1681.522	-57.174
105	Std Dev	57.982	59.970	22.818



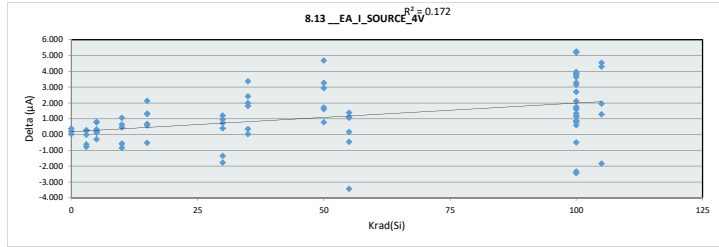
8.12 EA GM 14											
Test Site											
Tester											
Test Number											
Max Limit	2500 µA/V										
Min Limit	1150 µA/V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1745.045	1712.870	1682.956	1686.576	1681.522	1718.669	1687.781	1723.321	1772.422	1712.310	1712.302
Average	1777.308	1776.876	1819.850	1775.992	1819.540	1787.083	1832.004	1794.760	1851.411	1800.046	1770.110
Max	1818.008	1838.821	1892.342	1842.648	1887.923	1878.895	1904.586	1844.218	1950.442	1861.085	1870.547
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



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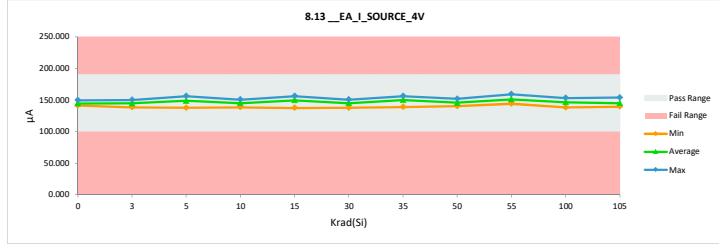
8.13 EA_I_SOURCE_4V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	140.651	140.845	0.194
0	118	143.043	143.056	0.013
0	122	148.956	149.325	0.369
3	61	149.422	148.622	-0.800
3	62	149.498	149.759	0.261
3	63	141.504	141.480	-0.024
3	64	149.209	149.443	0.234
3	65	138.718	138.083	-0.635
3	66	139.106	139.356	0.250
5	67	146.661	146.850	0.189
5	68	146.312	146.630	0.318
5	69	136.469	137.241	0.772
5	70	155.753	155.849	0.096
5	71	154.389	154.090	-0.299
5	72	149.521	150.307	0.786
10	73	149.422	148.824	-0.598
10	74	149.498	148.643	-0.855
10	75	141.504	142.134	0.630
10	76	149.209	150.253	1.044
10	77	138.718	138.119	-0.599
10	78	139.106	139.541	0.435
15	79	146.661	147.944	1.283
15	80	146.312	146.979	0.667
15	81	136.469	137.036	0.567
15	82	155.753	155.232	-0.521
15	83	154.389	155.704	1.315
15	84	149.521	151.637	2.116
30	86	149.422	150.369	0.947
30	89	149.498	150.229	0.731
30	91	141.504	141.899	0.396
30	92	149.209	147.419	-1.790
30	94	138.718	137.367	-1.351
30	95	139.106	140.292	1.186
35	100	146.661	149.075	2.414
35	101	146.312	149.672	3.360
35	102	136.469	138.460	1.991
35	103	155.753	155.768	0.015
35	104	154.389	154.730	0.341
35	105	149.521	151.318	1.797
50	106	140.265	141.965	1.700
50	107	144.182	147.111	2.929
50	108	143.729	146.991	3.262
50	109	146.800	151.469	4.669
50	110	143.722	145.314	1.592
50	111	139.166	139.936	0.770
55	113	143.822	143.988	0.166
55	114	153.088	154.152	1.064
55	115	150.260	151.642	1.382
55	116	146.426	147.488	1.062
55	126	159.480	159.006	-0.474
55	128	151.725	148.288	-3.437
100	129	145.716	148.860	3.144
100	131	142.808	143.585	0.777
100	132	143.352	140.904	-2.448
100	133	148.291	150.031	1.740
100	134	148.271	150.364	2.093
100	135	149.864	147.515	-2.349
100	138	144.962	146.296	1.334
100	139	141.841	145.607	3.766
100	140	146.809	147.587	0.778
100	141	145.189	146.352	1.163
100	142	147.741	148.609	0.868
100	143	147.641	152.881	5.240
100	144	146.013	147.617	1.604
100	220	150.885	152.024	1.139
100	221	138.278	142.215	3.937
100	224	139.091	139.675	0.584
100	225	143.554	145.220	1.666
100	226	142.215	146.118	3.903
100	228	144.558	149.731	5.173
100	232	140.989	144.614	3.625
100	233	146.943	150.226	3.283
100	236	135.132	137.831	2.699
100	238	144.767	148.572	3.805
100	243	139.397	138.889	-0.508
105	244	141.628	145.908	4.280
105	246	139.171	143.699	4.528
105	248	138.750	140.694	1.944
105	374	152.350	153.619	1.269
105	376	140.930	139.080	-1.850
105	Max	159.480	159.006	5.240
105	Average	145.527	146.641	1.114
105	Min	135.132	137.036	-3.437
105	Std Dev	5.370	5.293	1.761



8.13 EA_I_SOURCE	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

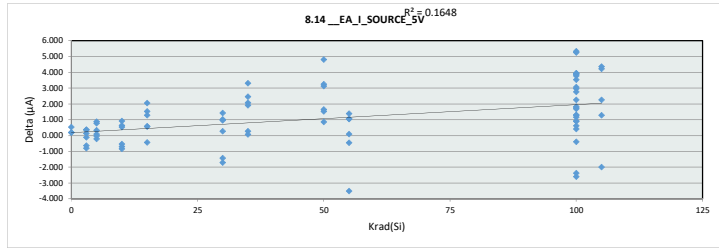
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	140.845	138.083	137.241	138.119	137.036	137.367	138.460	139.936	143.988	137.831	139.080
Average	144.409	144.457	148.495	144.586	149.089	144.594	149.837	145.464	150.761	146.305	144.600
Max	149.325	149.759	155.849	150.253	155.704	150.369	155.768	151.469	159.006	152.881	153.619
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



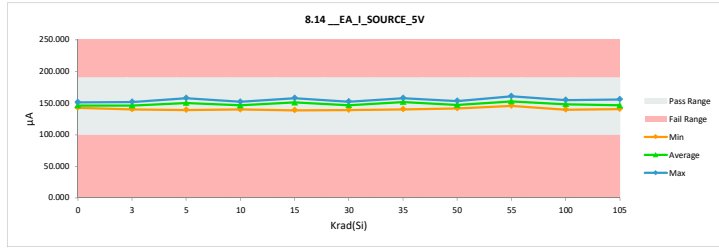
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8.14 EA_I_SOURCE_5V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	142.229	142.397	0.168
0	118	144.538	144.707	0.169
0	122	150.453	150.961	0.508
3	61	151.142	150.315	-0.827
3	62	151.076	151.420	0.344
3	63	143.219	143.068	-0.151
3	64	150.915	151.005	0.090
3	65	140.364	139.698	-0.666
3	66	140.653	140.971	0.318
5	67	148.308	148.240	-0.068
5	68	148.000	148.294	0.294
5	69	138.014	138.755	0.741
5	70	157.440	157.497	0.057
5	71	156.018	155.779	-0.239
5	72	151.268	152.119	0.851
10	73	151.142	150.572	-0.570
10	74	151.076	150.197	-0.879
10	75	143.219	143.815	0.596
10	76	150.915	151.796	0.881
10	77	140.364	139.632	-0.732
10	78	140.653	141.161	0.508
15	79	148.308	149.568	1.260
15	80	148.000	148.542	0.542
15	81	138.014	138.554	0.540
15	82	157.440	156.968	-0.472
15	83	156.018	157.506	1.488
15	84	151.268	153.289	2.021
30	86	151.142	152.131	0.989
30	89	151.076	152.012	0.936
30	91	143.219	143.465	0.246
30	92	150.915	149.172	-1.743
30	94	140.364	138.910	-1.454
30	95	140.653	142.042	1.389
35	100	148.308	150.736	2.428
35	101	148.000	151.284	3.284
35	102	138.014	140.048	2.034
35	103	157.440	157.470	0.030
35	104	156.018	156.251	0.233
35	105	151.268	153.157	1.889
50	106	141.837	143.460	1.623
50	107	145.852	148.955	3.103
50	108	145.361	148.584	3.223
50	109	148.365	153.140	4.775
50	110	145.448	146.940	1.492
50	111	140.712	141.543	0.831
55	113	145.549	145.600	0.051
55	114	154.708	155.747	1.039
55	115	152.045	153.389	1.344
55	116	147.833	148.834	1.001
55	126	161.156	160.659	-0.497
55	128	153.469	149.932	-3.537
100	129	147.386	150.323	2.937
100	131	144.541	145.135	0.594
100	132	145.153	142.517	-2.636
100	133	150.196	151.908	1.712
100	134	149.930	152.154	2.224
100	135	151.559	149.157	-2.402
100	138	146.704	147.956	1.252
100	139	143.249	147.158	3.909
100	140	148.448	149.313	0.865
100	141	146.932	148.073	1.141
100	142	149.375	150.268	0.893
100	143	149.355	154.566	5.211
100	144	147.727	149.375	1.648
100	220	152.619	153.882	1.263
100	221	139.870	143.701	3.831
100	224	140.788	141.168	0.380
100	225	145.108	146.901	1.793
100	226	143.843	147.746	3.903
100	228	146.180	151.488	5.308
100	232	142.707	146.207	3.500
100	233	148.801	151.828	3.027
100	236	136.601	139.338	2.737
100	238	146.480	150.216	3.736
100	243	140.947	140.511	-0.436
105	244	143.138	147.322	4.184
105	246	140.839	145.158	4.319
105	248	140.188	142.411	2.223
105	374	154.197	155.451	1.254
105	375	142.514	140.489	-2.025
105	376	161.156	160.659	-0.497
Max		161.156	160.659	-0.497
Average		147.177	148.275	1.098
Min		136.601	138.554	-3.537
Std Dev		5.410	5.344	1.775

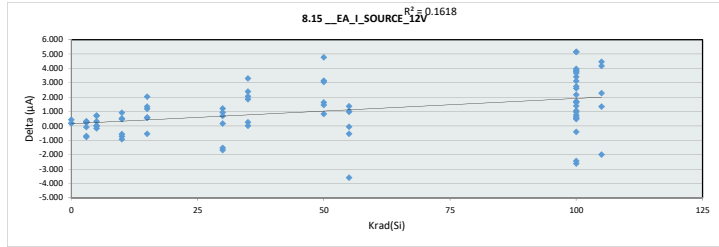


8.14 EA_I_SOUR											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.397	139.698	138.755	139.632	138.554	138.910	140.048	141.543	145.600	139.338	140.489
Average	146.022	146.080	150.114	146.196	150.738	146.289	151.491	147.104	152.360	147.954	146.166
Max	150.961	151.420	157.497	151.796	157.506	152.131	157.470	153.140	160.659	154.566	155.451
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

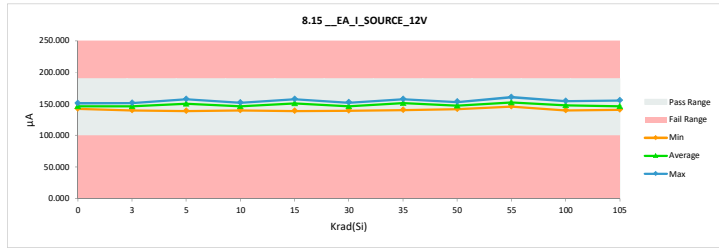


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8.15 EA_I_SOURCE_12V				
Test Site				
Tester				
Test Number				
Unit	µA	µA		
Max Limit	190	190		
Min Limit	100	100		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	142.269	142.456	0.187
0	118	144.699	144.876	0.177
0	122	150.635	151.057	0.422
3	61	151.213	150.441	-0.772
3	62	151.231	151.520	0.289
3	63	143.285	143.184	-0.101
3	64	150.936	151.164	0.228
3	65	140.503	139.794	-0.709
3	66	140.814	141.128	0.314
5	67	148.382	148.354	-0.028
5	68	148.033	148.349	0.316
5	69	138.129	138.820	0.691
5	70	157.610	157.620	0.010
5	71	156.078	155.899	-0.179
5	72	151.345	152.049	0.704
10	73	151.213	150.623	-0.590
10	74	151.231	150.293	-0.938
10	75	143.285	143.804	0.519
10	76	150.936	151.853	0.917
10	77	140.503	139.769	-0.734
10	78	140.814	141.277	0.463
15	79	148.382	149.566	1.184
15	80	148.033	148.610	0.577
15	81	138.129	138.688	0.559
15	82	157.610	157.051	-0.559
15	83	156.078	157.420	1.342
15	84	151.345	153.373	2.028
30	86	151.213	152.138	0.925
30	89	151.231	151.924	0.693
30	91	143.285	143.446	0.161
30	92	150.936	149.232	-1.704
30	94	140.503	138.971	-1.532
30	95	140.814	142.014	1.200
35	100	148.382	150.756	2.374
35	101	148.033	151.315	3.282
35	102	138.129	140.183	2.054
35	103	157.610	157.608	-0.002
35	104	156.078	156.331	0.253
35	105	151.345	153.184	1.839
50	106	141.868	143.491	1.623
50	107	146.073	149.200	3.127
50	108	145.504	148.542	3.038
50	109	148.391	153.147	4.756
50	110	145.612	147.058	1.446
50	111	140.913	141.733	0.820
55	113	145.737	145.678	-0.059
55	114	154.751	155.760	1.009
55	115	151.965	153.330	1.365
55	116	147.970	148.944	0.974
55	126	161.258	160.711	-0.547
55	128	153.559	149.951	-3.608
100	129	147.567	150.309	2.742
100	131	144.639	145.175	0.536
100	132	145.160	142.530	-2.630
100	133	150.303	151.988	1.685
100	134	150.026	152.187	2.161
100	135	151.672	149.218	-2.454
100	138	146.691	148.066	1.375
100	139	143.342	147.133	3.791
100	140	148.742	149.372	0.630
100	141	147.227	148.266	1.039
100	142	149.455	150.217	0.762
100	143	149.482	154.630	5.148
100	144	147.838	149.471	1.633
100	220	152.696	153.645	0.949
100	221	139.929	143.794	3.865
100	224	140.853	141.312	0.459
100	225	145.269	146.940	1.671
100	226	143.990	147.940	3.950
100	228	146.301	151.448	5.147
100	232	142.825	146.224	3.399
100	233	148.804	151.912	3.108
100	236	136.872	139.477	2.605
100	238	146.531	150.196	3.665
100	243	141.116	140.687	-0.429
105	244	143.336	147.495	4.159
105	246	140.912	145.367	4.455
105	248	140.304	142.573	2.269
105	374	154.309	155.640	1.331
105	376	142.728	140.720	-2.008
105	Max	161.258	160.711	-0.547
105	Average	147.285	148.345	1.060
105	Min	136.872	138.688	-3.608
105	Std Dev	5.395	5.319	1.768



8.15 EA_I_SOUR											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.456	139.794	138.820	139.769	138.688	138.971	140.183	141.733	145.678	139.477	140.720
Average	146.130	146.205	150.182	146.270	150.785	146.288	151.563	147.195	152.396	148.006	146.359
Max	151.057	151.520	157.620	151.853	157.420	152.138	157.608	153.147	160.711	154.630	155.640
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

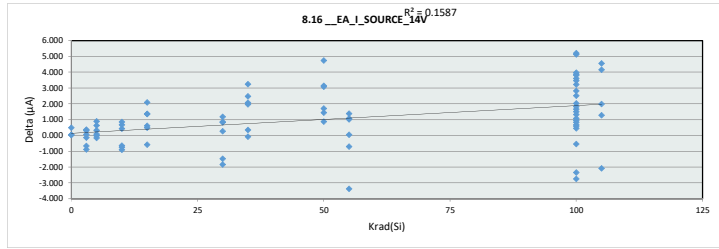




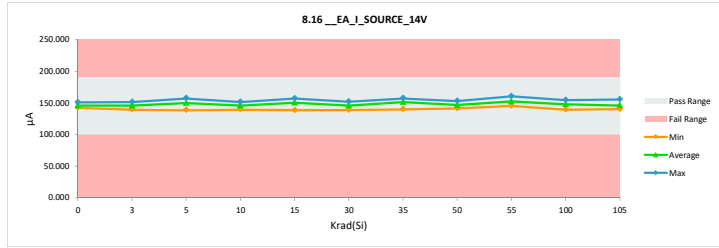
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8.16 EA_I_SOURCE_14V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	142.401	142.420	0.019
0	118	144.644	144.641	-0.003
0	122	150.561	151.022	0.461
3	61	151.180	150.269	-0.911
3	62	151.147	151.483	0.336
3	63	143.236	143.067	-0.169
3	64	150.870	150.900	0.030
3	65	140.498	139.803	-0.695
3	66	140.793	141.064	0.271
5	67	148.223	148.280	0.057
5	68	147.968	148.264	0.296
5	69	138.127	138.736	0.609
5	70	157.546	157.510	-0.036
5	71	156.024	155.834	-0.190
5	72	151.279	152.140	0.861
10	73	151.180	150.518	-0.662
10	74	151.147	150.211	-0.936
10	75	143.236	143.876	0.640
10	76	150.870	151.704	0.834
10	77	140.498	139.725	-0.773
10	78	140.793	141.188	0.395
15	79	148.223	149.565	1.342
15	80	147.968	148.559	0.591
15	81	138.127	138.600	0.473
15	82	157.546	156.942	-0.604
15	83	156.024	157.355	1.331
15	84	151.279	153.350	2.071
30	86	151.180	152.002	0.822
30	89	151.147	151.964	0.817
30	91	143.236	143.479	0.243
30	92	150.870	149.014	-1.856
30	94	140.498	138.990	-1.508
30	95	140.793	141.941	1.148
35	100	148.223	150.661	2.438
35	101	147.968	151.190	3.222
35	102	138.127	140.163	2.036
35	103	157.546	157.438	-0.108
35	104	156.024	156.351	0.327
35	105	151.279	153.214	1.935
50	106	141.867	143.547	1.680
50	107	145.981	149.042	3.061
50	108	145.466	148.573	3.107
50	109	148.441	153.164	4.723
50	110	145.598	147.020	1.422
50	111	140.791	141.642	0.851
55	113	145.596	145.623	0.027
55	114	154.741	155.735	0.994
55	115	152.003	153.360	1.357
55	116	147.907	148.961	1.054
55	126	161.248	160.518	-0.730
55	128	153.467	150.063	-3.404
100	129	147.495	150.287	2.792
100	131	144.585	145.222	0.637
100	132	145.232	142.454	-2.778
100	133	150.148	151.943	1.795
100	134	150.024	152.016	1.992
100	135	151.574	149.207	-2.367
100	138	146.730	148.024	1.294
100	139	143.358	147.133	3.775
100	140	148.710	149.130	0.420
100	141	147.167	148.092	0.925
100	142	149.442	150.259	0.817
100	143	149.477	154.656	5.179
100	144	147.881	149.357	1.476
100	220	152.673	153.733	1.060
100	221	139.926	143.770	3.844
100	224	140.836	141.402	0.566
100	225	145.247	146.879	1.632
100	226	143.917	147.866	3.949
100	228	146.237	151.340	5.103
100	232	142.786	146.230	3.444
100	233	148.780	151.972	3.192
100	236	136.879	139.375	2.496
100	238	146.420	150.008	3.588
100	243	141.064	140.493	-0.571
105	244	143.296	147.425	4.129
105	246	140.793	145.319	4.526
105	248	140.428	142.393	1.965
105	374	154.289	155.542	1.253
105	376	142.636	140.533	-2.103
105	375	161.248	160.518	-0.730
105	376	161.248	160.518	-0.730
105	Average	147.243	148.284	1.042
105	Min	136.879	138.600	-3.404
105	Std Dev	5.382	5.316	1.774



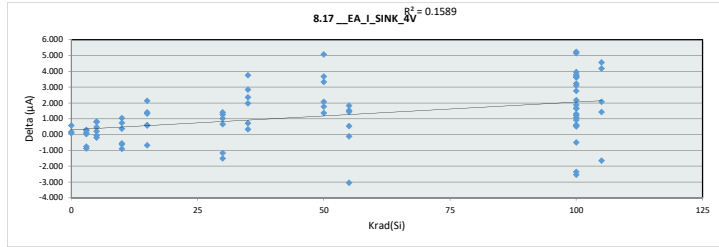
8.16 EA_I_SOURCE											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.420	139.803	138.736	139.725	138.600	138.990	140.163	141.642	145.623	139.375	140.533
Average	146.028	146.098	150.127	146.204	150.729	146.232	151.503	147.165	152.377	147.952	146.242
Max	151.022	151.483	157.510	151.704	157.355	152.002	157.438	153.164	160.518	154.656	155.542
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



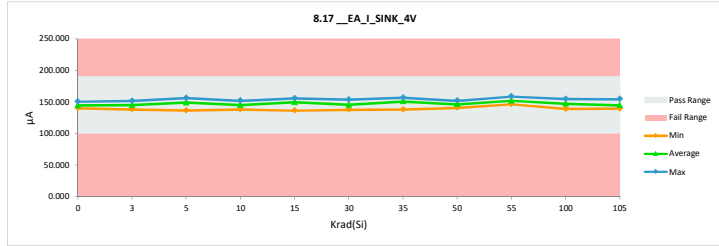
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8.17 EA_I_SINK_4V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	140.018	140.172	0.154
0	118	142.967	143.040	0.073
0	122	149.556	150.124	0.568
3	61	152.333	151.445	-0.888
3	62	148.168	148.430	0.262
3	63	142.376	142.391	0.015
3	64	149.422	149.525	0.103
3	65	138.736	137.972	-0.764
3	66	140.251	140.533	0.282
5	67	147.276	147.466	0.190
5	68	147.179	147.642	0.463
5	69	135.543	136.340	0.797
5	70	156.191	156.124	-0.067
5	71	154.011	153.816	-0.195
5	72	151.596	152.411	0.815
10	73	152.333	151.770	-0.563
10	74	148.168	147.255	-0.913
10	75	142.376	143.104	0.728
10	76	149.422	150.455	1.033
10	77	138.736	138.097	-0.639
10	78	140.251	140.614	0.363
15	79	147.276	148.672	1.396
15	80	147.179	147.766	0.587
15	81	135.543	136.106	0.563
15	82	156.191	155.491	-0.700
15	83	154.011	155.323	1.312
15	84	151.596	153.716	2.120
30	86	152.333	153.590	1.257
30	89	148.168	149.185	1.017
30	91	142.376	143.020	0.644
30	92	149.422	147.902	-1.520
30	94	138.736	137.558	-1.178
30	95	140.251	141.652	1.401
35	100	147.276	150.098	2.822
35	101	147.179	150.911	3.732
35	102	135.543	137.879	2.336
35	103	156.191	156.521	0.330
35	104	154.011	154.712	0.701
35	105	151.596	153.547	1.951
50	106	141.086	143.151	2.065
50	107	144.483	147.801	3.318
50	108	144.210	147.874	3.664
50	109	146.625	151.680	5.055
50	110	144.611	146.375	1.764
50	111	139.249	140.600	1.351
55	113	145.667	146.197	0.530
55	114	153.230	154.745	1.515
55	115	150.295	152.112	1.817
55	116	147.675	149.100	1.425
55	126	158.700	158.577	-0.123
55	128	152.617	149.549	-3.068
100	129	148.214	151.298	3.084
100	131	144.315	144.898	0.583
100	132	145.720	143.173	-2.547
100	133	149.941	151.594	1.653
100	134	149.543	151.714	2.171
100	135	149.652	147.289	-2.363
100	138	146.184	147.483	1.299
100	139	141.481	145.116	3.635
100	140	147.730	148.311	0.581
100	141	146.837	147.913	1.076
100	142	147.849	148.765	0.916
100	143	149.496	154.637	5.141
100	144	146.787	148.425	1.638
100	220	151.690	152.908	1.218
100	221	139.826	143.497	3.671
100	224	140.621	141.117	0.496
100	225	145.560	147.423	1.863
100	226	143.464	147.416	3.952
100	228	144.977	150.188	5.211
100	232	140.777	144.371	3.594
100	233	147.128	150.348	3.220
100	236	136.064	138.815	2.751
100	238	144.559	148.334	3.775
100	243	140.298	139.781	-0.517
105	244	140.434	144.603	4.169
105	246	139.344	143.905	4.561
105	248	139.197	141.253	2.056
105	374	152.775	154.191	1.416
105	376	141.010	139.347	-1.663
105	Max	158.700	158.577	5.211
105	Average	146.146	147.353	1.207
105	Min	135.543	136.106	-3.068
105	Std Dev	5.471	5.422	1.766



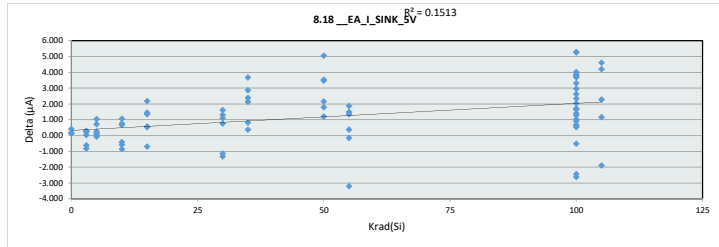
8.17 EA_I_SINK											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	140.172	137.972	136.340	138.097	136.106	137.558	137.879	140.600	146.197	138.815	139.347
Average	144.445	145.049	148.967	145.216	149.512	145.485	150.611	146.247	151.713	147.284	144.660
Max	150.124	151.445	156.124	151.770	155.491	153.590	156.521	151.680	158.577	154.637	154.191
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



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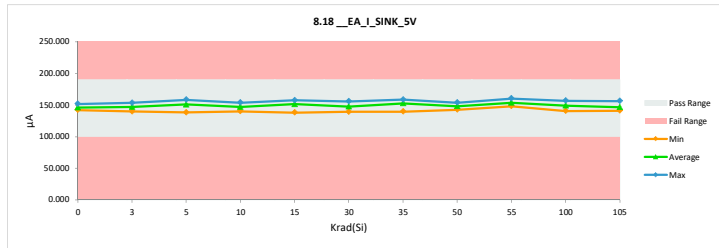
8.18 EA_I_SINK_5V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	141.845	141.951	0.106
0	118	144.567	144.704	0.137
0	122	151.324	151.699	0.375
3	61	154.118	153.261	-0.857
3	62	149.835	150.105	0.270
3	63	144.177	144.181	0.004
3	64	151.003	151.207	0.204
3	65	140.295	139.647	-0.648
3	66	141.977	142.221	0.244
5	67	148.969	149.083	0.114
5	68	149.037	149.258	0.221
5	69	137.352	138.046	0.694
5	70	157.986	157.977	-0.009
5	71	155.732	155.637	-0.095
5	72	153.292	154.311	1.019
10	73	154.118	153.670	-0.448
10	74	149.835	148.934	-0.901
10	75	144.177	144.896	0.719
10	76	151.003	152.024	1.021
10	77	140.295	139.681	-0.614
10	78	141.977	142.614	0.637
15	79	148.969	150.277	1.308
15	80	149.037	149.575	0.538
15	81	137.352	137.852	0.500
15	82	157.986	157.256	-0.730
15	83	155.732	157.150	1.418
15	84	153.292	155.439	2.147
30	86	154.118	155.381	1.263
30	89	149.835	150.896	1.061
30	91	144.177	144.895	0.718
30	92	151.003	149.637	-1.366
30	94	140.295	139.123	-1.172
30	95	141.977	143.555	1.578
35	100	148.969	151.796	2.827
35	101	149.037	152.671	3.634
35	102	137.352	139.445	2.093
35	103	157.986	158.329	0.343
35	104	155.732	156.520	0.788
35	105	153.292	155.647	2.355
50	106	142.742	144.857	2.115
50	107	146.312	149.759	3.447
50	108	146.019	149.512	3.493
50	109	148.334	153.360	5.026
50	110	146.378	148.144	1.766
50	111	141.073	142.242	1.169
55	113	147.581	147.931	0.350
55	114	155.024	156.476	1.452
55	115	152.045	153.878	1.833
55	116	149.290	150.594	1.304
55	126	160.462	160.276	-0.186
55	128	154.497	151.253	-3.244
100	129	150.153	153.079	2.926
100	131	146.071	146.635	0.564
100	132	147.512	144.852	-2.660
100	133	151.693	153.369	1.676
100	134	151.210	153.521	2.311
100	135	151.431	148.969	-2.462
100	138	147.885	149.252	1.367
100	139	143.029	146.805	3.776
100	140	149.629	150.135	0.506
100	141	148.739	149.736	0.997
100	142	149.627	150.501	0.874
100	143	151.304	156.550	5.246
100	144	148.652	150.271	1.619
100	220	153.552	154.808	1.256
100	221	141.409	145.256	3.847
100	224	142.244	142.893	0.649
100	225	147.208	149.217	2.009
100	226	145.156	149.150	3.994
100	228	146.783	152.024	5.241
100	232	142.562	146.223	3.661
100	233	149.032	152.310	3.278
100	236	137.731	140.329	2.598
100	238	146.393	150.064	3.671
100	243	141.911	141.369	-0.542
105	244	142.110	146.278	4.168
105	246	141.136	145.698	4.562
105	248	140.868	143.115	2.247
105	374	154.847	155.984	1.137
105	375	142.677	140.746	-1.931
105	376	160.462	160.276	-0.186
105	Max	147.892	149.100	1.208
105	Average	137.352	137.852	-3.244
105	Min	137.352	137.852	-3.244
105	Std Dev	5.496	5.464	1.785



8.18 EA_I_SINK	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

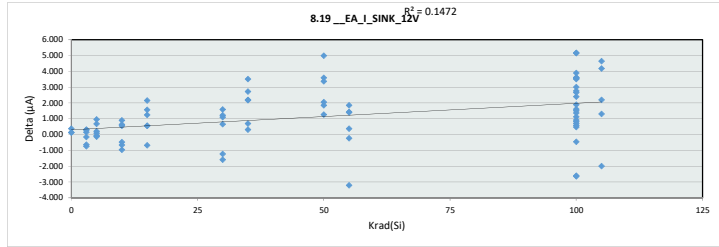
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	141.951	139.647	138.046	139.681	137.852	139.123	139.445	142.242	147.931	140.329	140.746
Average	146.118	146.770	150.719	146.970	151.258	147.248	152.401	147.979	153.401	149.055	146.364
Max	151.699	153.261	157.977	153.670	157.256	155.381	158.329	153.360	160.276	156.550	155.984
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



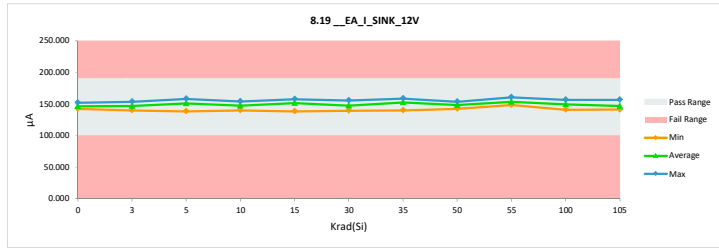
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8.19 EA I SINK 12V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	141.912	142.038	0.126
0	118	144.694	144.812	0.118
0	122	151.458	151.822	0.364
3	61	154.236	153.489	-0.747
3	62	150.007	150.312	0.305
3	63	144.245	144.089	-0.156
3	64	151.257	151.398	0.141
3	65	140.511	139.871	-0.640
3	66	142.085	142.369	0.284
5	67	149.118	149.198	0.080
5	68	149.139	149.335	0.196
5	69	137.458	138.128	0.670
5	70	158.143	158.067	-0.076
5	71	155.829	155.691	-0.138
5	72	153.489	154.447	0.958
10	73	154.236	153.750	-0.486
10	74	150.007	149.024	-0.983
10	75	144.245	144.871	0.626
10	76	151.257	152.136	0.879
10	77	140.511	139.838	-0.673
10	78	142.085	142.620	0.535
15	79	149.118	150.345	1.227
15	80	149.139	149.679	0.540
15	81	137.458	138.003	0.545
15	82	158.143	157.452	-0.691
15	83	155.829	157.394	1.565
15	84	153.489	155.623	2.134
30	86	154.236	155.449	1.213
30	89	150.007	151.104	1.097
30	91	144.245	144.899	0.654
30	92	151.257	149.663	-1.594
30	94	140.511	139.272	-1.239
30	95	142.085	143.662	1.577
35	100	149.118	151.832	2.714
35	101	149.139	152.631	3.492
35	102	137.458	139.617	2.159
35	103	158.143	158.437	0.294
35	104	155.829	156.511	0.682
35	105	153.489	155.673	2.184
50	106	142.884	144.917	2.033
50	107	146.447	149.813	3.366
50	108	146.137	149.709	3.572
50	109	148.412	153.384	4.972
50	110	146.542	148.376	1.834
50	111	141.178	142.428	1.250
55	113	147.727	148.082	0.355
55	114	155.100	156.510	1.410
55	115	152.234	154.065	1.831
55	116	149.392	150.780	1.388
55	126	160.623	160.371	-0.252
55	128	154.528	151.305	-3.223
100	129	150.271	153.028	2.757
100	131	146.180	146.741	0.561
100	132	147.602	144.943	-2.659
100	133	151.961	153.551	1.590
100	134	151.302	153.695	2.393
100	135	151.687	149.069	-2.618
100	138	147.996	149.341	1.345
100	139	143.120	146.660	3.540
100	140	149.779	150.248	0.469
100	141	148.889	149.799	0.910
100	142	149.734	150.548	0.814
100	143	151.401	156.543	5.142
100	144	148.704	150.200	1.496
100	220	153.704	154.809	1.105
100	221	141.642	145.237	3.595
100	224	142.311	142.996	0.685
100	225	147.413	149.291	1.878
100	226	145.314	149.204	3.890
100	228	146.865	152.021	5.156
100	232	142.626	146.207	3.581
100	233	149.205	152.204	2.999
100	236	137.838	140.494	2.656
100	238	146.468	149.976	3.508
100	243	142.022	141.559	-0.463
105	244	142.265	146.439	4.174
105	246	141.130	145.762	4.632
105	248	141.093	143.279	2.186
105	374	154.981	156.265	1.284
105	376	142.966	140.952	-2.014
105	Max	160.623	160.371	-0.252
105	Average	148.029	149.192	1.163
105	Min	137.458	138.003	-0.545
105	Std Dev	5.503	5.456	1.770



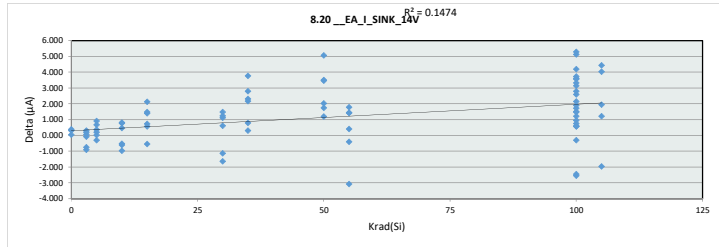
8.19 EA I SINK		0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.038	139.871	138.128	139.838	138.003	139.272	139.617	142.428	148.082	140.494	140.952	146.539
Average	146.224	146.921	150.811	147.040	151.416	147.342	152.450	148.105	153.519	149.099	146.539	156.265
Max	151.822	153.489	158.067	153.750	157.452	155.449	158.437	153.384	160.371	156.543	156.265	160.000
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



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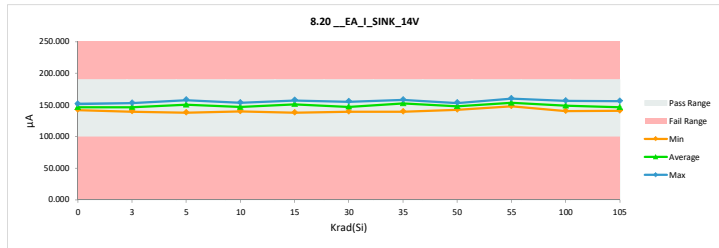
8.20 EA I SINK 14V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	141.925	142.215	0.290
0	118	144.791	144.812	0.021
0	122	151.436	151.799	0.363
3	61	154.209	153.280	-0.929
3	62	149.929	150.217	0.288
3	63	144.207	144.098	-0.109
3	64	151.270	151.309	0.039
3	65	140.457	139.680	-0.777
3	66	142.084	142.269	0.185
5	67	148.947	149.121	0.174
5	68	148.949	149.289	0.340
5	69	137.447	138.097	0.650
5	70	157.972	157.973	0.001
5	71	155.760	155.426	-0.334
5	72	153.439	154.328	0.889
10	73	154.209	153.664	-0.545
10	74	149.929	148.939	-0.990
10	75	144.207	144.960	0.753
10	76	151.270	152.048	0.778
10	77	140.457	139.837	-0.620
10	78	142.084	142.524	0.440
15	79	148.947	150.329	1.382
15	80	148.949	149.654	0.705
15	81	137.447	137.985	0.538
15	82	157.972	157.401	-0.571
15	83	155.760	157.220	1.460
15	84	153.439	155.545	2.106
30	86	154.209	155.418	1.209
30	89	149.929	151.022	1.093
30	91	144.207	144.800	0.593
30	92	151.270	149.616	-1.654
30	94	140.457	139.304	-1.153
30	95	142.084	143.547	1.463
35	100	148.947	151.719	2.772
35	101	148.949	152.687	3.738
35	102	137.447	139.593	2.146
35	103	157.972	158.251	0.279
35	104	155.760	156.526	0.766
35	105	153.439	155.730	2.291
50	106	142.888	144.895	2.007
50	107	146.460	149.945	3.485
50	108	146.010	149.455	3.445
50	109	148.325	153.358	5.033
50	110	146.582	148.308	1.726
50	111	141.168	142.336	1.168
55	113	147.660	148.044	0.384
55	114	155.106	156.503	1.397
55	115	152.197	153.954	1.757
55	116	149.295	150.711	1.416
55	126	160.594	160.176	-0.418
55	128	154.532	151.430	-3.102
100	129	150.204	152.971	2.767
100	131	146.173	146.731	0.558
100	132	147.569	144.998	-2.571
100	133	151.851	153.544	1.693
100	134	151.382	153.507	2.125
100	135	151.492	149.020	-2.472
100	138	147.915	149.373	1.458
100	139	143.079	146.626	3.547
100	140	149.696	150.230	0.534
100	141	148.890	149.613	0.723
100	142	149.630	150.566	0.936
100	143	151.425	156.681	5.256
100	144	148.709	150.191	1.482
100	220	153.626	154.818	1.192
100	221	141.598	145.290	3.692
100	224	142.451	143.024	0.573
100	225	147.407	149.290	1.883
100	226	145.230	149.390	4.160
100	228	146.896	151.992	5.096
100	232	142.615	146.185	3.570
100	233	149.156	152.268	3.112
100	236	137.872	140.433	2.561
100	238	146.488	149.793	3.305
100	243	141.998	141.666	-0.332
105	244	142.330	146.332	4.002
105	246	141.183	145.586	4.403
105	248	141.122	143.036	1.914
105	374	154.961	156.143	1.182
105	375	142.790	140.806	-1.984
105	376	160.594	160.176	-0.418
105	377	147.984	149.143	1.159
105	378	137.447	137.985	-0.538
105	379	5.479	5.442	-0.037



8.20 EA I SINK	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

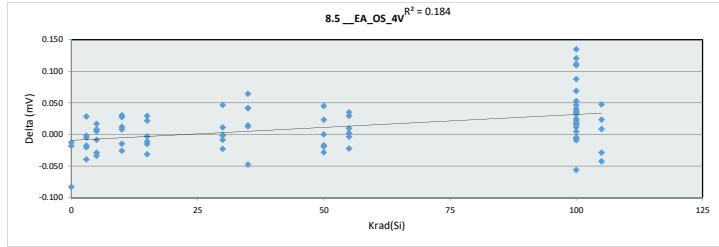
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.215	139.680	138.097	139.837	137.985	139.304	139.593	142.336	148.044	140.433	140.806
Average	146.275	146.809	150.706	146.995	151.356	147.285	152.418	148.050	153.470	149.092	146.381
Max	151.799	153.280	157.973	153.664	157.401	155.418	158.251	153.358	160.176	156.681	156.143
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



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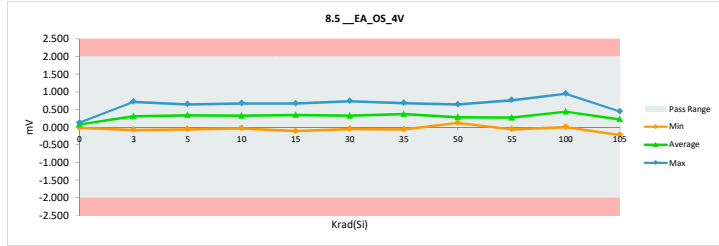
8.5_EA_OS_4V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

Krad(Si)	Serial #	PRE	POST	Delta
0	117	-0.003	-0.021	-0.018
0	118	0.137	0.125	-0.013
0	122	0.208	0.125	-0.083
3	61	0.688	0.716	0.028
3	62	0.010	-0.029	-0.040
3	63	0.373	0.353	-0.020
3	64	0.318	0.313	-0.005
3	65	-0.064	-0.081	-0.018
3	66	0.576	0.573	-0.002
5	67	0.393	0.398	0.005
5	68	0.455	0.426	-0.029
5	69	-0.080	-0.064	0.017
5	70	0.407	0.398	-0.009
5	71	0.256	0.222	-0.034
5	72	0.639	0.647	0.008
10	73	0.688	0.673	-0.015
10	74	0.010	0.019	0.008
10	75	0.373	0.385	0.012
10	76	0.318	0.292	-0.026
10	77	-0.064	-0.036	0.027
10	78	0.576	0.606	0.030
15	79	0.393	0.378	-0.015
15	80	0.455	0.477	0.022
15	81	-0.080	-0.112	-0.031
15	82	0.407	0.404	-0.003
15	83	0.256	0.245	-0.011
15	84	0.639	0.668	0.029
30	86	0.688	0.734	0.046
30	89	0.010	0.009	-0.001
30	91	0.373	0.372	-0.001
30	92	0.318	0.309	-0.009
30	94	-0.064	-0.052	0.011
30	95	0.576	0.553	-0.023
35	100	0.393	0.457	0.064
35	101	0.455	0.496	0.041
35	102	-0.080	-0.066	0.014
35	103	0.407	0.360	-0.047
35	104	0.256	0.269	0.012
35	105	0.639	0.681	0.042
50	106	0.594	0.638	0.045
50	107	0.125	0.124	0.000
50	108	0.236	0.260	0.023
50	109	0.140	0.123	-0.017
50	110	0.364	0.345	-0.019
50	111	0.226	0.197	-0.028
55	113	0.753	0.762	0.010
55	114	0.089	0.067	-0.022
55	115	0.094	0.096	0.002
55	116	0.328	0.362	0.035
55	126	-0.059	-0.062	-0.004
55	128	0.370	0.399	0.029
100	129	0.692	0.704	0.012
100	131	0.612	0.658	0.047
100	132	0.729	0.840	0.111
100	133	0.483	0.518	0.035
100	134	0.542	0.651	0.109
100	135	0.063	0.067	0.005
100	138	0.415	0.484	0.069
100	139	0.037	0.028	-0.009
100	140	0.299	0.324	0.025
100	141	0.493	0.514	0.021
100	142	0.361	0.305	-0.056
100	143	0.488	0.525	0.037
100	144	0.471	0.524	0.053
100	220	0.390	0.407	0.017
100	221	0.380	0.412	0.032
100	224	0.494	0.581	0.087
100	225	0.812	0.947	0.135
100	226	0.436	0.454	0.018
100	228	0.273	0.289	0.016
100	232	-0.118	0.003	0.120
100	233	0.187	0.228	0.041
100	236	0.461	0.456	-0.005
100	238	0.334	0.327	-0.007
100	243	0.285	0.319	0.034
105	244	-0.195	-0.224	-0.029
105	246	0.219	0.266	0.047
105	248	0.432	0.440	0.008
105	374	0.239	0.262	0.023
105	376	0.386	0.344	-0.042
105	Max	0.812	0.947	0.135
105	Average	0.328	0.340	0.012
105	Min	-0.195	-0.224	-0.083
105	Std Dev	0.239	0.254	0.039



8.5_EA_OS_4V	
Test Site	
Tester	
Test Number	
Max Limit	2 mV
Min Limit	-2 mV

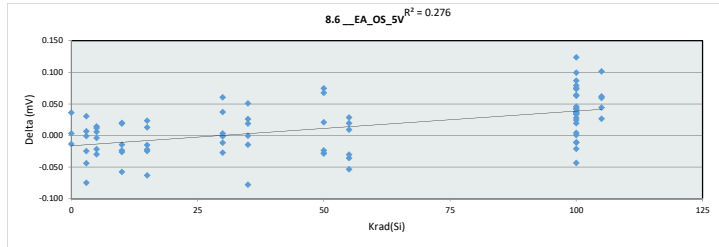
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.021	-0.081	-0.064	-0.036	-0.112	-0.052	-0.066	0.123	-0.062	0.003	-0.224
Average	0.076	0.308	0.338	0.323	0.343	0.321	0.366	0.281	0.271	0.440	0.218
Max	0.125	0.717	0.647	0.673	0.668	0.735	0.681	0.639	0.762	0.947	0.440
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000



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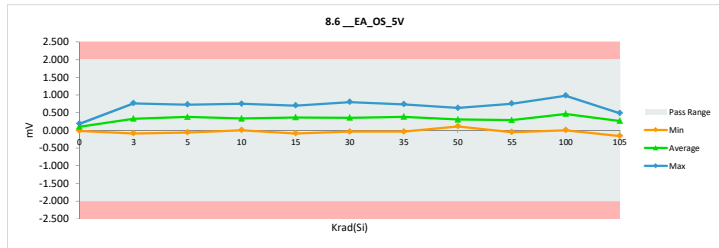
8.6_EA_OS_5V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

Krad(Si)	Serial #	PRE	POST	Delta
0	117	-0.023	-0.021	0.002
0	118	0.144	0.129	-0.014
0	122	0.146	0.181	0.036
3	61	0.760	0.759	-0.002
3	62	-0.021	0.009	0.030
3	63	0.389	0.363	-0.025
3	64	0.361	0.368	0.006
3	65	-0.016	-0.091	-0.075
3	66	0.591	0.547	-0.045
5	67	0.455	0.433	-0.023
5	68	0.519	0.488	-0.031
5	69	-0.069	-0.064	0.005
5	70	0.386	0.400	0.014
5	71	0.264	0.260	-0.004
5	72	0.713	0.724	0.011
10	73	0.760	0.745	-0.015
10	74	-0.021	-0.003	0.018
10	75	0.389	0.362	-0.027
10	76	0.361	0.337	-0.024
10	77	-0.016	0.004	0.019
10	78	0.591	0.533	-0.058
15	79	0.455	0.430	-0.025
15	80	0.519	0.455	-0.064
15	81	-0.069	-0.092	-0.023
15	82	0.386	0.409	0.023
15	83	0.264	0.277	0.012
15	84	0.713	0.697	-0.016
30	86	0.760	0.797	0.036
30	89	-0.021	0.038	0.060
30	91	0.389	0.377	-0.012
30	92	0.361	0.359	-0.002
30	94	-0.016	-0.044	-0.028
30	95	0.591	0.594	0.003
35	100	0.455	0.440	-0.015
35	101	0.519	0.518	-0.001
35	102	-0.069	-0.044	0.025
35	103	0.386	0.436	0.050
35	104	0.264	0.186	-0.078
35	105	0.713	0.731	0.018
50	106	0.657	0.628	-0.029
50	107	0.132	0.108	-0.024
50	108	0.270	0.290	0.020
50	109	0.100	0.174	0.074
50	110	0.404	0.375	-0.029
50	111	0.171	0.238	0.067
55	113	0.802	0.748	-0.054
55	114	0.091	0.110	0.019
55	115	0.146	0.109	-0.037
55	116	0.353	0.381	0.028
55	126	-0.026	-0.057	-0.031
55	128	0.390	0.399	0.009
100	129	0.648	0.721	0.073
100	131	0.645	0.677	0.033
100	132	0.817	0.858	0.041
100	133	0.558	0.620	0.062
100	134	0.592	0.691	0.099
100	135	0.057	0.135	0.078
100	138	0.475	0.499	0.024
100	139	0.083	0.061	-0.022
100	140	0.277	0.320	0.042
100	141	0.529	0.532	0.003
100	142	0.348	0.348	0.000
100	143	0.511	0.529	0.019
100	144	0.471	0.516	0.045
100	220	0.399	0.433	0.034
100	221	0.407	0.434	0.027
100	224	0.617	0.573	-0.044
100	225	0.850	0.974	0.123
100	226	0.484	0.472	-0.012
100	228	0.226	0.300	0.074
100	232	-0.067	-0.004	0.063
100	233	0.209	0.197	-0.012
100	236	0.424	0.510	0.086
100	238	0.323	0.360	0.037
100	243	0.331	0.367	0.036
105	244	-0.186	-0.160	0.026
105	246	0.203	0.247	0.044
105	248	0.378	0.479	0.100
105	374	0.249	0.310	0.061
105	376	0.365	0.423	0.058
105	Max	0.850	0.974	0.123
105	Average	0.350	0.362	0.012
105	Min	-0.186	-0.160	-0.078
105	Std Dev	0.255	0.257	0.042



8.6_EA_OS_5V	
Test Site	
Tester	
Test Number	
Max Limit	2
Min Limit	-2

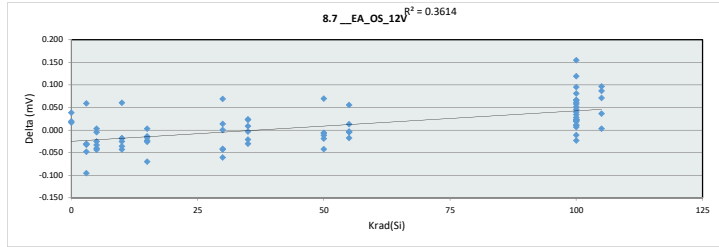
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.021	-0.091	-0.064	-0.003	-0.092	-0.044	-0.044	0.108	-0.057	-0.004	-0.160
Average	0.097	0.326	0.373	0.330	0.363	0.354	0.378	0.302	0.281	0.464	0.260
Max	0.181	0.759	0.724	0.745	0.697	0.797	0.731	0.628	0.748	0.974	0.479
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000



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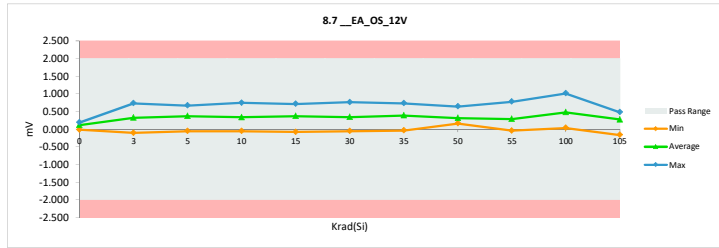
8.7_EA_OS_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

Krad(Si)	Serial #	PRE	POST	Delta
0	117	-0.038	-0.019	0.019
0	118	0.125	0.163	0.038
0	122	0.167	0.183	0.016
3	61	0.761	0.729	-0.032
3	62	0.043	0.013	-0.031
3	63	0.425	0.377	-0.048
3	64	0.288	0.346	0.058
3	65	-0.014	-0.110	-0.096
3	66	0.584	0.551	-0.033
5	67	0.450	0.417	-0.033
5	68	0.526	0.482	-0.044
5	69	-0.065	-0.063	0.003
5	70	0.424	0.419	-0.005
5	71	0.286	0.261	-0.025
5	72	0.705	0.664	-0.040
10	73	0.761	0.742	-0.018
10	74	0.043	0.024	-0.019
10	75	0.425	0.399	-0.026
10	76	0.288	0.347	0.060
10	77	-0.014	-0.057	-0.043
10	78	0.584	0.548	-0.036
15	79	0.450	0.436	-0.015
15	80	0.526	0.455	-0.071
15	81	-0.065	-0.083	-0.017
15	82	0.424	0.398	-0.026
15	83	0.286	0.263	-0.023
15	84	0.705	0.707	0.003
30	86	0.761	0.760	0.000
30	89	0.043	-0.018	-0.061
30	91	0.425	0.383	-0.042
30	92	0.288	0.356	0.069
30	94	-0.014	-0.058	-0.044
30	95	0.584	0.598	0.014
35	100	0.450	0.459	0.008
35	101	0.526	0.522	-0.004
35	102	-0.065	-0.042	0.023
35	103	0.424	0.394	-0.031
35	104	0.286	0.264	-0.022
35	105	0.705	0.728	0.023
50	106	0.644	0.637	-0.007
50	107	0.166	0.154	-0.012
50	108	0.289	0.247	-0.043
50	109	0.104	0.173	0.069
50	110	0.407	0.400	-0.008
50	111	0.265	0.246	-0.019
55	113	0.780	0.775	-0.005
55	114	0.107	0.120	0.013
55	115	0.070	0.065	-0.005
55	116	0.334	0.389	0.055
55	126	-0.028	-0.046	-0.018
55	128	0.404	0.401	-0.003
100	129	0.679	0.685	0.006
100	131	0.662	0.682	0.021
100	132	0.796	0.829	0.034
100	133	0.555	0.577	0.022
100	134	0.622	0.702	0.080
100	135	0.073	0.131	0.058
100	138	0.515	0.503	-0.011
100	139	0.077	0.087	0.010
100	140	0.308	0.359	0.051
100	141	0.554	0.599	0.045
100	142	0.342	0.352	0.011
100	143	0.511	0.532	0.021
100	144	0.456	0.575	0.119
100	220	0.399	0.422	0.023
100	221	0.388	0.454	0.067
100	224	0.581	0.620	0.039
100	225	0.854	1.008	0.154
100	226	0.478	0.496	0.018
100	228	0.310	0.286	-0.023
100	232	-0.066	0.029	0.095
100	233	0.198	0.263	0.065
100	236	0.490	0.512	0.022
100	238	0.347	0.374	0.027
100	243	0.315	0.375	0.060
105	244	-0.172	-0.169	0.003
105	246	0.260	0.296	0.036
105	248	0.380	0.476	0.096
105	374	0.242	0.313	0.071
105	376	0.351	0.438	0.087
105	Max	0.854	1.008	0.154
105	Average	0.357	0.366	0.010
105	Min	-0.172	-0.169	-0.006
105	Std Dev	0.250	0.257	0.045



8.7_EA_OS_12V	
Test Site	
Tester	
Test Number	
Max Limit	2
Min Limit	-2

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.019	-0.110	-0.063	-0.057	-0.083	-0.058	-0.042	0.154	-0.046	0.029	-0.169
Average	0.109	0.318	0.363	0.334	0.363	0.337	0.387	0.309	0.284	0.477	0.271
Max	0.183	0.729	0.665	0.742	0.708	0.760	0.728	0.637	0.775	1.008	0.476
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

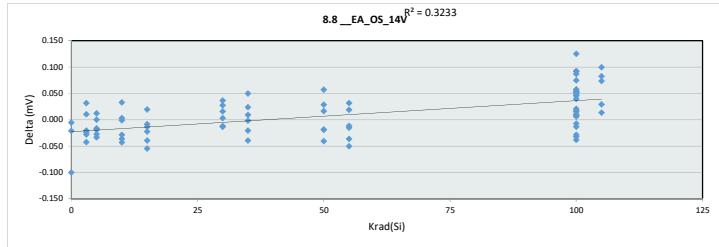




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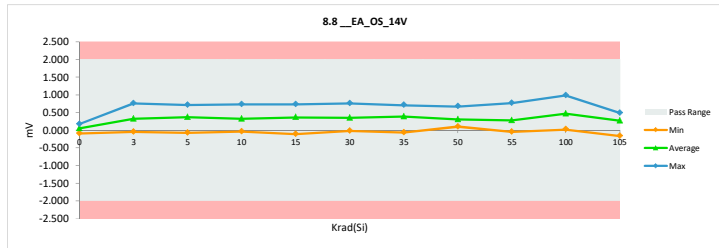
8.8_EA_OS_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.012	-0.090	-0.101
0	118	0.107	0.085	-0.022
0	122	0.182	0.176	-0.006
3	61	0.732	0.762	0.030
3	62	0.037	0.016	-0.021
3	63	0.402	0.373	-0.029
3	64	0.323	0.298	-0.024
3	65	-0.005	-0.049	-0.044
3	66	0.558	0.568	0.010
5	67	0.437	0.417	-0.020
5	68	0.526	0.492	-0.034
5	69	-0.086	-0.075	0.011
5	70	0.433	0.405	-0.028
5	71	0.301	0.283	-0.017
5	72	0.713	0.712	-0.001
10	73	0.732	0.730	-0.002
10	74	0.037	0.000	-0.037
10	75	0.402	0.358	-0.044
10	76	0.323	0.325	0.002
10	77	-0.005	-0.034	-0.029
10	78	0.558	0.590	0.032
15	79	0.437	0.428	-0.010
15	80	0.526	0.470	-0.056
15	81	-0.086	-0.109	-0.023
15	82	0.433	0.419	-0.014
15	83	0.301	0.260	-0.040
15	84	0.713	0.732	0.019
30	86	0.732	0.758	0.026
30	89	0.037	0.039	0.002
30	91	0.402	0.388	-0.014
30	92	0.323	0.338	0.015
30	94	-0.005	-0.018	-0.013
30	95	0.558	0.594	0.036
35	100	0.437	0.486	0.049
35	101	0.526	0.534	0.008
35	102	-0.086	-0.063	0.023
35	103	0.433	0.412	-0.021
35	104	0.301	0.260	-0.041
35	105	0.713	0.711	-0.003
50	106	0.646	0.674	0.027
50	107	0.145	0.103	-0.041
50	108	0.297	0.277	-0.020
50	109	0.120	0.176	0.056
50	110	0.410	0.390	-0.020
50	111	0.218	0.234	0.016
55	113	0.782	0.766	-0.016
55	114	0.127	0.090	-0.037
55	115	0.123	0.072	-0.051
55	116	0.337	0.367	0.030
55	126	-0.033	-0.046	-0.013
55	128	0.403	0.421	0.018
100	129	0.694	0.680	-0.014
100	131	0.659	0.669	0.009
100	132	0.777	0.782	0.005
100	133	0.541	0.594	0.053
100	134	0.597	0.688	0.091
100	135	0.087	0.133	0.045
100	138	0.488	0.533	0.045
100	139	0.086	0.106	0.020
100	140	0.323	0.284	-0.039
100	141	0.532	0.581	0.049
100	142	0.363	0.334	-0.030
100	143	0.513	0.527	0.014
100	144	0.472	0.546	0.074
100	220	0.398	0.415	0.016
100	221	0.379	0.429	0.051
100	224	0.563	0.615	0.053
100	225	0.859	0.983	0.124
100	226	0.468	0.435	-0.033
100	228	0.307	0.299	-0.008
100	232	-0.069	0.022	0.091
100	233	0.190	0.276	0.086
100	236	0.471	0.528	0.057
100	238	0.336	0.374	0.039
100	243	0.352	0.359	0.007
105	244	-0.235	-0.162	0.073
105	246	0.210	0.308	0.098
105	248	0.409	0.490	0.082
105	374	0.301	0.313	0.013
105	376	0.381	0.409	0.028
105	Max	0.859	0.983	0.124
105	Average	0.356	0.363	0.008
105	Min	-0.235	-0.162	-0.101
105	Std Dev	0.248	0.258	0.042



8.8_EA_OS_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

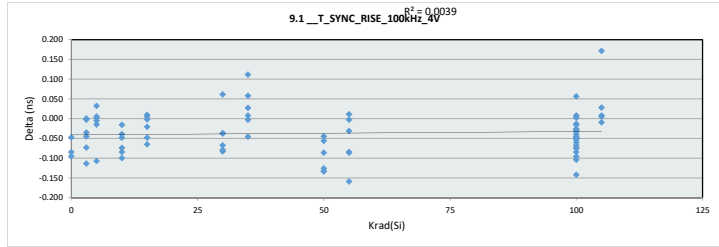
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.090	-0.049	-0.075	-0.034	-0.109	-0.018	-0.063	0.103	-0.046	0.022	-0.162
Average	0.057	0.328	0.373	0.328	0.367	0.350	0.390	0.309	0.278	0.466	0.272
Max	0.176	0.762	0.713	0.730	0.732	0.758	0.711	0.674	0.766	0.983	0.490
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000



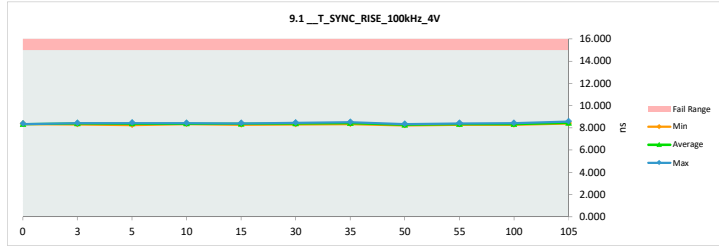
LDR TID Report  
TPS7H5001-SP

9.1 T_SYNC_RISE_100kHz_4V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	15	15
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.423	8.328	-0.095
0	118	8.422	8.337	-0.085
0	122	8.400	8.353	-0.047
3	61	8.510	8.396	-0.114
3	62	8.475	8.430	-0.045
3	63	8.378	8.375	-0.003
3	64	8.389	8.316	-0.073
3	65	8.426	8.426	0.000
3	66	8.431	8.396	-0.035
5	67	8.377	8.379	0.002
5	68	8.393	8.388	-0.005
5	69	8.404	8.436	0.032
5	70	8.350	8.243	-0.107
5	71	8.432	8.437	0.005
5	72	8.390	8.375	-0.015
10	73	8.510	8.425	-0.085
10	74	8.475	8.401	-0.074
10	75	8.378	8.330	-0.048
10	76	8.389	8.350	-0.039
10	77	8.426	8.410	-0.016
10	78	8.431	8.331	-0.100
15	79	8.377	8.386	0.009
15	80	8.393	8.398	0.005
15	81	8.404	8.402	-0.002
15	82	8.350	8.285	-0.065
15	83	8.432	8.411	-0.021
15	84	8.390	8.342	-0.048
30	86	8.510	8.442	-0.068
30	89	8.475	8.392	-0.083
30	91	8.378	8.439	0.061
30	92	8.389	8.310	-0.079
30	94	8.426	8.388	-0.038
30	95	8.431	8.394	-0.037
35	100	8.377	8.331	-0.046
35	101	8.393	8.504	0.111
35	102	8.404	8.462	0.058
35	103	8.350	8.347	-0.003
35	104	8.432	8.459	0.027
35	105	8.390	8.398	0.008
50	106	8.391	8.258	-0.133
50	107	8.316	8.230	-0.086
50	108	8.424	8.291	-0.133
50	109	8.351	8.225	-0.126
50	110	8.386	8.330	-0.056
50	111	8.372	8.327	-0.045
55	113	8.454	8.295	-0.159
55	114	8.397	8.311	-0.086
55	115	8.398	8.313	-0.085
55	116	8.376	8.345	-0.031
55	126	8.336	8.333	-0.003
55	128	8.400	8.411	0.011
100	129	8.309	8.311	0.002
100	131	8.373	8.269	-0.104
100	132	8.365	8.317	-0.048
100	133	8.415	8.383	-0.032
100	134	8.375	8.300	-0.075
100	135	8.383	8.298	-0.085
100	138	8.451	8.309	-0.142
100	139	8.284	8.340	0.056
100	140	8.337	8.298	-0.039
100	141	8.426	8.352	-0.074
100	142	8.364	8.371	0.007
100	143	8.419	8.366	-0.053
100	144	8.321	8.329	0.008
100	220	8.372	8.304	-0.068
100	221	8.329	8.302	-0.027
100	224	8.409	8.393	-0.016
100	225	8.373	8.360	-0.013
100	226	8.468	8.423	-0.045
100	228	8.390	8.364	-0.026
100	232	8.361	8.312	-0.049
100	233	8.383	8.287	-0.096
100	236	8.431	8.371	-0.060
100	238	8.401	8.351	-0.050
100	243	8.443	8.413	-0.030
105	244	8.451	8.442	-0.009
105	246	8.393	8.421	0.028
105	248	8.383	8.554	0.171
105	374	8.462	8.470	0.008
105	376	8.375	8.390	0.015
105	Max	8.510	8.554	0.171
105	Average	8.399	8.363	-0.037
105	Min	8.284	8.225	-0.159
105	Std Dev	0.044	0.062	0.056



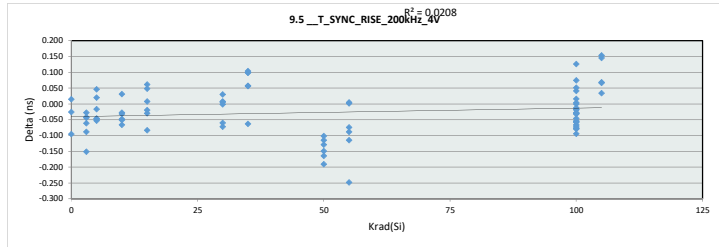
9.1 T_SYNC_RISE											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.328	8.316	8.243	8.330	8.285	8.310	8.331	8.225	8.295	8.269	8.380
Average	8.339	8.390	8.376	8.375	8.371	8.394	8.417	8.277	8.335	8.338	8.453
Max	8.353	8.430	8.437	8.425	8.411	8.442	8.504	8.330	8.411	8.423	8.554
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



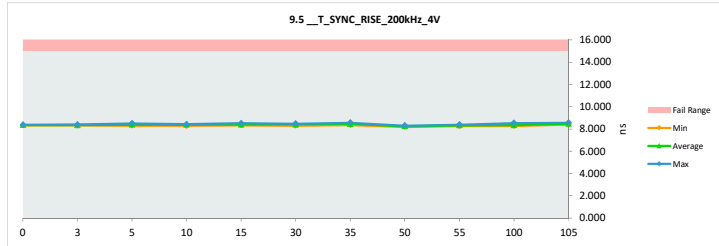
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9.5_T_SYNC_RISE_200kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.455	8.358	-0.097
0	118	8.294	8.307	0.013
0	122	8.384	8.357	-0.027
3	61	8.443	8.380	-0.063
3	62	8.408	8.363	-0.045
3	63	8.397	8.307	-0.090
3	64	8.341	8.298	-0.043
3	65	8.366	8.337	-0.029
3	66	8.451	8.298	-0.153
5	67	8.300	8.282	-0.018
5	68	8.433	8.478	0.045
5	69	8.382	8.400	0.018
5	70	8.329	8.274	-0.055
5	71	8.437	8.389	-0.048
5	72	8.401	8.351	-0.050
10	73	8.443	8.410	-0.033
10	74	8.408	8.379	-0.029
10	75	8.397	8.347	-0.050
10	76	8.341	8.273	-0.068
10	77	8.366	8.395	0.029
10	78	8.451	8.398	-0.053
15	79	8.300	8.306	0.006
15	80	8.433	8.493	0.060
15	81	8.382	8.351	-0.031
15	82	8.329	8.308	-0.021
15	83	8.437	8.484	0.047
15	84	8.401	8.316	-0.085
30	86	8.443	8.449	0.006
30	89	8.408	8.412	0.004
30	91	8.397	8.394	-0.003
30	92	8.341	8.279	-0.062
30	94	8.366	8.394	0.028
30	95	8.451	8.377	-0.074
35	100	8.300	8.402	0.102
35	101	8.433	8.530	0.097
35	102	8.382	8.437	0.055
35	103	8.329	8.385	0.056
35	104	8.437	8.534	0.097
35	105	8.401	8.336	-0.065
50	106	8.418	8.226	-0.192
50	107	8.314	8.184	-0.130
50	108	8.396	8.280	-0.116
50	109	8.306	8.203	-0.103
50	110	8.426	8.260	-0.166
50	111	8.423	8.272	-0.151
55	113	8.394	8.278	-0.116
55	114	8.373	8.283	-0.090
55	115	8.534	8.284	-0.250
55	116	8.375	8.375	0.000
55	126	8.320	8.244	-0.076
55	128	8.348	8.351	0.003
100	129	8.379	8.307	-0.072
100	131	8.367	8.309	-0.058
100	132	8.332	8.372	0.040
100	133	8.340	8.320	-0.020
100	134	8.328	8.309	-0.019
100	135	8.343	8.311	-0.032
100	138	8.358	8.298	-0.060
100	139	8.351	8.255	-0.096
100	140	8.326	8.324	-0.002
100	141	8.345	8.347	0.002
100	142	8.386	8.307	-0.079
100	143	8.393	8.360	-0.033
100	144	8.326	8.295	-0.031
100	220	8.312	8.362	0.050
100	221	8.337	8.256	-0.081
100	224	8.312	8.385	0.073
100	225	8.388	8.359	-0.029
100	226	8.407	8.339	-0.068
100	228	8.416	8.360	-0.056
100	232	8.295	8.247	-0.048
100	233	8.332	8.318	-0.014
100	236	8.400	8.322	-0.078
100	238	8.386	8.511	0.125
100	243	8.334	8.348	0.014
105	244	8.418	8.483	0.065
105	246	8.370	8.437	0.067
105	248	8.362	8.394	0.032
105	374	8.400	8.552	0.152
105	376	8.331	8.475	0.144
105	375	8.534	8.552	0.152
105	Average	8.377	8.351	-0.027
105	Min	8.294	8.184	-0.250
105	Std Dev	0.048	0.077	0.074



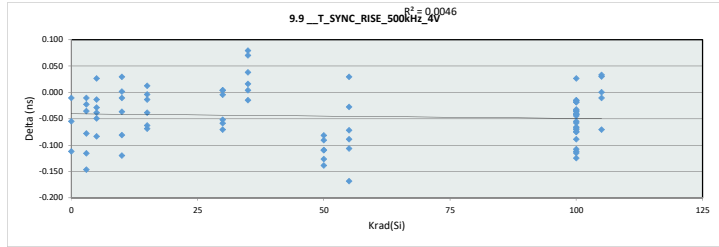
9.5_T_SYNC_RISE											
Test Site											
Tester											
Test Number											
Max Limit	15										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.307	8.298	8.274	8.273	8.306	8.279	8.336	8.184	8.244	8.247	8.394
Average	8.341	8.331	8.362	8.367	8.376	8.384	8.437	8.238	8.303	8.330	8.468
Max	8.358	8.380	8.478	8.410	8.493	8.449	8.534	8.280	8.375	8.511	8.552
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



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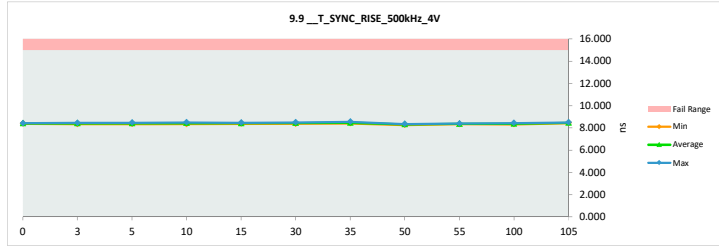
9.9_T_SYNC_RISE_500kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.513	8.401	-0.112
0	118	8.379	8.368	-0.011
0	122	8.418	8.363	-0.055
3	61	8.543	8.427	-0.116
3	62	8.449	8.426	-0.023
3	63	8.419	8.383	-0.036
3	64	8.392	8.314	-0.078
3	65	8.462	8.315	-0.147
3	66	8.398	8.387	-0.011
5	67	8.378	8.404	0.026
5	68	8.488	8.438	-0.050
5	69	8.488	8.449	-0.039
5	70	8.336	8.322	-0.014
5	71	8.439	8.410	-0.029
5	72	8.443	8.359	-0.084
10	73	8.543	8.423	-0.120
10	74	8.449	8.438	-0.011
10	75	8.419	8.382	-0.037
10	76	8.392	8.311	-0.081
10	77	8.462	8.463	0.001
10	78	8.398	8.427	0.029
15	79	8.378	8.374	-0.004
15	80	8.488	8.449	-0.039
15	81	8.488	8.419	-0.069
15	82	8.336	8.348	0.012
15	83	8.439	8.376	-0.063
15	84	8.443	8.429	-0.014
30	86	8.543	8.472	-0.071
30	89	8.449	8.390	-0.059
30	91	8.419	8.414	-0.005
30	92	8.392	8.340	-0.052
30	94	8.462	8.466	0.004
30	95	8.398	8.402	0.004
35	100	8.378	8.394	0.016
35	101	8.488	8.526	0.038
35	102	8.488	8.473	-0.015
35	103	8.336	8.415	0.079
35	104	8.439	8.509	0.070
35	105	8.443	8.447	0.004
50	106	8.401	8.319	-0.082
50	107	8.367	8.257	-0.110
50	108	8.415	8.324	-0.091
50	109	8.389	8.250	-0.139
50	110	8.425	8.315	-0.110
50	111	8.419	8.292	-0.127
55	113	8.462	8.355	-0.107
55	114	8.396	8.324	-0.072
55	115	8.460	8.371	-0.089
55	116	8.483	8.314	-0.169
55	126	8.300	8.329	0.029
55	128	8.375	8.347	-0.028
100	129	8.393	8.322	-0.071
100	131	8.413	8.347	-0.066
100	132	8.431	8.316	-0.115
100	133	8.421	8.380	-0.041
100	134	8.371	8.316	-0.055
100	135	8.433	8.365	-0.068
100	138	8.370	8.351	-0.019
100	139	8.342	8.307	-0.035
100	140	8.397	8.423	0.026
100	141	8.465	8.376	-0.089
100	142	8.434	8.376	-0.058
100	143	8.452	8.415	-0.037
100	144	8.386	8.311	-0.075
100	220	8.397	8.364	-0.033
100	221	8.420	8.312	-0.108
100	224	8.422	8.380	-0.042
100	225	8.440	8.395	-0.045
100	226	8.500	8.375	-0.125
100	228	8.429	8.413	-0.016
100	232	8.367	8.292	-0.075
100	233	8.397	8.378	-0.019
100	236	8.451	8.339	-0.112
100	238	8.437	8.422	-0.015
100	243	8.408	8.366	-0.042
105	244	8.504	8.433	-0.071
105	246	8.420	8.409	-0.011
105	248	8.443	8.476	0.033
105	374	8.480	8.480	0.000
105	375	8.406	8.436	0.030
105	Max	8.543	8.526	0.017
105	Average	8.427	8.382	-0.045
105	Min	8.300	8.250	-0.169
105	Std Dev	0.049	0.057	0.051



9.9_T_SYNC_RISE	
Test Site	
Tester	
Test Number	
Max Limit	15 ns
Min Limit	ns

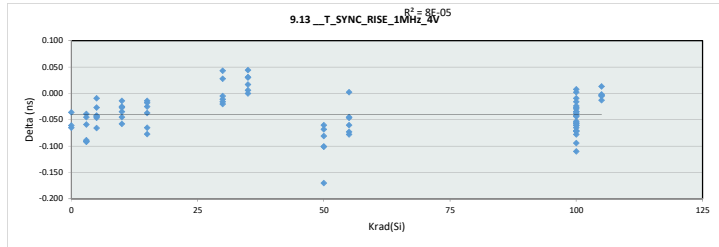
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.363	8.314	8.322	8.311	8.348	8.340	8.394	8.250	8.314	8.292	8.409
Average	8.377	8.375	8.397	8.407	8.399	8.414	8.461	8.293	8.340	8.360	8.447
Max	8.401	8.427	8.449	8.463	8.449	8.472	8.526	8.324	8.371	8.423	8.480
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



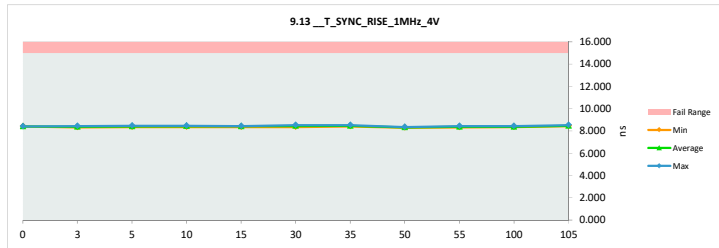
LDR TID Report  
TPS7H5001-SP

9.13 T_SYNC_RISE_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.469	8.407	-0.062
0	118	8.433	8.396	-0.037
0	122	8.461	8.395	-0.066
3	61	8.493	8.401	-0.092
3	62	8.476	8.436	-0.040
3	63	8.457	8.364	-0.093
3	64	8.380	8.320	-0.060
3	65	8.470	8.424	-0.046
3	66	8.460	8.370	-0.090
5	67	8.430	8.387	-0.043
5	68	8.511	8.466	-0.045
5	69	8.460	8.450	-0.010
5	70	8.377	8.349	-0.028
5	71	8.469	8.422	-0.047
5	72	8.447	8.380	-0.067
10	73	8.493	8.467	-0.026
10	74	8.476	8.430	-0.046
10	75	8.457	8.429	-0.028
10	76	8.380	8.344	-0.036
10	77	8.470	8.411	-0.059
10	78	8.460	8.445	-0.015
15	79	8.430	8.352	-0.078
15	80	8.511	8.445	-0.066
15	81	8.460	8.445	-0.015
15	82	8.377	8.351	-0.026
15	83	8.469	8.431	-0.038
15	84	8.447	8.428	-0.019
30	86	8.493	8.487	-0.006
30	89	8.476	8.464	-0.012
30	91	8.457	8.484	0.027
30	92	8.380	8.359	-0.021
30	94	8.470	8.512	0.042
30	95	8.460	8.443	-0.017
35	100	8.430	8.446	0.016
35	101	8.511	8.510	-0.001
35	102	8.460	8.465	0.005
35	103	8.377	8.406	0.029
35	104	8.469	8.512	0.043
35	105	8.447	8.477	0.030
50	106	8.437	8.335	-0.102
50	107	8.375	8.314	-0.061
50	108	8.419	8.350	-0.069
50	109	8.393	8.291	-0.102
50	110	8.466	8.295	-0.171
50	111	8.415	8.333	-0.082
55	113	8.440	8.366	-0.074
55	114	8.437	8.391	-0.046
55	115	8.490	8.411	-0.079
55	116	8.469	8.422	-0.047
55	126	8.324	8.325	0.001
55	128	8.447	8.386	-0.061
100	129	8.408	8.365	-0.043
100	131	8.459	8.402	-0.057
100	132	8.363	8.364	0.001
100	133	8.436	8.391	-0.045
100	134	8.372	8.342	-0.030
100	135	8.434	8.361	-0.073
100	138	8.374	8.381	0.007
100	139	8.353	8.336	-0.017
100	140	8.401	8.365	-0.036
100	141	8.463	8.424	-0.039
100	142	8.444	8.408	-0.036
100	143	8.447	8.381	-0.066
100	144	8.405	8.347	-0.058
100	220	8.465	8.354	-0.111
100	221	8.412	8.333	-0.079
100	224	8.462	8.400	-0.062
100	225	8.455	8.383	-0.072
100	226	8.452	8.442	-0.010
100	228	8.464	8.392	-0.072
100	232	8.387	8.333	-0.054
100	233	8.382	8.354	-0.028
100	236	8.455	8.360	-0.095
100	238	8.448	8.423	-0.025
100	243	8.463	8.402	-0.061
105	244	8.513	8.507	-0.006
105	246	8.428	8.440	0.012
105	248	8.471	8.466	-0.005
105	374	8.475	8.461	-0.014
105	376	8.427	8.424	-0.003
105	Max	8.513	8.512	0.043
105	Average	8.441	8.401	-0.040
105	Min	8.324	8.291	-0.171
105	Std Dev	0.041	0.053	0.038



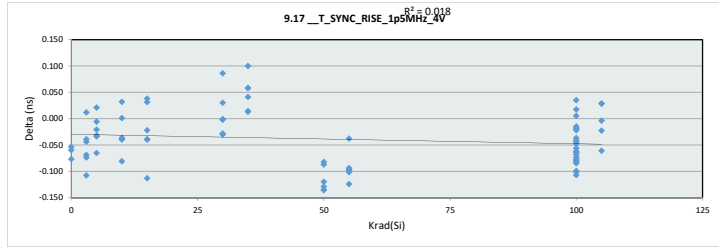
9.13 T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.395	8.320	8.349	8.344	8.351	8.359	8.406	8.291	8.325	8.333	8.424
Average	8.399	8.386	8.409	8.421	8.409	8.458	8.469	8.320	8.384	8.377	8.460
Max	8.407	8.436	8.466	8.467	8.445	8.512	8.512	8.350	8.422	8.442	8.507
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



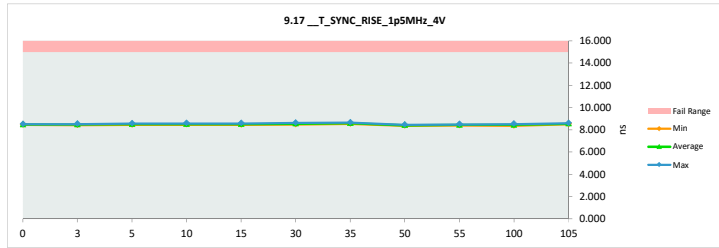
LDR TID Report  
TPS7H5001-SP

9.17_T_SYNC_RISE_1p5MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.537	8.460	-0.077
0	118	8.504	8.444	-0.060
0	122	8.566	8.512	-0.054
3	61	8.617	8.509	-0.108
3	62	8.594	8.520	-0.074
3	63	8.518	8.474	-0.044
3	64	8.501	8.432	-0.069
3	65	8.528	8.489	-0.039
3	66	8.494	8.506	0.012
5	67	8.449	8.470	0.021
5	68	8.639	8.574	-0.065
5	69	8.567	8.533	-0.034
5	70	8.479	8.447	-0.032
5	71	8.541	8.520	-0.021
5	72	8.520	8.514	-0.006
10	73	8.617	8.580	-0.037
10	74	8.594	8.513	-0.081
10	75	8.518	8.519	0.001
10	76	8.501	8.463	-0.038
10	77	8.528	8.488	-0.040
10	78	8.494	8.526	0.032
15	79	8.449	8.480	0.031
15	80	8.639	8.526	-0.113
15	81	8.567	8.528	-0.039
15	82	8.479	8.439	-0.040
15	83	8.541	8.579	0.038
15	84	8.520	8.498	-0.022
30	86	8.617	8.615	-0.002
30	89	8.594	8.564	-0.030
30	91	8.518	8.548	0.030
30	92	8.501	8.473	-0.028
30	94	8.528	8.527	-0.001
30	95	8.494	8.580	0.086
35	100	8.449	8.549	0.100
35	101	8.639	8.652	0.013
35	102	8.567	8.581	0.014
35	103	8.479	8.520	0.041
35	104	8.541	8.599	0.058
35	105	8.520	8.578	0.058
50	106	8.553	8.417	-0.136
50	107	8.435	8.353	-0.082
50	108	8.513	8.426	-0.087
50	109	8.529	8.400	-0.129
50	110	8.571	8.436	-0.135
50	111	8.513	8.393	-0.120
55	113	8.519	8.425	-0.094
55	114	8.557	8.455	-0.102
55	115	8.521	8.423	-0.098
55	116	8.595	8.471	-0.124
55	126	8.489	8.392	-0.097
55	128	8.532	8.494	-0.038
100	129	8.513	8.412	-0.101
100	131	8.552	8.490	-0.062
100	132	8.488	8.466	-0.022
100	133	8.543	8.461	-0.082
100	134	8.487	8.469	-0.018
100	135	8.515	8.435	-0.080
100	138	8.453	8.412	-0.041
100	139	8.446	8.378	-0.068
100	140	8.492	8.455	-0.037
100	141	8.543	8.436	-0.107
100	142	8.534	8.470	-0.064
100	143	8.529	8.509	-0.020
100	144	8.478	8.463	-0.015
100	220	8.518	8.433	-0.085
100	221	8.480	8.381	-0.099
100	224	8.474	8.509	0.035
100	225	8.565	8.491	-0.074
100	226	8.557	8.513	-0.044
100	228	8.504	8.521	0.017
100	232	8.420	8.372	-0.048
100	233	8.484	8.489	0.005
100	236	8.544	8.471	-0.073
100	238	8.528	8.472	-0.056
100	243	8.546	8.468	-0.078
105	244	8.628	8.567	-0.061
105	246	8.516	8.544	0.028
105	248	8.566	8.543	-0.023
105	374	8.574	8.603	0.029
105	376	8.574	8.511	-0.064
105	Max	8.639	8.652	0.100
105	Average	8.528	8.489	-0.039
105	Min	8.420	8.353	-0.136
105	Std Dev	0.049	0.062	0.053



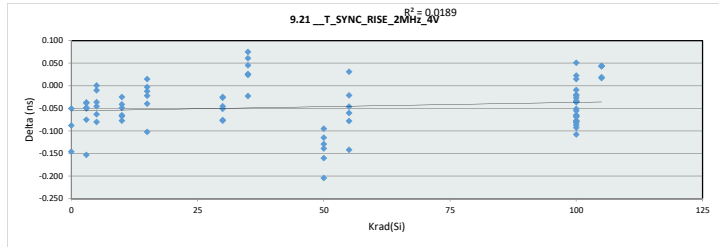
9.17_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.444	8.432	8.447	8.463	8.439	8.473	8.520	8.353	8.392	8.372	8.511
Average	8.472	8.488	8.510	8.515	8.508	8.551	8.580	8.404	8.443	8.457	8.554
Max	8.512	8.520	8.574	8.580	8.579	8.615	8.652	8.436	8.494	8.521	8.603
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



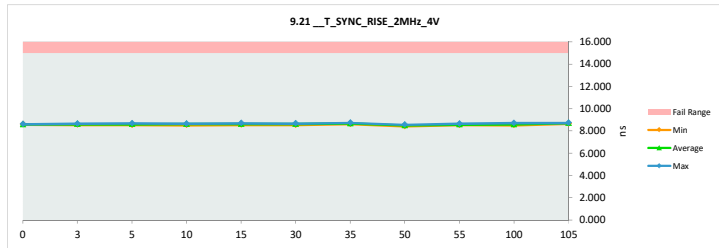
LDR TID Report  
TPS7H5001-SP

9.21_T_SYNC_RISE_2MHZ_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.712	8.565	-0.147
0	118	8.650	8.561	-0.089
0	122	8.651	8.600	-0.051
3	61	8.722	8.646	-0.076
3	62	8.663	8.614	-0.049
3	63	8.699	8.545	-0.154
3	64	8.578	8.539	-0.039
3	65	8.643	8.592	-0.051
3	66	8.664	8.626	-0.038
5	67	8.577	8.566	-0.011
5	68	8.694	8.613	-0.081
5	69	8.673	8.673	0.000
5	70	8.558	8.521	-0.037
5	71	8.689	8.625	-0.064
5	72	8.637	8.591	-0.046
10	73	8.722	8.644	-0.078
10	74	8.663	8.613	-0.050
10	75	8.699	8.633	-0.066
10	76	8.578	8.510	-0.068
10	77	8.643	8.601	-0.042
10	78	8.664	8.638	-0.026
15	79	8.577	8.564	-0.013
15	80	8.694	8.653	-0.041
15	81	8.673	8.687	0.014
15	82	8.558	8.535	-0.023
15	83	8.689	8.586	-0.103
15	84	8.637	8.633	-0.004
30	86	8.722	8.644	-0.078
30	89	8.663	8.586	-0.077
30	91	8.699	8.653	-0.046
30	92	8.578	8.526	-0.052
30	94	8.643	8.616	-0.027
30	95	8.664	8.638	-0.026
35	100	8.577	8.651	0.074
35	101	8.694	8.717	0.023
35	102	8.673	8.717	0.044
35	103	8.558	8.618	0.060
35	104	8.689	8.665	-0.024
35	105	8.637	8.662	0.025
50	106	8.627	8.531	-0.096
50	107	8.553	8.413	-0.140
50	108	8.721	8.516	-0.205
50	109	8.591	8.430	-0.161
50	110	8.676	8.546	-0.130
50	111	8.657	8.541	-0.116
55	113	8.663	8.520	-0.143
55	114	8.645	8.584	-0.061
55	115	8.646	8.567	-0.079
55	116	8.689	8.642	-0.047
55	126	8.489	8.519	0.030
55	128	8.597	8.575	-0.022
100	129	8.580	8.545	-0.035
100	131	8.644	8.556	-0.088
100	132	8.588	8.505	-0.083
100	133	8.654	8.584	-0.070
100	134	8.615	8.537	-0.078
100	135	8.615	8.535	-0.080
100	138	8.575	8.565	-0.010
100	139	8.579	8.551	-0.028
100	140	8.625	8.588	-0.037
100	141	8.642	8.576	-0.066
100	142	8.655	8.586	-0.069
100	143	8.627	8.677	0.050
100	144	8.582	8.561	-0.021
100	220	8.638	8.559	-0.079
100	221	8.588	8.536	-0.052
100	224	8.581	8.546	-0.035
100	225	8.629	8.643	0.014
100	226	8.679	8.623	-0.056
100	228	8.698	8.604	-0.094
100	232	8.576	8.540	-0.036
100	233	8.627	8.561	-0.066
100	236	8.709	8.600	-0.109
100	238	8.673	8.695	0.022
100	243	8.631	8.608	-0.023
105	244	8.675	8.691	0.016
105	246	8.631	8.673	0.042
105	248	8.673	8.716	0.043
105	374	8.695	8.713	0.018
105	376	8.595	8.638	0.043
105	Max	8.722	8.717	0.074
105	Average	8.640	8.595	-0.046
105	Min	8.489	8.413	-0.205
105	Std Dev	0.050	0.062	0.055



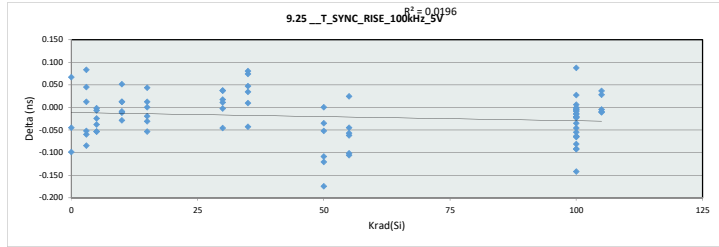
9.21_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.561	8.539	8.521	8.510	8.535	8.526	8.618	8.413	8.519	8.505	8.638
Average	8.575	8.594	8.598	8.607	8.610	8.611	8.672	8.496	8.568	8.578	8.686
Max	8.600	8.646	8.673	8.644	8.687	8.653	8.717	8.546	8.642	8.695	8.716
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



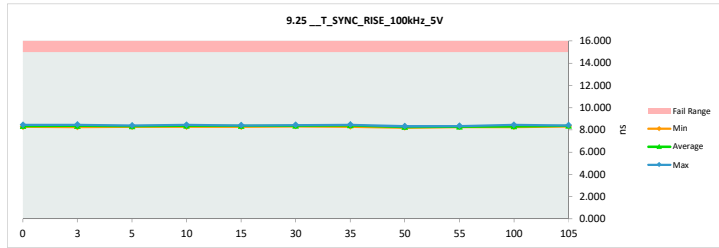
LDR TID Report  
TPS7H5001-SP

9.25_T_SYNC_RISE_100kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.360	8.427	0.067
0	118	8.314	8.269	-0.045
0	122	8.357	8.258	-0.099
3	61	8.374	8.386	0.012
3	62	8.322	8.367	0.045
3	63	8.355	8.438	0.083
3	64	8.283	8.223	-0.060
3	65	8.394	8.309	-0.085
3	66	8.360	8.308	-0.052
5	67	8.293	8.268	-0.025
5	68	8.376	8.369	-0.007
5	69	8.371	8.369	-0.002
5	70	8.307	8.253	-0.054
5	71	8.388	8.334	-0.054
5	72	8.337	8.299	-0.038
10	73	8.374	8.425	0.051
10	74	8.322	8.334	0.012
10	75	8.355	8.343	-0.012
10	76	8.283	8.254	-0.029
10	77	8.394	8.385	-0.009
10	78	8.360	8.372	0.012
15	79	8.293	8.336	0.043
15	80	8.376	8.376	0.000
15	81	8.371	8.383	0.012
15	82	8.307	8.253	-0.054
15	83	8.388	8.357	-0.031
15	84	8.337	8.317	-0.020
30	86	8.374	8.411	0.037
30	89	8.322	8.319	-0.003
30	91	8.355	8.309	-0.046
30	92	8.283	8.300	0.017
30	94	8.394	8.405	0.011
30	95	8.360	8.397	0.037
35	100	8.293	8.373	0.080
35	101	8.376	8.410	0.034
35	102	8.371	8.445	0.074
35	103	8.307	8.264	-0.043
35	104	8.388	8.397	0.009
35	105	8.337	8.384	0.047
50	106	8.380	8.205	-0.175
50	107	8.248	8.213	-0.035
50	108	8.368	8.316	-0.052
50	109	8.308	8.187	-0.121
50	110	8.392	8.283	-0.109
50	111	8.279	8.279	0.000
55	113	8.347	8.241	-0.106
55	114	8.339	8.237	-0.102
55	115	8.348	8.303	-0.045
55	116	8.381	8.324	-0.057
55	126	8.226	8.250	0.024
55	128	8.359	8.297	-0.062
100	129	8.309	8.245	-0.064
100	131	8.341	8.319	-0.022
100	132	8.268	8.247	-0.021
100	133	8.369	8.303	-0.066
100	134	8.294	8.321	0.027
100	135	8.320	8.326	0.006
100	138	8.337	8.322	-0.015
100	139	8.257	8.251	-0.006
100	140	8.341	8.260	-0.081
100	141	8.403	8.311	-0.092
100	142	8.319	8.304	-0.015
100	143	8.389	8.296	-0.093
100	144	8.293	8.270	-0.023
100	220	8.323	8.268	-0.055
100	221	8.273	8.238	-0.035
100	224	8.448	8.306	-0.142
100	225	8.371	8.350	-0.021
100	226	8.339	8.426	0.087
100	228	8.326	8.317	-0.009
100	232	8.298	8.234	-0.064
100	233	8.292	8.291	-0.001
100	236	8.362	8.316	-0.046
100	238	8.325	8.320	-0.005
100	243	8.334	8.333	-0.001
105	244	8.383	8.378	-0.005
105	246	8.325	8.315	-0.010
105	248	8.376	8.365	-0.011
105	374	8.346	8.382	0.036
105	376	8.315	8.343	0.028
105	Max	8.448	8.445	0.087
105	Average	8.340	8.319	-0.021
105	Min	8.226	8.187	-0.175
105	Std Dev	0.041	0.060	0.052



9.25_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.258	8.223	8.253	8.254	8.253	8.300	8.264	8.187	8.237	8.234	8.315
Average	8.318	8.339	8.315	8.352	8.337	8.357	8.379	8.247	8.275	8.299	8.357
Max	8.427	8.438	8.369	8.425	8.383	8.411	8.445	8.316	8.324	8.426	8.382
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

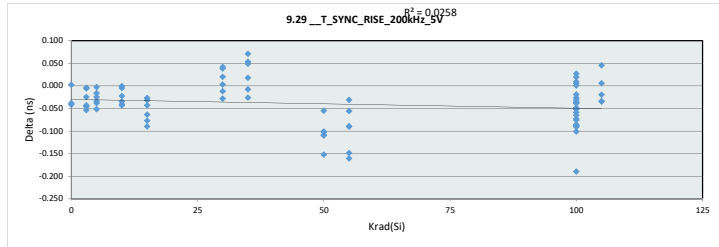




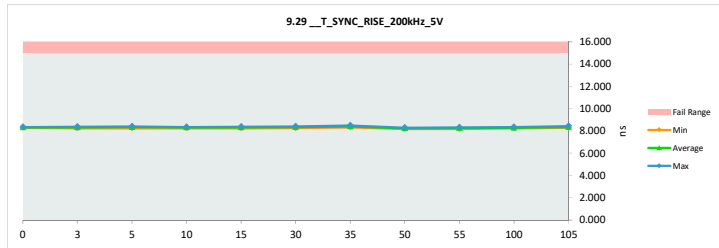
LDR TID Report  
TPS7H5001-SP

9.29_T_SYNC_RISE_200kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.331	8.332	0.001
0	118	8.352	8.309	-0.043
0	122	8.323	8.284	-0.039
3	61	8.372	8.365	-0.007
3	62	8.370	8.326	-0.044
3	63	8.370	8.315	-0.055
3	64	8.272	8.225	-0.047
3	65	8.333	8.307	-0.026
3	66	8.348	8.343	-0.005
5	67	8.334	8.317	-0.017
5	68	8.408	8.404	-0.004
5	69	8.415	8.376	-0.039
5	70	8.262	8.236	-0.026
5	71	8.375	8.322	-0.053
5	72	8.407	8.372	-0.035
10	73	8.372	8.336	-0.036
10	74	8.370	8.328	-0.042
10	75	8.370	8.346	-0.024
10	76	8.272	8.228	-0.044
10	77	8.333	8.331	-0.002
10	78	8.348	8.342	-0.006
15	79	8.334	8.290	-0.044
15	80	8.408	8.375	-0.033
15	81	8.415	8.337	-0.078
15	82	8.262	8.234	-0.028
15	83	8.375	8.310	-0.065
15	84	8.407	8.316	-0.091
30	86	8.372	8.413	0.041
30	89	8.370	8.357	-0.013
30	91	8.370	8.372	0.002
30	92	8.272	8.243	-0.029
30	94	8.333	8.370	0.037
30	95	8.348	8.367	0.019
35	100	8.334	8.351	0.017
35	101	8.408	8.478	0.070
35	102	8.415	8.406	-0.009
35	103	8.262	8.310	0.048
35	104	8.375	8.427	0.052
35	105	8.407	8.380	-0.027
50	106	8.373	8.220	-0.153
50	107	8.280	8.170	-0.110
50	108	8.360	8.249	-0.111
50	109	8.265	8.209	-0.056
50	110	8.385	8.281	-0.104
50	111	8.351	8.249	-0.102
55	113	8.392	8.242	-0.150
55	114	8.357	8.300	-0.057
55	115	8.391	8.229	-0.162
55	116	8.395	8.304	-0.091
55	126	8.269	8.178	-0.091
55	128	8.358	8.326	-0.032
100	129	8.409	8.218	-0.191
100	131	8.366	8.313	-0.053
100	132	8.303	8.266	-0.037
100	133	8.362	8.302	-0.060
100	134	8.309	8.282	-0.027
100	135	8.345	8.256	-0.089
100	138	8.316	8.284	-0.032
100	139	8.253	8.271	0.018
100	140	8.317	8.251	-0.066
100	141	8.395	8.318	-0.077
100	142	8.360	8.258	-0.102
100	143	8.344	8.349	0.005
100	144	8.310	8.290	-0.020
100	220	8.328	8.354	0.026
100	221	8.262	8.262	0.000
100	224	8.363	8.289	-0.074
100	225	8.392	8.355	-0.037
100	226	8.404	8.353	-0.051
100	228	8.379	8.292	-0.087
100	232	8.308	8.216	-0.092
100	233	8.305	8.313	0.008
100	236	8.359	8.309	-0.050
100	238	8.410	8.319	-0.091
100	243	8.343	8.303	-0.040
105	244	8.380	8.424	0.044
105	246	8.319	8.298	-0.021
105	248	8.370	8.375	0.005
105	374	8.372	8.336	-0.036
105	376	8.360	8.325	-0.035
105	Max	8.415	8.478	0.070
105	Average	8.350	8.310	-0.040
105	Min	8.253	8.170	-0.191
105	Std Dev	0.044	0.059	0.049



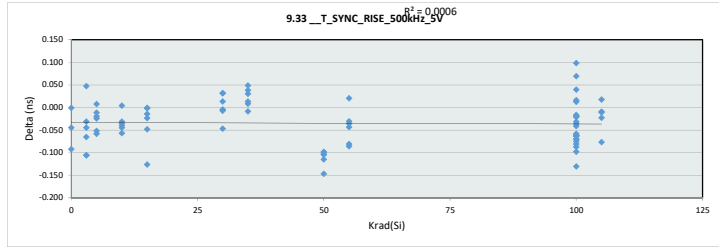
9.29_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.284	8.225	8.236	8.228	8.234	8.243	8.310	8.170	8.178	8.216	8.298
Average	8.308	8.314	8.338	8.319	8.310	8.354	8.392	8.230	8.263	8.293	8.352
Max	8.332	8.365	8.404	8.346	8.375	8.413	8.478	8.281	8.326	8.355	8.424
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



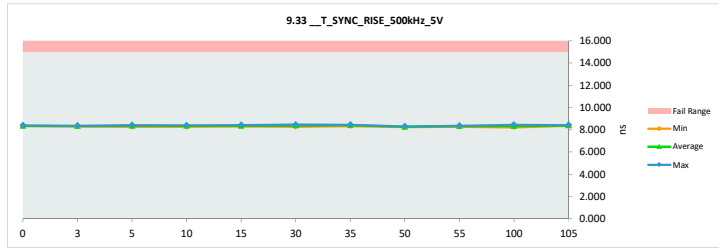
LDR TID Report  
TPS7H5001-SP

9.33 T_SYNC_RISE_500kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.419	8.374	-0.045
0	118	8.395	8.394	-0.001
0	122	8.425	8.332	-0.093
3	61	8.432	8.366	-0.066
3	62	8.395	8.350	-0.045
3	63	8.368	8.336	-0.032
3	64	8.273	8.320	0.047
3	65	8.423	8.317	-0.106
3	66	8.429	8.323	-0.106
5	67	8.357	8.332	-0.025
5	68	8.439	8.427	-0.012
5	69	8.449	8.390	-0.059
5	70	8.295	8.275	-0.020
5	71	8.434	8.382	-0.052
5	72	8.377	8.384	0.007
10	73	8.432	8.400	-0.032
10	74	8.395	8.360	-0.035
10	75	8.368	8.323	-0.045
10	76	8.273	8.277	0.004
10	77	8.423	8.383	-0.040
10	78	8.429	8.372	-0.057
15	79	8.357	8.355	-0.002
15	80	8.439	8.390	-0.049
15	81	8.449	8.322	-0.127
15	82	8.295	8.294	-0.001
15	83	8.434	8.419	-0.015
15	84	8.377	8.353	-0.024
30	86	8.432	8.445	0.013
30	89	8.395	8.387	-0.008
30	91	8.368	8.399	0.031
30	92	8.273	8.268	-0.005
30	94	8.423	8.376	-0.047
30	95	8.429	8.460	0.031
35	100	8.357	8.395	0.038
35	101	8.439	8.452	0.013
35	102	8.449	8.440	-0.009
35	103	8.295	8.325	0.030
35	104	8.434	8.442	0.008
35	105	8.377	8.425	0.048
50	106	8.357	8.253	-0.104
50	107	8.334	8.235	-0.099
50	108	8.376	8.277	-0.099
50	109	8.337	8.232	-0.105
50	110	8.442	8.295	-0.147
50	111	8.406	8.291	-0.115
55	113	8.428	8.347	-0.081
55	114	8.369	8.333	-0.036
55	115	8.348	8.368	0.020
55	116	8.407	8.321	-0.086
55	126	8.327	8.283	-0.044
55	128	8.353	8.322	-0.031
100	129	8.314	8.326	0.012
100	131	8.372	8.330	-0.042
100	132	8.334	8.313	-0.021
100	133	8.347	8.445	0.098
100	134	8.332	8.261	-0.071
100	135	8.426	8.328	-0.098
100	138	8.331	8.311	-0.020
100	139	8.331	8.268	-0.063
100	140	8.358	8.326	-0.032
100	141	8.374	8.303	-0.071
100	142	8.399	8.318	-0.081
100	143	8.426	8.367	-0.059
100	144	8.321	8.304	-0.017
100	220	8.347	8.386	0.039
100	221	8.344	8.256	-0.088
100	224	8.303	8.372	0.069
100	225	8.398	8.377	-0.021
100	226	8.400	8.416	0.016
100	228	8.416	8.354	-0.062
100	232	8.298	8.234	-0.064
100	233	8.366	8.290	-0.076
100	236	8.398	8.361	-0.037
100	238	8.411	8.280	-0.131
100	243	8.421	8.359	-0.062
105	244	8.431	8.408	-0.023
105	246	8.447	8.370	-0.077
105	248	8.371	8.388	0.017
105	374	8.407	8.397	-0.010
105	376	8.405	8.402	-0.011
105	Max	8.449	8.460	0.098
105	Average	8.382	8.347	-0.035
105	Min	8.273	8.232	-0.147
105	Std Dev	0.048	0.056	0.049



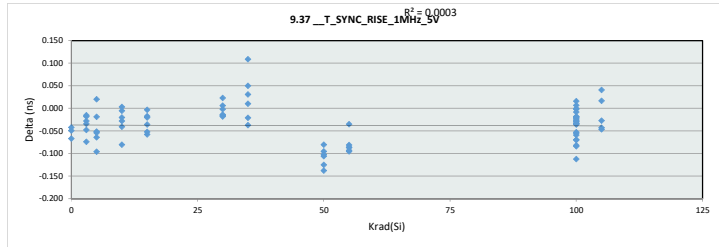
9.33 T_SYNC_R											
Test Site											
Tester											
Test Number											
Min Limit	15 ns										
Max Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.332	8.317	8.275	8.277	8.294	8.268	8.325	8.232	8.283	8.234	8.370
Average	8.367	8.335	8.365	8.353	8.356	8.389	8.413	8.264	8.329	8.329	8.393
Max	8.394	8.366	8.427	8.400	8.419	8.460	8.452	8.295	8.368	8.445	8.408
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



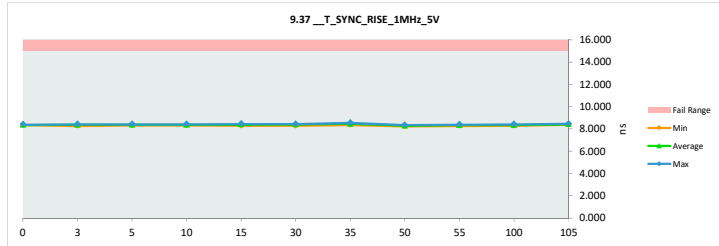
LDR TID Report  
TPS7H5001-SP

9.37_T_SYNC_RISE_1MHZ_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.443	8.393	-0.050
0	118	8.416	8.348	-0.068
0	122	8.411	8.368	-0.043
3	61	8.435	8.419	-0.016
3	62	8.425	8.376	-0.049
3	63	8.444	8.369	-0.075
3	64	8.312	8.283	-0.029
3	65	8.413	8.378	-0.035
3	66	8.427	8.408	-0.019
5	67	8.367	8.386	0.019
5	68	8.484	8.419	-0.065
5	69	8.474	8.419	-0.055
5	70	8.338	8.318	-0.020
5	71	8.484	8.387	-0.097
5	72	8.424	8.372	-0.052
10	73	8.435	8.406	-0.029
10	74	8.425	8.404	-0.021
10	75	8.444	8.363	-0.081
10	76	8.312	8.314	0.002
10	77	8.413	8.371	-0.042
10	78	8.427	8.421	-0.006
15	79	8.367	8.363	-0.004
15	80	8.484	8.431	-0.053
15	81	8.474	8.456	-0.018
15	82	8.338	8.301	-0.037
15	83	8.484	8.425	-0.059
15	84	8.424	8.403	-0.021
30	86	8.435	8.440	0.005
30	89	8.425	8.422	-0.003
30	91	8.444	8.429	-0.015
30	92	8.312	8.293	-0.019
30	94	8.413	8.435	0.022
30	95	8.427	8.410	-0.017
35	100	8.367	8.475	0.108
35	101	8.484	8.533	0.049
35	102	8.474	8.436	-0.038
35	103	8.338	8.368	0.030
35	104	8.484	8.462	-0.022
35	105	8.424	8.433	0.009
50	106	8.425	8.286	-0.139
50	107	8.358	8.251	-0.107
50	108	8.420	8.294	-0.126
50	109	8.313	8.232	-0.081
50	110	8.433	8.337	-0.096
50	111	8.431	8.328	-0.103
55	113	8.436	8.348	-0.088
55	114	8.444	8.357	-0.087
55	115	8.466	8.370	-0.096
55	116	8.428	8.392	-0.036
55	126	8.372	8.278	-0.094
55	128	8.424	8.342	-0.082
100	129	8.343	8.306	-0.037
100	131	8.425	8.371	-0.054
100	132	8.316	8.296	-0.020
100	133	8.383	8.382	-0.001
100	134	8.384	8.313	-0.071
100	135	8.375	8.390	0.015
100	138	8.343	8.324	-0.019
100	139	8.377	8.321	-0.056
100	140	8.399	8.286	-0.113
100	141	8.397	8.388	-0.009
100	142	8.401	8.372	-0.029
100	143	8.406	8.379	-0.027
100	144	8.365	8.331	-0.034
100	220	8.384	8.324	-0.060
100	221	8.361	8.326	-0.035
100	224	8.363	8.369	0.006
100	225	8.419	8.395	-0.024
100	226	8.447	8.417	-0.030
100	228	8.439	8.418	-0.021
100	232	8.341	8.338	-0.003
100	233	8.387	8.353	-0.034
100	236	8.429	8.346	-0.083
100	238	8.439	8.354	-0.085
100	243	8.427	8.356	-0.071
105	244	8.438	8.410	-0.028
105	246	8.470	8.426	-0.044
105	248	8.440	8.456	0.016
105	374	8.418	8.458	0.040
105	376	8.420	8.373	-0.047
105	375	8.484	8.533	0.108
Max		8.410	8.373	-0.038
Average		8.410	8.373	-0.038
Min		8.312	8.232	-0.139
Std Dev		0.046	0.055	0.042



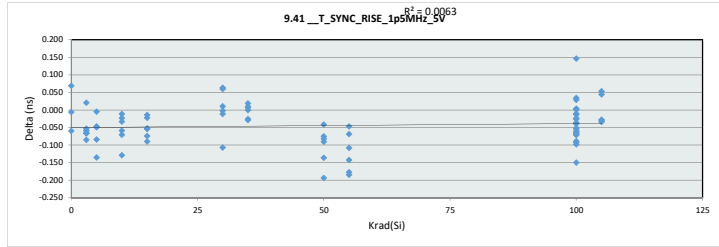
9.37_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.348	8.283	8.318	8.314	8.301	8.293	8.368	8.232	8.278	8.286	8.373
Average	8.370	8.372	8.384	8.380	8.397	8.405	8.451	8.288	8.348	8.352	8.425
Max	8.393	8.419	8.419	8.421	8.456	8.440	8.533	8.337	8.392	8.418	8.458
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



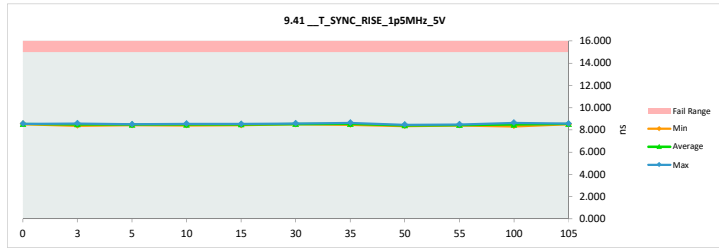
LDR TID Report  
TPS7H5001-SP

9.41 T_SYNC_RISE_1p5MHz_5V			
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.563	8.503	-0.060
0	118	8.501	8.495	-0.006
0	122	8.456	8.524	0.068
3	61	8.538	8.558	0.020
3	62	8.598	8.539	-0.059
3	63	8.493	8.407	-0.086
3	64	8.422	8.356	-0.066
3	65	8.492	8.425	-0.067
3	66	8.544	8.490	-0.054
5	67	8.462	8.457	-0.005
5	68	8.606	8.470	-0.136
5	69	8.531	8.482	-0.049
5	70	8.447	8.398	-0.049
5	71	8.560	8.475	-0.085
5	72	8.495	8.448	-0.047
10	73	8.538	8.526	-0.012
10	74	8.598	8.469	-0.129
10	75	8.493	8.434	-0.059
10	76	8.422	8.388	-0.034
10	77	8.492	8.468	-0.024
10	78	8.544	8.473	-0.071
15	79	8.462	8.439	-0.023
15	80	8.606	8.516	-0.090
15	81	8.531	8.476	-0.055
15	82	8.447	8.394	-0.053
15	83	8.560	8.485	-0.075
15	84	8.495	8.480	-0.015
30	86	8.538	8.548	0.010
30	89	8.598	8.491	-0.107
30	91	8.493	8.490	-0.003
30	92	8.422	8.485	0.063
30	94	8.492	8.551	0.059
30	95	8.544	8.532	-0.012
35	100	8.462	8.436	-0.026
35	101	8.606	8.612	0.006
35	102	8.531	8.540	0.009
35	103	8.447	8.465	0.018
35	104	8.560	8.531	-0.029
35	105	8.495	8.494	-0.001
50	106	8.553	8.359	-0.194
50	107	8.412	8.329	-0.083
50	108	8.534	8.443	-0.091
50	109	8.491	8.354	-0.137
50	110	8.451	8.409	-0.042
50	111	8.475	8.399	-0.076
55	113	8.562	8.377	-0.185
55	114	8.489	8.381	-0.108
55	115	8.576	8.433	-0.143
55	116	8.509	8.440	-0.069
55	126	8.568	8.390	-0.178
55	128	8.519	8.472	-0.047
100	129	8.456	8.432	-0.024
100	131	8.544	8.446	-0.098
100	132	8.468	8.318	-0.150
100	133	8.472	8.475	0.003
100	134	8.395	8.429	0.034
100	135	8.485	8.423	-0.062
100	138	8.456	8.430	-0.026
100	139	8.460	8.408	-0.052
100	140	8.476	8.421	-0.055
100	141	8.554	8.491	-0.063
100	142	8.464	8.610	0.146
100	143	8.489	8.453	-0.036
100	144	8.485	8.396	-0.089
100	220	8.466	8.452	-0.014
100	221	8.501	8.408	-0.093
100	224	8.483	8.486	0.003
100	225	8.543	8.476	-0.067
100	226	8.530	8.490	-0.040
100	228	8.523	8.454	-0.069
100	232	8.411	8.439	0.028
100	233	8.434	8.423	-0.011
100	236	8.530	8.468	-0.062
100	238	8.579	8.507	-0.072
100	243	8.465	8.465	0.000
105	244	8.525	8.494	-0.031
105	246	8.509	8.481	-0.028
105	248	8.564	8.529	-0.035
105	374	8.473	8.526	0.053
105	376	8.496	8.540	0.044
	Max	8.606	8.612	0.146
	Average	8.506	8.461	-0.044
	Min	8.395	8.318	-0.194
	Std Dev	0.051	0.058	0.059



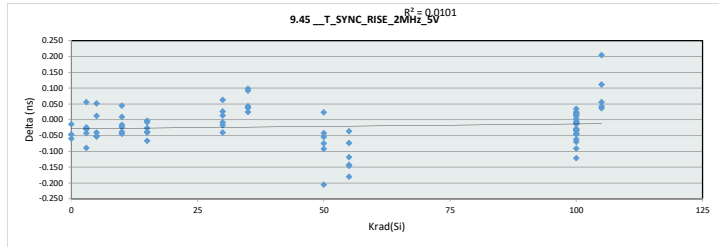
9.41 T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.495	8.356	8.398	8.388	8.394	8.485	8.436	8.329	8.377	8.318	8.481
Average	8.507	8.463	8.455	8.460	8.465	8.516	8.513	8.382	8.416	8.450	8.514
Max	8.524	8.558	8.482	8.526	8.516	8.551	8.612	8.443	8.472	8.610	8.540
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



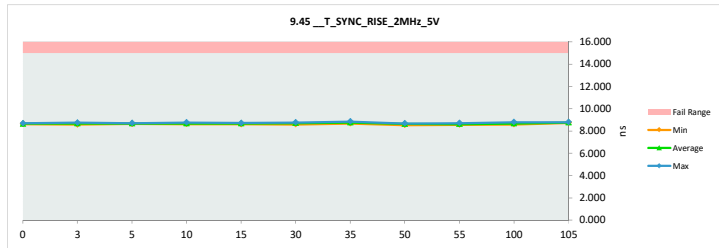
LDR TID Report  
TPS7H5001-SP

9.45_T_SYNC_RISE_2MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.672	8.611	-0.061
0	118	8.720	8.672	-0.048
0	122	8.659	8.643	-0.016
3	61	8.707	8.681	-0.026
3	62	8.676	8.730	0.054
3	63	8.665	8.574	-0.091
3	64	8.600	8.556	-0.044
3	65	8.701	8.670	-0.031
3	66	8.734	8.703	-0.031
5	67	8.655	8.613	-0.042
5	68	8.748	8.695	-0.053
5	69	8.716	8.662	-0.054
5	70	8.626	8.637	0.011
5	71	8.711	8.657	-0.054
5	72	8.646	8.696	0.050
10	73	8.707	8.750	0.043
10	74	8.676	8.636	-0.040
10	75	8.665	8.619	-0.046
10	76	8.600	8.608	0.008
10	77	8.701	8.683	-0.018
10	78	8.734	8.709	-0.025
15	79	8.655	8.613	-0.042
15	80	8.748	8.680	-0.068
15	81	8.716	8.688	-0.028
15	82	8.626	8.586	-0.040
15	83	8.711	8.701	-0.010
15	84	8.646	8.641	-0.005
30	86	8.707	8.697	-0.010
30	89	8.676	8.737	0.061
30	91	8.665	8.677	0.012
30	92	8.600	8.581	-0.019
30	94	8.701	8.726	0.025
30	95	8.734	8.692	-0.042
35	100	8.655	8.752	0.097
35	101	8.748	8.839	0.091
35	102	8.716	8.757	0.041
35	103	8.626	8.649	0.023
35	104	8.711	8.748	0.037
35	105	8.646	8.682	0.036
50	106	8.686	8.610	-0.076
50	107	8.718	8.511	-0.207
50	108	8.630	8.652	0.022
50	109	8.699	8.643	-0.056
50	110	8.713	8.669	-0.044
50	111	8.703	8.609	-0.094
55	113	8.725	8.687	-0.038
55	114	8.776	8.629	-0.147
55	115	8.752	8.608	-0.144
55	116	8.766	8.584	-0.182
55	126	8.659	8.539	-0.120
55	128	8.671	8.596	-0.075
100	129	8.651	8.669	0.018
100	131	8.701	8.630	-0.071
100	132	8.671	8.608	-0.063
100	133	8.712	8.677	-0.035
100	134	8.642	8.665	0.023
100	135	8.661	8.568	-0.093
100	138	8.665	8.635	-0.030
100	139	8.644	8.630	-0.014
100	140	8.659	8.644	-0.015
100	141	8.693	8.694	0.001
100	142	8.733	8.610	-0.123
100	143	8.680	8.713	0.033
100	144	8.655	8.675	0.020
100	220	8.688	8.675	-0.013
100	221	8.648	8.614	-0.034
100	224	8.653	8.645	-0.008
100	225	8.729	8.716	-0.013
100	226	8.772	8.783	0.011
100	228	8.747	8.739	-0.008
100	232	8.650	8.664	0.014
100	233	8.663	8.617	-0.046
100	236	8.720	8.675	-0.045
100	238	8.715	8.679	-0.036
100	243	8.745	8.698	-0.047
105	244	8.667	8.777	0.110
105	246	8.701	8.755	0.054
105	248	8.702	8.743	0.041
105	374	8.576	8.779	0.203
105	376	8.674	8.709	0.035
105	Max	8.776	8.839	0.203
105	Average	8.687	8.666	-0.021
105	Min	8.576	8.511	-0.207
105	Std Dev	0.043	0.061	0.063



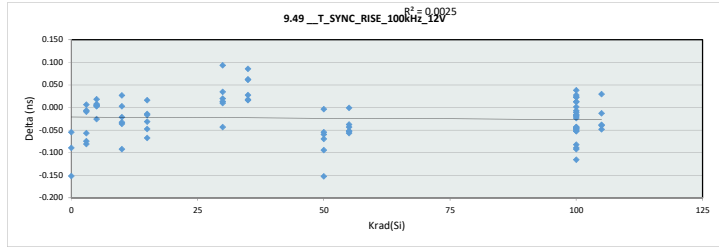
9.45_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.611	8.556	8.613	8.608	8.586	8.581	8.649	8.511	8.539	8.568	8.709
Average	8.642	8.652	8.660	8.668	8.652	8.685	8.738	8.616	8.607	8.663	8.753
Max	8.672	8.730	8.696	8.750	8.701	8.737	8.839	8.669	8.687	8.783	8.779
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



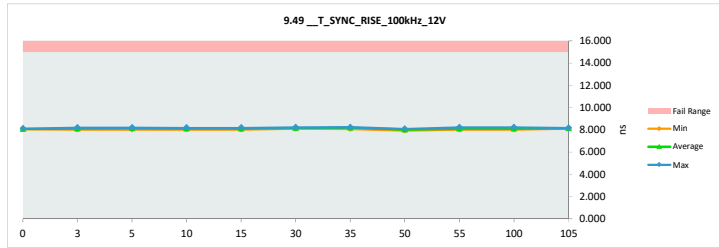
LDR TID Report  
TPS7H5001-SP

9.49_T_SYNC_RISE_100kHz_12V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	15	15
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.254	8.102	-0.152
0	118	8.087	8.032	-0.055
0	122	8.197	8.107	-0.090
3	61	8.168	8.087	-0.081
3	62	8.172	8.162	-0.010
3	63	8.195	8.188	-0.007
3	64	8.077	8.002	-0.075
3	65	8.004	8.010	0.006
3	66	8.193	8.136	-0.057
5	67	8.130	8.134	0.004
5	68	8.188	8.195	0.007
5	69	8.200	8.174	-0.026
5	70	7.998	8.003	0.005
5	71	8.143	8.145	0.002
5	72	8.143	8.161	0.018
10	73	8.168	8.131	-0.037
10	74	8.172	8.174	0.002
10	75	8.195	8.102	-0.093
10	76	8.077	8.055	-0.022
10	77	8.004	8.030	0.026
10	78	8.193	8.160	-0.033
15	79	8.130	8.062	-0.068
15	80	8.188	8.173	-0.015
15	81	8.200	8.168	-0.032
15	82	7.998	8.014	0.016
15	83	8.143	8.126	-0.017
15	84	8.143	8.095	-0.048
30	86	8.168	8.124	-0.044
30	89	8.172	8.206	0.034
30	91	8.195	8.204	0.009
30	92	8.077	8.096	0.019
30	94	8.004	8.097	0.093
30	95	8.193	8.206	0.013
35	100	8.130	8.191	0.061
35	101	8.188	8.215	0.027
35	102	8.200	8.216	0.016
35	103	7.998	8.060	0.062
35	104	8.143	8.228	0.085
35	105	8.143	8.160	0.017
50	106	8.143	8.073	-0.070
50	107	8.083	7.930	-0.153
50	108	8.090	8.030	-0.060
50	109	8.191	8.046	-0.055
50	110	8.032	8.028	-0.004
50	111	8.160	8.065	-0.095
55	113	8.043	8.005	-0.038
55	114	8.191	8.147	-0.044
55	115	8.183	8.129	-0.054
55	116	8.180	8.123	-0.057
55	126	8.076	8.024	-0.052
55	128	8.223	8.222	-0.001
100	129	8.056	8.012	-0.044
100	131	8.219	8.136	-0.083
100	132	8.118	8.110	-0.008
100	133	8.038	8.050	0.012
100	134	8.075	8.097	0.022
100	135	8.191	8.175	-0.016
100	138	8.133	8.086	-0.047
100	139	8.125	8.035	-0.090
100	140	8.120	8.071	-0.049
100	141	8.237	8.226	-0.011
100	142	8.145	8.146	0.001
100	143	8.161	8.143	-0.018
100	144	8.122	8.134	0.012
100	220	8.165	8.141	-0.024
100	221	8.153	8.132	-0.021
100	224	8.210	8.094	-0.116
100	225	8.062	8.016	-0.046
100	226	8.117	8.144	0.027
100	228	8.135	8.083	-0.052
100	232	8.023	8.046	0.023
100	233	8.099	8.055	-0.044
100	236	8.153	8.100	-0.053
100	238	8.266	8.173	-0.093
100	243	8.115	8.153	0.038
105	244	8.163	8.123	-0.040
105	246	8.153	8.140	-0.013
105	248	8.133	8.162	0.029
105	374	8.152	8.113	-0.039
105	375	8.157	8.108	-0.049
105	Max	8.266	8.228	0.093
105	Average	8.136	8.112	-0.024
105	Min	7.998	7.930	-0.153
105	Std Dev	0.064	0.065	0.047



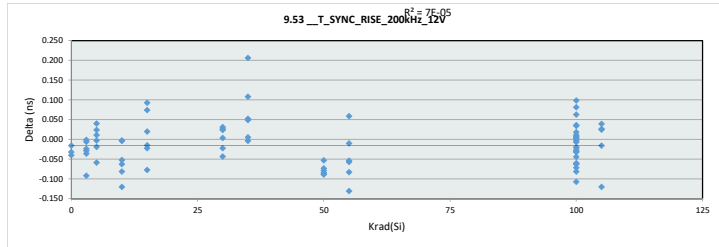
9.49_T_SYNC_R											
Test Site											
Tester											
Test Number											
Min Limit	15	ns									
Max Limit	ns	15									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.032	8.002	8.003	8.030	8.014	8.096	8.060	7.930	8.005	8.012	8.108
Average	8.080	8.098	8.135	8.109	8.106	8.156	8.178	8.029	8.108	8.107	8.129
Max	8.107	8.188	8.195	8.174	8.173	8.206	8.228	8.073	8.222	8.226	8.162
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



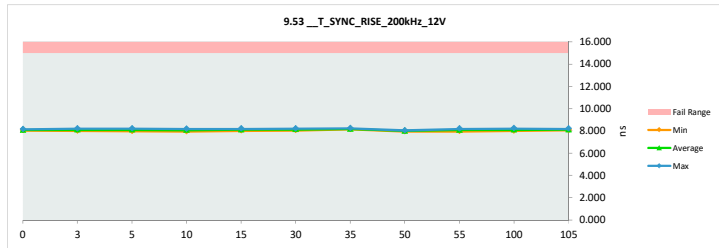
LDR TID Report  
TPS7H5001-SP

9.53 T_SYNC_RISE_200kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.189	8.156	-0.033
0	118	8.068	8.047	-0.041
0	122	8.112	8.095	-0.017
3	61	8.149	8.112	-0.037
3	62	8.175	8.150	-0.025
3	63	8.208	8.201	-0.007
3	64	8.093	8.000	-0.093
3	65	8.038	8.008	-0.030
3	66	8.139	8.137	-0.002
5	67	8.103	8.083	-0.020
5	68	8.186	8.196	0.010
5	69	8.175	8.171	-0.004
5	70	7.945	7.968	0.023
5	71	8.197	8.137	-0.060
5	72	8.052	8.091	0.039
10	73	8.149	8.096	-0.053
10	74	8.175	8.171	-0.004
10	75	8.208	8.144	-0.064
10	76	8.093	7.972	-0.121
10	77	8.038	7.956	-0.082
10	78	8.139	8.134	-0.005
15	79	8.103	8.087	-0.016
15	80	8.186	8.163	-0.023
15	81	8.175	8.194	0.019
15	82	7.945	8.018	0.073
15	83	8.197	8.119	-0.078
15	84	8.052	8.144	0.092
30	86	8.149	8.152	0.003
30	89	8.175	8.198	0.023
30	91	8.208	8.185	-0.023
30	92	8.093	8.049	-0.044
30	94	8.038	8.063	0.025
30	95	8.139	8.169	0.030
35	100	8.103	8.154	0.051
35	101	8.186	8.190	0.004
35	102	8.175	8.223	0.048
35	103	7.945	8.150	0.205
35	104	8.197	8.192	-0.005
35	105	8.052	8.159	0.107
50	106	8.105	8.016	-0.089
50	107	8.012	7.938	-0.074
50	108	8.093	8.007	-0.086
50	109	8.112	8.041	-0.090
50	110	8.064	8.010	-0.054
50	111	8.099	8.019	-0.080
55	113	8.093	7.961	-0.132
55	114	8.206	8.151	-0.055
55	115	8.235	8.151	-0.084
55	116	8.153	8.142	-0.011
55	126	8.051	7.993	-0.058
55	128	8.135	8.193	0.058
100	129	8.093	8.011	-0.082
100	131	8.133	8.136	0.003
100	132	8.113	8.085	-0.028
100	133	8.086	8.053	-0.033
100	134	8.115	8.053	-0.062
100	135	8.164	8.164	0.000
100	138	8.106	8.123	0.017
100	139	8.070	8.006	-0.064
100	140	8.097	8.104	0.007
100	141	8.184	8.162	-0.022
100	142	8.159	8.194	0.035
100	143	8.152	8.162	0.010
100	144	8.129	8.121	-0.008
100	220	8.106	8.186	0.080
100	221	8.155	8.123	-0.032
100	224	8.122	8.101	-0.021
100	225	8.059	8.014	-0.045
100	226	8.123	8.050	-0.073
100	228	8.048	8.145	0.097
100	232	8.030	8.063	0.033
100	233	8.119	8.058	-0.061
100	236	8.122	8.014	-0.108
100	238	8.155	8.217	0.062
100	243	8.095	8.090	-0.005
105	244	8.215	8.094	-0.121
105	246	8.152	8.190	0.038
105	248	8.085	8.109	0.024
105	374	8.078	8.061	-0.017
105	376	8.137	8.162	0.025
105	Max	8.235	8.223	0.205
105	Average	8.119	8.104	-0.016
105	Min	7.945	7.938	-0.132
105	Std Dev	0.062	0.073	0.058



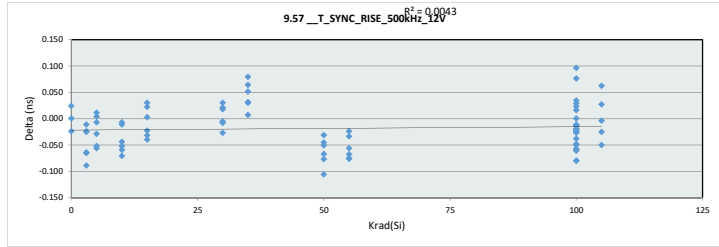
9.53 T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.047	8.000	7.968	7.956	8.018	8.049	8.150	7.938	7.961	8.006	8.061
Average	8.099	8.101	8.108	8.079	8.121	8.136	8.178	8.005	8.099	8.101	8.123
Max	8.156	8.201	8.196	8.171	8.194	8.198	8.223	8.041	8.193	8.217	8.190
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



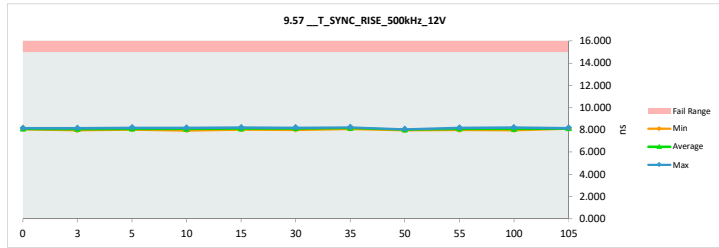
LDR TID Report  
TPS7H5001-SP

9.57_T_SYNC_RISE_500kHz_12V			
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.162	8.138	-0.024
0	118	8.063	8.063	0.000
0	122	8.046	8.070	0.024
3	61	8.133	8.044	-0.089
3	62	8.154	8.130	-0.024
3	63	8.209	8.144	-0.065
3	64	8.034	7.970	-0.064
3	65	8.013	7.988	-0.025
3	66	8.133	8.122	-0.011
5	67	8.070	8.063	-0.007
5	68	8.172	8.176	0.004
5	69	8.192	8.140	-0.052
5	70	8.019	8.030	0.011
5	71	8.139	8.110	-0.029
5	72	8.137	8.081	-0.056
10	73	8.133	8.062	-0.071
10	74	8.154	8.147	-0.007
10	75	8.209	8.165	-0.044
10	76	8.034	8.023	-0.011
10	77	8.013	7.954	-0.059
10	78	8.133	8.081	-0.052
15	79	8.070	8.100	0.030
15	80	8.172	8.149	-0.023
15	81	8.192	8.195	0.003
15	82	8.019	8.041	0.022
15	83	8.139	8.099	-0.040
15	84	8.137	8.105	-0.032
30	86	8.133	8.125	-0.008
30	89	8.154	8.172	0.018
30	91	8.209	8.182	-0.027
30	92	8.034	8.055	0.021
30	94	8.013	8.008	-0.005
30	95	8.133	8.163	0.030
35	100	8.070	8.134	0.064
35	101	8.172	8.223	0.051
35	102	8.192	8.199	0.007
35	103	8.019	8.098	0.079
35	104	8.139	8.169	0.030
35	105	8.137	8.168	0.031
50	106	8.089	7.983	-0.106
50	107	8.002	7.951	-0.051
50	108	8.083	8.038	-0.045
50	109	8.113	8.046	-0.067
50	110	8.024	7.993	-0.031
50	111	8.092	8.015	-0.077
55	113	8.101	8.026	-0.075
55	114	8.201	8.125	-0.076
55	115	8.180	8.146	-0.034
55	116	8.175	8.119	-0.056
55	126	8.054	7.986	-0.068
55	128	8.184	8.160	-0.024
100	129	8.000	7.980	-0.020
100	131	8.172	8.092	-0.080
100	132	8.063	8.049	-0.014
100	133	7.999	7.977	-0.022
100	134	8.022	8.098	0.076
100	135	8.122	8.097	-0.025
100	138	8.114	8.066	-0.048
100	139	8.067	8.009	-0.058
100	140	8.008	8.104	0.096
100	141	8.167	8.190	0.023
100	142	8.097	8.097	0.000
100	143	8.140	8.127	-0.013
100	144	8.115	8.035	-0.080
100	220	8.103	8.090	-0.013
100	221	8.144	8.083	-0.061
100	224	8.114	8.094	-0.020
100	225	8.027	7.989	-0.038
100	226	8.069	8.098	0.029
100	228	8.120	8.070	-0.050
100	232	8.024	8.058	0.034
100	233	8.026	8.042	0.016
100	236	8.117	8.059	-0.058
100	238	8.187	8.176	-0.011
100	243	8.116	8.089	-0.027
105	244	8.176	8.126	-0.050
105	246	8.088	8.150	0.062
105	248	8.091	8.118	0.027
105	374	8.141	8.116	-0.025
105	376	8.106	8.102	-0.004
105	Max	8.209	8.223	0.096
105	Average	8.106	8.088	-0.018
105	Min	7.999	7.951	-0.106
105	Std Dev	0.061	0.064	0.042



9.57_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.063	7.970	8.030	7.954	8.041	8.008	8.098	7.951	7.986	7.977	8.102
Average	8.090	8.066	8.100	8.072	8.115	8.118	8.165	8.004	8.094	8.074	8.122
Max	8.138	8.144	8.176	8.165	8.195	8.182	8.223	8.046	8.160	8.190	8.150
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

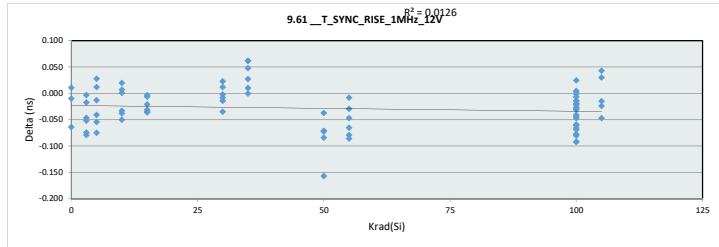




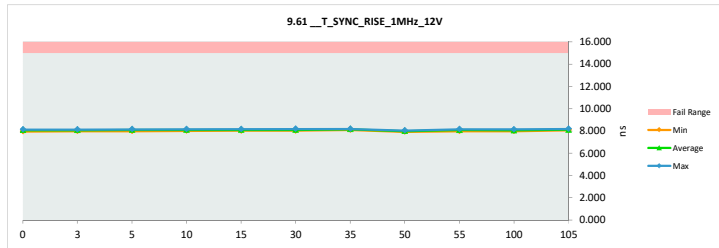
LDR TID Report  
TPS7H5001-SP

9.61_T_SYNC_RISE_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.130	8.119	-0.011
0	118	8.012	7.947	-0.065
0	122	8.088	8.098	0.010
3	61	8.092	8.039	-0.053
3	62	8.137	8.119	-0.018
3	63	8.183	8.103	-0.080
3	64	8.046	7.971	-0.075
3	65	7.996	7.992	-0.004
3	66	8.103	8.056	-0.047
5	67	8.080	8.107	0.027
5	68	8.180	8.125	-0.055
5	69	8.132	8.143	0.011
5	70	8.017	7.975	-0.042
5	71	8.132	8.056	-0.076
5	72	8.094	8.080	-0.014
10	73	8.092	8.098	0.006
10	74	8.137	8.163	-0.034
10	75	8.183	8.144	-0.039
10	76	8.046	7.995	-0.051
10	77	7.996	7.996	0.000
10	78	8.103	8.122	0.019
15	79	8.080	8.044	-0.036
15	80	8.180	8.143	-0.037
15	81	8.132	8.100	-0.032
15	82	8.017	8.010	-0.007
15	83	8.132	8.110	-0.022
15	84	8.094	8.090	-0.004
30	86	8.092	8.083	-0.009
30	89	8.137	8.134	-0.003
30	91	8.183	8.168	-0.015
30	92	8.046	8.068	0.022
30	94	7.996	8.007	0.011
30	95	8.103	8.067	-0.036
35	100	8.080	8.141	0.061
35	101	8.180	8.179	-0.001
35	102	8.132	8.179	0.047
35	103	8.017	8.078	0.061
35	104	8.132	8.141	0.009
35	105	8.094	8.120	0.026
50	106	8.113	7.955	-0.158
50	107	7.999	7.914	-0.085
50	108	8.062	7.989	-0.073
50	109	8.110	8.037	-0.073
50	110	7.972	7.934	-0.038
50	111	8.106	8.034	-0.072
55	113	8.048	7.961	-0.087
55	114	8.140	8.092	-0.048
55	115	8.167	8.101	-0.066
55	116	8.170	8.090	-0.080
55	126	8.025	7.995	-0.030
55	128	8.157	8.148	-0.009
100	129	8.007	7.992	-0.015
100	131	8.104	8.096	-0.008
100	132	8.089	8.029	-0.060
100	133	7.995	8.019	0.024
100	134	8.083	8.016	-0.067
100	135	8.135	8.112	-0.023
100	138	8.061	8.032	-0.029
100	139	8.036	8.034	-0.002
100	140	8.068	7.998	-0.070
100	141	8.148	8.128	-0.020
100	142	8.122	8.044	-0.078
100	143	8.083	8.038	-0.045
100	144	8.106	8.013	-0.093
100	220	8.098	8.066	-0.032
100	221	8.088	8.090	0.002
100	224	8.070	8.049	-0.021
100	225	8.069	7.988	-0.081
100	226	8.070	8.028	-0.042
100	228	8.116	8.023	-0.093
100	232	8.023	7.975	-0.048
100	233	8.055	7.993	-0.062
100	236	8.083	8.055	-0.028
100	238	8.118	8.122	0.004
100	243	8.087	8.072	-0.015
105	244	8.076	8.118	0.042
105	246	8.175	8.159	-0.016
105	248	8.099	8.051	-0.048
105	374	8.087	8.062	-0.025
105	376	8.073	8.102	0.029
105	Max	8.183	8.179	0.061
105	Average	8.091	8.063	-0.029
105	Min	7.972	7.914	-0.158
105	Std Dev	0.052	0.062	0.040



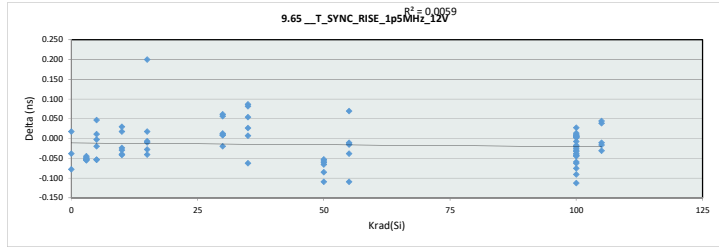
9.61_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.947	7.971	7.975	7.995	8.010	8.007	8.078	7.914	7.961	7.975	8.051
Average	8.055	8.047	8.081	8.076	8.083	8.088	8.140	7.977	8.065	8.042	8.098
Max	8.119	8.119	8.143	8.144	8.143	8.168	8.179	8.037	8.148	8.128	8.159
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



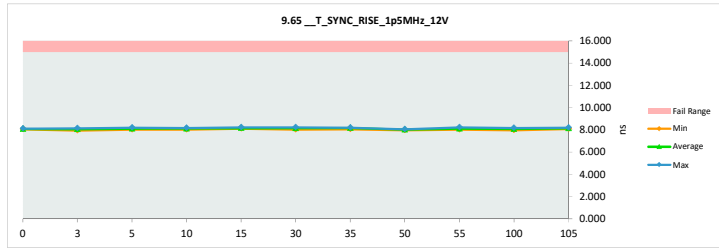
LDR TID Report  
TPS7H5001-SP

9.65_T_SYNC_RISE_1p5MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.131	8.092	-0.039
0	118	8.019	8.036	0.017
0	122	8.159	8.081	-0.078
3	61	8.123	8.069	-0.054
3	62	8.198	8.142	-0.056
3	63	8.165	8.112	-0.053
3	64	7.991	7.942	-0.049
3	65	8.008	7.963	-0.045
3	66	8.148	8.096	-0.052
5	67	8.096	8.093	-0.003
5	68	8.236	8.182	-0.054
5	69	8.159	8.105	-0.054
5	70	7.969	8.015	0.046
5	71	8.163	8.143	-0.020
5	72	8.115	8.126	0.011
10	73	8.123	8.099	-0.024
10	74	8.198	8.158	-0.040
10	75	8.165	8.135	-0.030
10	76	7.991	8.020	0.029
10	77	8.008	8.025	0.017
10	78	8.148	8.106	-0.042
15	79	8.096	8.113	0.017
15	80	8.236	8.208	-0.028
15	81	8.159	8.118	-0.041
15	82	7.969	8.168	0.199
15	83	8.163	8.156	-0.007
15	84	8.115	8.105	-0.010
30	86	8.123	8.131	0.008
30	89	8.198	8.178	-0.020
30	91	8.165	8.173	0.008
30	92	7.991	8.052	0.061
30	94	8.008	8.020	0.012
30	95	8.148	8.204	0.056
35	100	8.096	8.150	0.054
35	101	8.236	8.173	-0.063
35	102	8.159	8.166	0.007
35	103	7.969	8.055	0.086
35	104	8.163	8.189	0.026
35	105	8.115	8.196	0.081
50	106	8.118	8.008	-0.110
50	107	8.010	7.953	-0.057
50	108	8.094	8.041	-0.053
50	109	8.113	8.027	-0.086
50	110	8.046	7.985	-0.061
50	111	8.083	8.017	-0.066
55	113	8.032	8.017	-0.015
55	114	8.124	8.085	-0.039
55	115	8.139	8.208	0.069
55	116	8.177	8.067	-0.110
55	126	8.032	8.017	-0.015
55	128	8.193	8.182	-0.011
100	129	8.000	7.957	-0.043
100	131	8.183	8.153	-0.030
100	132	8.113	8.119	0.006
100	133	8.010	7.965	-0.045
100	134	8.105	8.080	-0.025
100	135	8.108	8.100	-0.008
100	138	8.068	8.095	0.027
100	139	8.107	8.031	-0.076
100	140	8.100	8.037	-0.063
100	141	8.125	8.107	-0.018
100	142	8.137	8.104	-0.033
100	143	8.124	8.131	0.007
100	144	8.154	8.041	-0.113
100	220	8.092	8.101	0.009
100	221	8.127	8.036	-0.091
100	224	8.082	8.084	0.002
100	225	8.026	7.987	-0.039
100	226	8.075	8.032	-0.043
100	228	8.117	8.057	-0.060
100	232	8.002	8.006	0.004
100	233	8.004	8.007	0.003
100	236	8.096	8.101	0.005
100	238	8.164	8.143	-0.021
100	243	8.064	8.077	0.013
105	244	8.136	8.119	-0.017
105	246	8.197	8.186	-0.011
105	248	8.092	8.130	0.038
105	374	8.036	8.080	0.044
105	376	8.109	8.078	-0.031
	Max	8.236	8.208	0.199
	Average	8.104	8.088	-0.016
	Min	7.969	7.942	-0.113
	Std Dev	0.068	0.067	0.049



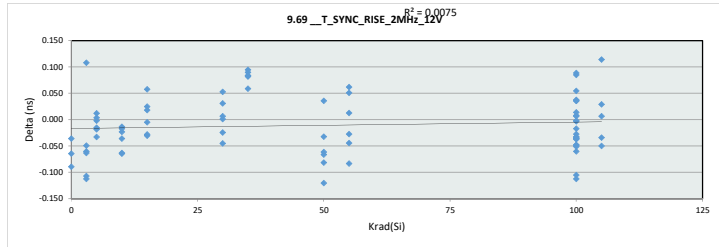
9.65_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.036	7.942	8.015	8.020	8.105	8.020	8.055	7.953	8.017	7.957	8.078
Average	8.070	8.054	8.111	8.091	8.145	8.126	8.155	8.005	8.096	8.065	8.119
Max	8.092	8.142	8.182	8.158	8.208	8.204	8.196	8.041	8.208	8.153	8.186
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



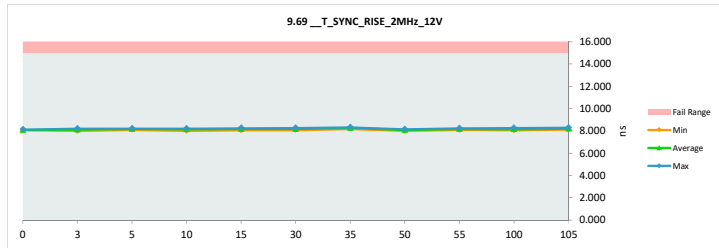
LDR TID Report  
TPS7H5001-SP

9.69_T_SYNC_RISE_2MHz_12V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	15	15
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.158	8.068	-0.090
0	118	8.098	8.061	-0.037
0	122	8.178	8.113	-0.065
3	61	8.189	8.139	-0.050
3	62	8.260	8.199	-0.061
3	63	8.239	8.175	-0.064
3	64	8.105	7.997	-0.108
3	65	8.024	8.131	0.107
3	66	8.218	8.105	-0.113
5	67	8.145	8.129	-0.016
5	68	8.224	8.190	-0.034
5	69	8.186	8.189	0.003
5	70	8.093	8.104	0.011
5	71	8.226	8.207	-0.019
5	72	8.172	8.169	-0.003
10	73	8.189	8.175	-0.014
10	74	8.260	8.195	-0.065
10	75	8.239	8.215	-0.024
10	76	8.105	8.068	-0.037
10	77	8.024	8.006	-0.018
10	78	8.218	8.154	-0.064
15	79	8.145	8.202	0.057
15	80	8.224	8.248	0.024
15	81	8.186	8.203	0.017
15	82	8.093	8.062	-0.031
15	83	8.226	8.220	-0.006
15	84	8.172	8.143	-0.029
30	86	8.189	8.164	-0.025
30	89	8.260	8.260	0.000
30	91	8.239	8.193	-0.046
30	92	8.105	8.111	0.006
30	94	8.024	8.076	0.052
30	95	8.218	8.248	0.030
35	100	8.145	8.234	0.089
35	101	8.224	8.307	0.083
35	102	8.186	8.267	0.081
35	103	8.093	8.187	0.094
35	104	8.226	8.284	0.058
35	105	8.172	8.254	0.082
50	106	8.175	8.108	-0.067
50	107	8.092	8.010	-0.082
50	108	8.049	8.084	0.035
50	109	8.180	8.147	-0.033
50	110	8.113	8.051	-0.062
50	111	8.166	8.045	-0.121
55	113	8.024	8.085	0.061
55	114	8.173	8.185	0.012
55	115	8.305	8.221	-0.084
55	116	8.248	8.203	-0.045
55	126	8.071	8.121	0.050
55	128	8.271	8.243	-0.028
100	129	8.052	8.089	0.037
100	131	8.225	8.176	-0.049
100	132	8.131	8.185	0.054
100	133	8.054	8.089	0.035
100	134	8.152	8.114	-0.038
100	135	8.225	8.260	0.035
100	138	8.163	8.057	-0.106
100	139	8.162	8.113	-0.049
100	140	8.131	8.095	-0.036
100	141	8.241	8.128	-0.113
100	142	8.236	8.175	-0.061
100	143	8.190	8.196	0.006
100	144	8.178	8.141	-0.037
100	220	8.211	8.193	-0.018
100	221	8.193	8.159	-0.034
100	224	8.177	8.174	-0.003
100	225	8.045	8.129	0.084
100	226	8.169	8.141	-0.028
100	228	8.211	8.159	-0.052
100	232	8.060	8.056	-0.004
100	233	8.122	8.088	-0.034
100	236	8.168	8.256	0.088
100	238	8.245	8.252	0.007
100	243	8.186	8.199	0.013
105	244	8.209	8.215	0.006
105	246	8.180	8.293	0.113
105	248	8.156	8.184	0.028
105	374	8.226	8.175	-0.051
105	376	8.160	8.125	-0.035
105		8.305	8.307	0.113
	Max			
	Average	8.168	8.157	-0.010
	Min	8.024	7.997	-0.121
	Std Dev	0.066	0.071	0.056



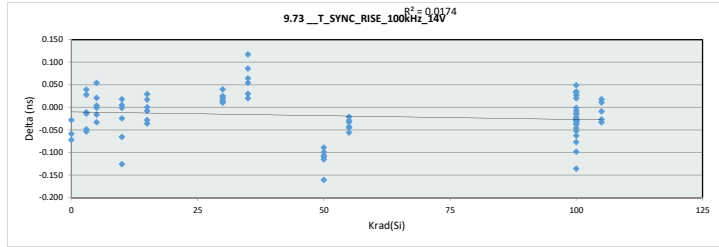
9.69_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.061	7.997	8.104	8.006	8.062	8.076	8.187	8.010	8.085	8.056	8.125
Average	8.081	8.124	8.165	8.136	8.180	8.175	8.256	8.074	8.176	8.151	8.198
Max	8.113	8.199	8.207	8.215	8.248	8.260	8.307	8.147	8.243	8.260	8.293
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



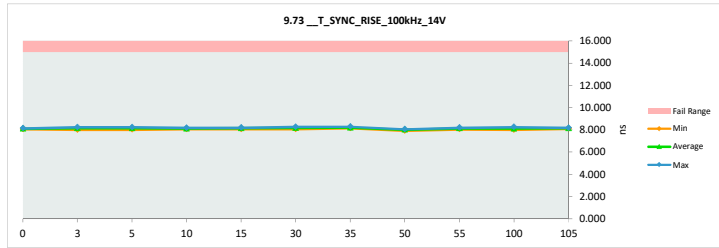
LDR TID Report  
TPS7H5001-SP

9.73_T_SYNC_RISE_100kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.197	8.125	-0.072
0	118	8.105	8.046	-0.059
0	122	8.120	8.092	-0.028
3	61	8.106	8.145	0.039
3	62	8.229	8.218	-0.011
3	63	8.219	8.165	-0.054
3	64	8.036	7.987	-0.049
3	65	8.055	8.083	0.028
3	66	8.181	8.167	-0.014
5	67	8.113	8.117	0.004
5	68	8.183	8.167	-0.016
5	69	8.160	8.214	0.054
5	70	8.043	8.010	-0.033
5	71	8.143	8.164	0.021
5	72	8.158	8.157	-0.001
10	73	8.106	8.111	0.005
10	74	8.229	8.163	-0.066
10	75	8.219	8.093	-0.126
10	76	8.036	8.054	0.018
10	77	8.055	8.054	-0.001
10	78	8.181	8.156	-0.025
15	79	8.113	8.142	0.029
15	80	8.183	8.147	-0.036
15	81	8.160	8.177	0.017
15	82	8.043	8.043	0.000
15	83	8.143	8.115	-0.028
15	84	8.158	8.150	-0.008
30	86	8.106	8.131	0.025
30	89	8.229	8.245	0.016
30	91	8.219	8.229	0.010
30	92	8.036	8.057	0.021
30	94	8.055	8.095	0.040
30	95	8.181	8.194	0.013
35	100	8.113	8.133	0.020
35	101	8.183	8.213	0.030
35	102	8.160	8.277	0.117
35	103	8.043	8.129	0.086
35	104	8.143	8.207	0.064
35	105	8.158	8.212	0.054
50	106	8.116	8.027	-0.089
50	107	8.064	7.903	-0.161
50	108	8.149	8.127	-0.022
50	109	8.147	8.040	-0.107
50	110	8.060	7.950	-0.110
50	111	8.150	8.051	-0.099
55	113	8.078	8.033	-0.045
55	114	8.216	8.183	-0.033
55	115	8.178	8.149	-0.029
55	116	8.173	8.130	-0.043
55	126	8.096	8.040	-0.056
55	128	8.205	8.184	-0.021
100	129	8.019	8.004	-0.015
100	131	8.156	8.182	0.026
100	132	8.163	8.127	-0.036
100	133	8.020	7.989	-0.031
100	134	8.060	8.109	0.049
100	135	8.172	8.204	0.032
100	138	8.113	8.061	-0.052
100	139	8.075	8.067	-0.008
100	140	8.082	8.081	-0.001
100	141	8.219	8.189	-0.030
100	142	8.136	8.073	-0.063
100	143	8.121	8.092	-0.029
100	144	8.128	8.114	-0.014
100	220	8.173	8.147	-0.026
100	221	8.151	8.105	-0.046
100	224	8.133	8.095	-0.038
100	225	8.103	8.026	-0.077
100	226	8.142	8.006	-0.136
100	228	8.144	8.118	-0.026
100	232	8.113	8.015	-0.098
100	233	8.091	8.069	-0.022
100	236	8.122	8.094	-0.028
100	238	8.205	8.240	0.035
100	243	8.145	8.165	0.020
105	244	8.156	8.147	-0.009
105	246	8.159	8.170	0.011
105	248	8.162	8.135	-0.027
105	374	8.128	8.095	-0.033
105	376	8.069	8.087	0.018
105	Max	8.229	8.277	0.117
105	Average	8.132	8.114	-0.018
105	Min	8.019	7.903	-0.161
105	Std Dev	0.056	0.073	0.050



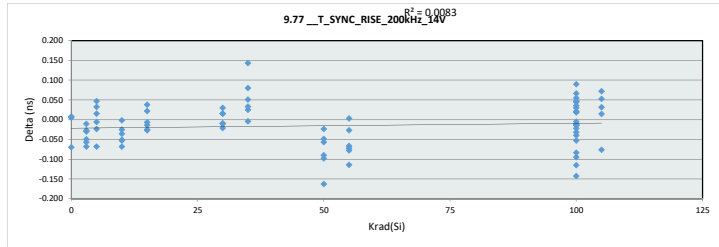
9.73_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.046	7.987	8.010	8.054	8.043	8.057	8.129	7.903	8.033	7.989	8.087
Average	8.088	8.128	8.138	8.105	8.129	8.159	8.195	8.001	8.120	8.099	8.127
Max	8.125	8.218	8.214	8.163	8.177	8.245	8.277	8.051	8.184	8.240	8.170
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



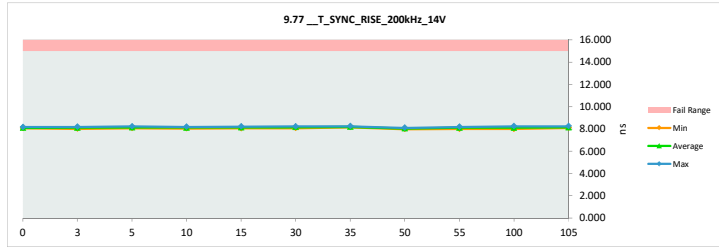
LDR TID Report  
TPS7H5001-SP

9.77_T_SYNC_RISE_200kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.148	8.152	0.004
0	118	8.039	8.046	0.007
0	122	8.132	8.061	-0.071
3	61	8.146	8.134	-0.012
3	62	8.215	8.146	-0.069
3	63	8.225	8.167	-0.058
3	64	8.027	7.996	-0.031
3	65	8.067	8.016	-0.051
3	66	8.160	8.132	-0.028
5	67	8.134	8.127	-0.007
5	68	8.176	8.207	0.031
5	69	8.206	8.137	-0.069
5	70	7.998	8.044	0.046
5	71	8.177	8.152	-0.025
5	72	8.089	8.103	0.014
10	73	8.146	8.092	-0.054
10	74	8.215	8.163	-0.052
10	75	8.225	8.156	-0.069
10	76	8.027	8.024	-0.003
10	77	8.067	8.030	-0.037
10	78	8.160	8.134	-0.026
15	79	8.134	8.108	-0.026
15	80	8.176	8.197	0.021
15	81	8.206	8.191	-0.015
15	82	7.998	8.035	0.037
15	83	8.177	8.169	-0.008
15	84	8.089	8.061	-0.028
30	86	8.146	8.124	-0.022
30	89	8.215	8.203	-0.012
30	91	8.225	8.215	-0.010
30	92	8.027	8.056	0.029
30	94	8.067	8.081	0.014
30	95	8.160	8.174	0.014
35	100	8.134	8.158	0.024
35	101	8.176	8.208	0.032
35	102	8.206	8.201	-0.005
35	103	7.998	8.140	0.142
35	104	8.177	8.227	0.050
35	105	8.089	8.168	0.079
50	106	8.126	8.035	-0.091
50	107	8.025	7.975	-0.050
50	108	8.130	8.031	-0.099
50	109	8.164	8.000	-0.164
50	110	8.105	8.047	-0.058
50	111	8.103	8.078	-0.025
55	113	8.047	7.979	-0.068
55	114	8.148	8.150	0.002
55	115	8.250	8.135	-0.115
55	116	8.182	8.154	-0.028
55	126	8.075	8.002	-0.073
55	128	8.249	8.170	-0.079
100	129	8.101	7.985	-0.116
100	131	8.154	8.148	-0.006
100	132	8.114	8.099	-0.015
100	133	8.052	8.070	0.018
100	134	8.121	8.025	-0.096
100	135	8.131	8.177	0.046
100	138	8.113	8.156	0.043
100	139	8.074	8.033	-0.041
100	140	8.086	8.071	-0.015
100	141	8.178	8.195	0.017
100	142	8.128	8.217	0.089
100	143	8.153	8.198	0.045
100	144	8.090	8.110	0.020
100	220	8.121	8.067	-0.054
100	221	8.138	8.123	-0.015
100	224	8.086	8.140	0.054
100	225	8.032	7.999	-0.033
100	226	8.072	8.101	0.029
100	228	8.149	8.005	-0.144
100	232	8.004	8.038	0.034
100	233	8.085	8.073	-0.012
100	236	8.087	8.152	0.065
100	238	8.197	8.112	-0.085
100	243	8.107	8.084	-0.023
105	244	8.151	8.074	-0.077
105	246	8.146	8.217	0.071
105	248	8.116	8.146	0.030
105	374	8.060	8.073	0.013
105	376	8.079	8.130	0.051
105	Max	8.250	8.227	0.142
105	Average	8.124	8.109	-0.015
105	Min	7.998	7.975	-0.164
105	Std Dev	0.063	0.068	0.055



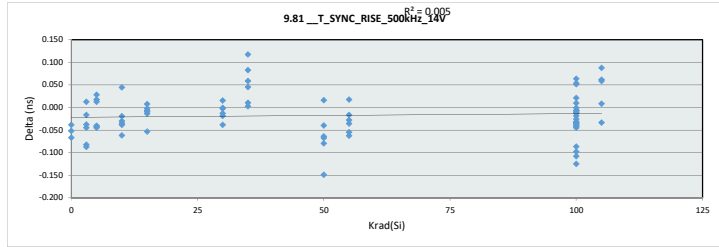
9.77_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.046	7.996	8.044	8.024	8.035	8.056	8.140	7.975	7.979	7.985	8.073
Average	8.086	8.099	8.128	8.100	8.127	8.142	8.184	8.028	8.098	8.099	8.128
Max	8.152	8.167	8.207	8.163	8.197	8.215	8.227	8.078	8.170	8.217	8.217
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



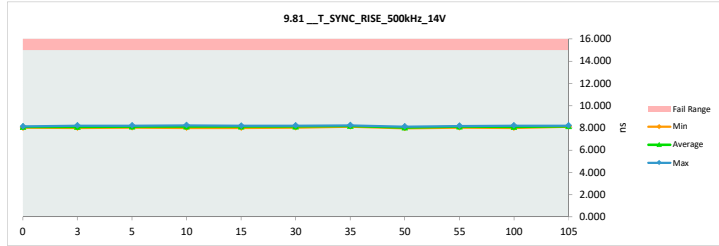
LDR TID Report  
TPS7H5001-SP

9.81_T_SYNC_RISE_500kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.163	8.111	-0.052
0	118	8.066	7.999	-0.067
0	122	8.122	8.083	-0.039
3	61	8.126	8.088	-0.038
3	62	8.165	8.177	0.012
3	63	8.174	8.157	-0.017
3	64	8.083	8.000	-0.083
3	65	8.023	7.978	-0.045
3	66	8.172	8.084	-0.088
5	67	8.067	8.095	0.028
5	68	8.224	8.183	-0.041
5	69	8.178	8.136	-0.042
5	70	7.989	8.006	0.017
5	71	8.118	8.130	0.012
5	72	8.127	8.082	-0.045
10	73	8.126	8.096	-0.030
10	74	8.165	8.209	0.044
10	75	8.174	8.154	-0.020
10	76	8.083	8.021	-0.062
10	77	8.023	7.984	-0.039
10	78	8.172	8.137	-0.035
15	79	8.067	8.074	0.007
15	80	8.224	8.170	-0.054
15	81	8.178	8.164	-0.014
15	82	7.989	7.982	-0.007
15	83	8.118	8.115	-0.003
15	84	8.127	8.117	-0.010
30	86	8.126	8.123	-0.003
30	89	8.165	8.146	-0.019
30	91	8.174	8.189	0.015
30	92	8.083	8.082	-0.001
30	94	8.023	8.010	-0.013
30	95	8.172	8.133	-0.039
35	100	8.067	8.125	0.058
35	101	8.224	8.226	0.002
35	102	8.178	8.188	0.010
35	103	7.989	8.106	0.117
35	104	8.118	8.200	0.082
35	105	8.127	8.172	0.045
50	106	8.094	8.030	-0.064
50	107	7.957	7.973	0.016
50	108	8.051	7.971	-0.080
50	109	8.126	8.058	-0.068
50	110	8.155	8.006	-0.149
50	111	8.138	8.098	-0.040
55	113	8.072	8.009	-0.063
55	114	8.163	8.135	-0.028
55	115	8.206	8.151	-0.055
55	116	8.171	8.135	-0.036
55	126	8.027	8.010	-0.017
55	128	8.150	8.167	0.017
100	129	8.000	7.981	-0.019
100	131	8.123	8.115	-0.008
100	132	8.075	8.062	-0.013
100	133	8.045	8.013	-0.032
100	134	8.110	8.068	-0.042
100	135	8.113	8.164	0.051
100	138	8.099	8.062	-0.037
100	139	8.039	8.031	-0.008
100	140	8.137	8.039	-0.098
100	141	8.140	8.133	-0.007
100	142	8.168	8.060	-0.108
100	143	8.086	8.149	0.063
100	144	8.102	8.068	-0.034
100	220	8.095	8.104	0.009
100	221	8.149	8.024	-0.125
100	224	8.120	8.094	-0.026
100	225	8.060	7.973	-0.087
100	226	8.088	8.087	-0.001
100	228	8.119	8.074	-0.045
100	232	8.012	7.972	-0.040
100	233	8.035	8.024	-0.011
100	236	8.096	8.117	0.021
100	238	8.136	8.189	0.053
100	243	8.090	8.053	-0.037
105	244	8.135	8.101	-0.034
105	246	8.169	8.177	0.008
105	248	8.050	8.137	0.087
105	374	8.060	8.118	0.058
105	376	8.067	8.128	0.061
105	Max	8.224	8.226	0.002
105	Average	8.109	8.091	-0.018
105	Min	7.957	7.971	-0.014
105	Std Dev	0.060	0.067	0.048



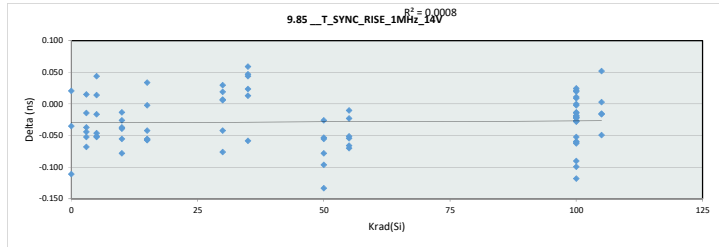
9.81_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.999	7.978	8.006	7.984	7.982	8.010	8.106	7.971	8.009	7.972	8.101
Average	8.064	8.081	8.105	8.100	8.104	8.114	8.170	8.023	8.101	8.069	8.132
Max	8.111	8.177	8.183	8.209	8.170	8.189	8.226	8.098	8.167	8.189	8.177
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



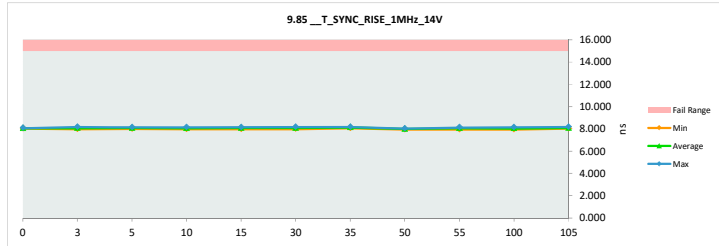
LDR TID Report  
TPS7H5001-SP

9.85_T_SYNC_RISE_1MHz_14V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	15	15
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.184	8.072	-0.112
0	118	8.006	8.026	0.020
0	122	8.089	8.053	-0.036
3	61	8.091	8.053	-0.038
3	62	8.157	8.171	0.014
3	63	8.170	8.117	-0.053
3	64	8.018	8.003	-0.015
3	65	8.029	7.984	-0.045
3	66	8.173	8.104	-0.069
5	67	8.070	8.083	0.013
5	68	8.193	8.140	-0.053
5	69	8.109	8.152	0.043
5	70	8.009	7.992	-0.017
5	71	8.144	8.092	-0.052
5	72	8.135	8.088	-0.047
10	73	8.091	8.051	-0.040
10	74	8.157	8.130	-0.027
10	75	8.170	8.132	-0.038
10	76	8.018	8.004	-0.014
10	77	8.029	7.973	-0.056
10	78	8.173	8.094	-0.079
15	79	8.070	8.067	-0.003
15	80	8.193	8.135	-0.058
15	81	8.109	8.142	0.033
15	82	8.009	7.966	-0.043
15	83	8.144	8.087	-0.057
15	84	8.135	8.079	-0.056
30	86	8.091	8.109	0.018
30	89	8.157	8.162	0.005
30	91	8.170	8.176	0.006
30	92	8.018	8.047	0.029
30	94	8.029	7.986	-0.043
30	95	8.173	8.096	-0.077
35	100	8.070	8.128	0.058
35	101	8.193	8.134	-0.059
35	102	8.109	8.152	0.043
35	103	8.009	8.055	0.046
35	104	8.144	8.167	0.023
35	105	8.135	8.147	0.012
50	106	8.104	8.007	-0.097
50	107	7.990	7.934	-0.056
50	108	8.051	8.024	-0.027
50	109	8.088	8.034	-0.054
50	110	8.077	7.943	-0.134
50	111	8.092	8.013	-0.079
55	113	8.003	7.992	-0.011
55	114	8.145	8.074	-0.071
55	115	8.154	8.130	-0.024
55	116	8.155	8.088	-0.067
55	126	8.007	7.955	-0.052
55	128	8.169	8.114	-0.055
100	129	7.973	7.983	0.010
100	131	8.169	8.069	-0.100
100	132	8.046	8.065	0.019
100	133	7.987	7.995	0.008
100	134	8.030	8.010	-0.020
100	135	8.118	8.115	-0.003
100	138	8.048	8.047	-0.001
100	139	8.064	8.041	-0.023
100	140	8.084	8.056	-0.028
100	141	8.136	8.073	-0.063
100	142	8.079	8.057	-0.022
100	143	8.104	8.089	-0.015
100	144	8.126	8.035	-0.091
100	220	8.102	8.088	-0.014
100	221	8.122	8.069	-0.053
100	224	8.072	8.044	-0.028
100	225	8.105	7.986	-0.119
100	226	8.019	8.043	0.024
100	228	8.086	8.026	-0.060
100	232	8.006	7.945	-0.061
100	233	7.985	8.006	0.021
100	236	8.075	8.056	-0.019
100	238	8.157	8.135	-0.022
100	243	8.069	8.040	-0.029
105	244	8.126	8.076	-0.050
105	246	8.113	8.164	0.051
105	248	8.055	8.038	-0.017
105	374	8.063	8.065	0.002
105	376	8.090	8.074	-0.016
105	Max	8.193	8.176	0.058
105	Average	8.093	8.064	-0.028
105	Min	7.973	7.934	-0.134
105	Std Dev	0.060	0.060	0.041



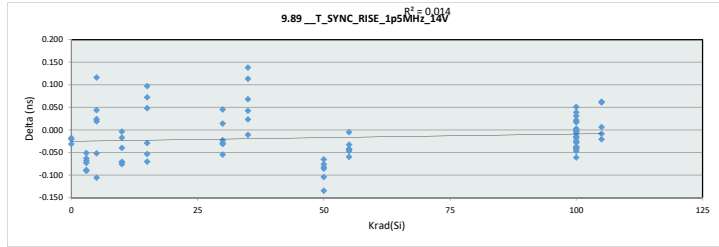
9.85_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.026	7.984	7.992	7.973	7.966	7.986	8.055	7.934	7.955	7.945	8.038
Average	8.050	8.072	8.091	8.064	8.079	8.096	8.131	7.993	8.059	8.045	8.083
Max	8.072	8.171	8.152	8.132	8.142	8.176	8.167	8.034	8.130	8.135	8.164
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



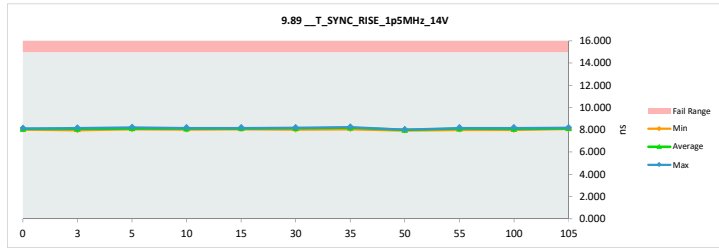
LDR TID Report  
TPS7H5001-SP

9.89_T_SYNC_RISE_1p5MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.156	8.138	-0.018
0	118	8.039	8.018	-0.021
0	122	8.130	8.099	-0.031
3	61	8.139	8.071	-0.068
3	62	8.219	8.156	-0.063
3	63	8.223	8.150	-0.073
3	64	8.092	8.003	-0.089
3	65	8.048	7.957	-0.091
3	66	8.137	8.086	-0.051
5	67	8.022	8.041	0.019
5	68	8.187	8.211	0.024
5	69	8.131	8.175	0.044
5	70	7.994	8.110	0.116
5	71	8.183	8.077	-0.106
5	72	8.159	8.107	-0.052
10	73	8.139	8.068	-0.071
10	74	8.219	8.179	-0.040
10	75	8.223	8.147	-0.076
10	76	8.092	8.021	-0.071
10	77	8.048	8.031	-0.017
10	78	8.137	8.133	-0.004
15	79	8.022	8.119	0.097
15	80	8.187	8.158	-0.029
15	81	8.131	8.179	0.048
15	82	7.994	8.066	0.072
15	83	8.183	8.113	-0.070
15	84	8.159	8.106	-0.053
30	86	8.139	8.153	0.014
30	89	8.219	8.188	-0.031
30	91	8.223	8.168	-0.055
30	92	8.092	8.063	-0.029
30	94	8.048	8.025	-0.023
30	95	8.137	8.182	0.045
35	100	8.022	8.160	0.138
35	101	8.187	8.176	-0.011
35	102	8.131	8.244	0.113
35	103	7.994	8.062	0.068
35	104	8.183	8.206	0.023
35	105	8.159	8.201	0.042
50	106	8.109	8.005	-0.104
50	107	8.003	7.938	-0.065
50	108	8.103	8.021	-0.082
50	109	8.152	8.017	-0.135
50	110	8.100	8.014	-0.086
50	111	8.080	8.004	-0.076
55	113	8.041	8.008	-0.033
55	114	8.146	8.099	-0.047
55	115	8.222	8.162	-0.060
55	116	8.145	8.102	-0.043
55	126	8.077	8.034	-0.043
55	128	8.183	8.178	-0.005
100	129	8.048	8.008	-0.040
100	131	8.148	8.134	-0.014
100	132	8.100	8.096	-0.004
100	133	8.009	8.010	0.001
100	134	8.112	8.095	-0.017
100	135	8.105	8.108	0.003
100	138	8.084	8.103	0.019
100	139	8.063	8.080	0.017
100	140	8.054	8.015	-0.039
100	141	8.140	8.179	0.039
100	142	8.185	8.142	-0.043
100	143	8.141	8.104	-0.037
100	144	8.104	8.102	-0.002
100	220	8.073	8.104	0.031
100	221	8.103	8.064	-0.039
100	224	8.141	8.113	-0.028
100	225	7.996	8.018	0.022
100	226	8.091	8.084	-0.007
100	228	8.073	8.049	-0.024
100	232	8.050	7.989	-0.061
100	233	7.980	8.031	0.051
100	236	8.079	8.062	-0.017
100	238	8.206	8.159	-0.047
100	243	8.067	8.039	-0.028
105	244	8.150	8.129	-0.021
105	246	8.125	8.187	0.062
105	248	8.109	8.100	-0.009
105	374	8.052	8.113	0.061
105	376	8.073	8.079	0.006
105	Max	8.223	8.244	0.138
105	Average	8.112	8.095	-0.017
105	Min	7.980	7.938	-0.135
105	Std Dev	0.065	0.066	0.054



9.89_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.018	7.957	8.041	8.021	8.066	8.025	8.062	7.938	8.008	7.989	8.079
Average	8.085	8.071	8.120	8.097	8.124	8.130	8.175	8.000	8.097	8.079	8.122
Max	8.138	8.156	8.211	8.179	8.179	8.188	8.244	8.021	8.178	8.179	8.187
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

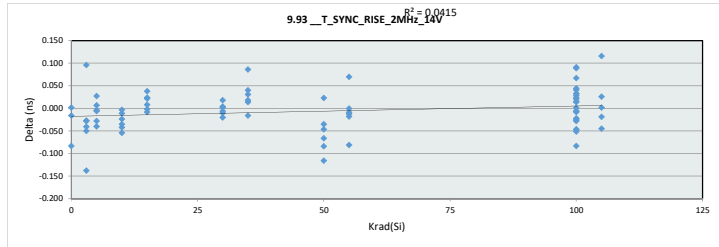




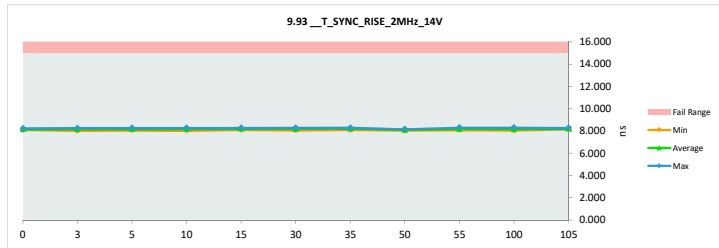
LDR TID Report  
TPS7H5001-SP

9.93 T_SYNC_RISE_2MHz_14V			
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.209	8.210	0.001
0	118	8.111	8.094	-0.017
0	122	8.206	8.122	-0.084
3	61	8.199	8.157	-0.042
3	62	8.260	8.232	-0.028
3	63	8.246	8.195	-0.051
3	64	8.129	7.990	-0.139
3	65	8.045	8.140	0.095
3	66	8.254	8.225	-0.029
5	67	8.151	8.157	0.006
5	68	8.232	8.258	0.026
5	69	8.209	8.202	-0.007
5	70	8.087	8.058	-0.029
5	71	8.220	8.179	-0.041
5	72	8.179	8.174	-0.005
10	73	8.199	8.163	-0.036
10	74	8.260	8.248	-0.012
10	75	8.246	8.242	-0.004
10	76	8.129	8.074	-0.055
10	77	8.045	8.020	-0.025
10	78	8.254	8.211	-0.043
15	79	8.151	8.174	0.023
15	80	8.232	8.229	-0.003
15	81	8.209	8.246	0.037
15	82	8.087	8.094	0.007
15	83	8.220	8.211	-0.009
15	84	8.179	8.200	0.021
30	86	8.199	8.216	0.017
30	89	8.260	8.261	0.001
30	91	8.246	8.225	-0.021
30	92	8.129	8.118	-0.011
30	94	8.045	8.048	0.003
30	95	8.254	8.247	-0.007
35	100	8.151	8.236	0.085
35	101	8.232	8.262	0.030
35	102	8.209	8.192	-0.017
35	103	8.087	8.100	0.013
35	104	8.220	8.237	0.017
35	105	8.179	8.218	0.039
50	106	8.194	8.109	-0.085
50	107	8.073	8.037	-0.036
50	108	8.041	8.069	0.022
50	109	8.181	8.134	-0.047
50	110	8.130	8.063	-0.067
50	111	8.185	8.068	-0.117
55	113	8.015	8.084	0.069
55	114	8.247	8.165	-0.082
55	115	8.269	8.259	-0.010
55	116	8.207	8.188	-0.019
55	126	8.098	8.085	-0.013
55	128	8.288	8.287	-0.001
100	129	8.050	8.041	-0.009
100	131	8.221	8.198	-0.023
100	132	8.157	8.171	0.014
100	133	8.078	8.100	0.022
100	134	8.141	8.112	-0.029
100	135	8.194	8.284	0.090
100	138	8.158	8.110	-0.048
100	139	8.119	8.067	-0.052
100	140	8.129	8.102	-0.027
100	141	8.212	8.212	0.000
100	142	8.215	8.242	0.027
100	143	8.165	8.205	0.040
100	144	8.204	8.120	-0.084
100	220	8.162	8.175	0.013
100	221	8.178	8.154	-0.024
100	224	8.146	8.177	0.031
100	225	8.049	8.069	0.020
100	226	8.143	8.136	-0.007
100	228	8.196	8.189	-0.007
100	232	8.104	8.057	-0.047
100	233	8.050	8.093	0.043
100	236	8.154	8.242	0.088
100	238	8.219	8.285	0.066
100	243	8.159	8.191	0.032
105	244	8.199	8.179	-0.020
105	246	8.257	8.258	0.001
105	248	8.181	8.206	0.025
105	374	8.062	8.177	0.115
105	375	8.188	8.142	-0.046
105	Max	8.288	8.287	0.115
105	Average	8.170	8.164	-0.006
105	Min	8.015	7.990	-0.139
105	Std Dev	0.067	0.072	0.046



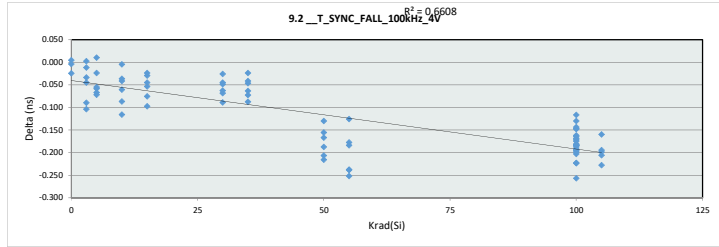
9.93 T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.094	7.990	8.058	8.020	8.094	8.048	8.100	8.037	8.084	8.041	8.142
Average	8.142	8.157	8.171	8.160	8.192	8.186	8.208	8.080	8.178	8.156	8.192
Max	8.210	8.232	8.258	8.248	8.246	8.261	8.262	8.134	8.287	8.285	8.258
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



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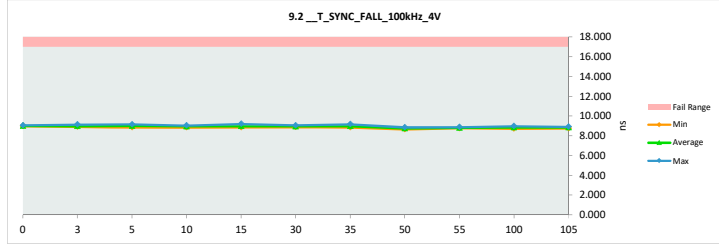
9.2_T_SYNC_FALL_100kHz_4V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	17	17
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.007	8.982	-0.025
0	118	9.034	9.030	-0.004
0	122	8.952	8.956	0.004
3	61	9.014	8.910	-0.104
3	62	9.097	9.099	0.002
3	63	8.939	8.894	-0.045
3	64	8.890	8.878	-0.012
3	65	8.935	8.901	-0.034
3	66	9.044	8.954	-0.090
5	67	8.882	8.827	-0.055
5	68	9.196	9.124	-0.072
5	69	9.038	8.979	-0.059
5	70	8.885	8.861	-0.024
5	71	8.940	8.950	0.010
5	72	9.140	9.073	-0.067
10	73	9.014	8.898	-0.116
10	74	9.097	9.010	-0.087
10	75	8.939	8.934	-0.005
10	76	8.890	8.829	-0.061
10	77	8.935	8.893	-0.042
10	78	9.044	9.007	-0.037
15	79	8.882	8.806	-0.076
15	80	9.196	9.172	-0.024
15	81	9.038	9.008	-0.030
15	82	8.885	8.831	-0.054
15	83	8.940	8.895	-0.045
15	84	9.140	9.042	-0.098
30	86	9.014	8.925	-0.089
30	89	9.097	9.028	-0.069
30	91	8.939	8.876	-0.063
30	92	8.890	8.845	-0.045
30	94	8.935	8.909	-0.026
30	95	9.044	8.995	-0.049
35	100	8.882	8.818	-0.064
35	101	9.196	9.149	-0.047
35	102	9.038	8.950	-0.088
35	103	8.885	8.861	-0.024
35	104	8.940	8.898	-0.042
35	105	9.140	9.067	-0.073
50	106	8.913	8.746	-0.167
50	107	8.809	8.621	-0.188
50	108	8.992	8.836	-0.156
50	109	8.956	8.636	-0.130
50	110	9.002	8.795	-0.207
50	111	8.930	8.714	-0.216
55	113	9.067	8.815	-0.252
55	114	8.998	8.759	-0.239
55	115	9.025	8.787	-0.238
55	116	8.982	8.798	-0.184
55	126	8.911	8.733	-0.178
55	128	8.967	8.841	-0.126
100	129	9.000	8.804	-0.196
100	131	9.086	8.893	-0.193
100	132	9.117	8.934	-0.183
100	133	9.121	8.937	-0.184
100	134	9.054	8.860	-0.194
100	135	9.027	8.852	-0.175
100	138	8.969	8.807	-0.162
100	139	8.949	8.765	-0.184
100	140	9.034	8.904	-0.130
100	141	9.004	8.823	-0.181
100	142	9.004	8.819	-0.185
100	143	8.942	8.798	-0.144
100	144	8.889	8.686	-0.203
100	220	9.036	8.889	-0.147
100	221	8.916	8.693	-0.223
100	224	8.989	8.872	-0.117
100	225	9.085	8.886	-0.199
100	226	8.992	8.768	-0.224
100	228	9.065	8.917	-0.148
100	232	8.962	8.793	-0.169
100	233	8.966	8.778	-0.188
100	236	8.945	8.688	-0.257
100	238	9.043	8.871	-0.172
100	243	9.001	8.836	-0.165
105	244	9.017	8.811	-0.206
105	246	9.100	8.872	-0.228
105	248	8.987	8.827	-0.160
105	374	8.918	8.723	-0.195
105	376	9.039	8.860	-0.179
105		9.196	9.172	0.010
	Max			
	Average	8.995	8.876	-0.119
	Min	8.766	8.621	-0.257
	Std Dev	0.087	0.112	0.076



9.2_T_SYNC_FALL		
Test Site		
Tester		
Test Number		
Max Limit	17	ns
Min Limit	ns	

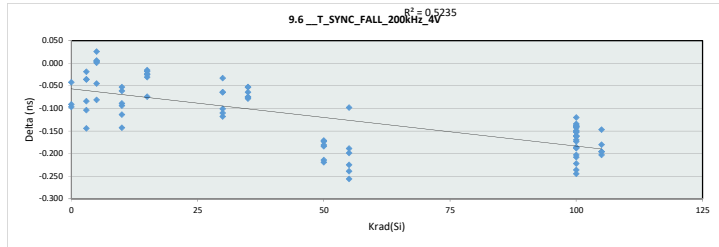
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.956	8.878	8.827	8.829	8.806	8.845	8.818	8.621	8.733	8.686	8.723
Average	8.989	8.939	8.969	8.929	8.959	8.930	8.957	8.725	8.789	8.828	8.819
Max	9.030	9.099	9.124	9.010	9.172	9.028	9.149	8.836	8.841	8.937	8.872
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



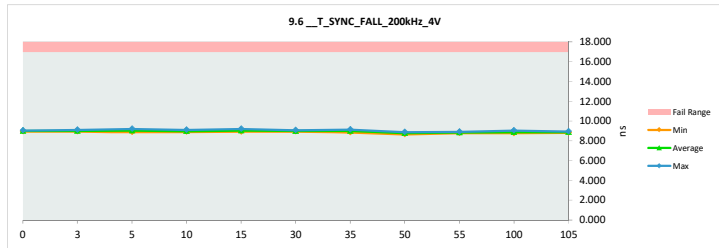
LDR TID Report  
TPS7H5001-SP

9.6_T_SYNC_FALL_200kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.072	9.029	-0.043
0	118	9.137	9.045	-0.092
0	122	9.047	8.949	-0.098
3	61	9.081	8.976	-0.105
3	62	9.190	9.105	-0.085
3	63	9.071	8.926	-0.145
3	64	8.968	8.931	-0.037
3	65	8.995	8.975	-0.020
3	66	9.093	9.056	-0.037
5	67	8.917	8.917	0.000
5	68	9.194	9.199	0.005
5	69	9.023	9.026	0.003
5	70	8.966	8.884	-0.082
5	71	9.042	8.996	-0.046
5	72	9.181	9.206	0.025
10	73	9.081	8.937	-0.144
10	74	9.190	9.095	-0.095
10	75	9.071	8.956	-0.115
10	76	8.968	8.906	-0.062
10	77	8.995	8.941	-0.054
10	78	9.093	9.003	-0.090
15	79	8.917	8.898	-0.019
15	80	9.194	9.178	-0.016
15	81	9.023	8.998	-0.025
15	82	8.966	8.891	-0.075
15	83	9.042	9.010	-0.032
15	84	9.181	9.155	-0.026
30	86	9.081	8.962	-0.119
30	89	9.190	9.079	-0.111
30	91	9.071	8.969	-0.102
30	92	8.968	8.934	-0.034
30	94	8.995	8.930	-0.065
30	95	9.093	9.028	-0.065
35	100	8.917	8.841	-0.076
35	101	9.194	9.140	-0.054
35	102	9.023	8.969	-0.054
35	103	8.966	8.901	-0.065
35	104	9.042	8.963	-0.079
35	105	9.181	9.105	-0.076
50	106	8.982	8.797	-0.185
50	107	8.871	8.699	-0.172
50	108	9.100	8.880	-0.220
50	109	8.814	8.632	-0.182
50	110	9.028	8.813	-0.215
50	111	8.953	8.779	-0.174
55	113	9.149	8.892	-0.257
55	114	9.024	8.784	-0.240
55	115	9.062	8.836	-0.226
55	116	9.068	8.868	-0.200
55	126	8.974	8.784	-0.190
55	128	8.967	8.868	-0.099
100	129	9.058	8.905	-0.153
100	131	9.123	8.935	-0.188
100	132	9.171	9.036	-0.135
100	133	9.113	8.939	-0.174
100	134	9.076	8.913	-0.163
100	135	9.054	8.865	-0.189
100	138	9.046	8.902	-0.144
100	139	8.946	8.784	-0.162
100	140	9.066	8.945	-0.121
100	141	9.032	8.823	-0.209
100	142	9.053	8.807	-0.246
100	143	8.963	8.825	-0.138
100	144	8.949	8.762	-0.187
100	220	9.115	8.961	-0.154
100	221	8.961	8.757	-0.204
100	224	9.100	8.950	-0.150
100	225	9.066	8.904	-0.162
100	226	9.013	8.872	-0.141
100	228	9.093	8.953	-0.140
100	232	9.009	8.847	-0.162
100	233	9.025	8.788	-0.237
100	236	8.984	8.761	-0.223
100	238	9.056	8.914	-0.142
100	243	9.028	8.857	-0.171
105	244	9.047	8.850	-0.197
105	246	9.109	8.928	-0.181
105	248	9.060	8.856	-0.204
105	374	8.959	8.811	-0.148
105	376	9.133	8.936	-0.197
105	375	9.194	9.206	0.025
105	Max			
105	Average	9.048	8.925	-0.122
105	Min	8.814	8.632	-0.257
105	Std Dev	0.081	0.113	0.071



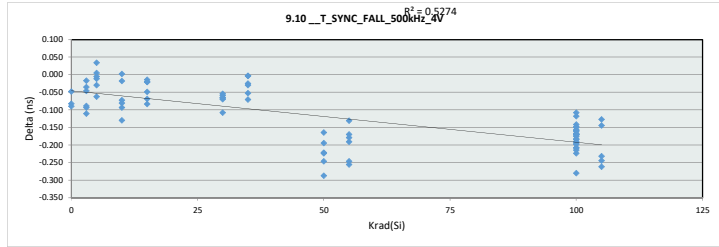
9.6_T_SYNC_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.949	8.926	8.884	8.906	8.891	8.930	8.841	8.632	8.784	8.757	8.811
Average	9.008	8.995	9.038	8.973	9.022	8.984	8.987	8.767	8.839	8.875	8.876
Max	9.045	9.105	9.206	9.095	9.178	9.079	9.140	8.880	8.892	9.036	8.936
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



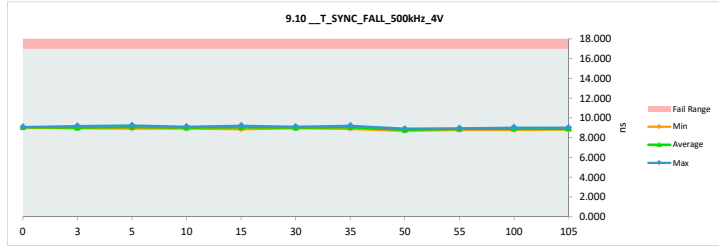
LDR TID Report  
TPS7H5001-SP

9.10_T_SYNC_FALL_500kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.155	9.072	-0.083
0	118	9.129	9.039	-0.090
0	122	9.067	9.019	-0.048
3	61	9.008	8.973	-0.035
3	62	9.191	9.174	-0.017
3	63	9.065	8.971	-0.094
3	64	9.065	8.954	-0.111
3	65	9.094	9.005	-0.089
3	66	9.109	9.063	-0.046
5	67	8.930	8.918	-0.012
5	68	9.273	9.243	-0.030
5	69	9.068	9.073	0.005
5	70	8.939	8.934	-0.005
5	71	9.087	9.024	-0.063
5	72	9.206	9.240	0.034
10	73	9.008	8.990	-0.018
10	74	9.191	9.097	-0.094
10	75	9.065	8.992	-0.073
10	76	9.065	8.935	-0.130
10	77	9.094	9.013	-0.081
10	78	9.109	9.111	0.002
15	79	8.930	8.861	-0.069
15	80	9.273	9.224	-0.049
15	81	9.068	9.047	-0.021
15	82	8.939	8.925	-0.014
15	83	9.087	9.003	-0.084
15	84	9.206	9.185	-0.021
30	86	9.008	8.954	-0.054
30	89	9.191	9.126	-0.065
30	91	9.065	9.006	-0.059
30	92	9.065	8.957	-0.108
30	94	9.094	9.024	-0.070
30	95	9.109	9.043	-0.066
35	100	8.930	8.905	-0.025
35	101	9.273	9.220	-0.053
35	102	9.068	9.065	-0.003
35	103	8.939	8.936	-0.003
35	104	9.087	9.016	-0.071
35	105	9.206	9.176	-0.030
50	106	8.981	8.759	-0.222
50	107	8.897	8.732	-0.165
50	108	9.190	8.902	-0.288
50	109	8.940	8.693	-0.247
50	110	9.100	8.876	-0.224
50	111	9.009	8.814	-0.195
55	113	9.160	8.913	-0.247
55	114	9.037	8.846	-0.191
55	115	9.065	8.886	-0.179
55	116	9.083	8.952	-0.131
55	126	9.046	8.790	-0.256
55	128	9.052	8.882	-0.170
100	129	9.035	8.885	-0.150
100	131	9.243	9.035	-0.208
100	132	9.198	9.022	-0.176
100	133	9.148	8.978	-0.170
100	134	9.117	8.934	-0.183
100	135	9.125	8.910	-0.215
100	138	9.058	8.897	-0.161
100	139	8.994	8.824	-0.170
100	140	9.159	8.988	-0.171
100	141	9.031	8.874	-0.157
100	142	9.088	8.903	-0.185
100	143	8.956	8.848	-0.108
100	144	8.982	8.834	-0.148
100	220	9.154	8.930	-0.224
100	221	8.986	8.791	-0.195
100	224	9.088	8.946	-0.142
100	225	9.185	8.905	-0.280
100	226	9.035	8.917	-0.118
100	228	9.204	9.013	-0.191
100	232	9.049	8.882	-0.167
100	233	9.065	8.894	-0.171
100	236	8.998	8.790	-0.208
100	238	9.098	8.900	-0.198
100	243	9.061	8.903	-0.158
105	244	9.141	8.909	-0.232
105	246	9.150	9.023	-0.127
105	248	9.138	8.876	-0.262
105	374	8.962	8.817	-0.145
105	376	9.165	8.920	-0.245
105	Max	9.273	9.243	0.034
105	Average	9.083	8.961	-0.122
105	Min	8.897	8.693	-0.288
105	Std Dev	0.089	0.116	0.081



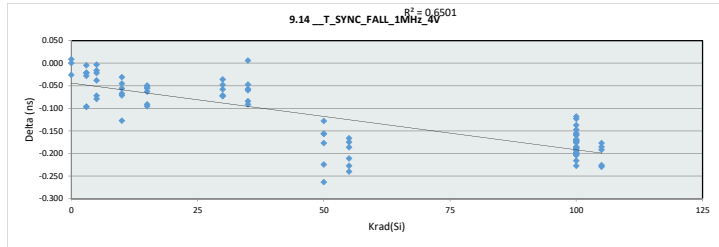
9.10_T_SYNC_FALL_500kHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.019	8.954	8.918	8.935	8.861	8.954	8.905	8.693	8.790	8.790	8.817
Average	9.043	9.023	9.072	9.023	9.041	9.018	9.053	8.796	8.878	8.908	8.909
Max	9.072	9.174	9.243	9.111	9.224	9.126	9.220	8.902	8.952	9.035	9.023
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



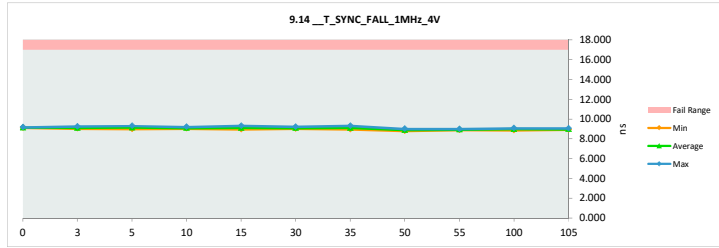
LDR TID Report  
TPS7H5001-SP

9.14_T_SYNC_FALL_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.159	9.158	-0.001
0	118	9.173	9.146	-0.027
0	122	9.076	9.084	0.008
3	61	9.133	9.104	-0.029
3	62	9.257	9.235	-0.022
3	63	9.136	9.039	-0.097
3	64	9.050	9.027	-0.023
3	65	9.151	9.053	-0.098
3	66	9.189	9.183	-0.006
5	67	8.999	8.982	-0.017
5	68	9.383	9.303	-0.080
5	69	9.165	9.161	-0.004
5	70	9.004	8.981	-0.023
5	71	9.158	9.085	-0.073
5	72	9.299	9.260	-0.039
10	73	9.133	9.076	-0.057
10	74	9.257	9.189	-0.068
10	75	9.136	9.064	-0.072
10	76	9.050	9.018	-0.032
10	77	9.151	9.023	-0.128
10	78	9.189	9.143	-0.046
15	79	8.999	8.945	-0.054
15	80	9.383	9.291	-0.092
15	81	9.165	9.115	-0.050
15	82	9.004	8.940	-0.064
15	83	9.158	9.062	-0.096
15	84	9.299	9.242	-0.057
30	86	9.133	9.096	-0.037
30	89	9.257	9.198	-0.059
30	91	9.136	9.064	-0.072
30	92	9.050	9.001	-0.049
30	94	9.151	9.077	-0.074
30	95	9.189	9.115	-0.074
35	100	8.999	8.951	-0.048
35	101	9.383	9.291	-0.092
35	102	9.165	9.107	-0.058
35	103	9.004	9.009	0.005
35	104	9.158	9.097	-0.061
35	105	9.299	9.214	-0.085
50	106	9.136	8.872	-0.264
50	107	8.990	8.765	-0.225
50	108	9.121	8.992	-0.129
50	109	8.949	8.771	-0.178
50	110	9.122	8.965	-0.157
50	111	9.043	8.886	-0.157
55	113	9.211	8.983	-0.228
55	114	9.136	8.895	-0.241
55	115	9.143	8.931	-0.212
55	116	9.102	8.926	-0.176
55	126	9.052	8.885	-0.167
55	128	9.124	8.937	-0.187
100	129	9.164	8.947	-0.217
100	131	9.227	9.027	-0.200
100	132	9.256	9.051	-0.205
100	133	9.223	9.048	-0.175
100	134	9.191	9.004	-0.187
100	135	9.126	9.007	-0.119
100	138	9.153	8.976	-0.177
100	139	9.043	8.882	-0.161
100	140	9.248	9.045	-0.203
100	141	9.143	8.984	-0.159
100	142	9.139	8.983	-0.156
100	143	9.105	8.981	-0.124
100	144	9.030	8.841	-0.189
100	220	9.206	9.034	-0.172
100	221	9.039	8.866	-0.173
100	224	9.181	9.043	-0.138
100	225	9.211	9.017	-0.194
100	226	9.125	8.977	-0.148
100	228	9.225	9.030	-0.195
100	232	9.095	8.909	-0.186
100	233	9.115	8.915	-0.200
100	236	9.071	8.843	-0.228
100	238	9.176	9.007	-0.169
100	243	9.158	8.968	-0.190
105	244	9.155	8.925	-0.230
105	246	9.201	8.974	-0.227
105	248	9.133	8.947	-0.186
105	374	9.082	8.904	-0.178
105	376	9.226	9.033	-0.193
	Max	9.383	9.303	0.008
	Average	9.147	9.026	-0.121
	Min	8.949	8.765	-0.264
	Std Dev	0.091	0.116	0.074



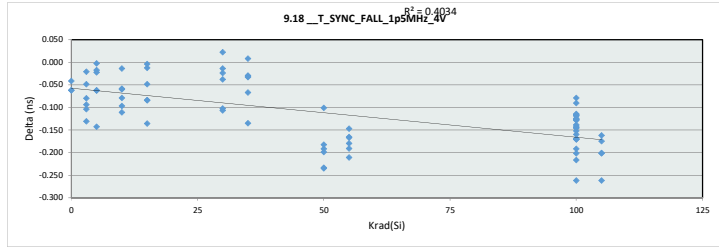
9.14_T_SYNC_FALL_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.084	9.027	8.981	9.018	8.940	9.001	8.951	8.765	8.885	8.841	8.904
Average	9.129	9.107	9.129	9.086	9.099	9.092	9.112	8.875	8.926	8.974	8.957
Max	9.158	9.235	9.303	9.189	9.291	9.198	9.291	8.992	8.983	9.051	9.033
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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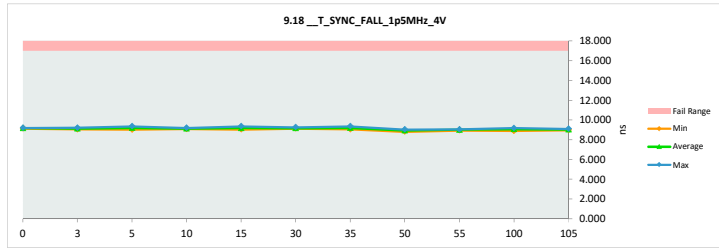
9.18_T_SYNC_FALL_1p5MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.244	9.182	-0.062
0	118	9.230	9.168	-0.062
0	122	9.145	9.103	-0.042
3	61	9.208	9.077	-0.131
3	62	9.289	9.185	-0.104
3	63	9.149	9.100	-0.049
3	64	9.120	9.026	-0.094
3	65	9.148	9.068	-0.080
3	66	9.209	9.188	-0.021
5	67	9.061	8.998	-0.063
5	68	9.480	9.337	-0.143
5	69	9.209	9.191	-0.018
5	70	9.055	9.052	-0.003
5	71	9.158	9.135	-0.023
5	72	9.347	9.285	-0.062
10	73	9.208	9.111	-0.097
10	74	9.289	9.178	-0.111
10	75	9.149	9.135	-0.014
10	76	9.120	9.060	-0.060
10	77	9.148	9.069	-0.079
10	78	9.209	9.150	-0.059
15	79	9.061	9.057	-0.004
15	80	9.480	9.344	-0.136
15	81	9.209	9.196	-0.013
15	82	9.055	9.006	-0.049
15	83	9.158	9.074	-0.084
15	84	9.347	9.263	-0.084
30	86	9.208	9.101	-0.107
30	89	9.289	9.186	-0.103
30	91	9.149	9.111	-0.038
30	92	9.120	9.106	-0.014
30	94	9.148	9.124	-0.024
30	95	9.209	9.231	0.022
35	100	9.061	9.031	-0.030
35	101	9.480	9.345	-0.135
35	102	9.209	9.177	-0.032
35	103	9.055	9.063	0.008
35	104	9.158	9.125	-0.033
35	105	9.347	9.280	-0.067
50	106	9.138	8.904	-0.234
50	107	8.962	8.881	-0.101
50	108	9.216	9.017	-0.199
50	109	8.968	8.776	-0.192
50	110	9.183	9.000	-0.183
50	111	9.152	8.917	-0.235
55	113	9.173	9.026	-0.147
55	114	9.202	9.011	-0.191
55	115	9.172	8.992	-0.180
55	116	9.214	9.003	-0.211
55	126	9.077	8.911	-0.166
55	128	9.160	8.993	-0.167
100	129	9.184	9.024	-0.160
100	131	9.284	9.115	-0.169
100	132	9.282	9.155	-0.127
100	133	9.291	9.146	-0.145
100	134	9.254	9.062	-0.192
100	135	9.202	9.075	-0.127
100	138	9.164	9.039	-0.125
100	139	9.077	8.935	-0.142
100	140	9.217	9.088	-0.129
100	141	9.249	8.987	-0.262
100	142	9.214	9.068	-0.146
100	143	9.102	9.011	-0.091
100	144	9.062	8.946	-0.116
100	220	9.242	9.071	-0.171
100	221	9.035	8.864	-0.171
100	224	9.223	9.104	-0.119
100	225	9.234	9.032	-0.202
100	226	9.139	9.024	-0.115
100	228	9.229	9.150	-0.079
100	232	9.127	8.988	-0.139
100	233	9.120	9.002	-0.118
100	236	9.122	8.951	-0.171
100	238	9.211	9.059	-0.152
100	243	9.251	9.034	-0.217
105	244	9.255	8.993	-0.262
105	246	9.230	9.068	-0.162
105	248	9.208	9.006	-0.202
105	374	9.112	8.937	-0.175
105	376	9.253	9.052	-0.201
105	Max	9.480	9.345	0.022
105	Average	9.189	9.075	-0.114
105	Min	8.968	8.776	-0.262
105	Std Dev	0.098	0.109	0.069



9.18_T_SYNC_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

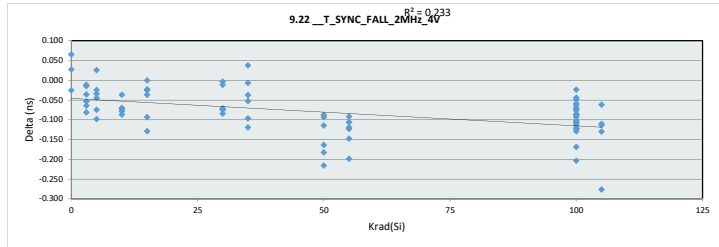
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.103	9.026	8.998	9.060	9.006	9.101	9.031	8.776	8.911	8.864	8.937
Average	9.151	9.107	9.166	9.117	9.157	9.143	9.170	8.916	8.989	9.039	9.011
Max	9.182	9.188	9.337	9.178	9.344	9.231	9.345	9.017	9.026	9.155	9.068
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



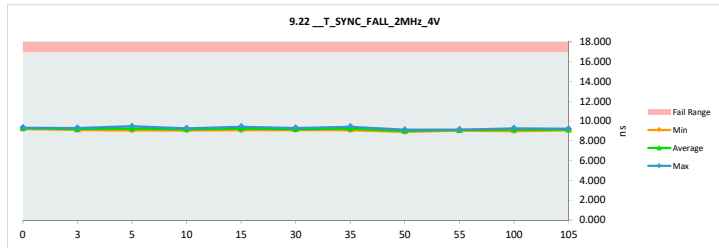
LDR TID Report  
TPS7H5001-SP

9.22_T_SYNC_FALL_2MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.207	9.271	0.064
0	118	9.335	9.308	-0.027
0	122	9.161	9.187	0.026
3	61	9.206	9.140	-0.066
3	62	9.351	9.296	-0.055
3	63	9.244	9.161	-0.083
3	64	9.116	9.079	-0.037
3	65	9.142	9.129	-0.013
3	66	9.264	9.248	-0.016
5	67	9.072	9.096	0.024
5	68	9.542	9.466	-0.076
5	69	9.246	9.200	-0.046
5	70	9.072	9.046	-0.026
5	71	9.236	9.136	-0.100
5	72	9.426	9.390	-0.036
10	73	9.206	9.135	-0.071
10	74	9.351	9.263	-0.088
10	75	9.244	9.164	-0.080
10	76	9.116	9.042	-0.074
10	77	9.142	9.104	-0.038
10	78	9.264	9.190	-0.074
15	79	9.072	9.070	-0.002
15	80	9.542	9.412	-0.130
15	81	9.246	9.208	-0.038
15	82	9.072	9.048	-0.024
15	83	9.236	9.141	-0.095
15	84	9.426	9.399	-0.027
30	86	9.206	9.130	-0.076
30	89	9.351	9.276	-0.075
30	91	9.244	9.158	-0.086
30	92	9.116	9.103	-0.013
30	94	9.142	9.137	-0.005
30	95	9.264	9.192	-0.072
35	100	9.072	9.064	-0.008
35	101	9.542	9.421	-0.121
35	102	9.246	9.207	-0.039
35	103	9.072	9.108	0.036
35	104	9.236	9.138	-0.098
35	105	9.426	9.372	-0.054
50	106	9.163	8.998	-0.165
50	107	9.018	8.902	-0.116
50	108	9.200	9.111	-0.089
50	109	9.013	8.918	-0.095
50	110	9.271	9.054	-0.217
50	111	9.118	8.934	-0.184
55	113	9.208	9.087	-0.121
55	114	9.240	9.091	-0.149
55	115	9.257	9.057	-0.200
55	116	9.247	9.139	-0.108
55	126	9.153	9.028	-0.125
55	128	9.175	9.082	-0.093
100	129	9.276	9.152	-0.124
100	131	9.383	9.213	-0.170
100	132	9.316	9.238	-0.078
100	133	9.412	9.207	-0.205
100	134	9.245	9.181	-0.064
100	135	9.141	9.091	-0.050
100	138	9.211	9.124	-0.087
100	139	9.087	9.062	-0.025
100	140	9.353	9.263	-0.090
100	141	9.158	9.113	-0.045
100	142	9.314	9.198	-0.116
100	143	9.191	9.102	-0.089
100	144	9.138	9.030	-0.108
100	220	9.340	9.229	-0.111
100	221	9.100	8.977	-0.123
100	224	9.297	9.226	-0.071
100	225	9.184	9.054	-0.130
100	226	9.205	9.081	-0.124
100	228	9.327	9.232	-0.095
100	232	9.186	9.112	-0.074
100	233	9.150	9.043	-0.107
100	236	9.070	9.010	-0.060
100	238	9.259	9.155	-0.104
100	243	9.270	9.147	-0.123
105	244	9.352	9.074	-0.278
105	246	9.340	9.209	-0.131
105	248	9.249	9.137	-0.112
105	374	9.151	9.088	-0.063
105	376	9.278	9.163	-0.115
	Max	9.542	9.466	0.064
	Average	9.231	9.149	-0.082
	Min	9.013	8.902	-0.278
	Std Dev	0.116	0.112	0.058



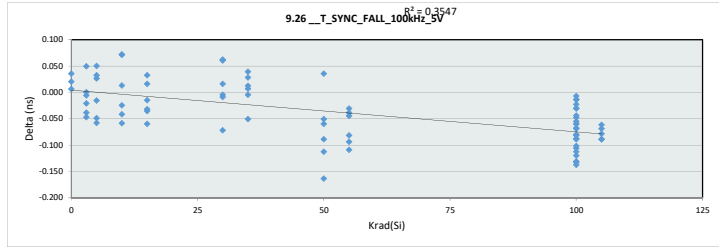
9.22_T_SYNC_FALL_2MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.187	9.079	9.046	9.042	9.048	9.103	9.064	8.902	9.028	8.977	9.074
Average	9.255	9.176	9.222	9.150	9.213	9.166	9.218	8.986	9.081	9.135	9.134
Max	9.308	9.296	9.466	9.263	9.412	9.276	9.421	9.111	9.139	9.263	9.209
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



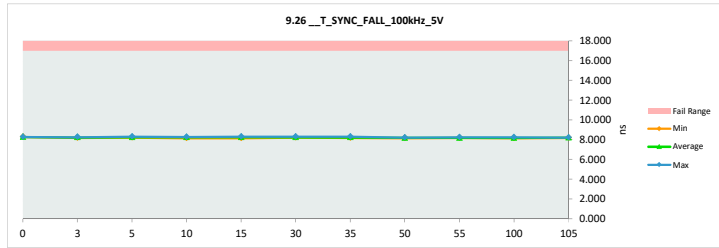
LDR TID Report  
TPS7H5001-SP

9.26_T_SYNC_FALL_100kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.261	8.281	0.020
0	118	8.236	8.271	0.035
0	122	8.261	8.267	0.006
3	61	8.186	8.180	-0.006
3	62	8.304	8.257	-0.047
3	63	8.230	8.209	-0.021
3	64	8.213	8.213	0.000
3	65	8.192	8.241	0.049
3	66	8.282	8.243	-0.039
5	67	8.174	8.224	0.050
5	68	8.266	8.298	0.032
5	69	8.277	8.219	-0.058
5	70	8.193	8.219	0.026
5	71	8.275	8.226	-0.049
5	72	8.284	8.268	-0.016
10	73	8.186	8.257	0.071
10	74	8.304	8.262	-0.042
10	75	8.230	8.243	0.013
10	76	8.213	8.154	-0.059
10	77	8.192	8.264	0.072
10	78	8.282	8.257	-0.025
15	79	8.174	8.138	-0.036
15	80	8.266	8.298	0.032
15	81	8.277	8.245	-0.032
15	82	8.193	8.209	0.016
15	83	8.275	8.260	-0.015
15	84	8.284	8.224	-0.060
30	86	8.186	8.246	0.060
30	89	8.304	8.299	-0.005
30	91	8.230	8.221	-0.009
30	92	8.213	8.229	0.016
30	94	8.192	8.254	0.062
30	95	8.282	8.210	-0.072
35	100	8.174	8.181	0.007
35	101	8.266	8.294	0.028
35	102	8.277	8.272	-0.005
35	103	8.193	8.232	0.039
35	104	8.275	8.224	-0.051
35	105	8.284	8.296	0.012
50	106	8.260	8.209	-0.051
50	107	8.216	8.156	-0.060
50	108	8.297	8.133	-0.164
50	109	8.262	8.149	-0.113
50	110	8.257	8.168	-0.089
50	111	8.161	8.196	0.035
55	113	8.218	8.173	-0.045
55	114	8.258	8.164	-0.094
55	115	8.277	8.238	-0.039
55	116	8.247	8.216	-0.031
55	126	8.276	8.167	-0.109
55	128	8.252	8.170	-0.082
100	129	8.297	8.191	-0.106
100	131	8.284	8.253	-0.031
100	132	8.291	8.231	-0.060
100	133	8.317	8.179	-0.138
100	134	8.329	8.196	-0.133
100	135	8.236	8.176	-0.060
100	138	8.266	8.164	-0.102
100	139	8.235	8.146	-0.089
100	140	8.259	8.179	-0.080
100	141	8.259	8.177	-0.082
100	142	8.277	8.189	-0.088
100	143	8.185	8.171	-0.014
100	144	8.239	8.195	-0.044
100	220	8.205	8.198	-0.007
100	221	8.271	8.151	-0.120
100	224	8.304	8.191	-0.113
100	225	8.286	8.217	-0.069
100	226	8.216	8.202	-0.014
100	228	8.292	8.224	-0.068
100	232	8.219	8.189	-0.030
100	233	8.245	8.197	-0.048
100	236	8.204	8.149	-0.055
100	238	8.315	8.184	-0.131
100	243	8.207	8.184	-0.023
105	244	8.299	8.210	-0.089
105	246	8.295	8.206	-0.089
105	248	8.255	8.193	-0.062
105	374	8.237	8.168	-0.069
105	376	8.277	8.198	-0.079
105	Max	8.329	8.299	0.072
105	Average	8.250	8.213	-0.037
105	Min	8.161	8.133	-0.164
105	Std Dev	0.041	0.042	0.054



9.26_T_SYNC_Fa											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.267	8.180	8.219	8.154	8.138	8.210	8.181	8.133	8.164	8.146	8.168
Average	8.273	8.224	8.242	8.240	8.229	8.243	8.250	8.169	8.188	8.189	8.195
Max	8.281	8.257	8.298	8.264	8.298	8.299	8.296	8.209	8.238	8.253	8.210
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

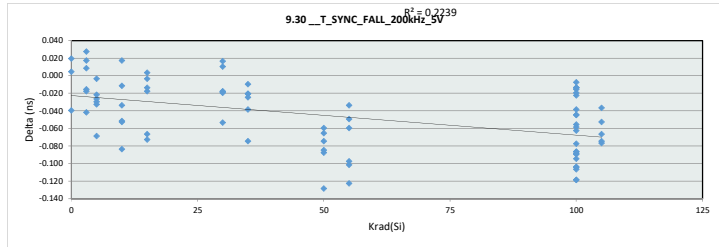




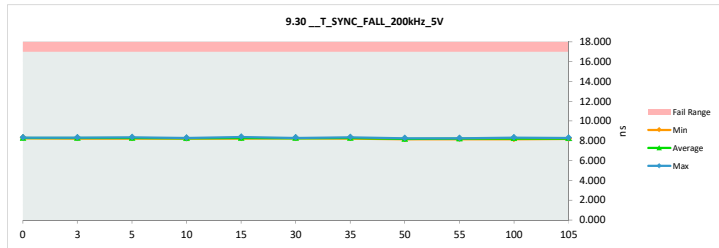
LDR TID Report  
TPS7H5001-SP

9.30_T_SYNC_FALL_200kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.313	8.332	0.019
0	118	8.323	8.283	-0.040
0	122	8.269	8.273	0.004
3	61	8.307	8.324	0.017
3	62	8.332	8.316	-0.016
3	63	8.282	8.290	0.008
3	64	8.284	8.242	-0.042
3	65	8.241	8.268	0.027
3	66	8.336	8.318	-0.018
5	67	8.280	8.258	-0.022
5	68	8.372	8.368	-0.004
5	69	8.312	8.282	-0.030
5	70	8.274	8.241	-0.033
5	71	8.339	8.270	-0.069
5	72	8.326	8.299	-0.027
10	73	8.307	8.223	-0.084
10	74	8.332	8.279	-0.053
10	75	8.282	8.248	-0.034
10	76	8.284	8.272	-0.012
10	77	8.241	8.258	0.017
10	78	8.336	8.284	-0.052
15	79	8.280	8.276	-0.004
15	80	8.372	8.375	0.003
15	81	8.312	8.245	-0.067
15	82	8.274	8.256	-0.018
15	83	8.339	8.266	-0.073
15	84	8.326	8.312	-0.014
30	86	8.307	8.289	-0.018
30	89	8.332	8.312	-0.020
30	91	8.282	8.298	0.016
30	92	8.284	8.265	-0.019
30	94	8.241	8.251	0.010
30	95	8.336	8.282	-0.054
35	100	8.280	8.241	-0.039
35	101	8.372	8.351	-0.021
35	102	8.312	8.287	-0.025
35	103	8.274	8.253	-0.021
35	104	8.339	8.264	-0.075
35	105	8.326	8.316	-0.010
50	106	8.266	8.178	-0.088
50	107	8.288	8.159	-0.129
50	108	8.327	8.252	-0.075
50	109	8.234	8.168	-0.066
50	110	8.308	8.223	-0.085
50	111	8.271	8.211	-0.060
55	113	8.279	8.245	-0.034
55	114	8.321	8.219	-0.102
55	115	8.304	8.206	-0.098
55	116	8.299	8.239	-0.060
55	126	8.307	8.184	-0.123
55	128	8.294	8.244	-0.050
100	129	8.337	8.250	-0.087
100	131	8.354	8.235	-0.119
100	132	8.349	8.260	-0.089
100	133	8.328	8.265	-0.063
100	134	8.325	8.220	-0.105
100	135	8.305	8.282	-0.023
100	138	8.298	8.239	-0.059
100	139	8.320	8.213	-0.107
100	140	8.336	8.291	-0.045
100	141	8.284	8.165	-0.119
100	142	8.329	8.273	-0.056
100	143	8.286	8.191	-0.095
100	144	8.254	8.209	-0.045
100	220	8.333	8.317	-0.016
100	221	8.269	8.249	-0.020
100	224	8.323	8.284	-0.039
100	225	8.316	8.238	-0.078
100	226	8.270	8.256	-0.014
100	228	8.329	8.225	-0.104
100	232	8.307	8.217	-0.090
100	233	8.275	8.267	-0.008
100	236	8.252	8.238	-0.014
100	238	8.295	8.281	-0.014
100	243	8.282	8.195	-0.087
105	244	8.327	8.290	-0.037
105	246	8.331	8.254	-0.077
105	248	8.310	8.257	-0.053
105	374	8.272	8.205	-0.067
105	375	8.307	8.232	-0.075
105	376	8.372	8.375	0.027
Max		8.305	8.259	-0.046
Average		8.234	8.159	-0.129
Min		8.234	8.159	-0.129
Std Dev		0.031	0.044	0.038



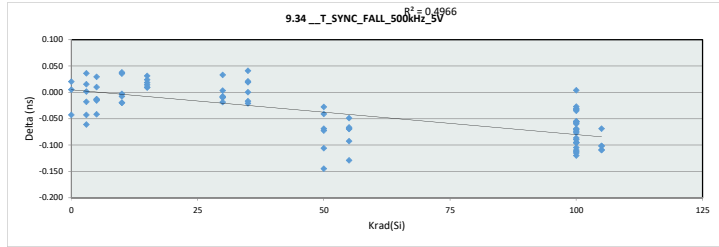
9.30_T_SYNC_FALL_200kHz_5V	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	8.273 8.242 8.241 8.223 8.245 8.251 8.241 8.159 8.184 8.165 8.205
Average	8.296 8.293 8.286 8.261 8.288 8.283 8.285 8.199 8.223 8.244 8.248
Max	8.332 8.324 8.368 8.284 8.375 8.312 8.351 8.252 8.245 8.317 8.290
UL	17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000



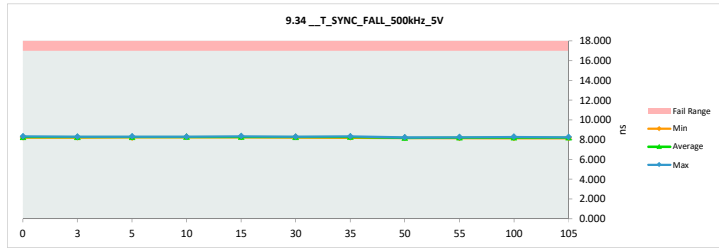
LDR TID Report  
TPS7H5001-SP

9.34 T_SYNC_FALL_500kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.303	8.308	0.005
0	118	8.240	8.197	-0.043
0	122	8.231	8.251	0.020
3	61	8.264	8.279	0.015
3	62	8.287	8.226	-0.061
3	63	8.236	8.218	-0.018
3	64	8.233	8.234	0.001
3	65	8.251	8.208	-0.043
3	66	8.246	8.282	0.036
5	67	8.199	8.186	-0.013
5	68	8.307	8.294	-0.013
5	69	8.246	8.230	-0.016
5	70	8.216	8.245	0.029
5	71	8.264	8.222	-0.042
5	72	8.270	8.280	0.010
10	73	8.264	8.244	-0.020
10	74	8.287	8.267	-0.020
10	75	8.236	8.271	0.035
10	76	8.233	8.225	-0.008
10	77	8.251	8.248	-0.003
10	78	8.246	8.284	0.038
15	79	8.199	8.230	0.031
15	80	8.307	8.325	0.018
15	81	8.246	8.270	0.024
15	82	8.216	8.225	0.009
15	83	8.264	8.273	0.009
15	84	8.270	8.284	0.014
30	86	8.264	8.245	-0.019
30	89	8.287	8.278	-0.009
30	91	8.236	8.269	0.033
30	92	8.233	8.223	-0.010
30	94	8.251	8.243	-0.008
30	95	8.246	8.249	0.003
35	100	8.199	8.218	0.019
35	101	8.307	8.307	0.000
35	102	8.246	8.267	0.021
35	103	8.216	8.199	-0.017
35	104	8.264	8.243	-0.021
35	105	8.270	8.311	0.041
50	106	8.291	8.185	-0.106
50	107	8.214	8.173	-0.041
50	108	8.293	8.220	-0.073
50	109	8.200	8.172	-0.028
50	110	8.272	8.127	-0.145
50	111	8.245	8.176	-0.069
55	113	8.272	8.223	-0.049
55	114	8.258	8.189	-0.069
55	115	8.296	8.203	-0.093
55	116	8.252	8.182	-0.070
55	126	8.259	8.130	-0.129
55	128	8.293	8.226	-0.067
100	129	8.248	8.143	-0.105
100	131	8.325	8.230	-0.095
100	132	8.314	8.218	-0.096
100	133	8.281	8.166	-0.115
100	134	8.299	8.179	-0.120
100	135	8.292	8.182	-0.110
100	138	8.220	8.162	-0.058
100	139	8.214	8.137	-0.077
100	140	8.277	8.206	-0.071
100	141	8.286	8.190	-0.096
100	142	8.266	8.192	-0.074
100	143	8.224	8.228	0.004
100	144	8.196	8.169	-0.027
100	220	8.294	8.207	-0.087
100	221	8.230	8.197	-0.033
100	224	8.296	8.261	-0.035
100	225	8.285	8.225	-0.060
100	226	8.261	8.205	-0.056
100	228	8.279	8.224	-0.055
100	232	8.233	8.202	-0.031
100	233	8.243	8.187	-0.056
100	236	8.268	8.178	-0.090
100	238	8.293	8.180	-0.113
100	243	8.241	8.172	-0.069
105	244	8.269	8.200	-0.069
105	246	8.327	8.225	-0.102
105	248	8.298	8.188	-0.110
105	374	8.238	8.129	-0.109
105	376	8.330	8.228	-0.102
105	375	8.330	8.325	0.004
	Max	8.330	8.325	0.004
	Average	8.260	8.221	-0.039
	Min	8.196	8.127	-0.069
	Std Dev	0.033	0.045	0.048



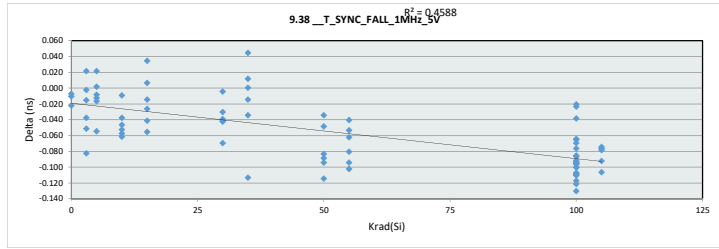
9.34 T_SYNC FA											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.197	8.208	8.186	8.225	8.225	8.223	8.199	8.127	8.130	8.137	8.129
Average	8.252	8.241	8.243	8.257	8.268	8.251	8.258	8.176	8.192	8.193	8.194
Max	8.308	8.282	8.294	8.284	8.325	8.278	8.311	8.220	8.226	8.261	8.228
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



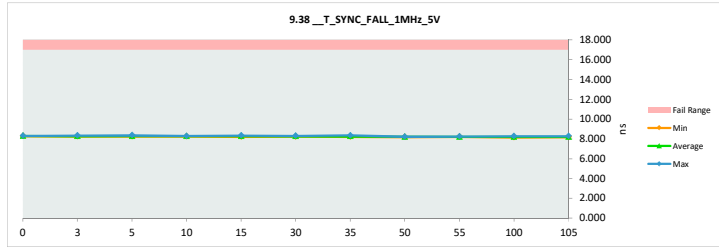
LDR TID Report  
TPS7H5001-SP

9.38 T_SYNC_FALL_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.313	8.290	-0.023
0	118	8.291	8.283	-0.008
0	122	8.273	8.262	-0.011
3	61	8.262	8.283	0.021
3	62	8.331	8.328	-0.003
3	63	8.296	8.244	-0.052
3	64	8.249	8.233	-0.016
3	65	8.303	8.220	-0.083
3	66	8.298	8.260	-0.038
5	67	8.227	8.218	-0.009
5	68	8.336	8.337	0.001
5	69	8.274	8.295	0.021
5	70	8.242	8.229	-0.013
5	71	8.295	8.240	-0.055
5	72	8.316	8.299	-0.017
10	73	8.262	8.252	-0.010
10	74	8.331	8.269	-0.062
10	75	8.296	8.249	-0.047
10	76	8.249	8.211	-0.038
10	77	8.303	8.250	-0.053
10	78	8.298	8.240	-0.058
15	79	8.227	8.261	0.034
15	80	8.336	8.321	-0.015
15	81	8.274	8.280	0.006
15	82	8.242	8.200	-0.042
15	83	8.295	8.239	-0.056
15	84	8.316	8.289	-0.027
30	86	8.262	8.222	-0.040
30	89	8.331	8.288	-0.043
30	91	8.296	8.265	-0.031
30	92	8.249	8.207	-0.042
30	94	8.303	8.233	-0.070
30	95	8.298	8.293	-0.005
35	100	8.227	8.271	0.044
35	101	8.336	8.336	0.000
35	102	8.274	8.239	-0.035
35	103	8.242	8.227	-0.015
35	104	8.295	8.181	-0.114
35	105	8.316	8.327	0.011
50	106	8.254	8.205	-0.049
50	107	8.252	8.168	-0.084
50	108	8.294	8.199	-0.095
50	109	8.275	8.161	-0.115
50	110	8.277	8.188	-0.089
50	111	8.211	8.176	-0.035
55	113	8.251	8.197	-0.054
55	114	8.262	8.181	-0.081
55	115	8.258	8.217	-0.041
55	116	8.263	8.200	-0.063
55	126	8.285	8.190	-0.095
55	128	8.307	8.204	-0.103
100	129	8.306	8.205	-0.101
100	131	8.343	8.247	-0.096
100	132	8.326	8.256	-0.070
100	133	8.293	8.171	-0.122
100	134	8.326	8.208	-0.118
100	135	8.274	8.209	-0.065
100	138	8.308	8.214	-0.094
100	139	8.268	8.191	-0.077
100	140	8.294	8.183	-0.111
100	141	8.286	8.190	-0.096
100	142	8.274	8.208	-0.066
100	143	8.268	8.160	-0.108
100	144	8.252	8.157	-0.095
100	220	8.318	8.226	-0.092
100	221	8.251	8.164	-0.087
100	224	8.294	8.185	-0.109
100	225	8.316	8.215	-0.101
100	226	8.258	8.219	-0.039
100	228	8.283	8.262	-0.021
100	232	8.271	8.140	-0.131
100	233	8.264	8.240	-0.024
100	236	8.246	8.160	-0.086
100	238	8.317	8.207	-0.110
100	243	8.274	8.177	-0.097
105	244	8.262	8.155	-0.107
105	246	8.317	8.240	-0.077
105	248	8.280	8.187	-0.093
105	374	8.225	8.150	-0.075
105	375	8.318	8.239	-0.079
105	Max	8.343	8.337	0.044
105	Average	8.283	8.228	-0.056
105	Min	8.211	8.140	-0.131
105	Std Dev	0.031	0.047	0.042



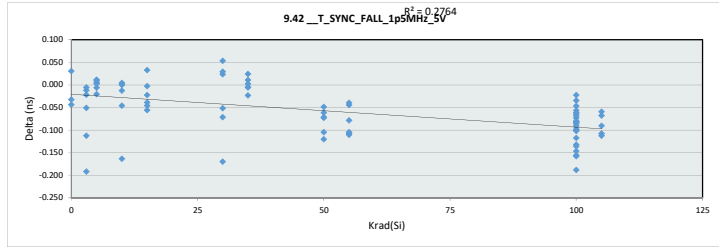
9.38 T_SYNC FA											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.262	8.220	8.218	8.211	8.200	8.207	8.181	8.161	8.181	8.140	8.150
Average	8.278	8.261	8.270	8.245	8.265	8.251	8.264	8.183	8.198	8.200	8.194
Max	8.290	8.328	8.337	8.269	8.321	8.293	8.336	8.205	8.217	8.262	8.240
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



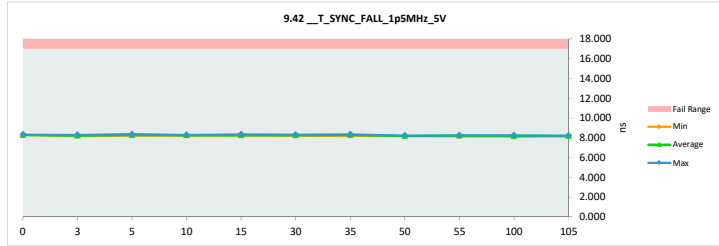
LDR TID Report  
TPS7H5001-SP

9.42_T_SYNC_FALL_1p5MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.302	8.258	-0.044
0	118	8.282	8.312	0.030
0	122	8.300	8.267	-0.033
3	61	8.300	8.187	-0.113
3	62	8.419	8.227	-0.192
3	63	8.220	8.208	-0.012
3	64	8.216	8.194	-0.022
3	65	8.249	8.198	-0.051
3	66	8.272	8.266	-0.006
5	67	8.215	8.208	-0.007
5	68	8.340	8.351	0.011
5	69	8.244	8.223	-0.021
5	70	8.238	8.240	0.002
5	71	8.277	8.282	0.005
5	72	8.332	8.341	0.009
10	73	8.300	8.254	-0.046
10	74	8.419	8.255	-0.164
10	75	8.220	8.221	0.001
10	76	8.216	8.220	0.004
10	77	8.249	8.249	0.000
10	78	8.272	8.259	-0.013
15	79	8.215	8.192	-0.023
15	80	8.340	8.337	-0.003
15	81	8.244	8.276	0.032
15	82	8.238	8.199	-0.039
15	83	8.277	8.231	-0.046
15	84	8.332	8.276	-0.056
30	86	8.300	8.228	-0.072
30	89	8.419	8.249	-0.170
30	91	8.220	8.249	0.029
30	92	8.216	8.239	0.023
30	94	8.249	8.302	0.053
30	95	8.272	8.220	-0.052
35	100	8.215	8.239	0.024
35	101	8.340	8.334	-0.006
35	102	8.244	8.254	0.010
35	103	8.238	8.214	-0.024
35	104	8.277	8.279	0.002
35	105	8.332	8.327	-0.005
50	106	8.258	8.137	-0.121
50	107	8.211	8.162	-0.049
50	108	8.300	8.195	-0.105
50	109	8.250	8.177	-0.073
50	110	8.272	8.200	-0.072
50	111	8.244	8.182	-0.062
55	113	8.243	8.138	-0.105
55	114	8.274	8.167	-0.107
55	115	8.253	8.213	-0.040
55	116	8.221	8.176	-0.045
55	126	8.321	8.242	-0.079
55	128	8.325	8.214	-0.111
100	129	8.280	8.199	-0.081
100	131	8.265	8.230	-0.035
100	132	8.394	8.205	-0.189
100	133	8.385	8.249	-0.136
100	134	8.342	8.184	-0.158
100	135	8.230	8.147	-0.083
100	138	8.256	8.164	-0.092
100	139	8.252	8.151	-0.101
100	140	8.283	8.218	-0.065
100	141	8.270	8.183	-0.087
100	142	8.310	8.241	-0.069
100	143	8.220	8.158	-0.062
100	144	8.287	8.188	-0.099
100	220	8.284	8.237	-0.047
100	221	8.245	8.112	-0.133
100	224	8.258	8.177	-0.081
100	225	8.302	8.245	-0.057
100	226	8.302	8.184	-0.118
100	228	8.299	8.152	-0.147
100	232	8.254	8.231	-0.023
100	233	8.272	8.190	-0.082
100	236	8.218	8.116	-0.102
100	238	8.248	8.174	-0.074
100	243	8.294	8.138	-0.156
105	244	8.237	8.169	-0.068
105	246	8.306	8.198	-0.108
105	248	8.296	8.205	-0.091
105	374	8.190	8.130	-0.060
105	376	8.302	8.189	-0.113
105	Max	8.419	8.351	0.053
105	Average	8.276	8.218	-0.058
105	Min	8.190	8.112	-0.192
105	Std Dev	0.050	0.053	0.056



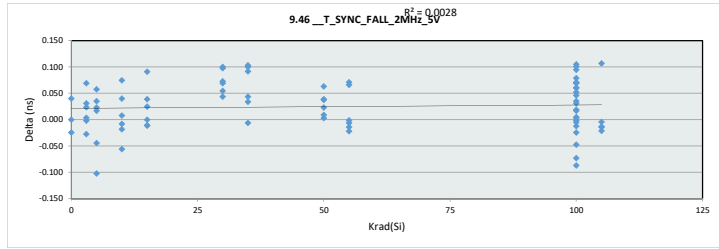
9.42_T_SYNC_FALL_1p5MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.258	8.187	8.208	8.220	8.192	8.220	8.214	8.137	8.138	8.112	8.130
Average	8.279	8.213	8.274	8.243	8.252	8.248	8.275	8.176	8.192	8.186	8.178
Max	8.312	8.266	8.351	8.259	8.337	8.302	8.334	8.200	8.242	8.249	8.205
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



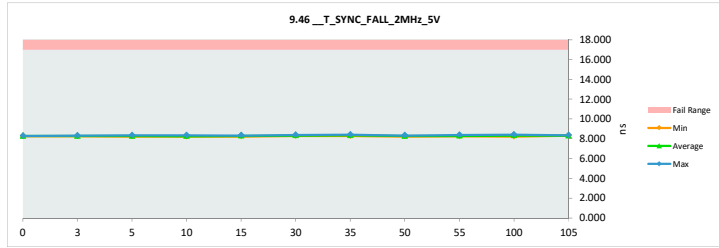
LDR TID Report  
TPS7H5001-SP

9.46 T_SYNC_FALL_2MHZ_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.228	8.267	0.039
0	118	8.296	8.295	-0.001
0	122	8.289	8.264	-0.025
3	61	8.283	8.280	-0.003
3	62	8.291	8.314	0.023
3	63	8.232	8.300	0.068
3	64	8.283	8.255	-0.028
3	65	8.233	8.263	0.030
3	66	8.296	8.299	0.003
5	67	8.330	8.285	-0.045
5	68	8.313	8.347	0.034
5	69	8.254	8.270	0.016
5	70	8.228	8.285	0.057
5	71	8.272	8.294	0.022
5	72	8.345	8.242	-0.103
10	73	8.283	8.226	-0.057
10	74	8.291	8.365	0.074
10	75	8.232	8.271	0.039
10	76	8.283	8.274	-0.009
10	77	8.233	8.214	-0.019
10	78	8.296	8.303	0.007
15	79	8.330	8.329	-0.001
15	80	8.313	8.337	0.024
15	81	8.254	8.242	-0.012
15	82	8.228	8.318	0.090
15	83	8.272	8.310	0.038
15	84	8.345	8.333	-0.012
30	86	8.283	8.380	0.097
30	89	8.291	8.363	0.072
30	91	8.232	8.286	0.054
30	92	8.283	8.326	0.043
30	94	8.233	8.301	0.068
30	95	8.296	8.395	0.099
35	100	8.330	8.363	0.033
35	101	8.313	8.412	0.099
35	102	8.254	8.297	0.043
35	103	8.228	8.330	0.102
35	104	8.272	8.363	0.091
35	105	8.345	8.338	-0.007
50	106	8.326	8.334	0.008
50	107	8.249	8.251	0.002
50	108	8.263	8.300	0.037
50	109	8.207	8.245	0.038
50	110	8.281	8.343	0.062
50	111	8.260	8.282	0.022
55	113	8.317	8.387	0.070
55	114	8.257	8.322	0.065
55	115	8.299	8.292	-0.007
55	116	8.312	8.297	-0.015
55	126	8.280	8.257	-0.023
55	128	8.305	8.303	-0.002
100	129	8.278	8.347	0.069
100	131	8.377	8.411	0.034
100	132	8.318	8.422	0.104
100	133	8.370	8.388	0.018
100	134	8.330	8.408	0.078
100	135	8.282	8.234	-0.048
100	138	8.261	8.361	0.100
100	139	8.252	8.320	0.068
100	140	8.312	8.406	0.094
100	141	8.312	8.310	-0.002
100	142	8.313	8.225	-0.088
100	143	8.299	8.286	-0.013
100	144	8.328	8.322	-0.006
100	220	8.389	8.315	-0.074
100	221	8.283	8.353	0.070
100	224	8.287	8.346	0.059
100	225	8.311	8.356	0.045
100	226	8.337	8.388	0.051
100	228	8.394	8.369	-0.025
100	232	8.341	8.344	0.003
100	233	8.266	8.326	0.060
100	236	8.275	8.279	0.004
100	238	8.344	8.374	0.030
100	243	8.335	8.351	0.016
105	244	8.351	8.329	-0.022
105	246	8.343	8.338	-0.005
105	248	8.354	8.339	-0.015
105	374	8.208	8.314	0.106
105	375	8.363	8.349	-0.014
105	376	8.394	8.422	0.028
Max		8.394	8.422	0.028
Average		8.293	8.318	0.025
Min		8.207	8.214	-0.103
Std Dev		0.043	0.049	0.047



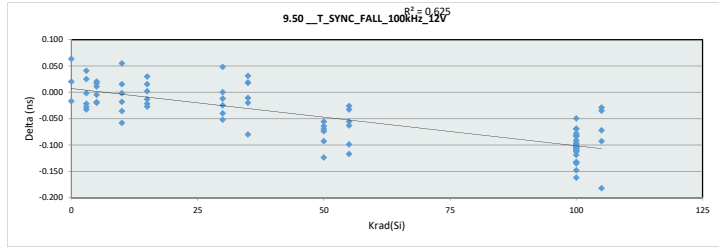
9.46 T_SYNC FA	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	8.264 8.255 8.242 8.214 8.242 8.286 8.297 8.245 8.257 8.225 8.314
Average	8.275 8.285 8.287 8.276 8.312 8.342 8.351 8.293 8.310 8.343 8.334
Max	8.295 8.314 8.347 8.365 8.337 8.395 8.412 8.343 8.387 8.422 8.349
UL	17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000



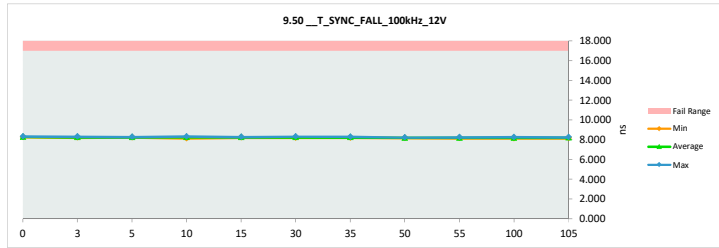
LDR TID Report  
TPS7H5001-SP

9.50_T_SYNC_FALL_100kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.242	8.305	0.063
0	118	8.306	8.289	-0.017
0	122	8.213	8.233	0.020
3	61	8.267	8.234	-0.033
3	62	8.236	8.234	-0.002
3	63	8.161	8.202	0.041
3	64	8.189	8.214	0.025
3	65	8.323	8.301	-0.022
3	66	8.263	8.235	-0.028
5	67	8.220	8.231	0.011
5	68	8.256	8.236	-0.020
5	69	8.203	8.220	0.017
5	70	8.268	8.249	-0.019
5	71	8.230	8.225	-0.005
5	72	8.246	8.266	0.020
10	73	8.267	8.282	0.015
10	74	8.236	8.234	-0.002
10	75	8.161	8.125	-0.036
10	76	8.189	8.244	0.055
10	77	8.323	8.305	-0.018
10	78	8.263	8.205	-0.058
15	79	8.220	8.193	-0.027
15	80	8.256	8.234	-0.022
15	81	8.203	8.233	0.030
15	82	8.268	8.255	-0.013
15	83	8.230	8.232	0.002
15	84	8.246	8.261	0.015
30	86	8.267	8.215	-0.052
30	89	8.236	8.196	-0.040
30	91	8.161	8.209	0.048
30	92	8.189	8.189	0.000
30	94	8.323	8.311	-0.012
30	95	8.263	8.238	-0.025
35	100	8.220	8.251	0.031
35	101	8.256	8.274	0.018
35	102	8.203	8.192	-0.011
35	103	8.268	8.188	-0.080
35	104	8.230	8.210	-0.020
35	105	8.246	8.264	0.018
50	106	8.264	8.190	-0.074
50	107	8.265	8.172	-0.093
50	108	8.263	8.207	-0.056
50	109	8.209	8.145	-0.064
50	110	8.324	8.200	-0.124
50	111	8.257	8.187	-0.070
55	113	8.282	8.226	-0.056
55	114	8.229	8.166	-0.063
55	115	8.183	8.150	-0.033
55	116	8.248	8.131	-0.117
55	126	8.292	8.193	-0.099
55	128	8.213	8.187	-0.026
100	129	8.317	8.214	-0.103
100	131	8.306	8.158	-0.148
100	132	8.264	8.195	-0.069
100	133	8.340	8.242	-0.098
100	134	8.280	8.145	-0.135
100	135	8.244	8.161	-0.083
100	138	8.280	8.173	-0.107
100	139	8.286	8.181	-0.105
100	140	8.302	8.224	-0.078
100	141	8.269	8.159	-0.110
100	142	8.233	8.128	-0.105
100	143	8.287	8.153	-0.134
100	144	8.240	8.157	-0.083
100	220	8.277	8.145	-0.132
100	221	8.225	8.114	-0.111
100	224	8.311	8.149	-0.162
100	225	8.338	8.247	-0.091
100	226	8.260	8.190	-0.070
100	228	8.278	8.228	-0.050
100	232	8.274	8.192	-0.082
100	233	8.312	8.193	-0.119
100	236	8.281	8.168	-0.113
100	238	8.299	8.198	-0.101
100	243	8.314	8.218	-0.096
105	244	8.292	8.220	-0.072
105	246	8.216	8.187	-0.029
105	248	8.280	8.187	-0.093
105	374	8.270	8.235	-0.035
105	376	8.316	8.134	-0.182
105	Max	8.340	8.311	0.029
105	Average	8.257	8.208	-0.049
105	Min	8.161	8.114	-0.047
105	Std Dev	0.042	0.045	0.055



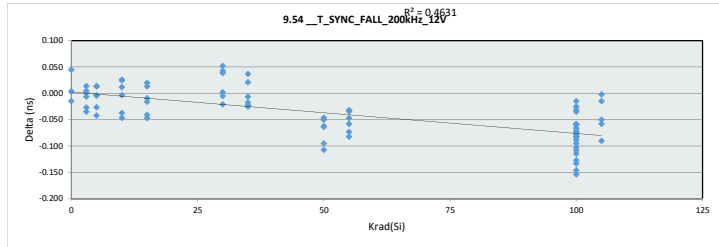
9.50_T_SYNC_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.233	8.202	8.220	8.125	8.193	8.189	8.188	8.145	8.131	8.114	8.134
Average	8.276	8.237	8.238	8.233	8.235	8.226	8.230	8.184	8.176	8.181	8.193
Max	8.305	8.301	8.266	8.305	8.261	8.311	8.274	8.207	8.226	8.247	8.235
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



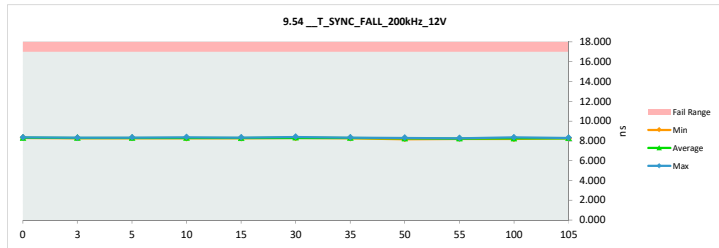
LDR TID Report  
TPS7H5001-SP

9.54_T_SYNC_FALL_200kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.302	8.286	-0.016
0	118	8.333	8.336	0.003
0	122	8.246	8.290	0.044
3	61	8.291	8.295	0.004
3	62	8.302	8.274	-0.028
3	63	8.230	8.243	0.013
3	64	8.273	8.273	0.000
3	65	8.339	8.303	-0.036
3	66	8.291	8.284	-0.007
5	67	8.249	8.244	-0.005
5	68	8.314	8.326	0.012
5	69	8.298	8.311	0.013
5	70	8.280	8.253	-0.027
5	71	8.263	8.258	-0.005
5	72	8.309	8.266	-0.043
10	73	8.291	8.287	-0.004
10	74	8.302	8.255	-0.047
10	75	8.230	8.254	0.024
10	76	8.273	8.235	-0.038
10	77	8.339	8.350	0.011
10	78	8.291	8.316	0.025
15	79	8.249	8.268	0.019
15	80	8.314	8.304	-0.010
15	81	8.298	8.310	0.012
15	82	8.280	8.232	-0.048
15	83	8.263	8.246	-0.017
15	84	8.309	8.267	-0.042
30	86	8.291	8.342	0.051
30	89	8.302	8.280	-0.022
30	91	8.230	8.268	0.038
30	92	8.273	8.274	0.001
30	94	8.339	8.381	0.042
30	95	8.291	8.285	-0.006
35	100	8.249	8.231	-0.018
35	101	8.314	8.307	-0.007
35	102	8.298	8.272	-0.026
35	103	8.280	8.316	0.036
35	104	8.263	8.283	0.020
35	105	8.309	8.286	-0.023
50	106	8.300	8.253	-0.047
50	107	8.298	8.202	-0.096
50	108	8.357	8.293	-0.064
50	109	8.245	8.157	-0.108
50	110	8.340	8.277	-0.063
50	111	8.270	8.219	-0.051
55	113	8.340	8.257	-0.083
55	114	8.236	8.203	-0.033
55	115	8.252	8.178	-0.074
55	116	8.263	8.228	-0.035
55	126	8.261	8.213	-0.048
55	128	8.253	8.194	-0.059
100	129	8.347	8.288	-0.059
100	131	8.351	8.236	-0.115
100	132	8.339	8.255	-0.084
100	133	8.357	8.268	-0.089
100	134	8.312	8.229	-0.083
100	135	8.279	8.196	-0.083
100	138	8.324	8.249	-0.075
100	139	8.342	8.195	-0.147
100	140	8.363	8.331	-0.032
100	141	8.267	8.171	-0.096
100	142	8.324	8.220	-0.104
100	143	8.285	8.206	-0.079
100	144	8.273	8.213	-0.060
100	220	8.289	8.180	-0.109
100	221	8.262	8.202	-0.060
100	224	8.308	8.292	-0.016
100	225	8.388	8.320	-0.068
100	226	8.350	8.283	-0.067
100	228	8.384	8.229	-0.155
100	232	8.303	8.169	-0.134
100	233	8.275	8.249	-0.026
100	236	8.326	8.198	-0.128
100	238	8.285	8.249	-0.036
100	243	8.318	8.245	-0.073
105	244	8.292	8.241	-0.051
105	246	8.302	8.243	-0.059
105	248	8.291	8.288	-0.003
105	374	8.304	8.288	-0.016
105	376	8.330	8.239	-0.091
	Max	8.388	8.381	0.051
	Average	8.298	8.259	-0.038
	Min	8.230	8.157	-0.155
	Std Dev	0.036	0.046	0.047



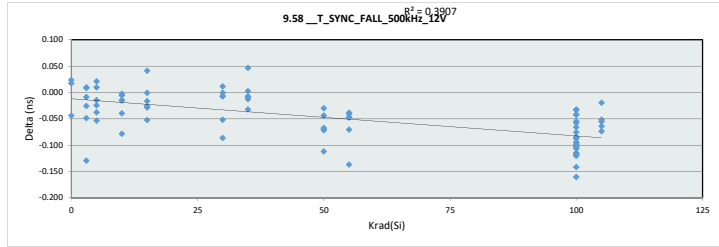
9.54_T_SYNC_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.286	8.243	8.244	8.235	8.232	8.268	8.231	8.157	8.178	8.169	8.239
Average	8.304	8.279	8.276	8.283	8.271	8.305	8.283	8.234	8.212	8.236	8.260
Max	8.336	8.303	8.326	8.350	8.310	8.381	8.316	8.293	8.257	8.331	8.288
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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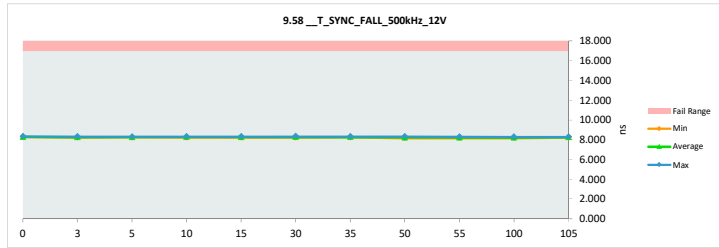
9.58_T_SYNC_FALL_500kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.312	8.268	-0.044
0	118	8.302	8.319	0.017
0	122	8.240	8.263	0.023
3	61	8.311	8.285	-0.026
3	62	8.243	8.251	0.008
3	63	8.198	8.207	0.009
3	64	8.321	8.191	-0.130
3	65	8.349	8.300	-0.049
3	66	8.273	8.264	-0.009
5	67	8.235	8.220	-0.015
5	68	8.298	8.273	-0.025
5	69	8.295	8.241	-0.054
5	70	8.231	8.240	0.009
5	71	8.290	8.252	-0.038
5	72	8.238	8.259	0.021
10	73	8.311	8.308	-0.003
10	74	8.243	8.227	-0.016
10	75	8.198	8.192	-0.006
10	76	8.321	8.242	-0.079
10	77	8.349	8.309	-0.040
10	78	8.273	8.258	-0.015
15	79	8.235	8.206	-0.029
15	80	8.298	8.297	-0.001
15	81	8.295	8.278	-0.017
15	82	8.231	8.205	-0.026
15	83	8.290	8.237	-0.053
15	84	8.238	8.279	0.041
30	86	8.311	8.259	-0.052
30	89	8.243	8.236	-0.007
30	91	8.198	8.197	-0.001
30	92	8.321	8.234	-0.087
30	94	8.349	8.341	-0.008
30	95	8.273	8.284	0.011
35	100	8.235	8.227	-0.008
35	101	8.298	8.265	-0.033
35	102	8.295	8.297	0.002
35	103	8.231	8.222	-0.009
35	104	8.290	8.277	-0.013
35	105	8.238	8.284	0.046
50	106	8.289	8.217	-0.072
50	107	8.271	8.241	-0.030
50	108	8.293	8.249	-0.044
50	109	8.226	8.158	-0.068
50	110	8.357	8.288	-0.069
50	111	8.248	8.136	-0.112
55	113	8.322	8.283	-0.039
55	114	8.230	8.182	-0.048
55	115	8.243	8.202	-0.041
55	116	8.281	8.210	-0.071
55	126	8.236	8.189	-0.047
55	128	8.274	8.137	-0.137
100	129	8.323	8.228	-0.095
100	131	8.333	8.214	-0.119
100	132	8.295	8.194	-0.101
100	133	8.381	8.282	-0.099
100	134	8.351	8.275	-0.076
100	135	8.237	8.152	-0.085
100	138	8.296	8.201	-0.095
100	139	8.303	8.142	-0.161
100	140	8.305	8.217	-0.088
100	141	8.294	8.152	-0.142
100	142	8.292	8.206	-0.086
100	143	8.294	8.236	-0.058
100	144	8.231	8.197	-0.034
100	220	8.300	8.185	-0.115
100	221	8.206	8.163	-0.043
100	224	8.286	8.231	-0.055
100	225	8.337	8.271	-0.066
100	226	8.317	8.214	-0.103
100	228	8.329	8.222	-0.107
100	232	8.323	8.207	-0.116
100	233	8.320	8.199	-0.121
100	236	8.289	8.247	-0.042
100	238	8.300	8.194	-0.106
100	243	8.289	8.256	-0.033
105	244	8.290	8.234	-0.056
105	246	8.276	8.212	-0.064
105	248	8.290	8.238	-0.052
105	374	8.290	8.270	-0.020
105	376	8.276	8.202	-0.074
105	Max	8.381	8.341	0.046
105	Average	8.283	8.234	-0.049
105	Min	8.198	8.136	-0.161
105	Std Dev	0.041	0.044	0.045



9.58_T_SYNC_FALL_500kHz_12V	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.263	8.191	8.220	8.192	8.205	8.197	8.222	8.136	8.137	8.142	8.202
Average	8.283	8.250	8.248	8.256	8.250	8.259	8.262	8.215	8.201	8.212	8.231
Max	8.319	8.300	8.273	8.309	8.297	8.341	8.297	8.288	8.283	8.282	8.270
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

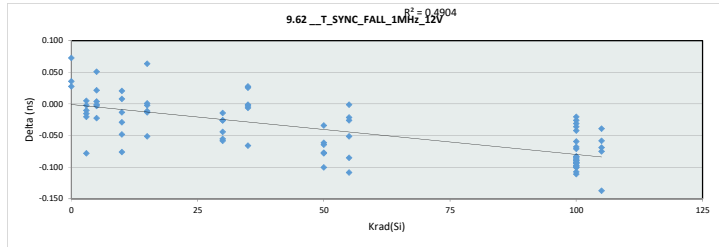




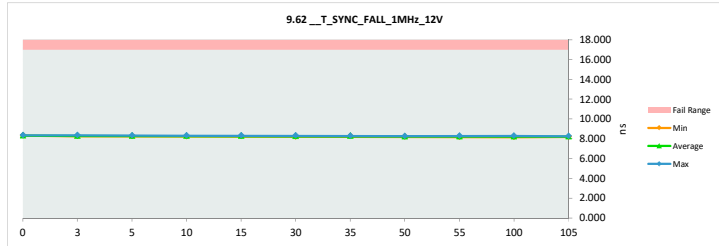
LDR TID Report  
TPS7H5001-SP

9.62_T_SYNC_FALL_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.281	8.308	0.027
0	118	8.306	8.341	0.035
0	122	8.276	8.348	0.072
3	61	8.296	8.280	-0.016
3	62	8.280	8.259	-0.021
3	63	8.262	8.251	-0.011
3	64	8.266	8.270	0.004
3	65	8.351	8.348	-0.003
3	66	8.335	8.256	-0.079
5	67	8.255	8.258	0.003
5	68	8.306	8.302	-0.004
5	69	8.291	8.312	0.021
5	70	8.216	8.266	0.050
5	71	8.294	8.292	-0.002
5	72	8.308	8.285	-0.023
10	73	8.296	8.303	0.007
10	74	8.280	8.266	-0.014
10	75	8.262	8.213	-0.049
10	76	8.266	8.286	0.020
10	77	8.351	8.321	-0.030
10	78	8.335	8.258	-0.077
15	79	8.255	8.243	-0.012
15	80	8.306	8.303	-0.003
15	81	8.291	8.277	-0.014
15	82	8.216	8.279	0.063
15	83	8.294	8.242	-0.052
15	84	8.308	8.308	0.000
30	86	8.296	8.281	-0.015
30	89	8.280	8.253	-0.027
30	91	8.262	8.235	-0.027
30	92	8.266	8.207	-0.059
30	94	8.351	8.306	-0.045
30	95	8.335	8.279	-0.056
35	100	8.255	8.280	0.025
35	101	8.306	8.304	-0.002
35	102	8.291	8.284	-0.007
35	103	8.216	8.243	0.027
35	104	8.294	8.290	-0.004
35	105	8.308	8.241	-0.067
50	106	8.295	8.230	-0.065
50	107	8.312	8.233	-0.079
50	108	8.331	8.253	-0.078
50	109	8.222	8.187	-0.035
50	110	8.368	8.267	-0.101
50	111	8.282	8.220	-0.062
55	113	8.298	8.296	-0.002
55	114	8.247	8.195	-0.052
55	115	8.277	8.250	-0.027
55	116	8.259	8.237	-0.022
55	126	8.318	8.232	-0.086
55	128	8.279	8.170	-0.109
100	129	8.371	8.281	-0.090
100	131	8.305	8.221	-0.084
100	132	8.312	8.243	-0.069
100	133	8.389	8.301	-0.088
100	134	8.330	8.237	-0.093
100	135	8.244	8.160	-0.084
100	138	8.292	8.193	-0.099
100	139	8.299	8.239	-0.060
100	140	8.330	8.237	-0.093
100	141	8.298	8.208	-0.090
100	142	8.265	8.244	-0.021
100	143	8.263	8.236	-0.027
100	144	8.296	8.184	-0.112
100	220	8.303	8.202	-0.101
100	221	8.278	8.170	-0.108
100	224	8.329	8.231	-0.098
100	225	8.380	8.286	-0.094
100	226	8.313	8.270	-0.043
100	228	8.294	8.257	-0.037
100	232	8.302	8.201	-0.101
100	233	8.330	8.235	-0.095
100	236	8.288	8.202	-0.086
100	238	8.244	8.212	-0.032
100	243	8.311	8.239	-0.072
105	244	8.317	8.247	-0.070
105	246	8.274	8.198	-0.076
105	248	8.307	8.267	-0.040
105	374	8.300	8.241	-0.059
105	376	8.332	8.194	-0.138
	Max	8.389	8.348	0.072
	Average	8.295	8.254	-0.042
	Min	8.216	8.160	-0.138
	Std Dev	0.036	0.042	0.046



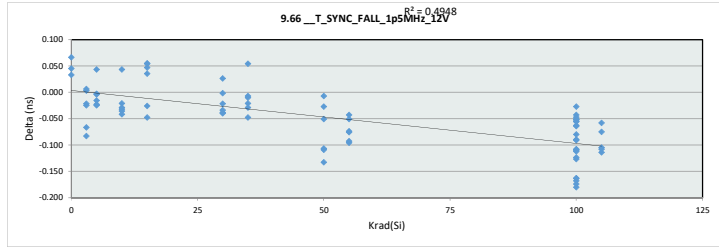
9.62_T_SYNC_Fa											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.308	8.251	8.258	8.213	8.242	8.207	8.241	8.187	8.170	8.160	8.194
Average	8.332	8.277	8.286	8.275	8.275	8.260	8.274	8.232	8.230	8.229	8.229
Max	8.348	8.348	8.312	8.321	8.308	8.306	8.304	8.267	8.296	8.301	8.267
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



LDR TID Report  
TPS7H5001-SP

9.66_T_SYNC_FALL_1p5MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

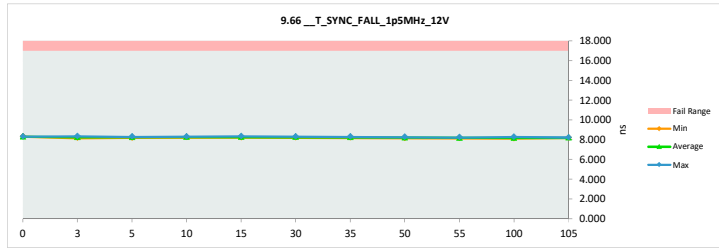
Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.251	8.296	0.045
0	118	8.260	8.293	0.033
0	122	8.252	8.318	0.066
3	61	8.302	8.305	0.003
3	62	8.311	8.228	-0.083
3	63	8.187	8.162	-0.025
3	64	8.277	8.210	-0.067
3	65	8.339	8.345	0.006
3	66	8.285	8.263	-0.022
5	67	8.201	8.185	-0.016
5	68	8.319	8.296	-0.023
5	69	8.219	8.262	0.043
5	70	8.268	8.265	-0.003
5	71	8.252	8.248	-0.004
5	72	8.297	8.272	-0.025
10	73	8.302	8.273	-0.029
10	74	8.311	8.269	-0.042
10	75	8.187	8.230	0.043
10	76	8.277	8.241	-0.036
10	77	8.339	8.318	-0.021
10	78	8.285	8.253	-0.032
15	79	8.201	8.256	0.055
15	80	8.319	8.293	-0.026
15	81	8.219	8.266	0.047
15	82	8.268	8.322	0.054
15	83	8.252	8.204	-0.048
15	84	8.297	8.332	0.035
30	86	8.302	8.268	-0.034
30	89	8.311	8.309	-0.002
30	91	8.187	8.213	0.026
30	92	8.277	8.238	-0.039
30	94	8.339	8.299	-0.040
30	95	8.285	8.263	-0.022
35	100	8.201	8.255	0.054
35	101	8.319	8.271	-0.048
35	102	8.219	8.190	-0.029
35	103	8.268	8.258	-0.010
35	104	8.252	8.231	-0.021
35	105	8.297	8.290	-0.007
50	106	8.299	8.248	-0.051
50	107	8.283	8.256	-0.027
50	108	8.309	8.200	-0.109
50	109	8.231	8.224	-0.007
50	110	8.357	8.250	-0.107
50	111	8.286	8.153	-0.133
55	113	8.281	8.230	-0.051
55	114	8.222	8.126	-0.096
55	115	8.213	8.170	-0.043
55	116	8.245	8.169	-0.076
55	126	8.321	8.228	-0.093
55	128	8.275	8.201	-0.074
100	129	8.336	8.212	-0.124
100	131	8.307	8.195	-0.112
100	132	8.304	8.248	-0.056
100	133	8.319	8.272	-0.047
100	134	8.374	8.211	-0.163
100	135	8.206	8.143	-0.063
100	138	8.265	8.213	-0.052
100	139	8.251	8.187	-0.064
100	140	8.297	8.189	-0.108
100	141	8.278	8.098	-0.180
100	142	8.303	8.176	-0.127
100	143	8.212	8.169	-0.043
100	144	8.219	8.139	-0.080
100	220	8.228	8.173	-0.055
100	221	8.225	8.115	-0.110
100	224	8.338	8.175	-0.163
100	225	8.415	8.241	-0.174
100	226	8.299	8.248	-0.051
100	228	8.363	8.195	-0.168
100	232	8.263	8.154	-0.109
100	233	8.238	8.186	-0.052
100	236	8.245	8.154	-0.091
100	238	8.245	8.218	-0.027
100	243	8.290	8.200	-0.090
105	244	8.284	8.176	-0.108
105	246	8.232	8.174	-0.058
105	248	8.297	8.222	-0.075
105	374	8.280	8.175	-0.105
105	376	8.319	8.205	-0.114
	Max	8.415	8.345	0.066
	Average	8.276	8.228	-0.049
	Min	8.187	8.098	-0.180
	Std Dev	0.047	0.054	0.058



9.66_T_SYNC_Fa	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
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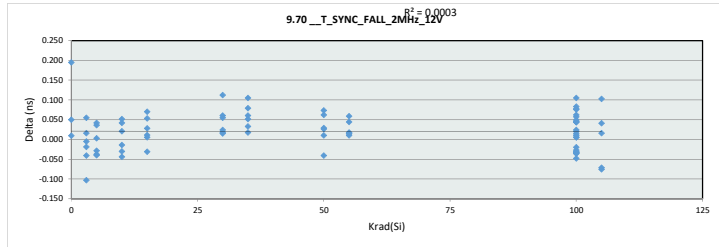
LL											
Min	8.293	8.162	8.185	8.230	8.204	8.213	8.190	8.153	8.126	8.098	8.174
Average	8.302	8.252	8.255	8.264	8.279	8.265	8.249	8.222	8.187	8.188	8.190
Max	8.318	8.345	8.296	8.318	8.332	8.309	8.290	8.256	8.230	8.272	8.222
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



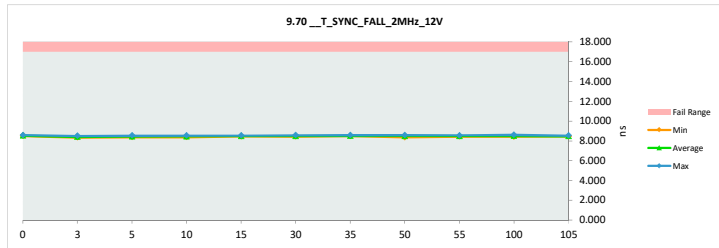
LDR TID Report  
TPS7H5001-SP

9.70_T_SYNC_FALL_2MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.359	8.553	0.194
0	118	8.487	8.495	0.008
0	122	8.419	8.468	0.049
3	61	8.492	8.472	-0.020
3	62	8.424	8.439	0.015
3	63	8.397	8.391	-0.006
3	64	8.370	8.328	-0.042
3	65	8.467	8.363	-0.104
3	66	8.362	8.416	0.054
5	67	8.411	8.446	0.035
5	68	8.501	8.503	0.002
5	69	8.418	8.388	-0.030
5	70	8.388	8.429	0.041
5	71	8.381	8.340	-0.041
5	72	8.521	8.482	-0.039
10	73	8.492	8.512	0.020
10	74	8.424	8.409	-0.015
10	75	8.397	8.352	-0.045
10	76	8.370	8.420	0.050
10	77	8.467	8.436	-0.031
10	78	8.362	8.403	0.041
15	79	8.411	8.438	0.027
15	80	8.501	8.511	0.010
15	81	8.418	8.422	0.004
15	82	8.388	8.440	0.052
15	83	8.381	8.450	0.069
15	84	8.521	8.489	-0.032
30	86	8.492	8.546	0.054
30	89	8.424	8.438	0.014
30	91	8.397	8.414	0.017
30	92	8.370	8.429	0.059
30	94	8.467	8.490	0.023
30	95	8.362	8.473	0.111
35	100	8.411	8.461	0.050
35	101	8.501	8.517	0.016
35	102	8.418	8.477	0.059
35	103	8.388	8.492	0.104
35	104	8.381	8.459	0.078
35	105	8.521	8.553	0.032
50	106	8.467	8.476	0.009
50	107	8.452	8.524	0.072
50	108	8.432	8.457	0.025
50	109	8.414	8.442	0.028
50	110	8.496	8.557	0.061
50	111	8.373	8.331	-0.042
55	113	8.482	8.525	0.043
55	114	8.355	8.413	0.058
55	115	8.419	8.434	0.015
55	116	8.458	8.471	0.013
55	126	8.496	8.505	0.009
55	128	8.425	8.441	0.016
100	129	8.531	8.535	0.004
100	131	8.431	8.473	0.042
100	132	8.413	8.495	0.082
100	133	8.554	8.518	-0.036
100	134	8.480	8.523	0.043
100	135	8.365	8.421	0.056
100	138	8.461	8.509	0.048
100	139	8.454	8.420	-0.034
100	140	8.488	8.532	0.044
100	141	8.385	8.408	0.023
100	142	8.477	8.440	-0.037
100	143	8.474	8.425	-0.049
100	144	8.474	8.453	-0.021
100	220	8.467	8.486	0.019
100	221	8.382	8.391	0.009
100	224	8.463	8.429	-0.034
100	225	8.460	8.521	0.061
100	226	8.455	8.530	0.075
100	228	8.516	8.592	0.076
100	232	8.481	8.494	0.013
100	233	8.436	8.440	0.004
100	236	8.421	8.389	-0.032
100	238	8.386	8.490	0.104
100	243	8.483	8.455	-0.028
105	244	8.500	8.427	-0.073
105	246	8.500	8.423	-0.077
105	248	8.403	8.504	0.101
105	374	8.396	8.436	0.040
105	376	8.424	8.439	0.015
105	Max	8.554	8.592	0.194
105	Average	8.439	8.459	0.020
105	Min	8.355	8.328	-0.104
105	Std Dev	0.051	0.055	0.049



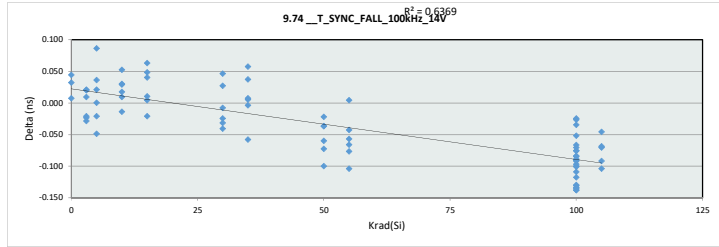
9.70_T_SYNC_FALL_2MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.468	8.328	8.340	8.352	8.422	8.414	8.459	8.331	8.413	8.389	8.423
Average	8.505	8.402	8.431	8.422	8.458	8.465	8.493	8.465	8.474	8.446	
Max	8.553	8.472	8.503	8.512	8.511	8.546	8.553	8.557	8.525	8.592	8.504
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



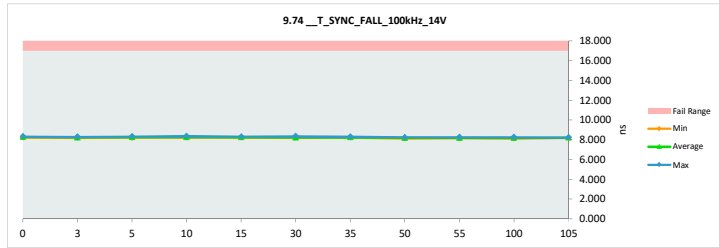
LDR TID Report  
TPS7H5001-SP

9.74_T_SYNC_FALL_100kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.269	8.301	0.032
0	118	8.251	8.295	0.044
0	122	8.215	8.222	0.007
3	61	8.221	8.242	0.021
3	62	8.216	8.187	-0.029
3	63	8.186	8.162	-0.024
3	64	8.203	8.212	0.009
3	65	8.298	8.277	-0.021
3	66	8.218	8.238	0.020
5	67	8.165	8.201	0.036
5	68	8.279	8.300	0.021
5	69	8.201	8.287	0.086
5	70	8.204	8.204	0.000
5	71	8.236	8.187	-0.049
5	72	8.257	8.236	-0.021
10	73	8.221	8.238	0.017
10	74	8.216	8.245	0.029
10	75	8.186	8.216	0.030
10	76	8.203	8.212	0.009
10	77	8.298	8.350	0.052
10	78	8.218	8.204	-0.014
15	79	8.165	8.213	0.048
15	80	8.279	8.283	0.004
15	81	8.201	8.241	0.040
15	82	8.204	8.267	0.063
15	83	8.236	8.215	-0.021
15	84	8.257	8.267	0.010
30	86	8.221	8.267	0.046
30	89	8.216	8.208	-0.008
30	91	8.186	8.161	-0.025
30	92	8.203	8.162	-0.041
30	94	8.298	8.325	0.027
30	95	8.218	8.186	-0.032
35	100	8.165	8.202	0.037
35	101	8.279	8.284	0.005
35	102	8.201	8.258	0.057
35	103	8.204	8.211	0.007
35	104	8.236	8.232	-0.004
35	105	8.257	8.199	-0.058
50	106	8.254	8.154	-0.100
50	107	8.235	8.213	-0.022
50	108	8.266	8.193	-0.073
50	109	8.170	8.110	-0.060
50	110	8.258	8.221	-0.037
50	111	8.205	8.168	-0.037
55	113	8.292	8.235	-0.057
55	114	8.186	8.143	-0.043
55	115	8.198	8.121	-0.077
55	116	8.187	8.191	0.004
55	126	8.264	8.160	-0.104
55	128	8.222	8.156	-0.066
100	129	8.306	8.222	-0.084
100	131	8.268	8.167	-0.101
100	132	8.246	8.170	-0.076
100	133	8.342	8.204	-0.138
100	134	8.325	8.195	-0.130
100	135	8.296	8.161	-0.135
100	138	8.206	8.181	-0.025
100	139	8.267	8.136	-0.131
100	140	8.286	8.215	-0.071
100	141	8.268	8.134	-0.134
100	142	8.259	8.183	-0.076
100	143	8.266	8.214	-0.052
100	144	8.205	8.113	-0.092
100	220	8.270	8.152	-0.118
100	221	8.201	8.110	-0.091
100	224	8.280	8.191	-0.089
100	225	8.345	8.236	-0.109
100	226	8.326	8.229	-0.097
100	228	8.266	8.239	-0.027
100	232	8.244	8.177	-0.067
100	233	8.269	8.131	-0.138
100	236	8.276	8.191	-0.085
100	238	8.219	8.184	-0.035
100	243	8.279	8.179	-0.100
105	244	8.269	8.198	-0.071
105	246	8.267	8.163	-0.104
105	248	8.271	8.202	-0.069
105	374	8.254	8.208	-0.046
105	376	8.307	8.215	-0.092
105	Max	8.345	8.350	0.006
105	Average	8.243	8.207	-0.036
105	Min	8.165	8.110	-0.138
105	Std Dev	0.043	0.050	0.057



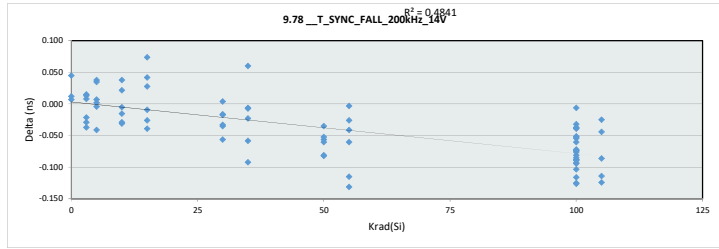
9.74_T_SYNC_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.222	8.162	8.187	8.204	8.213	8.161	8.199	8.110	8.121	8.110	8.163
Average	8.273	8.220	8.236	8.244	8.248	8.218	8.231	8.177	8.168	8.180	8.197
Max	8.301	8.277	8.300	8.350	8.283	8.325	8.284	8.221	8.235	8.239	8.215
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



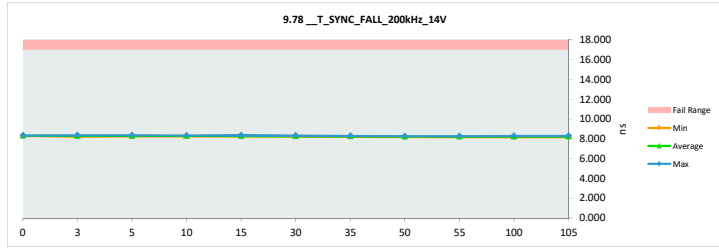
LDR TID Report  
TPS7H5001-SP

9.78_T_SYNC_FALL_200kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.296	8.307	0.011
0	118	8.284	8.328	0.044
0	122	8.284	8.290	0.006
3	61	8.315	8.322	0.007
3	62	8.286	8.256	-0.030
3	63	8.256	8.234	-0.022
3	64	8.271	8.285	0.014
3	65	8.328	8.340	0.012
3	66	8.314	8.276	-0.038
5	67	8.216	8.253	0.037
5	68	8.294	8.300	0.006
5	69	8.286	8.287	0.001
5	70	8.294	8.252	-0.042
5	71	8.299	8.294	-0.005
5	72	8.320	8.354	0.034
10	73	8.315	8.285	-0.030
10	74	8.286	8.254	-0.032
10	75	8.256	8.240	-0.016
10	76	8.271	8.308	0.037
10	77	8.328	8.322	-0.006
10	78	8.314	8.335	0.021
15	79	8.216	8.257	0.041
15	80	8.294	8.367	0.073
15	81	8.286	8.259	-0.027
15	82	8.294	8.284	-0.010
15	83	8.299	8.326	0.027
15	84	8.320	8.280	-0.040
30	86	8.315	8.279	-0.036
30	89	8.286	8.252	-0.034
30	91	8.256	8.259	0.003
30	92	8.271	8.254	-0.017
30	94	8.328	8.310	-0.018
30	95	8.314	8.257	-0.057
35	100	8.216	8.275	0.059
35	101	8.294	8.270	-0.024
35	102	8.286	8.279	-0.007
35	103	8.294	8.201	-0.093
35	104	8.299	8.291	-0.008
35	105	8.320	8.261	-0.059
50	106	8.289	8.232	-0.057
50	107	8.306	8.223	-0.083
50	108	8.312	8.230	-0.082
50	109	8.234	8.170	-0.061
50	110	8.305	8.252	-0.053
50	111	8.279	8.243	-0.036
55	113	8.300	8.239	-0.061
55	114	8.261	8.219	-0.042
55	115	8.262	8.235	-0.027
55	116	8.258	8.254	-0.004
55	126	8.364	8.232	-0.132
55	128	8.302	8.186	-0.116
100	129	8.336	8.303	-0.033
100	131	8.322	8.240	-0.082
100	132	8.284	8.244	-0.040
100	133	8.342	8.267	-0.075
100	134	8.330	8.269	-0.061
100	135	8.277	8.270	-0.007
100	138	8.314	8.225	-0.089
100	139	8.288	8.233	-0.055
100	140	8.314	8.261	-0.053
100	141	8.307	8.180	-0.127
100	142	8.331	8.241	-0.090
100	143	8.288	8.236	-0.052
100	144	8.320	8.203	-0.117
100	220	8.291	8.202	-0.089
100	221	8.282	8.156	-0.126
100	224	8.323	8.250	-0.073
100	225	8.405	8.301	-0.104
100	226	8.340	8.265	-0.075
100	228	8.291	8.252	-0.039
100	232	8.304	8.218	-0.086
100	233	8.316	8.222	-0.094
100	236	8.304	8.227	-0.077
100	238	8.289	8.251	-0.038
100	243	8.333	8.238	-0.095
105	244	8.345	8.220	-0.125
105	246	8.277	8.162	-0.115
105	248	8.323	8.236	-0.087
105	374	8.325	8.280	-0.045
105	376	8.300	8.274	-0.026
105	375	8.405	8.367	0.038
	Max	8.298	8.259	-0.039
	Average	8.308	8.286	-0.022
	Min	8.328	8.340	0.012
	Std Dev	0.031	0.042	0.047



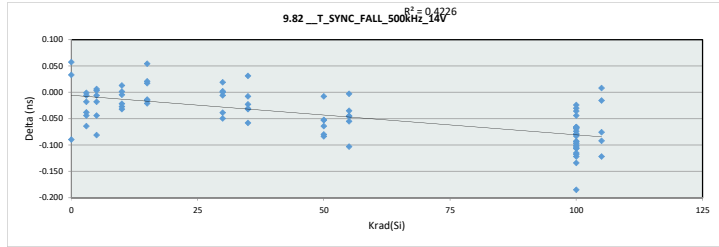
9.78_T_SYNC_FALL_200kHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.290	8.234	8.252	8.240	8.257	8.252	8.201	8.170	8.186	8.156	8.162
Average	8.308	8.286	8.290	8.291	8.296	8.269	8.263	8.225	8.228	8.240	8.234
Max	8.328	8.340	8.354	8.335	8.367	8.310	8.291	8.252	8.254	8.303	8.280
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



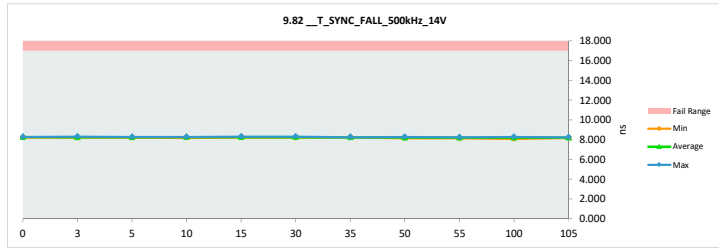
LDR TID Report  
TPS7H5001-SP

9.82_T_SYNC_FALL_500kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.320	8.230	-0.090
0	118	8.244	8.277	0.033
0	122	8.228	8.285	0.057
3	61	8.265	8.227	-0.038
3	62	8.275	8.211	-0.064
3	63	8.218	8.200	-0.018
3	64	8.252	8.246	-0.006
3	65	8.310	8.309	-0.001
3	66	8.283	8.239	-0.044
5	67	8.240	8.222	-0.018
5	68	8.264	8.270	0.006
5	69	8.260	8.254	-0.006
5	70	8.220	8.223	0.003
5	71	8.304	8.223	-0.081
5	72	8.291	8.247	-0.044
10	73	8.265	8.266	0.001
10	74	8.275	8.248	-0.027
10	75	8.218	8.196	-0.022
10	76	8.252	8.265	0.013
10	77	8.310	8.278	-0.032
10	78	8.283	8.278	-0.005
15	79	8.240	8.226	-0.014
15	80	8.264	8.285	0.021
15	81	8.260	8.314	0.054
15	82	8.220	8.237	0.017
15	83	8.304	8.283	-0.021
15	84	8.291	8.275	-0.016
30	86	8.265	8.284	0.019
30	89	8.275	8.236	-0.039
30	91	8.218	8.218	0.000
30	92	8.252	8.246	-0.006
30	94	8.310	8.312	0.002
30	95	8.283	8.233	-0.050
35	100	8.240	8.232	-0.008
35	101	8.264	8.233	-0.031
35	102	8.260	8.237	-0.023
35	103	8.220	8.251	0.031
35	104	8.304	8.246	-0.058
35	105	8.291	8.259	-0.032
50	106	8.294	8.210	-0.084
50	107	8.248	8.240	-0.008
50	108	8.354	8.273	-0.080
50	109	8.205	8.152	-0.053
50	110	8.296	8.243	-0.053
50	111	8.243	8.179	-0.064
55	113	8.291	8.236	-0.055
55	114	8.245	8.142	-0.103
55	115	8.216	8.171	-0.045
55	116	8.247	8.212	-0.035
55	126	8.248	8.245	-0.003
55	128	8.231	8.185	-0.046
100	129	8.343	8.237	-0.106
100	131	8.306	8.213	-0.093
100	132	8.300	8.231	-0.069
100	133	8.354	8.253	-0.101
100	134	8.352	8.218	-0.134
100	135	8.252	8.145	-0.107
100	138	8.247	8.173	-0.074
100	139	8.253	8.149	-0.104
100	140	8.301	8.277	-0.024
100	141	8.264	8.079	-0.185
100	142	8.225	8.189	-0.036
100	143	8.223	8.179	-0.044
100	144	8.227	8.197	-0.030
100	220	8.261	8.182	-0.079
100	221	8.269	8.147	-0.122
100	224	8.290	8.210	-0.080
100	225	8.351	8.257	-0.094
100	226	8.296	8.178	-0.118
100	228	8.294	8.228	-0.066
100	232	8.315	8.218	-0.097
100	233	8.289	8.222	-0.067
100	236	8.293	8.210	-0.083
100	238	8.274	8.208	-0.066
100	243	8.323	8.208	-0.115
105	244	8.276	8.200	-0.076
105	246	8.297	8.175	-0.122
105	248	8.261	8.245	-0.016
105	374	8.245	8.253	0.008
105	376	8.291	8.199	-0.092
	Max	8.354	8.314	0.057
	Average	8.272	8.227	-0.044
	Min	8.205	8.079	-0.185
	Std Dev	0.036	0.043	0.047



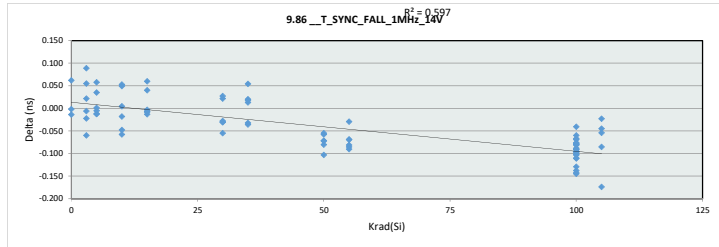
9.82_T_SYNC_FALL_500kHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.230	8.200	8.222	8.196	8.226	8.218	8.232	8.152	8.142	8.079	8.175
Average	8.264	8.239	8.240	8.255	8.270	8.255	8.243	8.216	8.199	8.200	8.214
Max	8.285	8.309	8.270	8.278	8.314	8.312	8.259	8.273	8.245	8.277	8.253
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



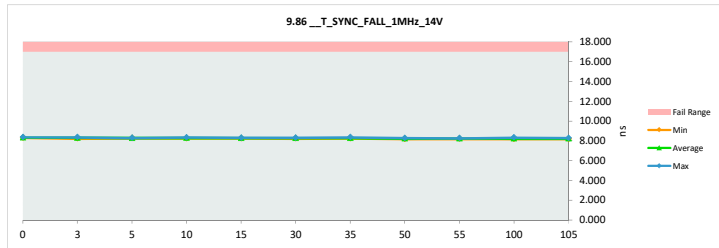
LDR TID Report  
TPS7H5001-SP

9.86_T_SYNC_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.288	8.285	-0.003
0	118	8.302	8.363	0.061
0	122	8.306	8.291	-0.015
3	61	8.256	8.310	0.054
3	62	8.215	8.303	0.088
3	63	8.246	8.223	-0.023
3	64	8.318	8.257	-0.061
3	65	8.318	8.339	0.021
3	66	8.314	8.307	-0.007
5	67	8.242	8.242	0.000
5	68	8.294	8.288	-0.006
5	69	8.274	8.261	-0.013
5	70	8.266	8.253	-0.013
5	71	8.252	8.286	0.034
5	72	8.224	8.281	0.057
10	73	8.256	8.304	0.048
10	74	8.215	8.267	0.052
10	75	8.246	8.227	-0.019
10	76	8.318	8.259	-0.059
10	77	8.318	8.322	0.004
10	78	8.314	8.265	-0.049
15	79	8.242	8.233	-0.009
15	80	8.294	8.286	-0.008
15	81	8.274	8.270	-0.004
15	82	8.266	8.252	-0.014
15	83	8.252	8.291	0.039
15	84	8.224	8.283	0.059
30	86	8.256	8.282	0.026
30	89	8.215	8.236	0.021
30	91	8.246	8.214	-0.032
30	92	8.318	8.289	-0.029
30	94	8.318	8.287	-0.031
30	95	8.314	8.258	-0.056
35	100	8.242	8.254	0.012
35	101	8.294	8.347	0.053
35	102	8.274	8.241	-0.033
35	103	8.266	8.229	-0.037
35	104	8.252	8.269	0.017
35	105	8.224	8.243	0.019
50	106	8.288	8.229	-0.059
50	107	8.305	8.232	-0.073
50	108	8.287	8.231	-0.056
50	109	8.246	8.157	-0.081
50	110	8.364	8.260	-0.104
50	111	8.253	8.180	-0.073
55	113	8.311	8.241	-0.070
55	114	8.295	8.213	-0.082
55	115	8.263	8.177	-0.086
55	116	8.303	8.212	-0.091
55	126	8.288	8.218	-0.070
55	128	8.236	8.206	-0.030
100	129	8.332	8.250	-0.082
100	131	8.332	8.228	-0.104
100	132	8.333	8.194	-0.139
100	133	8.361	8.281	-0.080
100	134	8.321	8.210	-0.111
100	135	8.260	8.183	-0.077
100	138	8.271	8.174	-0.097
100	139	8.296	8.228	-0.068
100	140	8.336	8.255	-0.081
100	141	8.310	8.219	-0.091
100	142	8.253	8.183	-0.070
100	143	8.262	8.165	-0.097
100	144	8.289	8.177	-0.112
100	220	8.280	8.177	-0.103
100	221	8.252	8.163	-0.089
100	224	8.324	8.228	-0.096
100	225	8.366	8.305	-0.061
100	226	8.323	8.231	-0.092
100	228	8.280	8.238	-0.042
100	232	8.323	8.223	-0.100
100	233	8.361	8.215	-0.146
100	236	8.328	8.198	-0.130
100	238	8.349	8.205	-0.144
100	243	8.287	8.208	-0.079
105	244	8.289	8.234	-0.055
105	246	8.272	8.226	-0.046
105	248	8.334	8.159	-0.175
105	374	8.286	8.262	-0.024
105	376	8.321	8.235	-0.086
105	375	8.366	8.363	0.008
105	Average	8.286	8.244	-0.043
105	Min	8.215	8.157	-0.175
105	Std Dev	0.038	0.045	0.057



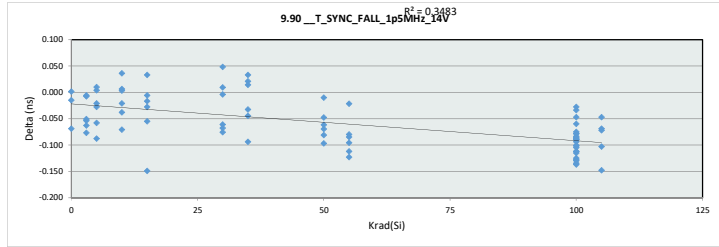
9.86_T_SYNC_FALL_1MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.285	8.223	8.242	8.227	8.233	8.214	8.229	8.157	8.177	8.163	8.159
Average	8.313	8.290	8.269	8.274	8.269	8.261	8.264	8.215	8.211	8.214	8.223
Max	8.363	8.339	8.288	8.322	8.291	8.289	8.347	8.260	8.241	8.305	8.262
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



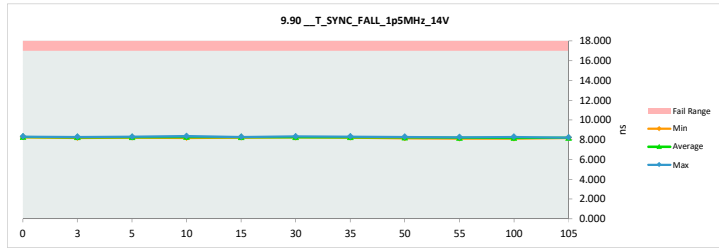
LDR TID Report  
TPS7H5001-SP

9.90_T_SYNC_FALL_1p5MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.243	8.244	0.001
0	118	8.306	8.291	-0.015
0	122	8.330	8.261	-0.069
3	61	8.316	8.262	-0.054
3	62	8.314	8.251	-0.063
3	63	8.166	8.160	-0.006
3	64	8.230	8.222	-0.008
3	65	8.315	8.264	-0.051
3	66	8.316	8.239	-0.077
5	67	8.260	8.202	-0.058
5	68	8.274	8.284	0.010
5	69	8.217	8.196	-0.021
5	70	8.354	8.266	-0.088
5	71	8.235	8.239	0.004
5	72	8.284	8.256	-0.028
10	73	8.316	8.245	-0.071
10	74	8.314	8.293	-0.021
10	75	8.166	8.172	0.006
10	76	8.230	8.233	0.003
10	77	8.315	8.351	0.036
10	78	8.316	8.278	-0.038
15	79	8.260	8.205	-0.055
15	80	8.274	8.268	-0.006
15	81	8.217	8.200	-0.017
15	82	8.354	8.205	-0.149
15	83	8.235	8.268	0.033
15	84	8.284	8.256	-0.028
30	86	8.316	8.240	-0.076
30	89	8.314	8.253	-0.061
30	91	8.166	8.214	0.048
30	92	8.230	8.239	0.009
30	94	8.315	8.311	-0.004
30	95	8.316	8.248	-0.068
35	100	8.260	8.215	-0.045
35	101	8.274	8.307	0.033
35	102	8.217	8.231	0.014
35	103	8.354	8.260	-0.094
35	104	8.235	8.202	-0.033
35	105	8.284	8.305	0.021
50	106	8.281	8.200	-0.081
50	107	8.300	8.252	-0.048
50	108	8.304	8.207	-0.097
50	109	8.217	8.207	-0.010
50	110	8.326	8.264	-0.062
50	111	8.215	8.145	-0.070
55	113	8.232	8.210	-0.022
55	114	8.211	8.099	-0.112
55	115	8.269	8.173	-0.096
55	116	8.256	8.176	-0.080
55	126	8.361	8.238	-0.123
55	128	8.285	8.200	-0.085
100	129	8.343	8.213	-0.130
100	131	8.301	8.207	-0.094
100	132	8.322	8.197	-0.125
100	133	8.350	8.238	-0.112
100	134	8.320	8.234	-0.086
100	135	8.224	8.134	-0.090
100	138	8.330	8.193	-0.137
100	139	8.205	8.130	-0.075
100	140	8.266	8.187	-0.079
100	141	8.216	8.104	-0.112
100	142	8.281	8.197	-0.084
100	143	8.218	8.184	-0.034
100	144	8.264	8.129	-0.135
100	220	8.283	8.155	-0.128
100	221	8.204	8.099	-0.105
100	224	8.260	8.172	-0.088
100	225	8.314	8.267	-0.047
100	226	8.289	8.174	-0.115
100	228	8.266	8.238	-0.028
100	232	8.254	8.152	-0.102
100	233	8.250	8.190	-0.060
100	236	8.245	8.155	-0.090
100	238	8.243	8.142	-0.101
100	243	8.247	8.169	-0.078
105	244	8.258	8.189	-0.069
105	246	8.223	8.176	-0.047
105	248	8.338	8.190	-0.148
105	374	8.287	8.215	-0.072
105	376	8.275	8.172	-0.103
	Max	8.361	8.351	0.048
	Average	8.273	8.215	-0.058
	Min	8.166	8.099	-0.149
	Std Dev	0.047	0.051	0.048



9.90_T_SYNC_Fa											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.244	8.160	8.196	8.172	8.200	8.214	8.202	8.145	8.099	8.099	8.172
Average	8.265	8.233	8.241	8.262	8.234	8.251	8.253	8.213	8.183	8.178	8.188
Max	8.291	8.264	8.284	8.351	8.268	8.311	8.307	8.264	8.238	8.267	8.215
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

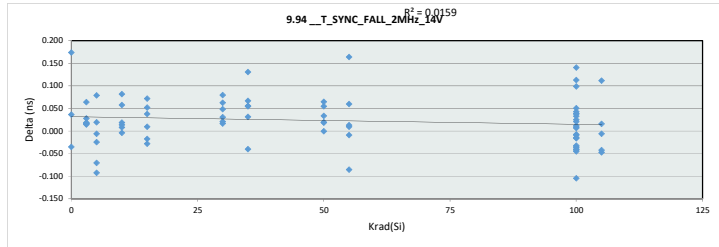




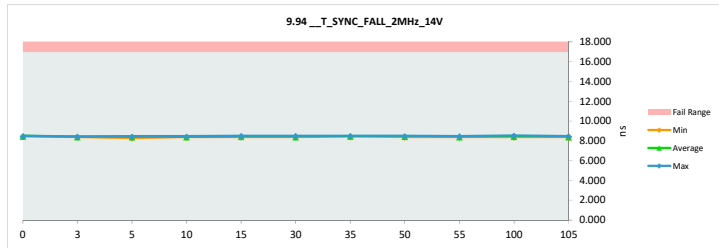
LDR TID Report  
TPS7H5001-SP

9.94_T_SYNC_FALL_2MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.330	8.505	0.175
0	118	8.535	8.501	-0.034
0	122	8.444	8.482	0.038
3	61	8.448	8.464	0.016
3	62	8.432	8.461	0.029
3	63	8.373	8.391	0.018
3	64	8.366	8.386	0.020
3	65	8.376	8.441	0.065
3	66	8.397	8.415	0.018
5	67	8.448	8.425	-0.023
5	68	8.539	8.470	-0.069
5	69	8.410	8.405	-0.005
5	70	8.357	8.437	0.080
5	71	8.395	8.304	-0.091
5	72	8.446	8.467	0.021
10	73	8.448	8.463	0.015
10	74	8.432	8.429	-0.003
10	75	8.373	8.393	0.020
10	76	8.366	8.425	0.059
10	77	8.376	8.459	0.083
10	78	8.397	8.406	0.009
15	79	8.448	8.459	0.011
15	80	8.539	8.523	-0.016
15	81	8.410	8.383	-0.027
15	82	8.357	8.430	0.073
15	83	8.395	8.434	0.039
15	84	8.446	8.499	0.053
30	86	8.448	8.529	0.081
30	89	8.432	8.450	0.018
30	91	8.373	8.437	0.064
30	92	8.366	8.416	0.050
30	94	8.376	8.398	0.022
30	95	8.397	8.429	0.032
35	100	8.448	8.505	0.057
35	101	8.539	8.500	-0.039
35	102	8.410	8.443	0.033
35	103	8.357	8.489	0.132
35	104	8.395	8.451	0.056
35	105	8.446	8.514	0.068
50	106	8.484	8.505	0.021
50	107	8.449	8.505	0.056
50	108	8.395	8.461	0.066
50	109	8.430	8.431	0.001
50	110	8.490	8.510	0.020
50	111	8.351	8.386	0.035
55	113	8.330	8.495	0.165
55	114	8.385	8.396	0.011
55	115	8.394	8.455	0.061
55	116	8.442	8.456	0.014
55	126	8.531	8.447	-0.084
55	128	8.431	8.423	-0.008
100	129	8.552	8.512	-0.040
100	131	8.414	8.448	0.034
100	132	8.429	8.543	0.114
100	133	8.535	8.548	0.013
100	134	8.470	8.492	0.022
100	135	8.375	8.401	0.026
100	138	8.488	8.528	0.040
100	139	8.491	8.451	-0.040
100	140	8.448	8.458	0.010
100	141	8.387	8.395	0.008
100	142	8.559	8.456	-0.103
100	143	8.458	8.422	-0.036
100	144	8.462	8.454	-0.008
100	220	8.483	8.449	-0.034
100	221	8.426	8.413	-0.013
100	224	8.457	8.451	-0.006
100	225	8.515	8.500	-0.015
100	226	8.478	8.522	0.044
100	228	8.510	8.549	0.039
100	232	8.537	8.506	-0.031
100	233	8.348	8.448	0.100
100	236	8.400	8.452	0.052
100	238	8.351	8.493	0.142
100	243	8.453	8.409	-0.044
105	244	8.502	8.461	-0.041
105	246	8.389	8.406	0.017
105	248	8.383	8.496	0.113
105	374	8.451	8.446	-0.005
105	376	8.432	8.386	-0.046
105	375	8.559	8.549	-0.010
Max		8.559	8.549	0.010
Average		8.432	8.455	0.023
Min		8.330	8.304	-0.026
Std Dev		0.058	0.046	0.053



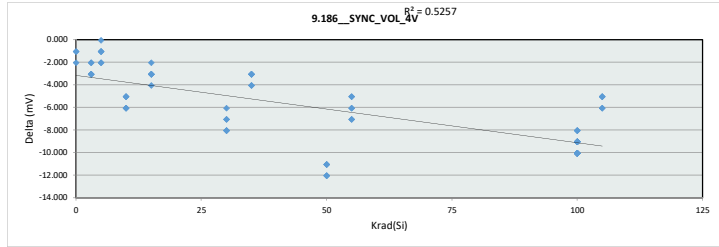
9.94_T_SYNC_FALL_2MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.482	8.386	8.304	8.393	8.383	8.398	8.443	8.386	8.396	8.395	8.386
Average	8.496	8.426	8.418	8.429	8.455	8.443	8.484	8.466	8.445	8.471	8.439
Max	8.505	8.464	8.470	8.463	8.523	8.529	8.514	8.510	8.495	8.549	8.496
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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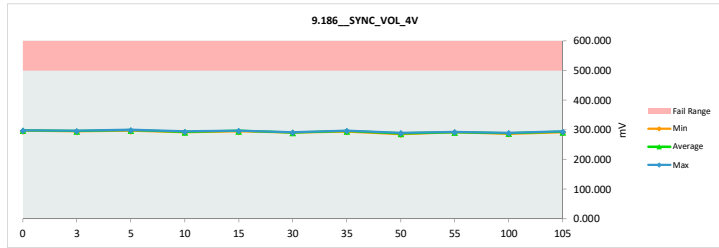
9.186_SYNC_VOL_4V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	117	300.000	299.000	-1.000
0	118	300.000	299.000	-1.000
0	122	299.000	297.000	-2.000
3	61	299.000	296.000	-3.000
3	62	300.000	297.000	-3.000
3	63	299.000	296.000	-3.000
3	64	299.000	296.000	-3.000
3	65	297.000	295.000	-2.000
3	66	299.000	297.000	-2.000
5	67	300.000	300.000	0.000
5	68	301.000	300.000	-1.000
5	69	299.000	298.000	-1.000
5	70	301.000	299.000	-2.000
5	71	298.000	297.000	-1.000
5	72	302.000	300.000	-2.000
10	73	299.000	293.000	-6.000
10	74	300.000	295.000	-5.000
10	75	299.000	293.000	-6.000
10	76	299.000	294.000	-5.000
10	77	297.000	292.000	-5.000
10	78	299.000	293.000	-6.000
15	79	300.000	298.000	-2.000
15	80	301.000	298.000	-3.000
15	81	299.000	296.000	-3.000
15	82	301.000	298.000	-3.000
15	83	298.000	295.000	-3.000
15	84	302.000	298.000	-4.000
30	86	299.000	291.000	-8.000
30	89	300.000	292.000	-8.000
30	91	299.000	292.000	-7.000
30	92	299.000	292.000	-7.000
30	94	297.000	291.000	-6.000
30	95	299.000	291.000	-8.000
35	100	300.000	297.000	-3.000
35	101	301.000	298.000	-3.000
35	102	299.000	296.000	-3.000
35	103	301.000	298.000	-3.000
35	104	298.000	294.000	-4.000
35	105	302.000	298.000	-4.000
50	106	298.000	286.000	-12.000
50	107	300.000	289.000	-11.000
50	108	301.000	290.000	-11.000
50	109	299.000	288.000	-11.000
50	110	301.000	289.000	-12.000
50	111	299.000	287.000	-12.000
55	113	299.000	293.000	-6.000
55	114	299.000	292.000	-7.000
55	115	298.000	292.000	-6.000
55	116	299.000	292.000	-7.000
55	126	297.000	292.000	-5.000
55	128	298.000	293.000	-5.000
100	129	296.000	287.000	-9.000
100	131	299.000	289.000	-10.000
100	132	299.000	290.000	-9.000
100	133	298.000	288.000	-10.000
100	134	299.000	290.000	-9.000
100	135	297.000	287.000	-10.000
100	138	297.000	288.000	-9.000
100	139	298.000	289.000	-9.000
100	140	299.000	289.000	-10.000
100	141	298.000	288.000	-10.000
100	142	298.000	288.000	-10.000
100	143	297.000	289.000	-8.000
100	144	297.000	289.000	-8.000
100	220	300.000	290.000	-10.000
100	221	298.000	288.000	-10.000
100	224	298.000	290.000	-8.000
100	225	299.000	290.000	-9.000
100	226	298.000	288.000	-10.000
100	228	298.000	288.000	-10.000
100	232	299.000	290.000	-9.000
100	233	300.000	290.000	-10.000
100	236	297.000	287.000	-10.000
100	238	298.000	289.000	-9.000
100	243	298.000	288.000	-10.000
105	244	299.000	293.000	-6.000
105	246	300.000	295.000	-5.000
105	248	299.000	294.000	-5.000
105	374	297.000	292.000	-5.000
105	376	301.000	295.000	-6.000
	Max	302.000	300.000	0.000
	Average	299.000	292.750	-6.250
	Min	296.000	286.000	-12.000
	Std Dev	1.331	3.957	3.320



9.186_SYNC_VOL	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

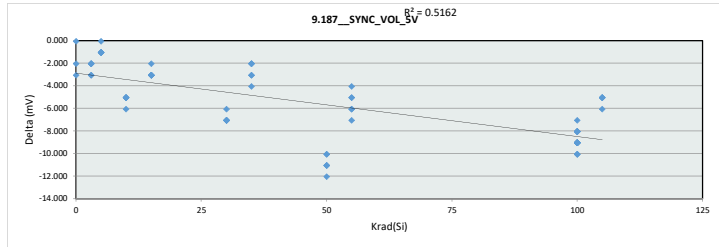
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	297.000	295.000	297.000	292.000	295.000	291.000	294.000	286.000	292.000	287.000	292.000
Average	298.333	296.167	299.000	293.333	297.167	291.500	296.833	288.167	292.333	288.708	293.800
Max	299.000	297.000	300.000	295.000	298.000	292.000	298.000	290.000	293.000	290.000	295.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000



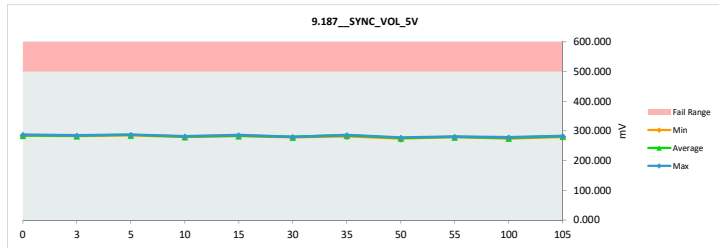
LDR TID Report  
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9.187_SYNC_VOL_5V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	117	288.000	286.000	-2.000
0	118	287.000	287.000	0.000
0	122	287.000	284.000	-3.000
3	61	286.000	284.000	-2.000
3	62	287.000	285.000	-2.000
3	63	287.000	284.000	-3.000
3	64	287.000	284.000	-3.000
3	65	285.000	283.000	-2.000
3	66	286.000	284.000	-2.000
5	67	288.000	287.000	-1.000
5	68	288.000	287.000	-1.000
5	69	286.000	286.000	0.000
5	70	288.000	287.000	-1.000
5	71	286.000	285.000	-1.000
5	72	289.000	288.000	-1.000
10	73	286.000	281.000	-5.000
10	74	287.000	282.000	-5.000
10	75	287.000	281.000	-6.000
10	76	287.000	282.000	-5.000
10	77	285.000	280.000	-5.000
10	78	286.000	281.000	-5.000
15	79	288.000	285.000	-3.000
15	80	288.000	285.000	-3.000
15	81	286.000	284.000	-2.000
15	82	288.000	285.000	-3.000
15	83	286.000	283.000	-3.000
15	84	289.000	286.000	-3.000
30	86	286.000	279.000	-7.000
30	89	287.000	280.000	-7.000
30	91	287.000	280.000	-7.000
30	92	287.000	280.000	-7.000
30	94	285.000	279.000	-6.000
30	95	286.000	279.000	-7.000
35	100	288.000	286.000	-2.000
35	101	288.000	285.000	-3.000
35	102	286.000	284.000	-2.000
35	103	288.000	286.000	-2.000
35	104	286.000	282.000	-4.000
35	105	289.000	286.000	-3.000
50	106	286.000	274.000	-12.000
50	107	288.000	278.000	-10.000
50	108	289.000	278.000	-11.000
50	109	287.000	276.000	-11.000
50	110	288.000	277.000	-11.000
50	111	286.000	276.000	-10.000
55	113	286.000	281.000	-5.000
55	114	286.000	280.000	-6.000
55	115	286.000	280.000	-6.000
55	116	287.000	280.000	-7.000
55	126	285.000	280.000	-5.000
55	128	285.000	281.000	-4.000
100	129	284.000	276.000	-8.000
100	131	286.000	278.000	-8.000
100	132	286.000	278.000	-8.000
100	133	285.000	276.000	-9.000
100	134	287.000	278.000	-9.000
100	135	284.000	275.000	-9.000
100	138	284.000	276.000	-8.000
100	139	286.000	278.000	-8.000
100	140	286.000	277.000	-9.000
100	141	286.000	277.000	-9.000
100	142	286.000	276.000	-10.000
100	143	285.000	277.000	-8.000
100	144	285.000	277.000	-8.000
100	220	288.000	279.000	-9.000
100	221	286.000	276.000	-10.000
100	224	286.000	279.000	-7.000
100	225	287.000	278.000	-9.000
100	226	285.000	276.000	-9.000
100	228	286.000	277.000	-9.000
100	232	287.000	278.000	-9.000
100	233	288.000	279.000	-9.000
100	236	285.000	275.000	-10.000
100	238	286.000	277.000	-9.000
100	243	286.000	277.000	-9.000
105	244	287.000	281.000	-6.000
105	246	288.000	283.000	-5.000
105	248	287.000	282.000	-5.000
105	374	285.000	280.000	-5.000
105	375	288.000	283.000	-5.000
105	Max	289.000	288.000	0.000
105	Average	286.563	280.775	-5.788
105	Min	284.000	274.000	-12.000
105	Std Dev	1.210	3.684	3.161



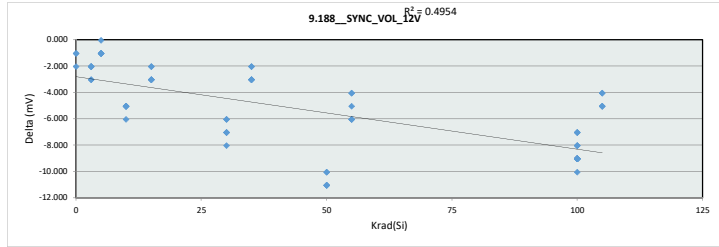
9.187_SYNC_VOL											
Test Site											
Tester											
Test Number											
Max Limit	500										
Min Limit	mV										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	284.000	283.000	285.000	280.000	283.000	279.000	282.000	274.000	280.000	275.000	280.000
Average	285.667	284.000	286.667	281.167	284.667	279.500	284.833	276.500	280.333	277.083	281.800
Max	287.000	285.000	288.000	282.000	286.000	280.000	286.000	278.000	281.000	279.000	283.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000



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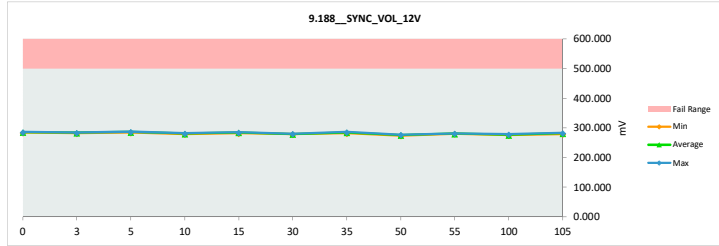
9.188_SYNC_VOL_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	117	287.000	286.000	-1.000
0	118	287.000	286.000	-1.000
0	122	286.000	284.000	-2.000
3	61	286.000	284.000	-2.000
3	62	287.000	284.000	-3.000
3	63	286.000	283.000	-3.000
3	64	286.000	284.000	-2.000
3	65	284.000	282.000	-2.000
3	66	286.000	284.000	-2.000
5	67	287.000	287.000	0.000
5	68	288.000	287.000	-1.000
5	69	286.000	285.000	-1.000
5	70	288.000	287.000	-1.000
5	71	285.000	284.000	-1.000
5	72	288.000	287.000	-1.000
10	73	286.000	280.000	-6.000
10	74	287.000	282.000	-5.000
10	75	286.000	281.000	-5.000
10	76	286.000	281.000	-5.000
10	77	284.000	279.000	-5.000
10	78	286.000	281.000	-5.000
15	79	287.000	285.000	-2.000
15	80	288.000	285.000	-3.000
15	81	286.000	284.000	-2.000
15	82	288.000	285.000	-3.000
15	83	285.000	282.000	-3.000
15	84	288.000	285.000	-3.000
30	86	286.000	279.000	-7.000
30	89	287.000	280.000	-7.000
30	91	286.000	280.000	-6.000
30	92	286.000	279.000	-7.000
30	94	284.000	278.000	-6.000
30	95	286.000	278.000	-8.000
35	100	287.000	285.000	-2.000
35	101	288.000	285.000	-3.000
35	102	286.000	283.000	-3.000
35	103	288.000	286.000	-2.000
35	104	285.000	282.000	-3.000
35	105	288.000	285.000	-3.000
50	106	285.000	274.000	-11.000
50	107	287.000	277.000	-10.000
50	108	288.000	277.000	-11.000
50	109	286.000	276.000	-10.000
50	110	288.000	277.000	-11.000
50	111	286.000	275.000	-11.000
55	113	285.000	280.000	-5.000
55	114	286.000	280.000	-6.000
55	115	286.000	280.000	-6.000
55	116	286.000	280.000	-6.000
55	126	284.000	280.000	-4.000
55	128	285.000	281.000	-4.000
100	129	283.000	275.000	-8.000
100	131	286.000	277.000	-9.000
100	132	286.000	277.000	-9.000
100	133	285.000	276.000	-9.000
100	134	286.000	277.000	-9.000
100	135	284.000	275.000	-9.000
100	138	284.000	276.000	-8.000
100	139	286.000	277.000	-9.000
100	140	286.000	277.000	-9.000
100	141	285.000	276.000	-9.000
100	142	286.000	276.000	-10.000
100	143	284.000	277.000	-7.000
100	144	284.000	277.000	-7.000
100	220	287.000	278.000	-9.000
100	221	285.000	276.000	-9.000
100	224	285.000	278.000	-7.000
100	225	286.000	277.000	-9.000
100	226	285.000	276.000	-9.000
100	228	285.000	276.000	-9.000
100	232	286.000	278.000	-8.000
100	233	287.000	278.000	-9.000
100	236	284.000	275.000	-9.000
100	238	286.000	277.000	-9.000
100	243	285.000	276.000	-9.000
105	244	286.000	281.000	-5.000
105	246	287.000	283.000	-4.000
105	248	286.000	282.000	-4.000
105	374	284.000	279.000	-5.000
105	376	287.000	282.000	-5.000
105	Max	288.000	287.000	0.000
105	Average	285.988	280.325	-5.663
105	Min	283.000	274.000	-11.000
105	Std Dev	1.227	3.697	3.150



9.188_SYNC_VOL	
Test Site	
Tester	
Test Number	
Max Limit	500
Min Limit	mV
Krad(Si)	0 3

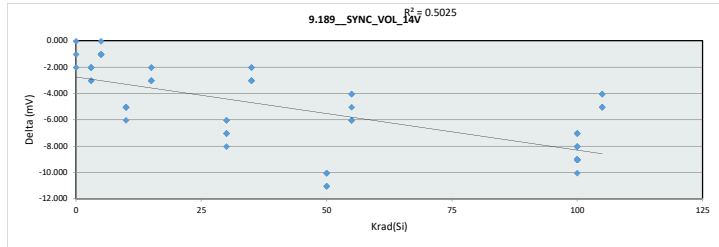
LL	5	10	15	30	35	50	55	100	105		
Min	284.000	282.000	284.000	279.000	282.000	278.000	282.000	274.000	280.000	275.000	279.000
Average	285.333	283.500	286.167	280.667	284.333	279.000	284.333	276.000	280.167	276.583	281.400
Max	286.000	284.000	287.000	282.000	285.000	280.000	286.000	277.000	281.000	278.000	283.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000



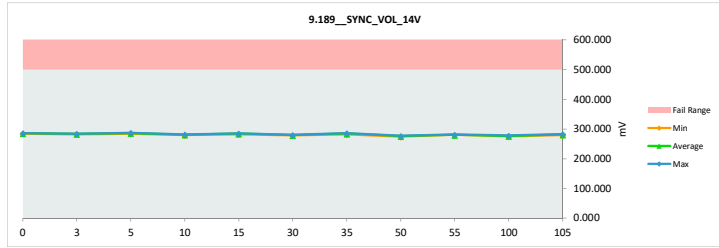
LDR TID Report  
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9.189_SYNC_VOL_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	117	287.000	286.000	-1.000
0	118	286.000	286.000	0.000
0	122	286.000	284.000	-2.000
3	61	286.000	284.000	-2.000
3	62	287.000	284.000	-3.000
3	63	286.000	283.000	-3.000
3	64	286.000	284.000	-2.000
3	65	284.000	282.000	-2.000
3	66	286.000	284.000	-2.000
5	67	287.000	287.000	0.000
5	68	288.000	287.000	-1.000
5	69	286.000	285.000	-1.000
5	70	288.000	287.000	-1.000
5	71	285.000	284.000	-1.000
5	72	288.000	287.000	-1.000
10	73	286.000	280.000	-6.000
10	74	287.000	282.000	-5.000
10	75	286.000	281.000	-5.000
10	76	286.000	281.000	-5.000
10	77	284.000	279.000	-5.000
10	78	286.000	281.000	-5.000
15	79	287.000	285.000	-2.000
15	80	288.000	285.000	-3.000
15	81	286.000	284.000	-2.000
15	82	288.000	285.000	-3.000
15	83	285.000	282.000	-3.000
15	84	288.000	285.000	-3.000
30	86	286.000	279.000	-7.000
30	89	287.000	280.000	-7.000
30	91	286.000	280.000	-6.000
30	92	286.000	279.000	-7.000
30	94	284.000	278.000	-6.000
30	95	286.000	278.000	-8.000
35	100	287.000	285.000	-2.000
35	101	288.000	285.000	-3.000
35	102	286.000	283.000	-3.000
35	103	288.000	286.000	-2.000
35	104	285.000	282.000	-3.000
35	105	288.000	285.000	-3.000
50	106	285.000	274.000	-11.000
50	107	287.000	277.000	-10.000
50	108	288.000	277.000	-11.000
50	109	286.000	276.000	-10.000
50	110	287.000	277.000	-10.000
50	111	286.000	275.000	-11.000
55	113	285.000	280.000	-5.000
55	114	286.000	280.000	-6.000
55	115	286.000	280.000	-6.000
55	116	286.000	280.000	-6.000
55	126	284.000	280.000	-4.000
55	128	285.000	281.000	-4.000
100	129	283.000	275.000	-8.000
100	131	286.000	277.000	-9.000
100	132	286.000	277.000	-9.000
100	133	285.000	276.000	-9.000
100	134	286.000	277.000	-9.000
100	135	284.000	275.000	-9.000
100	138	284.000	276.000	-8.000
100	139	286.000	277.000	-9.000
100	140	286.000	277.000	-9.000
100	141	285.000	276.000	-9.000
100	142	286.000	276.000	-10.000
100	143	284.000	277.000	-7.000
100	144	284.000	277.000	-7.000
100	220	287.000	278.000	-9.000
100	221	285.000	276.000	-9.000
100	224	285.000	278.000	-7.000
100	225	286.000	277.000	-9.000
100	226	285.000	276.000	-9.000
100	228	285.000	276.000	-9.000
100	232	286.000	278.000	-8.000
100	233	287.000	278.000	-9.000
100	236	284.000	275.000	-9.000
100	238	286.000	277.000	-9.000
100	243	285.000	276.000	-9.000
105	244	286.000	281.000	-5.000
105	246	287.000	283.000	-4.000
105	248	286.000	282.000	-4.000
105	374	284.000	279.000	-5.000
105	375	287.000	282.000	-5.000
105	Max	288.000	287.000	0.000
105	Average	285.963	280.325	-5.638
105	Min	283.000	274.000	-11.000
105	Std Dev	1.206	3.697	3.151



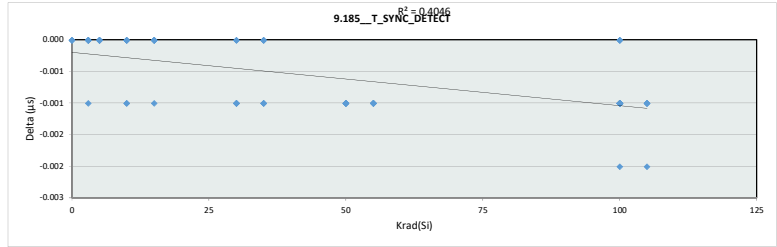
9.189_SYNC_VOL		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		284.000	282.000	284.000	279.000	282.000	278.000	282.000	274.000	280.000	275.000	279.000
Average		285.333	283.500	286.167	280.667	284.333	279.000	284.333	276.000	280.167	276.583	281.400
Max		286.000	284.000	287.000	282.000	285.000	280.000	286.000	277.000	281.000	278.000	283.000
UL		500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000



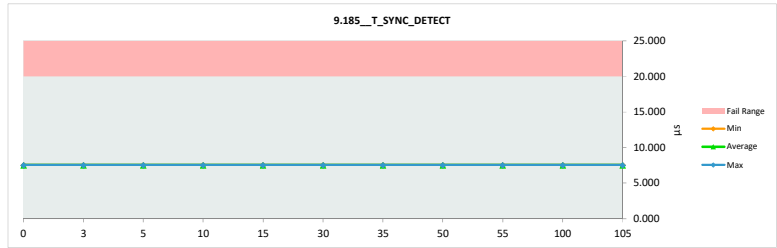
LDR TID Report  
TPS7H5001-SP

9.185_T_SYNC_DETECT	
Test Site	
Tester	
Test Number	
Unit	µs
Max Limit	20
Min Limit	20

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.587	7.587	0.000
0	118	7.586	7.586	0.000
0	122	7.587	7.587	0.000
3	61	7.588	7.588	0.000
3	62	7.587	7.587	0.000
3	63	7.587	7.587	0.000
3	64	7.588	7.587	-0.001
3	65	7.587	7.587	0.000
3	66	7.588	7.588	0.000
5	67	7.587	7.587	0.000
5	68	7.586	7.586	0.000
5	69	7.587	7.587	0.000
5	70	7.587	7.587	0.000
5	71	7.588	7.588	0.000
5	72	7.588	7.588	0.000
10	73	7.588	7.588	0.000
10	74	7.587	7.587	0.000
10	75	7.587	7.587	0.000
10	76	7.588	7.587	-0.001
10	77	7.587	7.587	0.000
10	78	7.588	7.587	-0.001
15	79	7.587	7.587	0.000
15	80	7.586	7.586	0.000
15	81	7.587	7.587	0.000
15	82	7.587	7.587	0.000
15	83	7.588	7.588	0.000
15	84	7.588	7.587	-0.001
30	86	7.588	7.587	-0.001
30	89	7.587	7.587	0.000
30	91	7.587	7.586	-0.001
30	92	7.588	7.587	-0.001
30	94	7.587	7.586	-0.001
30	95	7.588	7.588	0.000
35	100	7.587	7.587	0.000
35	101	7.586	7.586	0.000
35	102	7.587	7.586	-0.001
35	103	7.587	7.587	0.000
35	104	7.588	7.587	-0.001
35	105	7.588	7.587	-0.001
50	106	7.588	7.587	-0.001
50	107	7.588	7.587	-0.001
50	108	7.586	7.585	-0.001
50	109	7.587	7.586	-0.001
50	110	7.587	7.586	-0.001
50	111	7.588	7.587	-0.001
55	113	7.588	7.587	-0.001
55	114	7.587	7.586	-0.001
55	115	7.588	7.587	-0.001
55	116	7.588	7.587	-0.001
55	126	7.586	7.585	-0.001
55	128	7.588	7.587	-0.001
100	129	7.587	7.587	0.000
100	131	7.586	7.585	-0.001
100	132	7.587	7.586	-0.001
100	133	7.586	7.585	-0.001
100	134	7.588	7.587	-0.001
100	135	7.587	7.586	-0.001
100	138	7.588	7.587	-0.001
100	139	7.585	7.584	-0.001
100	140	7.586	7.585	-0.001
100	141	7.588	7.587	-0.001
100	142	7.586	7.585	-0.001
100	143	7.587	7.585	-0.002
100	144	7.587	7.586	-0.001
100	220	7.587	7.586	-0.001
100	221	7.587	7.586	-0.001
100	224	7.586	7.585	-0.001
100	225	7.587	7.586	-0.001
100	226	7.588	7.587	-0.001
100	228	7.588	7.587	-0.001
100	232	7.586	7.586	0.000
100	233	7.587	7.586	-0.001
100	236	7.586	7.585	-0.001
100	238	7.588	7.588	0.000
100	243	7.587	7.586	-0.001
105	244	7.587	7.585	-0.002
105	246	7.586	7.585	-0.001
105	248	7.587	7.586	-0.001
105	374	7.586	7.585	-0.001
105	376	7.588	7.587	-0.001
	Max	7.588	7.588	0.000
	Average	7.587	7.587	-0.001
	Min	7.585	7.584	-0.002
	Std Dev	0.001	0.001	0.001



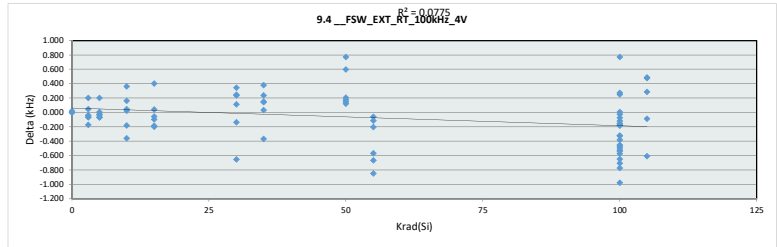
9.185_T_SYNC_D											
Test Site											
Tester											
Test Number											
Max Limit	20 µs										
Min Limit	µs										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.586	7.587	7.586	7.587	7.586	7.586	7.586	7.585	7.585	7.584	7.585
Average	7.587	7.587	7.587	7.587	7.587	7.587	7.587	7.586	7.587	7.586	7.586
Max	7.587	7.588	7.588	7.588	7.588	7.588	7.587	7.587	7.587	7.588	7.587
UL	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000



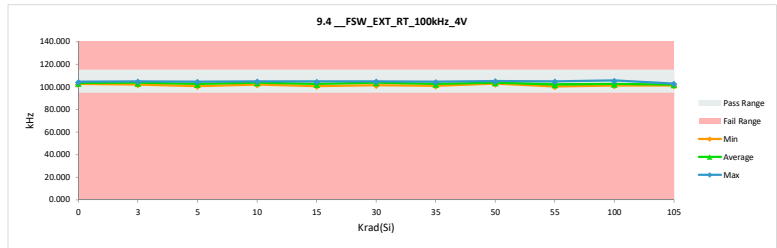
LDR TID Report  
TPS7H5001-SP

9.4_FSW_EXT_RT_100kHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	115
Min Limit	95

Krad(Si)	Serial #	PRE	POST	Delta
0	117	104.640	104.650	0.010
0	118	103.382	103.390	0.008
0	122	102.764	102.792	0.028
3	61	104.230	104.175	-0.055
3	62	104.811	105.021	0.210
3	63	102.449	102.285	-0.164
3	64	103.076	103.047	-0.029
3	65	104.387	104.331	-0.056
3	66	103.906	103.960	0.054
5	67	102.778	102.792	0.014
5	68	102.320	102.304	-0.016
5	69	102.636	102.574	-0.062
5	70	104.512	104.725	0.213
5	71	102.929	102.904	-0.025
5	72	100.854	100.796	-0.058
10	73	104.230	104.287	0.057
10	74	104.811	105.182	0.371
10	75	102.449	102.277	-0.172
10	76	103.076	102.728	-0.348
10	77	104.387	104.556	0.169
10	78	103.906	103.940	0.034
15	79	102.778	102.831	0.053
15	80	102.320	102.132	-0.188
15	81	102.636	102.550	-0.086
15	82	104.512	104.925	0.413
15	83	102.929	102.752	-0.177
15	84	100.854	100.805	-0.049
30	86	104.230	104.475	0.245
30	89	104.811	105.066	0.255
30	91	102.449	101.804	-0.645
30	92	103.076	102.948	-0.128
30	94	104.387	104.740	0.353
30	95	103.906	104.030	0.124
35	100	102.778	102.931	0.153
35	101	102.320	101.963	-0.357
35	102	102.636	103.027	0.391
35	103	104.512	104.668	0.156
35	104	102.929	102.970	0.041
35	105	100.854	101.101	0.247
50	106	103.512	103.728	0.216
50	107	103.243	103.399	0.156
50	108	105.201	105.387	0.186
50	109	103.760	104.367	0.607
50	110	102.999	103.132	0.133
50	111	102.147	102.930	0.783
55	113	101.779	100.939	-0.840
55	114	102.137	101.576	-0.561
55	115	102.736	102.079	-0.657
55	116	100.590	100.483	-0.107
55	126	105.037	104.985	-0.052
55	128	103.920	103.727	-0.193
100	129	102.274	102.533	0.259
100	131	104.163	103.706	-0.457
100	132	104.663	104.022	-0.641
100	133	102.170	102.154	-0.016
100	134	102.823	102.713	-0.110
100	135	102.850	102.476	-0.374
100	138	101.988	102.768	0.780
100	139	102.710	102.012	-0.698
100	140	102.917	102.743	-0.174
100	141	104.554	103.792	-0.762
100	142	102.335	102.619	0.284
100	143	102.129	101.645	-0.484
100	144	101.800	101.487	-0.313
100	220	103.914	102.945	-0.969
100	221	103.711	103.644	-0.067
100	224	102.665	102.102	-0.563
100	225	104.684	104.170	-0.514
100	226	105.891	105.746	-0.145
100	228	102.169	102.182	0.013
100	232	102.740	102.570	-0.170
100	233	101.931	101.782	-0.149
100	236	102.864	102.551	-0.313
100	238	104.019	103.493	-0.526
100	243	103.319	102.874	-0.445
105	244	102.036	101.436	-0.600
105	246	102.537	102.458	-0.079
105	248	102.469	102.957	0.488
105	374	101.129	101.623	0.494
105	376	102.782	103.077	0.295
105	378	105.891	105.746	-0.145
Max		105.891	105.746	0.783
Average		103.134	103.068	-0.066
Min		100.590	100.483	-0.969
Std Dev		1.122	1.189	0.358



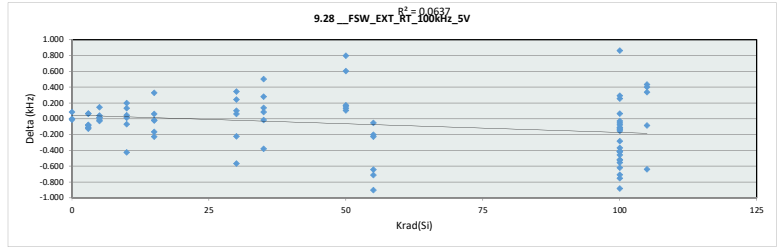
9.4_FSW_EXT_R												
Test Site												
Tester												
Test Number												
Max Limit	115	kHz										
Min Limit	95	kHz										
Krad(Si)	0	5	10	15	30	35	50	55	100	105		
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	
Min	102.792	102.285	100.796	102.277	100.805	101.804	101.101	102.930	100.483	101.487	101.436	
Average	103.611	103.803	102.683	103.828	102.666	103.844	102.777	103.824	102.298	102.864	102.310	
Max	104.650	105.021	104.725	105.182	104.925	105.066	104.668	105.387	104.985	105.746	103.077	
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	



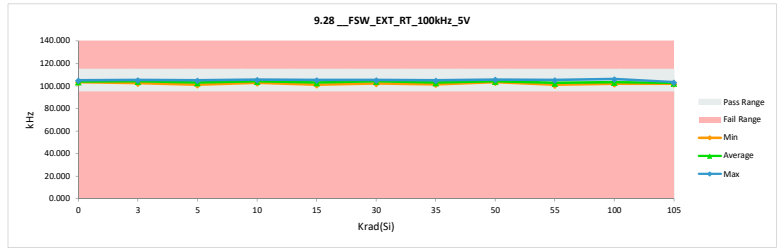
LDR TID Report  
TPS7H5001-SP

9.28 FSW_EXT_RT_100kHz_5V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	115
Min Limit	95

Krad(Si)	Serial #	PRE	POST	Delta
0	117	105.035	105.031	-0.004
0	118	103.714	103.720	0.006
0	122	103.134	103.226	0.092
3	61	104.589	104.520	-0.069
3	62	105.327	105.400	0.073
3	63	102.728	102.610	-0.118
3	64	103.503	103.403	-0.100
3	65	104.779	104.702	-0.077
3	66	104.331	104.404	0.073
5	67	103.126	103.145	0.019
5	68	102.663	102.683	0.020
5	69	102.892	102.941	0.049
5	70	104.968	105.120	0.152
5	71	103.389	103.380	-0.009
5	72	101.198	101.178	-0.020
10	73	104.589	104.640	0.051
10	74	105.327	105.534	0.207
10	75	102.728	102.668	-0.060
10	76	103.503	103.086	-0.417
10	77	104.779	104.919	0.140
10	78	104.331	104.349	0.018
15	79	103.126	103.193	0.067
15	80	102.663	102.503	-0.160
15	81	102.892	102.880	-0.012
15	82	104.968	105.303	0.335
15	83	103.389	103.169	-0.220
15	84	101.198	101.184	-0.014
30	86	104.589	104.839	0.250
30	89	105.327	105.437	0.110
30	91	102.728	102.168	-0.560
30	92	103.503	103.289	-0.214
30	94	104.779	105.130	0.351
30	95	104.331	104.399	0.068
35	100	103.126	103.273	0.147
35	101	102.663	102.292	-0.371
35	102	102.892	103.403	0.511
35	103	104.968	105.060	0.092
35	104	103.389	103.380	-0.009
35	105	101.198	101.483	0.285
50	106	104.031	104.143	0.112
50	107	103.647	103.787	0.140
50	108	105.593	105.761	0.168
50	109	104.066	104.675	0.609
50	110	103.360	103.538	0.178
50	111	102.476	103.281	0.805
55	113	102.250	101.353	-0.897
55	114	102.543	101.906	-0.637
55	115	103.130	102.427	-0.703
55	116	101.027	100.834	-0.193
55	126	105.394	105.349	-0.045
55	128	104.320	104.100	-0.220
100	129	102.686	102.949	0.263
100	131	104.487	104.077	-0.410
100	132	104.993	104.383	-0.610
100	133	102.535	102.515	-0.020
100	134	103.186	103.117	-0.069
100	135	103.216	102.815	-0.401
100	138	102.297	103.165	0.868
100	139	103.081	102.382	-0.699
100	140	103.240	103.115	-0.125
100	141	104.932	104.188	-0.744
100	142	102.746	103.043	0.297
100	143	102.472	102.024	-0.448
100	144	102.165	101.890	-0.275
100	220	104.235	103.361	-0.874
100	221	104.091	104.046	-0.045
100	224	102.983	102.468	-0.515
100	225	105.071	104.523	-0.548
100	226	106.325	106.177	-0.148
100	228	102.540	102.612	0.072
100	232	103.117	103.009	-0.108
100	233	102.255	102.152	-0.103
100	236	103.291	102.931	-0.360
100	238	104.399	103.887	-0.512
100	243	103.672	103.264	-0.408
105	244	102.495	101.863	-0.632
105	246	102.852	102.775	-0.077
105	248	102.837	103.244	0.407
105	374	101.500	101.942	0.442
105	376	103.099	103.441	0.342
105	Max	106.325	106.177	0.868
105	Average	103.512	103.444	-0.068
105	Min	101.027	100.834	-0.897
105	Std Dev	1.143	1.191	0.354



9.28 FSW_EXT		5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	103.226	102.610	101.178	102.668	101.184	102.168	101.483	103.281	100.834	101.890
Average	103.992	104.173	103.075	104.199	103.039	104.210	103.149	104.198	102.662	103.254
Max	105.031	105.400	105.120	105.534	105.303	105.437	105.060	105.761	105.349	106.177
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

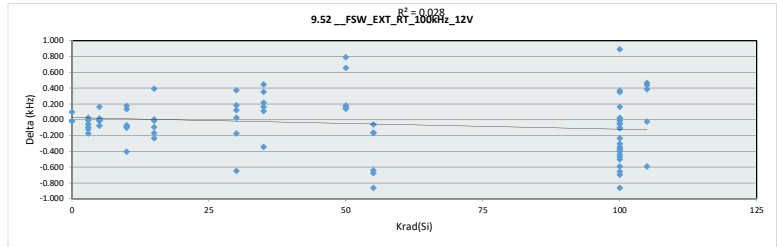




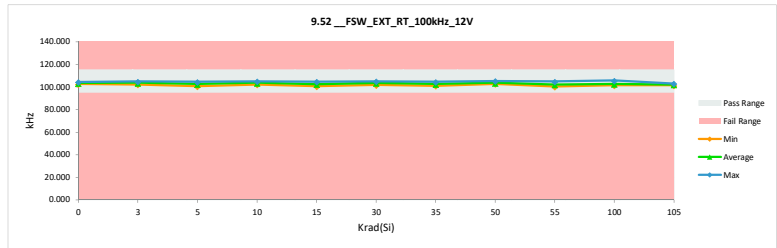
LDR TID Report  
TPS7H5001-SP

9.52_FSW_EXT_RT_100kHz_12V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	115
Min Limit	95

Krad(Si)	Serial #	PRE	POST	Delta
0	117	104.472	104.461	-0.011
0	118	103.196	103.188	-0.008
0	122	102.578	102.681	0.103
3	61	104.152	104.038	-0.114
3	62	104.842	104.873	0.031
3	63	102.303	102.136	-0.167
3	64	103.005	102.919	-0.086
3	65	104.219	104.169	-0.050
3	66	103.858	103.856	-0.002
5	67	102.620	102.637	0.017
5	68	102.169	102.160	-0.009
5	69	102.449	102.381	-0.068
5	70	104.327	104.495	0.168
5	71	102.790	102.790	0.000
5	72	100.669	100.694	0.025
10	73	104.152	104.089	-0.063
10	74	104.842	105.024	0.182
10	75	102.303	102.211	-0.092
10	76	103.005	102.606	-0.399
10	77	104.219	104.361	0.142
10	78	103.858	103.781	-0.077
15	79	102.620	102.632	0.012
15	80	102.169	101.940	-0.229
15	81	102.449	102.365	-0.084
15	82	104.327	104.726	0.399
15	83	102.790	102.628	-0.162
15	84	100.669	100.663	-0.006
30	86	104.152	104.340	0.188
30	89	104.842	104.969	0.127
30	91	102.303	101.663	-0.640
30	92	103.005	102.840	-0.165
30	94	104.219	104.597	0.378
30	95	103.858	103.889	0.031
35	100	102.620	102.789	0.169
35	101	102.169	101.832	-0.337
35	102	102.449	102.903	0.454
35	103	104.327	104.548	0.221
35	104	102.790	102.906	0.116
35	105	100.669	101.030	0.361
50	106	103.432	103.603	0.171
50	107	103.111	103.298	0.187
50	108	105.073	105.254	0.181
50	109	103.530	104.193	0.663
50	110	102.882	103.029	0.147
50	111	101.992	102.788	0.796
55	113	101.731	100.875	-0.856
55	114	102.079	101.448	-0.631
55	115	102.578	101.908	-0.670
55	116	100.514	100.356	-0.158
55	126	104.874	104.819	-0.055
55	128	103.756	103.598	-0.158
100	129	102.130	102.487	0.357
100	131	103.957	103.595	-0.362
100	132	104.489	103.906	-0.583
100	133	102.005	102.038	0.033
100	134	102.627	102.621	-0.006
100	135	102.717	102.380	-0.337
100	138	101.808	102.708	0.900
100	139	102.561	101.911	-0.650
100	140	102.740	102.698	-0.042
100	141	104.401	103.711	-0.690
100	142	102.166	102.543	0.377
100	143	102.004	101.544	-0.460
100	144	101.686	101.459	-0.227
100	220	103.742	102.886	-0.856
100	221	103.558	103.579	0.021
100	224	102.436	102.002	-0.434
100	225	104.508	104.015	-0.493
100	226	105.824	105.729	-0.095
100	228	101.962	102.131	0.169
100	232	102.611	102.509	-0.102
100	233	101.724	101.679	-0.045
100	236	102.749	102.452	-0.297
100	238	103.803	103.411	-0.392
100	243	103.169	102.779	-0.390
105	244	101.976	101.394	-0.582
105	246	102.336	102.321	-0.015
105	248	102.354	102.826	0.472
105	374	101.013	101.457	0.444
105	376	102.592	102.985	0.393
105	375	105.824	105.729	-0.095
105	Max	102.996	102.947	-0.049
105	Min	100.514	100.356	-0.856
105	Std Dev	1.135	1.177	0.351



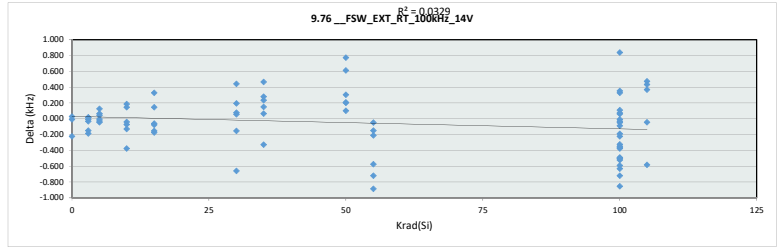
9.52_FSW_EXT		0	5	10	15	30	35	50	55	100	105
LL		95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min		102.681	102.136	100.694	102.211	100.663	101.663	101.030	102.788	100.356	101.459
Average		103.443	103.665	102.526	103.679	102.492	103.716	102.668	103.694	102.167	102.782
Max		104.461	104.873	104.495	105.024	104.726	104.969	104.548	105.254	104.819	105.729
UL		115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000



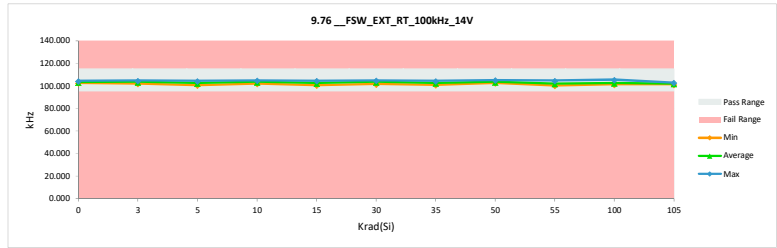
LDR TID Report  
TPS7H5001-SP

9.76_FSW_EXT_RT_100kHz_14V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	115
Min Limit	95

Krad(Si)	Serial #	PRE	POST	Delta
0	117	104.456	104.490	0.034
0	118	103.396	103.180	-0.216
0	122	102.660	102.661	0.001
3	61	104.141	103.999	-0.142
3	62	104.859	104.860	0.001
3	63	102.293	102.115	-0.178
3	64	102.961	102.938	-0.023
3	65	104.157	104.182	0.025
3	66	103.812	103.835	0.023
5	67	102.543	102.617	0.074
5	68	102.110	102.112	0.002
5	69	102.412	102.390	-0.022
5	70	104.391	104.526	0.135
5	71	102.808	102.860	0.052
5	72	100.731	100.693	-0.038
10	73	104.141	104.077	-0.064
10	74	104.859	105.014	0.155
10	75	102.293	102.173	-0.120
10	76	102.961	102.594	-0.367
10	77	104.157	104.350	0.193
10	78	103.812	103.779	-0.033
15	79	102.543	102.696	0.153
15	80	102.110	101.965	-0.145
15	81	102.412	102.358	-0.054
15	82	104.391	104.727	0.336
15	83	102.808	102.643	-0.165
15	84	100.731	100.660	-0.071
30	86	104.141	104.343	0.202
30	89	104.859	104.922	0.063
30	91	102.293	101.642	-0.651
30	92	102.961	102.814	-0.147
30	94	104.157	104.605	0.448
30	95	103.812	103.895	0.083
35	100	102.543	102.786	0.243
35	101	102.110	101.788	-0.322
35	102	102.412	102.886	0.474
35	103	104.391	104.548	0.157
35	104	102.808	102.881	0.073
35	105	100.731	101.017	0.286
50	106	103.357	103.571	0.214
50	107	103.102	103.209	0.107
50	108	105.032	105.242	0.210
50	109	103.557	104.174	0.617
50	110	102.724	103.037	0.313
50	111	101.987	102.768	0.781
55	113	101.718	100.841	-0.877
55	114	102.047	101.478	-0.569
55	115	102.611	101.897	-0.714
55	116	100.554	100.351	-0.203
55	126	104.852	104.810	-0.042
55	128	103.748	103.604	-0.144
100	129	102.125	102.462	0.337
100	131	103.949	103.579	-0.370
100	132	104.460	103.878	-0.582
100	133	101.983	102.053	0.070
100	134	102.648	102.647	-0.001
100	135	102.709	102.367	-0.342
100	138	101.842	102.687	0.845
100	139	102.527	101.905	-0.622
100	140	102.708	102.683	-0.025
100	141	104.401	103.690	-0.711
100	142	102.162	102.521	0.359
100	143	102.028	101.515	-0.513
100	144	101.666	101.452	-0.214
100	220	103.735	102.887	-0.848
100	221	103.480	103.562	0.082
100	224	102.447	101.963	-0.484
100	225	104.509	104.012	-0.497
100	226	105.783	105.602	-0.181
100	228	102.016	102.131	0.115
100	232	102.589	102.508	-0.081
100	233	101.740	101.698	-0.042
100	236	102.754	102.438	-0.316
100	238	103.890	103.400	-0.490
100	243	103.141	102.789	-0.352
105	244	101.981	101.405	-0.576
105	246	102.330	102.295	-0.035
105	248	102.316	102.798	0.482
105	374	101.006	101.448	0.442
105	376	102.595	102.970	0.375
Max		105.783	105.602	0.845
Average		102.987	102.937	-0.050
Min		100.554	100.351	-0.877
Std Dev		1.129	1.173	0.352



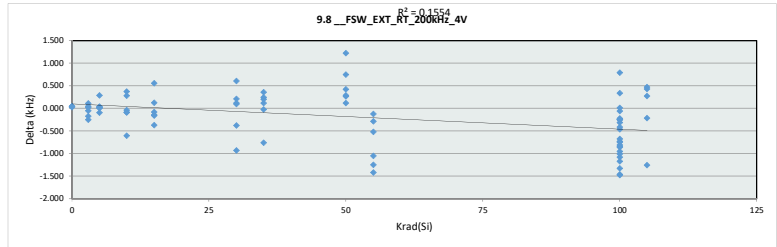
9.76_FSW_EXT										
Test Site										
Tester										
Test Number										
Max Limit	115 kHz									
Min Limit	95 kHz									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.661	102.115	100.693	102.173	100.660	101.642	101.017	102.768	100.351	101.452
Average	103.444	103.655	102.533	103.665	102.508	103.704	102.651	103.667	102.164	102.768
Max	104.490	104.860	104.526	105.014	104.727	104.922	104.548	105.242	104.810	105.602
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000



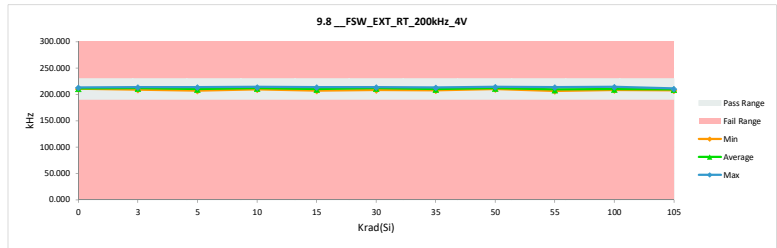
LDR TID Report  
TPS7H5001-SP

9.8_FSW_EXT_RT_200kHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz kHz
Max Limit	230 230
Min Limit	190 190

Krad(Si)	Serial #	PRE	POST	Delta
0	117	213.421	213.463	0.042
0	118	211.339	211.402	0.063
0	122	210.901	210.964	0.063
3	61	212.744	212.588	-0.156
3	62	214.187	214.311	0.124
3	63	209.805	209.571	-0.234
3	64	211.436	211.395	-0.041
3	65	212.923	212.956	0.033
3	66	212.524	212.582	0.058
5	67	210.526	210.565	0.039
5	68	210.165	210.221	0.056
5	69	209.898	209.919	0.021
5	70	213.848	214.153	0.305
5	71	210.860	210.894	0.034
5	72	207.661	207.580	-0.081
10	73	212.744	212.718	-0.026
10	74	214.187	214.483	0.296
10	75	209.805	209.730	-0.075
10	76	211.436	210.842	-0.594
10	77	212.923	213.309	0.386
10	78	212.524	212.450	-0.074
15	79	210.526	210.668	0.142
15	80	210.165	209.811	-0.354
15	81	209.898	209.751	-0.147
15	82	213.848	214.416	0.568
15	83	210.860	210.730	-0.130
15	84	207.661	207.597	-0.064
30	86	212.744	212.966	0.222
30	89	214.187	214.315	0.128
30	91	209.805	208.885	-0.920
30	92	211.436	211.071	-0.365
30	94	212.923	213.542	0.619
30	95	212.524	212.631	0.107
35	100	210.526	210.744	0.218
35	101	210.165	209.415	-0.750
35	102	209.898	210.273	0.375
35	103	213.848	213.840	-0.008
35	104	210.860	210.994	0.134
35	105	207.661	207.919	0.258
50	106	211.872	212.307	0.435
50	107	211.244	211.523	0.279
50	108	214.334	214.637	0.303
50	109	212.645	213.410	0.765
50	110	211.133	211.263	0.130
50	111	209.385	210.625	1.240
55	113	209.110	207.702	-1.408
55	114	209.297	208.263	-1.034
55	115	210.432	209.198	-1.234
55	116	207.142	206.871	-0.271
55	126	214.090	213.979	-0.111
55	128	212.584	212.075	-0.509
100	129	209.878	209.901	0.023
100	131	212.831	212.033	-0.798
100	132	213.772	212.611	-1.161
100	133	209.795	209.555	-0.240
100	134	210.997	210.596	-0.401
100	135	211.036	210.320	-0.716
100	138	209.658	210.462	0.804
100	139	210.994	209.683	-1.311
100	140	211.072	210.626	-0.446
100	141	213.150	211.705	-1.445
100	142	210.387	210.738	0.351
100	143	209.516	208.694	-0.822
100	144	209.757	209.025	-0.732
100	220	212.146	210.682	-1.464
100	221	212.042	211.835	-0.207
100	224	210.437	209.497	-0.940
100	225	213.663	212.665	-0.998
100	226	214.902	214.664	-0.238
100	228	209.741	209.697	-0.044
100	232	210.375	210.076	-0.299
100	233	209.668	209.438	-0.230
100	236	210.633	209.973	-0.660
100	238	212.774	211.705	-1.069
100	243	211.106	210.262	-0.844
105	244	209.543	208.298	-1.245
105	246	210.424	210.223	-0.201
105	248	210.848	211.336	0.488
105	374	207.830	208.276	0.446
105	376	211.083	211.368	0.285
105	375	214.902	214.664	-0.240
Max				
Average		211.234	211.043	-0.191
Min		207.142	206.871	-1.464
Std Dev		1.773	1.892	0.568



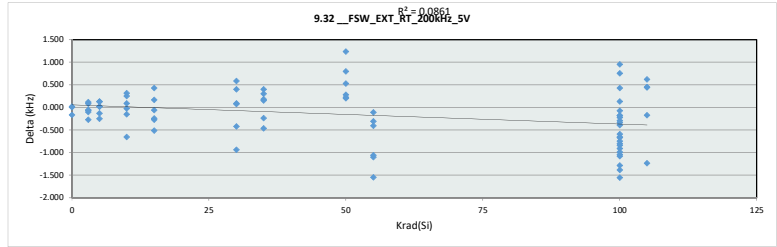
9.8_FSW_EXT_R										
Test Site										
Tester										
Test Number										
Max Limit	230 kHz									
Min Limit	190 kHz									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	210.964	209.571	207.580	209.730	207.597	208.885	207.919	210.625	206.871	208.694
Average	211.943	212.234	210.555	212.255	210.496	212.235	210.531	212.294	209.681	210.685
Max	213.463	214.311	214.153	214.483	214.416	214.315	213.840	214.637	213.979	214.664
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000



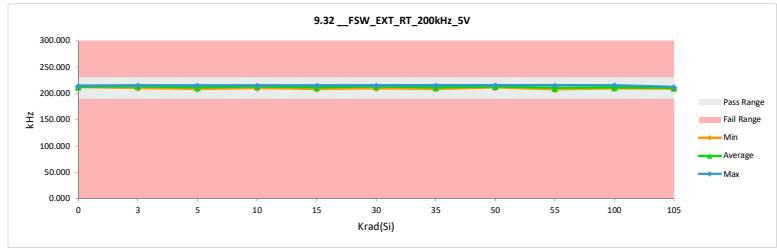
LDR TID Report  
TPS7H5001-SP

9.32_FSW_EXT_RT_200kHz_5V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	230
Min Limit	190

Krad(Si)	Serial #	PRE	POST	Delta
0	117	214.455	214.472	0.017
0	118	212.473	212.321	-0.152
0	122	211.885	211.917	0.032
3	61	213.621	213.574	-0.047
3	62	215.194	215.322	0.128
3	63	210.776	210.516	-0.260
3	64	212.470	212.379	-0.091
3	65	214.025	213.981	-0.044
3	66	213.538	213.631	0.093
5	67	211.528	211.558	0.030
5	68	211.007	211.042	0.035
5	69	210.888	210.770	-0.118
5	70	215.030	215.178	0.148
5	71	212.178	211.944	-0.234
5	72	208.620	208.758	0.138
10	73	213.621	213.723	0.102
10	74	215.194	215.324	0.327
10	75	210.776	210.640	-0.136
10	76	212.470	211.827	-0.643
10	77	214.025	214.291	0.266
10	78	213.538	213.528	-0.010
15	79	211.528	211.707	0.179
15	80	211.007	210.752	-0.255
15	81	210.888	210.656	-0.232
15	82	215.030	215.473	0.443
15	83	212.178	211.679	-0.499
15	84	208.620	208.576	-0.044
30	86	213.621	214.035	0.414
30	89	215.194	215.299	0.105
30	91	210.776	209.854	-0.922
30	92	212.470	212.062	-0.408
30	94	214.025	214.626	0.601
30	95	213.538	213.624	0.086
35	100	211.528	211.722	0.194
35	101	211.007	210.555	-0.452
35	102	210.888	211.303	0.415
35	103	215.030	215.196	0.166
35	104	212.178	211.958	-0.220
35	105	208.620	208.939	0.319
50	106	213.012	213.556	0.544
50	107	212.328	212.546	0.218
50	108	215.417	215.714	0.297
50	109	213.535	214.350	0.815
50	110	212.063	212.304	0.241
50	111	210.406	211.658	1.252
55	113	210.256	208.722	-1.534
55	114	210.324	209.234	-1.090
55	115	211.427	210.380	-1.047
55	116	208.126	207.835	-0.291
55	126	215.130	215.032	-0.098
55	128	213.633	213.237	-0.396
100	129	210.654	211.099	0.445
100	131	213.770	213.038	-0.732
100	132	214.703	213.636	-1.067
100	133	210.810	210.602	-0.208
100	134	211.994	211.671	-0.323
100	135	212.002	211.346	-0.656
100	138	210.517	211.483	0.966
100	139	211.997	210.726	-1.271
100	140	212.060	211.681	-0.379
100	141	214.148	212.780	-1.368
100	142	211.376	212.144	0.768
100	143	210.435	209.609	-0.826
100	144	210.774	210.130	-0.644
100	220	213.166	211.628	-1.538
100	221	213.062	212.999	-0.063
100	224	211.411	210.516	-0.895
100	225	214.699	213.723	-0.976
100	226	215.937	215.770	-0.167
100	228	210.677	210.824	0.147
100	232	211.451	211.174	-0.277
100	233	210.604	210.437	-0.167
100	236	211.639	211.063	-0.576
100	238	213.791	212.764	-1.027
100	243	212.101	211.314	-0.787
105	244	210.578	209.358	-1.220
105	246	211.300	211.138	-0.162
105	248	211.711	212.342	0.631
105	374	208.731	209.198	0.467
105	376	211.964	212.420	0.456
105	Max	215.937	215.770	1.252
105	Average	212.239	212.076	-0.163
105	Min	208.126	207.835	-1.538
105	Std Dev	1.802	1.910	0.578



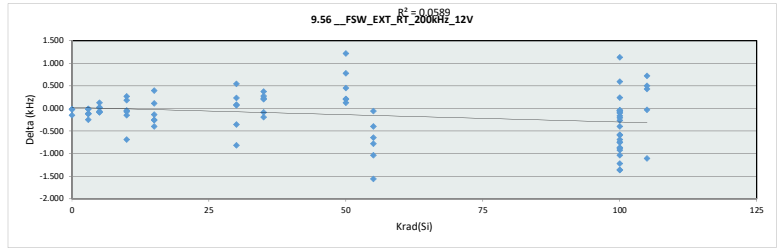
9.32_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	230 kHz										
Min Limit	190 kHz										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	211.917	210.516	208.758	210.640	208.576	209.854	208.939	211.658	207.835	209.609	209.198
Average	212.903	213.234	211.542	213.255	211.474	213.250	211.612	213.355	210.740	211.757	210.891
Max	214.472	215.322	215.178	215.521	215.473	215.299	215.196	215.714	215.032	215.770	212.420
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000



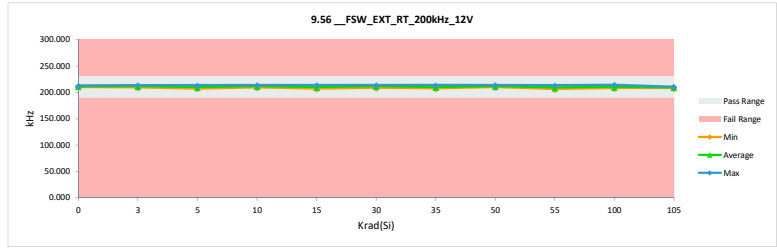
LDR TID Report  
TPS7H5001-SP

9.56_FSW_EXT_RT_200kHz_12V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	230
Min Limit	190

Krad(S)	Serial #	PRE	POST	Delta
0	117	213.135	213.133	-0.002
0	118	211.168	211.155	-0.013
0	122	210.703	210.568	-0.135
3	61	212.508	212.400	-0.108
3	62	214.067	214.067	0.000
3	63	209.594	209.358	-0.236
3	64	211.123	211.019	-0.104
3	65	212.731	212.629	-0.102
3	66	212.295	212.300	0.005
5	67	210.260	210.301	0.041
5	68	209.634	209.776	0.142
5	69	209.701	209.634	-0.067
5	70	213.677	213.699	0.022
5	71	210.805	210.729	-0.076
5	72	207.495	207.441	-0.054
10	73	212.508	212.475	-0.033
10	74	214.067	214.263	0.196
10	75	209.594	209.458	-0.136
10	76	211.123	210.446	-0.677
10	77	212.731	213.008	0.277
10	78	212.295	212.236	-0.059
15	79	210.260	210.385	0.125
15	80	209.634	209.513	-0.121
15	81	209.701	209.459	-0.242
15	82	213.677	214.084	0.407
15	83	210.805	210.419	-0.386
15	84	207.495	207.251	-0.244
30	86	212.508	212.752	0.244
30	89	214.067	214.164	0.097
30	91	209.594	208.788	-0.806
30	92	211.123	210.781	-0.342
30	94	212.731	213.287	0.556
30	95	212.295	212.374	0.079
35	100	210.260	210.544	0.284
35	101	209.634	209.557	-0.077
35	102	209.701	210.089	0.388
35	103	213.677	213.896	0.219
35	104	210.805	210.624	-0.181
35	105	207.495	207.734	0.239
50	106	211.830	212.296	0.466
50	107	210.955	211.177	0.222
50	108	214.121	214.334	0.213
50	109	212.191	212.981	0.790
50	110	210.867	211.005	0.138
50	111	209.152	210.384	1.232
55	113	209.039	207.495	-1.544
55	114	209.122	208.102	-1.020
55	115	210.132	209.361	-0.771
55	116	206.964	206.579	-0.385
55	126	213.795	213.748	-0.047
55	128	212.721	212.086	-0.635
100	129	209.553	209.802	0.249
100	131	212.484	211.811	-0.673
100	132	213.423	212.397	-1.026
100	133	209.509	209.444	-0.065
100	134	210.695	210.545	-0.150
100	135	210.750	210.182	-0.568
100	138	209.251	210.398	1.147
100	139	210.741	209.533	-1.208
100	140	210.751	210.499	-0.252
100	141	212.897	211.551	-1.346
100	142	210.145	210.752	0.607
100	143	209.218	208.305	-0.913
100	144	209.435	209.049	-0.386
100	220	211.892	210.540	-1.352
100	221	211.778	211.751	-0.027
100	224	210.099	209.365	-0.734
100	225	213.321	212.471	-0.850
100	226	214.766	214.675	-0.091
100	228	209.464	209.426	-0.038
100	232	210.168	209.971	-0.197
100	233	209.312	209.272	-0.040
100	236	210.419	209.844	-0.575
100	238	212.556	211.691	-0.865
100	243	210.885	210.147	-0.738
105	244	209.382	208.289	-1.093
105	246	210.011	209.997	-0.014
105	248	210.485	211.222	0.737
105	374	207.704	208.216	0.512
105	376	210.740	211.183	0.443
105	375	214.766	214.675	-0.091
105	Max	210.992	210.846	-0.147
105	Average	206.964	206.579	-1.544
105	Min	206.964	206.579	-1.544
105	Std Dev	1.786	1.873	0.539



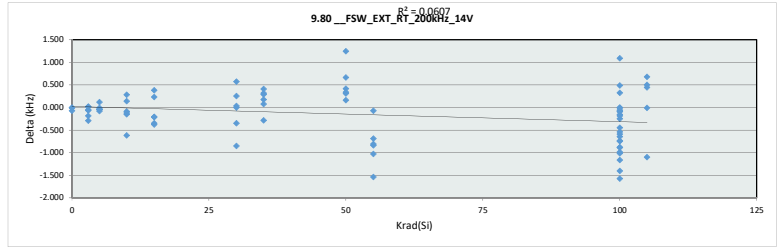
9.56_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	211.568	209.358	207.441	209.458	207.251	208.788	207.734	210.384	206.579	208.305	208.216
Average	211.619	211.962	210.263	211.981	210.185	212.024	210.407	212.030	209.562	210.559	209.781
Max	213.133	214.067	213.699	214.263	214.084	214.164	213.896	214.334	213.748	214.675	211.222
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000



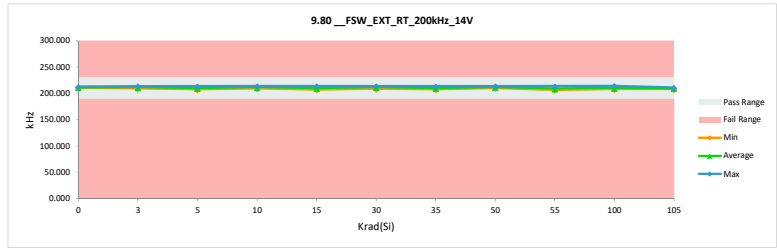
LDR TID Report  
TPS7H5001-SP

9.80_FSW_EXT_RT_200kHz_14V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	230
Min Limit	190

Krad(Si)	Serial #	PRE	POST	Delta
0	117	213.132	213.134	0.002
0	118	211.047	211.059	0.012
0	122	210.668	210.607	-0.061
3	61	212.505	212.333	-0.172
3	62	214.048	214.071	0.023
3	63	209.588	209.307	-0.281
3	64	211.074	211.021	-0.053
3	65	212.690	212.649	-0.041
3	66	212.308	212.339	0.031
5	67	210.179	210.311	0.132
5	68	209.794	209.765	-0.029
5	69	209.651	209.628	-0.023
5	70	213.681	213.614	-0.067
5	71	210.755	210.721	-0.034
5	72	207.408	207.412	0.004
10	73	212.505	212.434	-0.071
10	74	214.048	214.202	0.154
10	75	209.588	209.449	-0.139
10	76	211.074	210.472	-0.602
10	77	212.690	212.986	0.296
10	78	212.308	212.196	-0.112
15	79	210.179	210.424	0.245
15	80	209.794	209.460	-0.334
15	81	209.651	209.448	-0.203
15	82	213.681	214.078	0.397
15	83	210.755	210.390	-0.365
15	84	207.408	207.217	-0.191
30	86	212.505	212.768	0.263
30	89	214.048	214.098	0.050
30	91	209.588	208.751	-0.837
30	92	211.074	210.737	-0.337
30	94	212.690	213.274	0.584
30	95	212.308	212.321	0.013
35	100	210.179	210.478	0.299
35	101	209.794	209.524	-0.270
35	102	209.651	210.072	0.421
35	103	213.681	213.770	0.089
35	104	210.755	210.945	0.190
35	105	207.408	207.735	0.327
50	106	211.825	212.252	0.427
50	107	210.826	211.175	0.349
50	108	214.049	214.371	0.322
50	109	212.311	212.987	0.676
50	110	210.836	211.008	0.172
50	111	209.136	210.396	1.260
55	113	209.001	207.477	-1.524
55	114	209.122	208.104	-1.018
55	115	210.026	209.230	-0.796
55	116	207.314	206.637	-0.677
55	126	213.766	213.706	-0.060
55	128	212.869	212.046	-0.823
100	129	209.320	209.660	0.340
100	131	212.448	211.818	-0.630
100	132	213.388	212.414	-0.974
100	133	209.508	209.425	-0.083
100	134	210.663	210.488	-0.175
100	135	210.722	210.145	-0.577
100	138	209.224	210.323	1.099
100	139	210.688	209.541	-1.147
100	140	210.696	210.456	-0.240
100	141	212.900	211.511	-1.389
100	142	210.133	210.632	0.499
100	143	209.236	208.237	-0.999
100	144	209.477	209.041	-0.436
100	220	211.880	210.318	-1.562
100	221	211.750	211.758	0.008
100	224	210.073	209.343	-0.730
100	225	213.306	212.442	-0.864
100	226	214.730	214.660	-0.070
100	228	209.436	209.437	0.001
100	232	210.120	209.978	-0.142
100	233	209.299	209.250	-0.049
100	236	210.416	209.892	-0.524
100	238	212.556	211.684	-0.872
100	243	210.897	210.174	-0.723
105	244	209.386	208.302	-1.084
105	246	209.997	210.001	0.004
105	248	210.497	211.189	0.692
105	374	207.640	208.156	0.516
105	376	210.678	211.137	0.459
105	Max	214.730	214.660	1.260
105	Average	210.975	210.825	-0.150
105	Min	207.314	206.637	-1.562
105	Std Dev	1.784	1.871	0.550



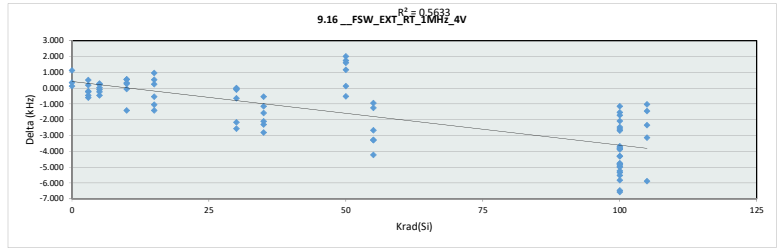
9.80_FSW_EXT		0	5	10	15	30	35	50	55	100	105
LL		190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min		210.607	209.307	207.412	209.449	207.217	208.751	207.735	210.396	206.637	208.237
Average		211.600	211.953	210.242	211.957	210.170	211.992	210.421	212.032	209.533	210.526
Max		213.134	214.071	213.614	214.202	214.078	214.098	213.770	214.371	213.706	214.660
UL		230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000



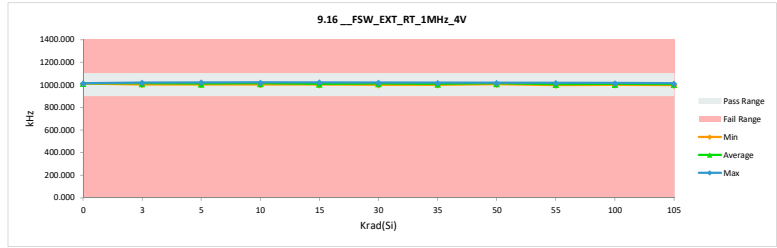
LDR TID Report  
TPS7H5001-SP

9.16_FSW_EXT_RT_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz kHz
Max Limit	1100 1100
Min Limit	900 900

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1016.625	1017.001	0.376
0	118	1013.508	1013.677	0.169
0	122	1014.885	1016.047	1.162
3	61	1014.566	1014.006	-0.560
3	62	1021.207	1021.760	0.553
3	63	1005.657	1005.223	-0.434
3	64	1016.414	1016.219	-0.195
3	65	1014.372	1014.213	-0.159
3	66	1016.652	1016.883	0.231
5	67	1010.625	1010.194	-0.431
5	68	1013.291	1013.554	0.263
5	69	1005.295	1005.100	-0.195
5	70	1023.880	1024.197	0.317
5	71	1010.413	1010.344	-0.069
5	72	1007.923	1007.986	0.063
10	73	1014.566	1014.933	0.367
10	74	1021.207	1021.790	0.583
10	75	1005.657	1005.962	0.305
10	76	1016.414	1015.032	-1.382
10	77	1014.372	1014.959	0.587
10	78	1016.652	1016.634	-0.018
15	79	1010.625	1011.188	0.563
15	80	1013.291	1012.789	-0.502
15	81	1005.295	1003.927	-1.368
15	82	1023.880	1024.882	1.002
15	83	1010.413	1009.396	-1.017
15	84	1007.923	1008.198	0.275
30	86	1014.566	1014.497	-0.069
30	89	1021.207	1021.203	-0.004
30	91	1005.657	1003.123	-2.534
30	92	1016.414	1014.298	-2.116
30	94	1014.372	1014.408	0.036
30	95	1016.652	1016.052	-0.600
35	100	1010.625	1010.111	-0.514
35	101	1013.291	1011.221	-2.070
35	102	1005.295	1003.035	-2.260
35	103	1023.880	1021.109	-2.771
35	104	1010.413	1008.871	-1.542
35	105	1007.923	1006.807	-1.116
50	106	1015.360	1016.560	1.200
50	107	1013.268	1013.429	0.161
50	108	1017.042	1018.829	1.787
50	109	1018.953	1020.584	1.631
50	110	1014.793	1014.317	-0.476
50	111	1005.771	1007.808	2.037
55	113	1007.197	1002.999	-4.198
55	114	1002.511	999.264	-3.247
55	115	1009.986	1007.351	-2.635
55	116	1000.700	999.798	-0.902
55	126	1019.883	1018.665	-1.218
55	128	1017.985	1014.734	-3.251
100	129	1010.195	1006.548	-3.647
100	131	1018.490	1013.585	-4.905
100	132	1022.562	1017.759	-4.803
100	133	1012.650	1008.866	-3.784
100	134	1016.399	1012.134	-4.265
100	135	1011.502	1006.778	-4.724
100	138	1011.230	1009.551	-1.679
100	139	1015.098	1009.319	-5.779
100	140	1014.474	1010.639	-3.835
100	141	1013.734	1007.206	-6.528
100	142	1015.802	1014.301	-1.501
100	143	1007.293	1001.808	-5.485
100	144	1015.989	1010.685	-5.304
100	220	1014.570	1008.139	-6.431
100	221	1013.342	1011.303	-2.039
100	224	1011.951	1007.201	-4.750
100	225	1018.230	1013.269	-4.961
100	226	1015.057	1012.508	-2.549
100	228	1010.427	1009.322	-1.105
100	232	1011.534	1009.099	-2.435
100	233	1013.168	1010.525	-2.643
100	236	1005.189	1001.439	-3.750
100	238	1016.712	1011.511	-5.201
100	243	1007.856	1003.596	-4.260
105	244	1007.450	1001.602	-5.848
105	246	1012.746	1011.321	-1.425
105	248	1016.178	1015.196	-0.982
105	374	1002.257	999.959	-2.298
105	376	1016.223	1013.118	-3.105
Max		1023.880	1024.882	2.037
Average		1013.096	1011.418	-1.678
Min		1000.700	999.264	-6.528
Std Dev		5.180	5.796	2.167



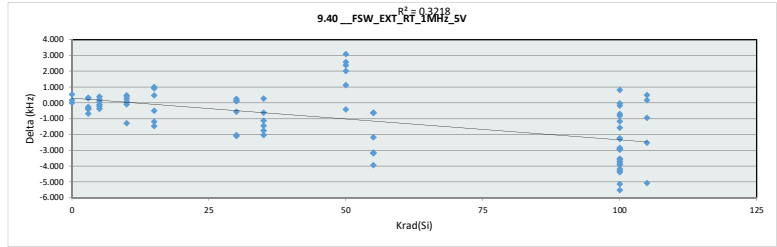
9.16_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	1100 kHz										
Min Limit	900 kHz										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1013.677	1005.223	1005.100	1005.962	1003.927	1003.123	1003.035	1007.808	999.264	1001.439	999.959
Average	1015.575	1014.717	1011.896	1014.885	1011.730	1013.930	1010.192	1015.255	1007.135	1009.462	1008.239
Max	1017.001	1021.760	1024.197	1021.790	1024.882	1021.203	1021.109	1020.584	1018.665	1017.759	1015.196
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000



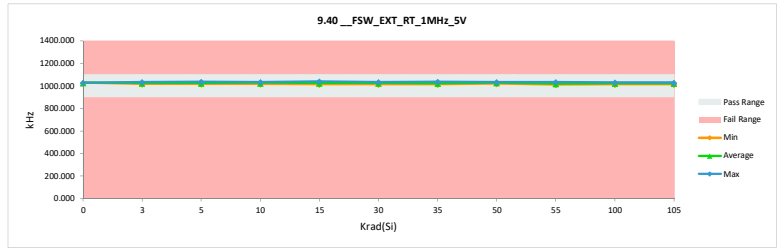
LDR TID Report  
TPS7H5001-SP

9.40_FSW_EXT_RT_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	kHz kHz
Max Limit	1100 1100
Min Limit	900 900

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1029.916	1030.121	0.205
0	118	1028.590	1028.630	0.040
0	122	1030.407	1030.987	0.580
3	61	1029.874	1029.227	-0.647
3	62	1036.045	1036.408	0.363
3	63	1018.789	1018.433	-0.356
3	64	1030.200	1029.993	-0.207
3	65	1028.048	1027.739	-0.309
3	66	1030.224	1030.579	0.355
5	67	1025.484	1025.145	-0.339
5	68	1027.840	1028.276	0.436
5	69	1018.230	1018.157	-0.073
5	70	1039.545	1039.790	0.245
5	71	1025.306	1025.141	-0.165
5	72	1022.378	1022.464	0.086
10	73	1029.874	1030.005	0.131
10	74	1036.045	1036.367	0.322
10	75	1018.789	1019.254	0.465
10	76	1030.200	1028.959	-1.241
10	77	1028.048	1028.557	0.509
10	78	1030.224	1030.177	-0.047
15	79	1025.484	1026.447	0.963
15	80	1027.840	1027.388	-0.452
15	81	1018.230	1016.804	-1.426
15	82	1039.545	1040.592	1.047
15	83	1025.306	1024.161	-1.145
15	84	1022.378	1022.884	0.506
30	86	1029.874	1030.044	0.170
30	89	1036.045	1036.335	0.290
30	91	1018.789	1016.729	-2.060
30	92	1030.200	1028.210	-1.990
30	94	1028.048	1028.205	0.157
30	95	1030.224	1029.715	-0.509
35	100	1025.484	1025.800	0.316
35	101	1027.840	1026.429	-1.411
35	102	1018.230	1016.513	-1.717
35	103	1039.545	1037.550	-1.995
35	104	1025.306	1024.215	-1.091
35	105	1022.378	1021.798	-0.580
50	106	1030.758	1032.802	2.044
50	107	1028.423	1029.585	1.162
50	108	1028.567	1032.691	3.124
50	109	1034.527	1036.949	2.422
50	110	1030.313	1029.933	-0.380
50	111	1019.126	1021.745	2.619
55	113	1021.368	1017.473	-3.895
55	114	1016.476	1013.368	-3.108
55	115	1024.572	1022.441	-2.131
55	116	1016.054	1015.451	-0.603
55	126	1035.414	1034.827	-0.587
55	128	1032.844	1029.697	-3.147
100	129	1025.279	1023.077	-2.202
100	131	1032.519	1028.261	-4.258
100	132	1037.206	1033.543	-3.663
100	133	1027.861	1025.065	-2.796
100	134	1031.544	1028.629	-2.915
100	135	1023.547	1019.701	-3.846
100	138	1025.798	1025.674	-0.124
100	139	1028.561	1024.225	-4.336
100	140	1028.938	1026.076	-2.862
100	141	1027.002	1021.904	-5.098
100	142	1030.534	1030.542	0.008
100	143	1021.890	1017.752	-4.138
100	144	1030.712	1027.019	-3.693
100	220	1028.749	1023.284	-5.465
100	221	1027.657	1027.005	-0.652
100	224	1026.219	1022.732	-3.487
100	225	1031.446	1027.924	-3.522
100	226	1029.761	1028.639	-1.122
100	228	1024.312	1025.174	0.862
100	232	1026.460	1025.677	-0.783
100	233	1026.759	1025.224	-1.535
100	236	1018.095	1015.850	-2.245
100	238	1031.033	1027.154	-3.879
100	243	1021.927	1019.030	-2.897
105	244	1022.117	1017.082	-5.035
105	246	1026.084	1026.298	0.214
105	248	1029.882	1030.424	0.542
105	374	1016.573	1015.670	-0.903
105	376	1029.804	1027.317	-2.487
105	Max	1039.545	1040.592	3.124
105	Average	1027.406	1026.339	-1.067
105	Min	1016.054	1013.368	-5.465
105	Std Dev	5.510	5.998	1.876



9.40_FSW_EXT											
Test Site											
Tester											
Test Number											
Unit	kHz kHz										
Max Limit	1100 kHz										
Min Limit	900 kHz										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1028.630	1018.433	1018.157	1019.254	1016.804	1016.729	1016.513	1021.745	1013.368	1015.850	1015.670
Average	1029.913	1028.730	1026.496	1028.887	1026.379	1028.206	1025.384	1030.618	1022.210	1024.965	1023.358
Max	1030.987	1036.408	1039.790	1036.367	1040.592	1036.335	1037.550	1036.949	1034.827	1033.543	1030.424
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

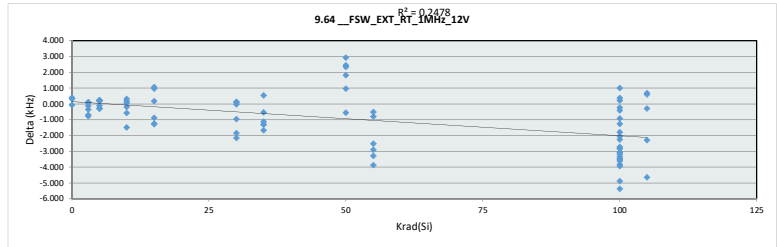




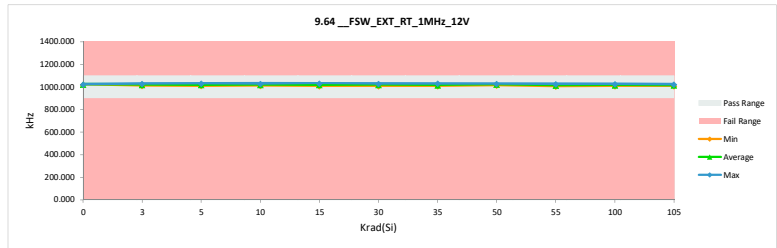
LDR TID Report  
TPS7H5001-SP

		9.64_FSW_EXT_RT_1MHz_12V	
Test Site			
Tester			
Test Number			
Unit	kHz	kHz	
Max Limit	1100	1100	
Min Limit	900	900	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1024.335	1024.688	0.353
0	118	1023.558	1023.517	-0.041
0	122	1025.143	1025.587	0.444
3	61	1024.929	1024.194	-0.735
3	62	1031.106	1031.201	0.095
3	63	1013.840	1013.187	-0.653
3	64	1024.646	1024.558	-0.088
3	65	1022.912	1022.607	-0.305
3	66	1025.168	1025.310	0.142
5	67	1020.286	1020.206	-0.080
5	68	1022.943	1023.222	0.279
5	69	1012.705	1012.985	0.280
5	70	1034.288	1034.499	0.211
5	71	1020.404	1020.135	-0.269
5	72	1017.515	1017.279	-0.236
10	73	1024.929	1024.767	-0.162
10	74	1031.106	1031.196	0.090
10	75	1013.840	1014.048	0.208
10	76	1024.646	1023.203	-1.443
10	77	1022.912	1023.261	0.349
10	78	1025.168	1024.637	-0.531
15	79	1020.286	1021.392	1.106
15	80	1022.943	1022.108	-0.835
15	81	1012.705	1011.493	-1.212
15	82	1034.288	1035.285	0.997
15	83	1020.404	1019.154	-1.250
15	84	1017.515	1017.717	0.202
30	86	1024.929	1024.947	0.018
30	89	1031.106	1031.236	0.130
30	91	1013.840	1011.719	-2.121
30	92	1024.646	1022.841	-1.805
30	94	1022.912	1023.087	0.175
30	95	1025.168	1024.250	-0.918
35	100	1020.286	1020.852	0.566
35	101	1022.943	1021.668	-1.275
35	102	1012.705	1011.447	-1.258
35	103	1034.288	1032.661	-1.627
35	104	1020.404	1019.322	-1.082
35	105	1017.515	1017.017	-0.498
50	106	1025.569	1027.413	1.844
50	107	1022.929	1023.939	1.010
50	108	1024.224	1027.180	2.956
50	109	1028.888	1031.358	2.470
50	110	1025.526	1025.001	-0.525
50	111	1013.970	1016.355	2.385
55	113	1016.250	1012.416	-3.834
55	114	1011.376	1008.131	-3.245
55	115	1019.639	1017.160	-2.479
55	116	1011.238	1010.470	-0.768
55	126	1030.121	1029.650	-0.471
55	128	1027.253	1024.388	-2.865
100	129	1020.295	1018.541	-1.754
100	131	1026.917	1023.131	-3.786
100	132	1031.566	1028.528	-3.038
100	133	1022.548	1020.338	-2.210
100	134	1026.367	1023.587	-2.780
100	135	1018.068	1014.571	-3.497
100	138	1020.583	1020.827	0.244
100	139	1023.121	1019.229	-3.892
100	140	1023.679	1020.902	-2.777
100	141	1021.707	1016.867	-4.840
100	142	1025.232	1025.636	0.404
100	143	1016.769	1013.196	-3.573
100	144	1025.703	1022.269	-3.434
100	220	1023.530	1018.201	-5.329
100	221	1022.242	1021.868	-0.374
100	224	1020.964	1017.954	-3.010
100	225	1026.029	1022.712	-3.317
100	226	1024.782	1023.904	-0.878
100	228	1019.163	1020.211	1.048
100	232	1020.997	1020.806	-0.191
100	233	1021.469	1020.247	-1.222
100	236	1013.068	1011.059	-2.009
100	238	1025.352	1022.213	-3.139
100	243	1017.043	1014.355	-2.688
105	244	1017.237	1012.622	-4.615
105	246	1020.770	1021.416	0.646
105	248	1024.936	1025.645	0.709
105	374	1011.764	1011.506	-0.258
105	376	1024.516	1022.258	-2.258
105	375	1034.288	1035.285	0.996
Max		1022.233	1021.257	-0.976
Average		1011.238	1008.131	-5.329
Min		1011.238	1008.131	-5.329
Std Dev		5.447	5.895	1.739



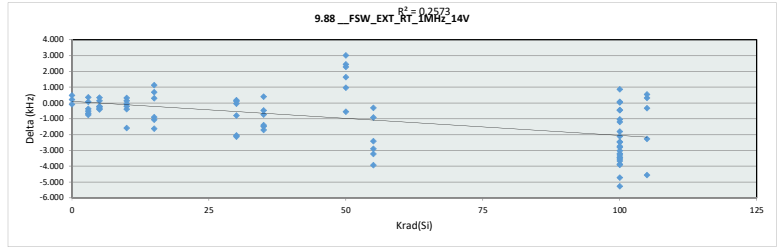
		9.64_FSW_EXT												
Test Site														
Tester														
Test Number														
Max Limit	1100	kHz												
Min Limit	900	kHz												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1023.517	1013.187	1012.985	1014.048	1011.493	1011.719	1011.447	1016.355	1008.131	1011.059	1011.506	1024.597	1023.510	1021.388
Average	1024.597	1023.510	1021.388	1023.519	1021.192	1023.013	1020.495	1025.208	1017.036	1020.048	1018.689	1025.587	1031.201	1034.499
Max	1025.587	1031.201	1034.499	1031.196	1035.285	1031.236	1032.661	1031.358	1029.650	1028.528	1025.645	1100.000	1100.000	1100.000
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000



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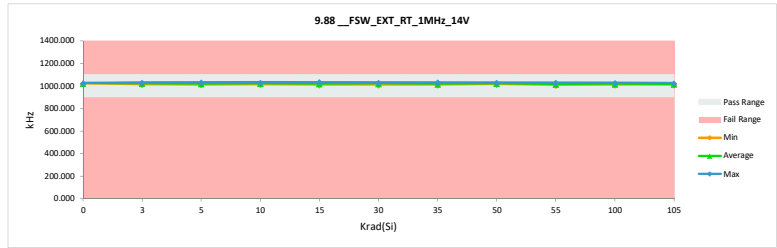
9.88_FSW_EXT_RT_1MHz_14V		
Test Site		
Tester		
Test Number		
Unit	kHz	kHz
Max Limit	1100	1100
Min Limit	900	900

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1024.104	1024.354	0.250
0	118	1023.471	1023.429	-0.042
0	122	1025.067	1025.586	0.519
3	61	1024.771	1024.049	-0.722
3	62	1031.050	1031.164	0.114
3	63	1013.696	1013.085	-0.611
3	64	1024.752	1024.422	-0.330
3	65	1022.833	1022.363	-0.470
3	66	1024.908	1025.297	0.389
5	67	1020.381	1020.002	-0.379
5	68	1022.923	1023.096	0.173
5	69	1013.022	1012.725	-0.297
5	70	1033.903	1034.276	0.373
5	71	1020.063	1019.890	-0.173
5	72	1017.332	1017.117	-0.215
10	73	1024.771	1024.591	-0.180
10	74	1031.050	1031.032	-0.018
10	75	1013.696	1013.858	0.162
10	76	1024.752	1023.209	-1.543
10	77	1022.833	1023.181	0.348
10	78	1024.908	1024.544	-0.364
15	79	1020.381	1021.090	0.709
15	80	1022.923	1022.065	-0.858
15	81	1013.022	1011.425	-1.597
15	82	1033.903	1035.074	1.171
15	83	1020.063	1019.049	-1.014
15	84	1017.332	1017.674	0.342
30	86	1024.771	1024.916	0.145
30	89	1031.050	1031.041	-0.009
30	91	1013.696	1011.597	-2.099
30	92	1024.752	1022.736	-2.016
30	94	1022.833	1023.040	0.207
30	95	1024.908	1024.158	-0.750
35	100	1020.381	1020.807	0.426
35	101	1022.923	1021.485	-1.438
35	102	1013.022	1011.352	-1.670
35	103	1033.903	1032.531	-1.372
35	104	1020.063	1019.367	-0.696
35	105	1017.332	1016.890	-0.442
50	106	1025.488	1027.166	1.678
50	107	1023.029	1024.034	1.005
50	108	1024.036	1027.076	3.040
50	109	1028.759	1031.230	2.471
50	110	1025.228	1024.710	-0.518
50	111	1013.966	1016.292	2.326
55	113	1016.182	1012.290	-3.892
55	114	1011.301	1008.118	-3.183
55	115	1019.525	1017.140	-2.385
55	116	1011.108	1010.226	-0.882
55	126	1029.869	1029.597	-0.272
55	128	1027.167	1024.299	-2.868
100	129	1020.155	1018.383	-1.772
100	131	1026.874	1023.006	-3.868
100	132	1031.613	1028.450	-3.163
100	133	1022.526	1020.095	-2.431
100	134	1026.188	1023.490	-2.698
100	135	1017.974	1014.342	-3.632
100	138	1020.473	1020.537	0.064
100	139	1023.022	1019.178	-3.844
100	140	1023.600	1020.842	-2.758
100	141	1021.522	1016.841	-4.681
100	142	1025.215	1025.331	0.116
100	143	1016.637	1013.129	-3.508
100	144	1025.610	1022.180	-3.430
100	220	1023.406	1018.168	-5.238
100	221	1022.230	1021.823	-0.407
100	224	1020.772	1017.786	-2.986
100	225	1025.859	1022.604	-3.255
100	226	1024.774	1023.767	-1.007
100	228	1019.152	1020.061	0.909
100	232	1020.994	1020.583	-0.411
100	233	1021.294	1020.130	-1.164
100	236	1012.926	1010.841	-2.085
100	238	1025.336	1021.940	-3.396
100	243	1016.798	1014.376	-2.422
105	244	1017.013	1012.488	-4.525
105	246	1020.772	1021.134	0.362
105	248	1024.827	1025.404	0.577
105	374	1011.664	1011.362	-0.302
105	376	1024.416	1022.174	-2.242
105	Max	1033.903	1035.074	3.040
105	Average	1022.136	1021.127	-1.008
105	Min	1011.108	1008.118	-5.238
105	Std Dev	5.406	5.888	1.716



9.88_FSW_EXT		
Test Site		
Tester		
Test Number		
Unit	kHz	kHz
Max Limit	1100	1100
Min Limit	900	900

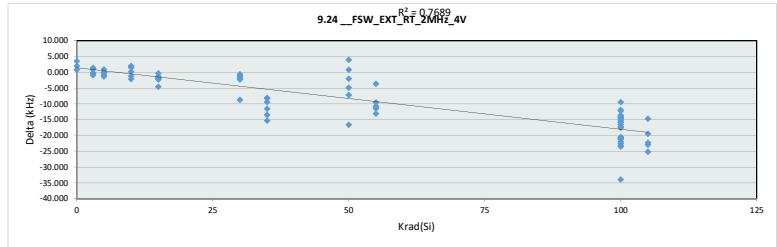
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1023.429	1013.085	1012.725	1013.858	1011.425	1011.597	1011.352	1016.292	1008.118	1010.841	1011.362
Average	1024.456	1023.397	1021.184	1023.403	1021.063	1022.915	1020.405	1025.085	1016.945	1019.912	1018.512
Max	1025.586	1031.164	1034.276	1031.032	1035.074	1031.041	1032.531	1031.230	1029.597	1028.450	1025.404
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000



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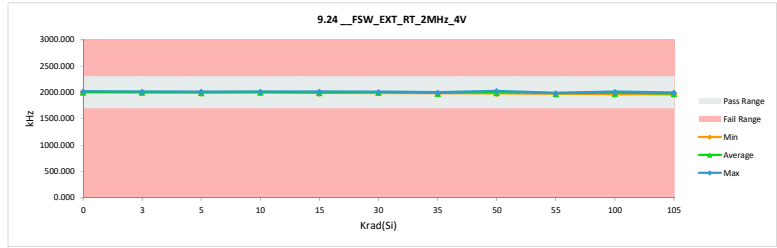
9.24_FSW_EXT_RT_2MHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	2300
Min Limit	1700

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2023.508	2025.746	2.238
0	118	2001.757	2002.727	0.970
0	122	2005.692	2009.369	3.677
3	61	1997.022	1996.747	-0.275
3	62	2013.080	2014.639	1.559
3	63	2010.961	2010.901	-0.060
3	64	2016.920	2018.351	1.431
3	65	2019.328	2020.417	1.089
3	66	2016.633	2015.897	-0.736
5	67	1995.582	1995.654	0.072
5	68	2004.039	2002.902	-1.137
5	69	2012.047	2011.293	-0.754
5	70	2019.587	2019.647	0.060
5	71	2003.940	2004.122	0.182
5	72	1991.611	1992.695	1.084
10	73	1997.022	1998.701	1.679
10	74	2013.080	2014.936	1.856
10	75	2010.961	2009.877	-1.084
10	76	2016.920	2014.992	-1.928
10	77	2019.328	2019.735	0.407
10	78	2016.633	2018.816	2.183
15	79	1995.582	1993.568	-2.014
15	80	2004.039	2003.892	-0.147
15	81	2012.047	2007.637	-4.410
15	82	2019.587	2018.466	-1.121
15	83	2003.940	2002.380	-1.560
15	84	1991.611	1989.642	-1.969
30	86	1997.022	1995.971	-1.051
30	89	2013.080	2011.052	-2.028
30	91	2010.961	2009.385	-1.576
30	92	2016.920	2014.966	-1.954
30	94	2019.328	2018.092	-1.236
30	95	2016.633	2016.217	-0.416
35	100	1995.582	1986.376	-9.206
35	101	2004.039	1992.634	-11.405
35	102	2012.047	1998.761	-13.286
35	103	2019.587	2004.438	-15.149
35	104	2003.940	1995.936	-8.004
35	105	1991.611	1983.649	-7.962
50	106	2002.471	2000.658	-1.813
50	107	1993.519	1986.541	-6.978
50	108	2048.079	2031.658	-16.421
50	109	2004.684	1999.961	-4.723
50	110	1996.240	1997.218	0.978
50	111	2012.861	2016.961	4.100
55	113	2005.858	1992.934	-12.924
55	114	1996.174	1986.794	-9.380
55	115	1992.387	1981.278	-11.109
55	116	1977.691	1974.263	-3.428
55	126	2002.051	1991.503	-10.548
55	128	2005.270	1993.957	-11.313
100	129	1992.369	1978.430	-13.939
100	131	2019.744	2007.774	-11.970
100	132	2031.054	2009.250	-21.804
100	133	1995.935	1981.008	-14.927
100	134	2008.763	1995.391	-13.372
100	135	2042.157	2008.398	-33.759
100	138	1993.894	1976.597	-17.297
100	139	2032.074	2008.725	-23.349
100	140	2013.620	2004.424	-9.196
100	141	2009.016	1988.297	-20.719
100	142	2005.849	1990.283	-15.566
100	143	1993.605	1973.296	-20.309
100	144	2012.828	1991.854	-20.974
100	220	2011.347	1994.343	-17.004
100	221	2005.736	1991.468	-14.268
100	224	2010.572	1994.287	-16.285
100	225	2037.975	2017.328	-20.647
100	226	2001.795	1987.793	-14.002
100	228	2000.855	1986.439	-14.416
100	232	2009.142	1993.478	-15.664
100	233	2019.858	2008.059	-11.799
100	236	2017.787	1995.362	-22.425
100	238	2022.068	1999.018	-23.050
100	243	1993.908	1973.359	-20.549
105	244	1992.496	1969.833	-22.663
105	246	2013.902	1991.842	-22.060
105	248	2018.358	1999.112	-19.246
105	374	1991.605	1966.720	-24.885
105	376	2015.153	2000.609	-14.544
105	Max	2048.079	2031.658	4.100
105	Average	2008.549	1999.884	-8.665
105	Min	1977.691	1966.720	-33.759
105	Std Dev	12.518	13.926	8.952



9.24_FSW_EXT	
Test Site	
Tester	
Test Number	
Max Limit	2300 kHz
Min Limit	1700 kHz

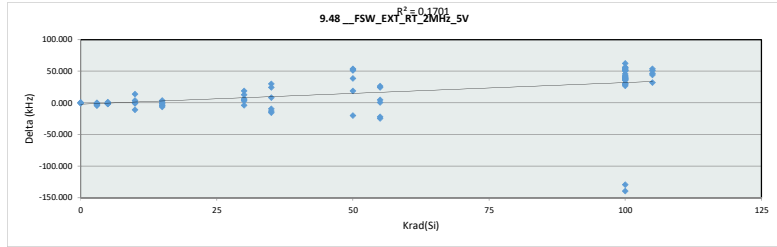
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2002.727	1996.747	1992.695	1998.701	1989.642	1995.971	1983.649	1986.541	1974.263	1973.296
Average	2012.614	2012.825	2004.386	2012.843	2002.598	2009.781	1993.632	2005.500	1986.788	1993.944
Max	2025.746	2020.417	2019.647	2019.735	2018.466	2018.092	2004.438	2031.658	1993.957	2017.328
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



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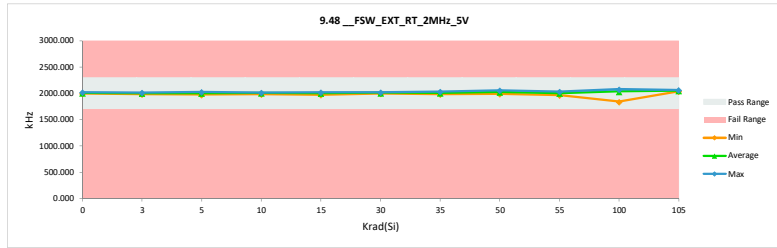
9.48_FSW_EXT_RT_2MHz_5V	
Test Site	
Tester	
Test Number	
Unit	kHz kHz
Max Limit	2300 2300
Min Limit	1700 1700

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2001.204	2002.346	1.142
0	118	2007.489	2007.550	0.061
0	122	2020.877	2022.769	1.892
3	61	2010.382	2007.273	-3.109
3	62	2015.456	2012.991	-2.465
3	63	1988.663	1987.751	-0.912
3	64	2017.967	2018.290	0.323
3	65	2003.998	2002.594	-1.404
3	66	2012.483	2013.544	1.061
5	67	2006.492	2008.817	2.325
5	68	2007.270	2006.816	-0.454
5	69	1979.653	1979.388	-0.265
5	70	2030.783	2031.872	1.089
5	71	2009.942	2009.148	-0.794
5	72	2002.110	2002.872	0.762
10	73	2010.382	2000.543	-9.839
10	74	2015.456	2017.434	1.978
10	75	1988.663	1989.950	1.287
10	76	2017.967	2018.155	0.188
10	77	2003.998	2019.107	15.109
10	78	2012.483	2017.217	4.734
15	79	2006.492	2011.449	4.957
15	80	2007.270	2007.280	0.010
15	81	1979.653	1976.148	-3.505
15	82	2030.783	2025.672	-5.111
15	83	2009.942	2012.020	2.078
15	84	2002.110	2000.011	-2.099
30	86	2010.382	2007.488	-2.894
30	89	2015.456	2021.360	5.904
30	91	1988.663	2022.414	13.751
30	92	2017.967	2022.343	4.376
30	94	2003.998	2023.847	19.849
30	95	2012.483	2019.848	7.365
35	100	2006.492	2037.831	31.339
35	101	2007.270	1998.648	-8.622
35	102	1979.653	2005.197	25.544
35	103	2030.783	2018.958	-11.825
35	104	2009.942	2019.213	9.271
35	105	2002.110	1987.463	-14.647
50	106	2008.332	2060.823	52.491
50	107	1990.873	2010.659	19.786
50	108	2007.606	2052.268	54.662
50	109	2015.657	1996.780	-18.877
50	110	2005.850	2059.907	54.057
50	111	1981.006	2020.625	39.619
55	113	2009.724	2035.006	25.282
55	114	1998.642	2026.052	27.410
55	115	2001.947	2003.939	1.992
55	116	1989.818	1968.767	-21.051
55	126	2007.673	1984.087	-23.586
55	128	2008.646	2014.259	5.613
100	129	2008.291	2060.763	52.472
100	131	2026.050	2065.102	39.052
100	132	2040.527	2080.941	40.414
100	133	2008.754	2066.027	57.273
100	134	2022.495	2074.495	52.000
100	135	1982.894	1844.614	-138.280
100	138	2002.880	2059.730	56.850
100	139	2011.298	2047.868	36.570
100	140	2017.494	2061.103	43.609
100	141	2001.669	2033.442	31.773
100	142	2018.753	1890.593	-128.160
100	143	1996.672	2043.197	46.525
100	144	2015.989	2067.735	51.746
100	220	2023.464	2053.778	30.314
100	221	2010.524	2048.897	38.373
100	224	2007.539	2050.753	43.214
100	225	2007.875	2047.913	40.038
100	226	2004.278	2057.932	53.654
100	228	2008.405	2071.860	63.455
100	232	2018.803	2074.394	55.591
100	233	2021.978	2053.737	31.759
100	236	1978.872	2007.011	28.139
100	238	2024.573	2065.979	41.406
100	243	2001.873	2039.521	37.648
105	244	1993.588	2041.423	47.835
105	246	2009.189	2063.965	54.776
105	248	2020.812	2066.145	45.333
105	374	1992.688	2045.139	52.451
105	376	2016.309	2049.182	32.873
105	Max	2040.527	2080.941	63.455
105	Average	2007.443	2023.000	15.557
105	Min	1978.872	1844.614	-138.280
105	Std Dev	12.705	36.868	33.580



9.48_FSW_EXT	
Test Site	
Tester	
Test Number	
Max Limit	2300 kHz
Min Limit	1700 kHz

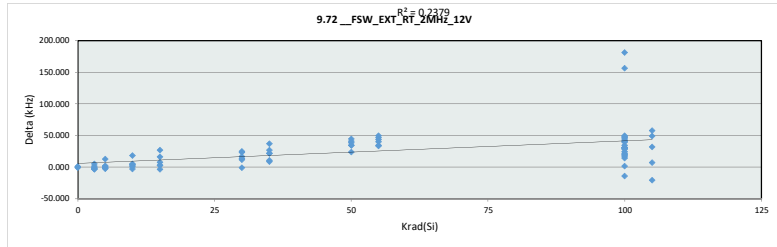
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2002.346	1987.751	1979.388	1989.950	1976.148	2002.414	1987.463	1996.780	1968.767	1844.614
Average	2010.888	2007.074	2006.486	2010.401	2005.430	2016.217	2011.218	2033.510	2005.352	2040.308
Max	2022.769	2018.290	2031.872	2019.107	2025.672	2023.847	2037.831	2060.823	2035.006	2080.941
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



LDR TID Report  
TPS7H5001-SP

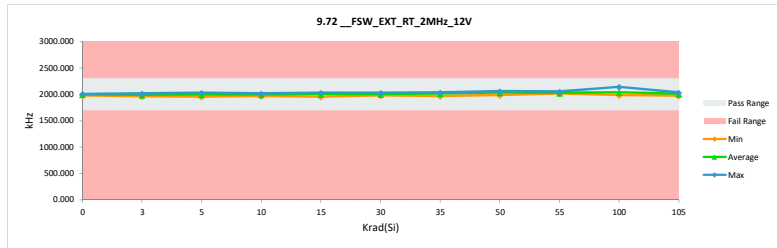
9.72_FSW_EXT_RT_2MHz_12V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	2300
Min Limit	1700

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1982.972	1983.936	0.964
0	118	2012.898	2013.201	0.303
0	122	2009.199	2011.075	1.876
3	61	2016.957	2017.047	0.090
3	62	2018.609	2020.975	2.366
3	63	1969.194	1968.078	-1.116
3	64	1993.494	1991.170	-2.324
3	65	1988.109	1986.109	-2.000
3	66	1984.093	1990.191	6.098
5	67	2016.338	2017.737	1.399
5	68	2005.131	2004.018	-1.113
5	69	1961.563	1962.149	0.586
5	70	2017.945	2031.567	13.622
5	71	1994.290	1992.924	-1.366
5	72	2007.119	2010.362	3.243
10	73	2016.957	2021.209	4.252
10	74	2018.609	2023.868	5.259
10	75	1969.194	1970.733	1.539
10	76	1993.494	2012.656	19.162
10	77	1988.109	1986.278	-1.831
10	78	1984.093	1990.308	6.215
15	79	2016.338	2025.148	8.810
15	80	2005.131	2009.045	3.914
15	81	1961.563	1958.882	-2.681
15	82	2017.945	2035.169	17.224
15	83	1994.290	2021.934	27.644
15	84	2007.119	2010.504	3.385
30	86	2016.957	2031.595	14.638
30	89	2018.609	2031.243	12.634
30	91	1969.194	1987.003	18.009
30	92	1993.494	2017.649	24.155
30	94	1988.109	1988.056	-0.053
30	95	1984.093	2009.972	25.879
35	100	2016.338	2038.931	22.593
35	101	2005.131	2014.623	9.492
35	102	1961.563	1973.280	11.717
35	103	2017.945	2040.540	22.595
35	104	1994.290	2032.140	37.850
35	105	2007.119	2034.998	27.879
50	106	2018.758	2054.307	35.549
50	107	2002.646	2041.934	39.288
50	108	1980.379	2023.524	42.145
50	109	2017.433	2062.966	45.533
50	110	2017.314	2052.605	35.291
50	111	1963.476	1988.531	25.055
55	113	1984.658	2032.242	47.584
55	114	1970.891	2021.534	50.643
55	115	2000.666	2035.190	34.524
55	116	1995.892	2036.713	40.821
55	126	2011.177	2055.867	44.690
55	128	2000.420	2035.838	35.418
100	129	2013.590	2045.331	31.741
100	131	2008.828	2040.530	31.702
100	132	2023.213	2070.709	47.496
100	133	2012.165	2047.537	35.372
100	134	2017.629	2058.166	40.537
100	135	1966.018	2123.195	157.177
100	138	1999.090	2044.697	45.607
100	139	1992.131	2022.298	30.167
100	140	2001.269	2043.002	41.733
100	141	1973.350	1993.066	19.716
100	142	2014.042	2063.237	49.195
100	143	2004.535	2021.567	17.032
100	144	2021.518	2042.424	20.906
100	220	2033.885	2020.960	-12.925
100	221	2013.798	2016.355	2.557
100	224	1991.006	2034.479	43.473
100	225	1990.429	2020.017	29.588
100	226	2008.332	2031.310	22.978
100	228	2018.925	2050.271	31.346
100	232	2031.257	2056.421	25.164
100	233	1990.509	2021.887	31.378
100	236	1964.552	2146.612	182.060
100	238	1997.122	2047.768	50.646
100	243	1996.059	2011.087	15.028
105	244	2000.347	2008.708	8.361
105	246	1983.611	2042.111	58.500
105	248	1994.498	2027.238	32.740
105	374	1977.431	2027.464	50.033
105	376	2001.693	1981.810	-19.883
105	Max	2033.885	2146.612	182.060
105	Average	1999.102	2023.338	24.236
105	Min	1961.563	1958.882	-19.883
105	Std Dev	18.120	30.893	29.635



9.72_FSW_EXT	
Test Site	
Tester	
Test Number	
Max Limit	2300 kHz
Min Limit	1700 kHz

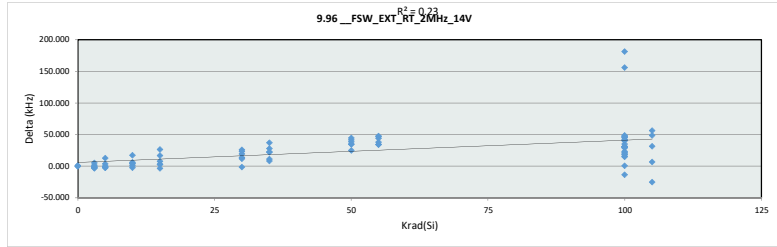
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	1983.936	1968.078	1962.149	1970.733	1958.882	1987.203	1973.280	1988.531	2021.534	1993.066	1981.810
Average	2002.737	1995.595	2003.126	2000.842	2010.114	2010.953	2022.419	2037.145	2036.231	2044.705	2017.466
Max	2013.201	2020.975	2031.567	2023.868	2035.169	2031.595	2040.540	2062.966	2055.867	2146.612	2042.111
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



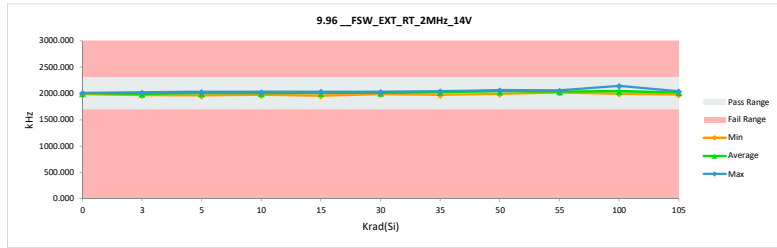
LDR TID Report  
TPS7H5001-SP

9.96_FSW_EXT_RT_2MHz_14V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	2300
Min Limit	1700

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1982.915	1983.758	0.843
0	118	2012.703	2013.065	0.362
0	122	2008.769	2010.867	2.098
3	61	2016.400	2016.911	0.511
3	62	2018.336	2020.942	2.606
3	63	1968.601	1967.894	-0.707
3	64	1993.417	1990.784	-2.633
3	65	1987.840	1986.178	-1.662
3	66	1983.859	1990.128	6.269
5	67	2016.495	2017.640	1.145
5	68	2004.941	2003.702	-1.239
5	69	1961.210	1961.465	0.255
5	70	2017.511	2031.112	13.601
5	71	1994.259	1992.658	-1.601
5	72	2005.896	2010.342	4.446
10	73	2016.400	2020.934	4.534
10	74	2018.336	2024.191	5.855
10	75	1968.601	1969.966	1.365
10	76	1993.417	2011.901	18.484
10	77	1987.840	1986.262	-1.578
10	78	1983.859	1990.940	7.081
15	79	2016.495	2025.165	8.670
15	80	2004.941	2008.771	3.830
15	81	1961.210	1958.626	-2.584
15	82	2017.511	2035.030	17.519
15	83	1994.259	2021.622	27.363
15	84	2005.896	2010.244	4.348
30	86	2016.400	2031.131	14.731
30	89	2018.336	2031.018	12.682
30	91	1968.601	1988.839	20.238
30	92	1993.417	2017.909	24.492
30	94	1987.840	1987.523	-0.317
30	95	1983.859	2010.561	26.702
35	100	2016.495	2039.613	23.118
35	101	2004.941	2014.262	9.321
35	102	1961.210	1973.660	12.450
35	103	2017.511	2040.823	23.312
35	104	1994.259	2032.102	37.843
35	105	2005.896	2034.843	28.947
50	106	2018.246	2053.996	35.750
50	107	2002.005	2041.689	39.684
50	108	1980.000	2022.495	42.495
50	109	2017.093	2062.471	45.378
50	110	2016.963	2052.620	35.657
50	111	1963.190	1988.857	25.667
55	113	1984.471	2032.220	47.749
55	114	1970.548	2019.123	48.575
55	115	2000.165	2034.944	34.779
55	116	1997.635	2036.696	39.061
55	126	2010.903	2055.873	44.970
55	128	2000.000	2035.353	35.353
100	129	2013.097	2044.789	31.692
100	131	2008.324	2039.623	31.299
100	132	2023.025	2070.243	47.218
100	133	2011.535	2047.279	35.744
100	134	2016.881	2058.132	41.251
100	135	1965.645	2122.393	156.748
100	138	1998.038	2044.642	46.604
100	139	1991.795	2022.204	30.409
100	140	2000.955	2042.900	41.945
100	141	1973.312	1992.532	19.220
100	142	2013.510	2063.287	49.777
100	143	2004.422	2020.986	16.564
100	144	2021.420	2041.882	20.462
100	220	2033.181	2020.658	-12.523
100	221	2014.090	2015.660	1.570
100	224	1990.482	2033.903	43.421
100	225	1990.083	2019.742	29.659
100	226	2009.138	2031.444	22.306
100	228	2018.378	2050.173	31.795
100	232	2031.208	2055.711	24.503
100	233	1990.180	2022.159	31.979
100	236	1964.300	2146.471	182.171
100	238	1996.626	2044.182	47.556
100	243	1996.188	2011.719	15.531
105	244	2000.241	2007.634	7.393
105	246	1983.747	2040.881	57.134
105	248	1994.573	2026.946	32.373
105	374	1977.505	2027.338	49.833
105	376	2004.640	1980.654	-23.986
105	Max	2033.181	2146.471	182.171
105	Average	1998.855	2023.074	24.218
105	Min	1961.210	1958.626	-23.986
105	Std Dev	18.107	30.817	29.615



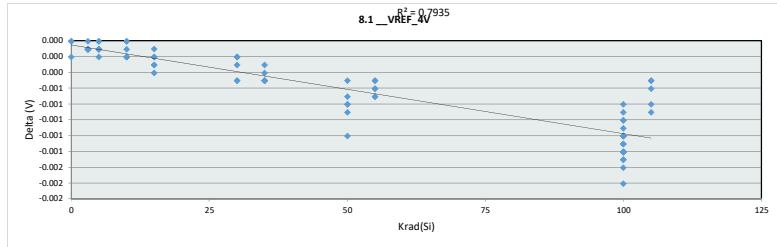
9.96_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	2300 kHz										
Min Limit	1700 kHz										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	1983.758	1967.894	1961.465	1969.966	1958.626	1987.523	1973.660	1988.857	2019.123	1992.532	1980.654
Average	2002.563	1995.473	2002.820	2000.699	2009.910	2011.164	2022.551	2037.021	2035.702	2044.280	2016.691
Max	2013.065	2020.942	2031.112	2024.191	2035.030	2031.131	2040.823	2062.471	2055.873	2146.471	2040.881
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



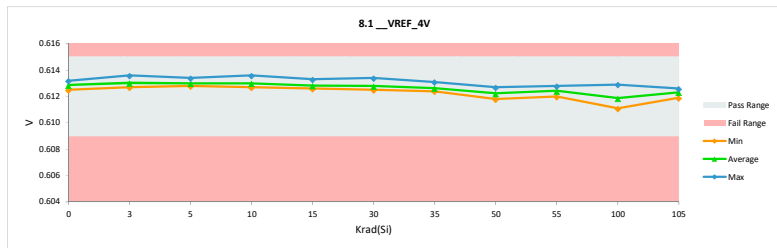
LDR TID Report  
TPS7H5001-SP

		8.1_VREF_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.615	0.615	
Min Limit	0.609	0.609	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	0.000
30	89	0.613	0.613	0.000
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	0.000
35	101	0.613	0.612	-0.001
35	102	0.613	0.612	-0.001
35	103	0.613	0.613	0.000
35	104	0.613	0.613	0.000
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	-0.001
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	-0.001
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.001
100	133	0.613	0.612	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.001
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.611	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.611	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.613	0.612	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	0.000
105	248	0.613	0.613	-0.001
105	374	0.613	0.612	-0.001
105	376	0.613	0.612	-0.001
	Max	0.614	0.614	0.000
	Average	0.613	0.612	-0.001
	Min	0.613	0.611	-0.002
	Std Dev	0.000	0.001	0.001



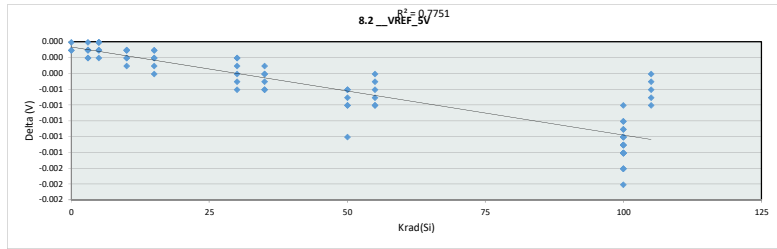
		8.1_VREF_4V	
Test Site			
Tester			
Test Number			
Max Limit	0.615	V	
Min Limit	0.609	V	
Krad(Si)	0	3	5
LL	0.609	0.609	0.609
Min	0.613	0.613	0.613
Average	0.613	0.613	0.613
Max	0.613	0.614	0.614
UL	0.615	0.615	0.615



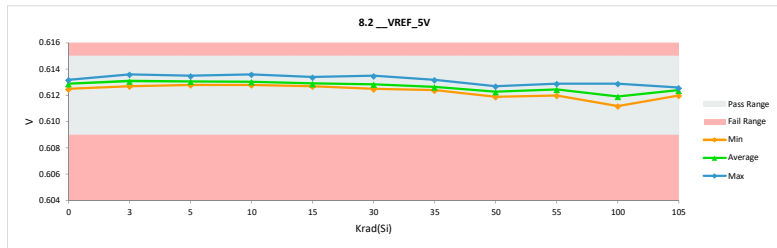
LDR TID Report  
TPS7H5001-SP

8.2_VREF_5V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	0.615	0.615
Min Limit	0.609	0.609

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	-0.001
30	89	0.613	0.613	0.000
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	-0.001
35	101	0.613	0.612	-0.001
35	102	0.613	0.613	0.000
35	103	0.613	0.613	0.000
35	104	0.613	0.613	0.000
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	-0.001
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	0.000
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.002
100	133	0.614	0.613	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.001
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.612	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.612	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.614	0.612	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	-0.001
105	248	0.613	0.613	0.000
105	374	0.613	0.613	0.000
105	375	0.613	0.612	-0.001
	Max	0.614	0.614	0.000
	Average	0.613	0.613	-0.001
	Min	0.613	0.611	-0.002
	Std Dev	0.000	0.001	0.001



8.2_VREF_5V											
Test Site											
Tester											
Test Number											
Max Limit	0.615	V									
Min Limit	0.609	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.611	0.612
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.614	0.614	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615

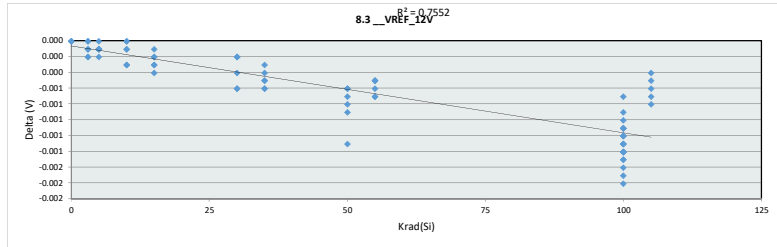




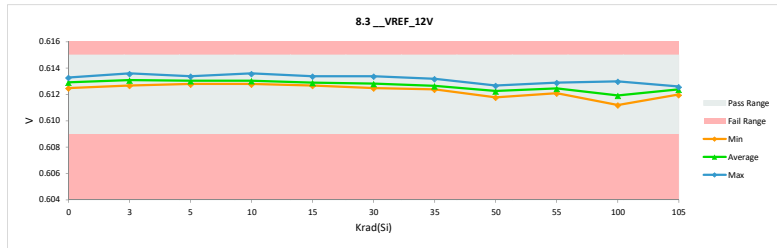
LDR TID Report  
TPS7H5001-SP

8.3_VREF_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.615
Min Limit	0.609

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	-0.001
30	89	0.613	0.613	-0.001
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	-0.001
35	101	0.613	0.612	-0.001
35	102	0.613	0.613	0.000
35	103	0.613	0.613	-0.001
35	104	0.613	0.613	-0.001
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	0.000
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	0.000
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.002
100	133	0.614	0.613	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.002
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.612	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.611	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.614	0.613	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	-0.001
105	248	0.613	0.613	0.000
105	374	0.613	0.613	0.000
105	375	0.613	0.612	-0.001
105	Max	0.614	0.614	0.000
105	Average	0.613	0.613	-0.001
105	Min	0.613	0.611	-0.002
105	Std Dev	0.000	0.001	0.001



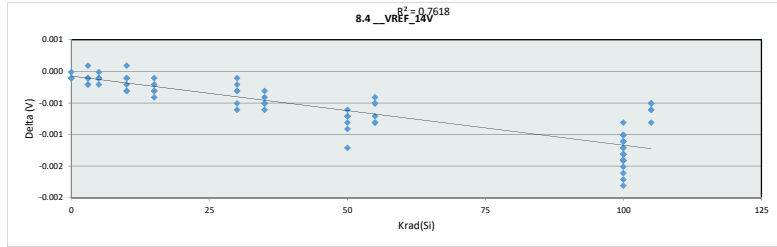
8.3_VREF_12V											
Test Site											
Tester											
Test Number											
Max Limit	0.615 V										
Min Limit	0.609 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.611	0.612
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613	0.613	0.613
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615



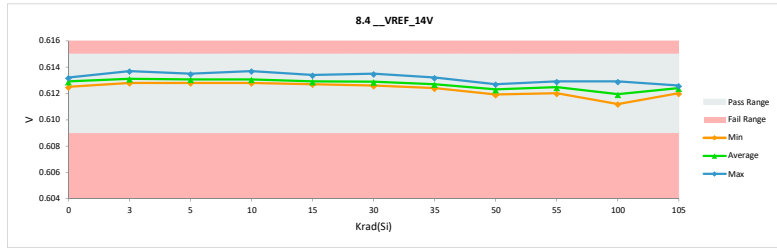
LDR TID Report  
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8.4_VREF_14V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	0.615	0.615
Min Limit	0.609	0.609

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	-0.001
30	89	0.613	0.613	-0.001
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	0.000
35	101	0.613	0.612	-0.001
35	102	0.613	0.613	0.000
35	103	0.613	0.613	-0.001
35	104	0.613	0.613	-0.001
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	0.000
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	0.000
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.002
100	133	0.614	0.613	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.001
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.612	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.611	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.614	0.613	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	-0.001
105	248	0.613	0.613	0.000
105	374	0.613	0.613	0.000
105	375	0.613	0.612	-0.001
	Max	0.614	0.614	0.000
	Average	0.613	0.613	-0.001
	Min	0.613	0.611	-0.002
	Std Dev	0.000	0.001	0.001



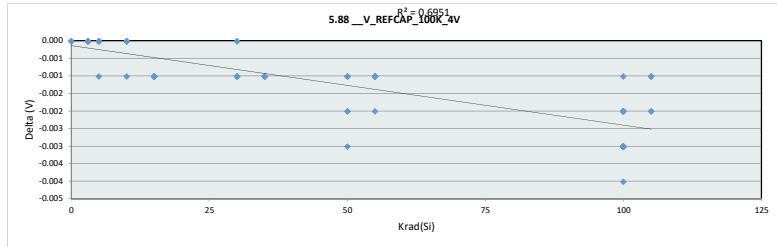
8.4_VREF_14V											
Test Site											
Tester											
Test Number											
Max Limit	0.615	V									
Min Limit	0.609	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.611	0.612
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.614	0.614	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615



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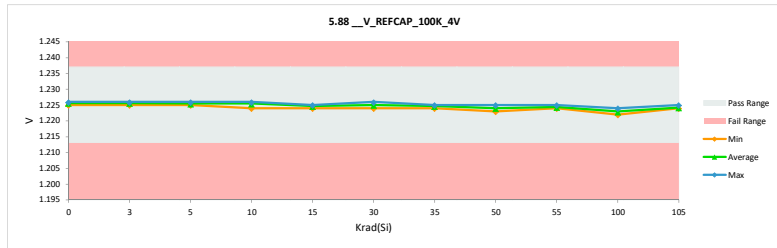
5.88_V_REFCAP_100K_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.225	-0.001
10	73	1.225	1.224	-0.001
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.224	-0.001
15	80	1.225	1.224	-0.001
15	81	1.226	1.225	-0.001
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.226	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.225	1.224	-0.001
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.225	1.223	-0.002
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.222	-0.003
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



5.88_V_REFCAP	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

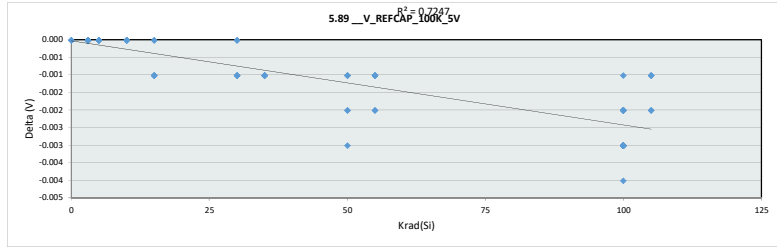
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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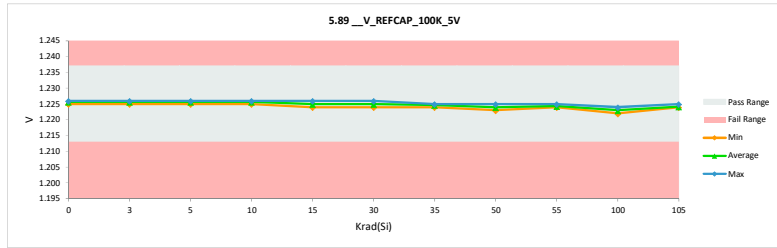
5.89_V_REFCAP_100K_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.223	-0.002
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



5.89_V_REFCAP	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

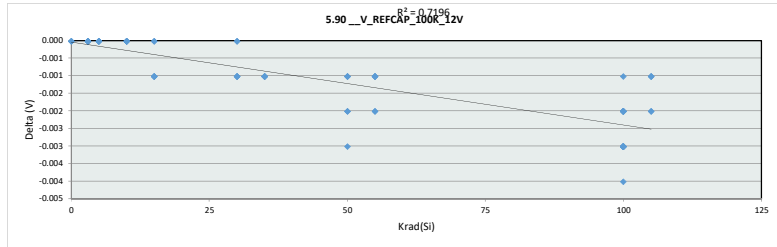
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



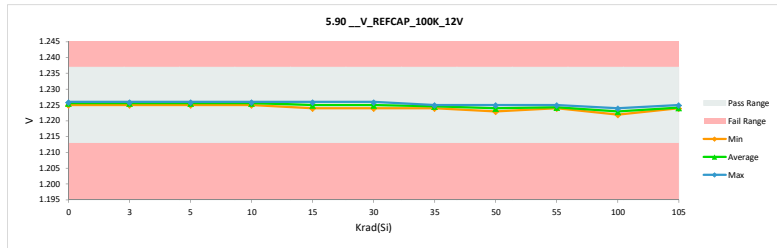
LDR TID Report  
TPS7H5001-SP

		5.90_V_REFCAP_100K_12V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.225	1.223	-0.002
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.223	-0.002
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	375	1.226	1.224	-0.002
	Max	1.226	1.226	0.000
	Average	1.226	1.224	-0.001
	Min	1.225	1.222	-0.004
	Std Dev	0.000	0.001	0.001

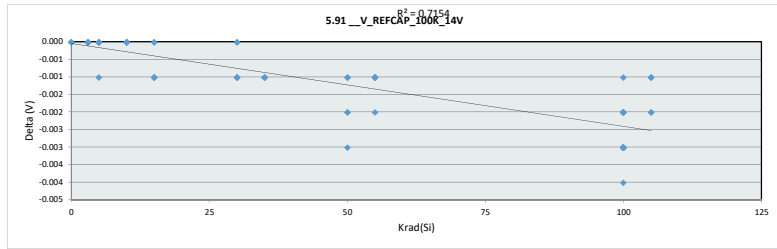


		5.90_V_REFCAP								
Test Site										
Tester										
Test Number										
Max Limit	1.237	V								
Min Limit	1.213	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

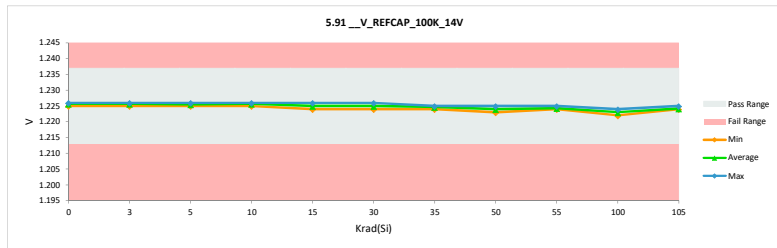


LDR TID Report  
TPS7H5001-SP

5.91 V_REFCAP_100K_14V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	117	1.225	1.225	0.000	
0	118	1.226	1.226	0.000	
0	122	1.226	1.226	0.000	
3	61	1.225	1.225	0.000	
3	62	1.226	1.226	0.000	
3	63	1.226	1.226	0.000	
3	64	1.225	1.225	0.000	
3	65	1.226	1.226	0.000	
3	66	1.226	1.226	0.000	
5	67	1.225	1.225	0.000	
5	68	1.225	1.225	0.000	
5	69	1.226	1.226	0.000	
5	70	1.226	1.226	0.000	
5	71	1.226	1.226	0.000	
5	72	1.226	1.225	-0.001	
10	73	1.225	1.225	0.000	
10	74	1.226	1.225	-0.001	
10	75	1.226	1.226	0.000	
10	76	1.225	1.225	0.000	
10	77	1.226	1.226	0.000	
10	78	1.226	1.226	0.000	
15	79	1.225	1.225	0.000	
15	80	1.225	1.224	-0.001	
15	81	1.226	1.226	0.000	
15	82	1.226	1.225	-0.001	
15	83	1.226	1.225	-0.001	
15	84	1.226	1.225	-0.001	
30	86	1.225	1.224	-0.001	
30	89	1.226	1.225	-0.001	
30	91	1.226	1.225	-0.001	
30	92	1.225	1.225	0.000	
30	94	1.226	1.225	-0.001	
30	95	1.226	1.226	0.000	
35	100	1.225	1.224	-0.001	
35	101	1.225	1.224	-0.001	
35	102	1.226	1.225	-0.001	
35	103	1.226	1.225	-0.001	
35	104	1.226	1.225	-0.001	
35	105	1.226	1.225	-0.001	
50	106	1.225	1.224	-0.001	
50	107	1.226	1.224	-0.002	
50	108	1.226	1.223	-0.003	
50	109	1.226	1.224	-0.002	
50	110	1.226	1.225	-0.001	
50	111	1.225	1.224	-0.001	
55	113	1.225	1.224	-0.001	
55	114	1.225	1.224	-0.001	
55	115	1.225	1.224	-0.001	
55	116	1.226	1.224	-0.002	
55	126	1.226	1.225	-0.001	
55	128	1.226	1.225	-0.001	
100	129	1.225	1.222	-0.003	
100	131	1.226	1.223	-0.003	
100	132	1.226	1.223	-0.003	
100	133	1.226	1.224	-0.002	
100	134	1.226	1.223	-0.003	
100	135	1.226	1.222	-0.004	
100	138	1.226	1.223	-0.003	
100	139	1.226	1.223	-0.003	
100	140	1.226	1.223	-0.003	
100	141	1.225	1.222	-0.003	
100	142	1.225	1.223	-0.002	
100	143	1.225	1.223	-0.002	
100	144	1.225	1.224	-0.001	
100	220	1.225	1.222	-0.003	
100	221	1.226	1.223	-0.003	
100	224	1.225	1.222	-0.003	
100	225	1.226	1.224	-0.002	
100	226	1.226	1.223	-0.003	
100	228	1.226	1.223	-0.003	
100	232	1.226	1.223	-0.003	
100	233	1.226	1.224	-0.002	
100	236	1.226	1.224	-0.002	
100	238	1.225	1.223	-0.002	
100	243	1.225	1.223	-0.002	
105	244	1.226	1.224	-0.002	
105	246	1.226	1.225	-0.001	
105	248	1.225	1.224	-0.001	
105	374	1.225	1.224	-0.001	
105	375	1.226	1.224	-0.002	
105	Max	1.226	1.226	0.000	
105	Average	1.226	1.224	-0.001	
105	Min	1.225	1.222	-0.004	
105	Std Dev	0.000	0.001	0.001	



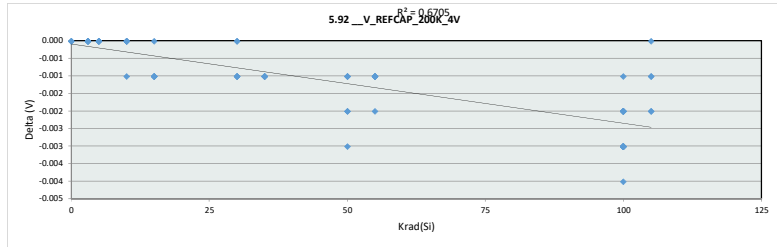
5.91 V_REFCAP													
Test Site													
Tester													
Test Number													
Max Limit	1.237	V											
Min Limit	1.213	V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.222	1.224		
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224		
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225		
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237		



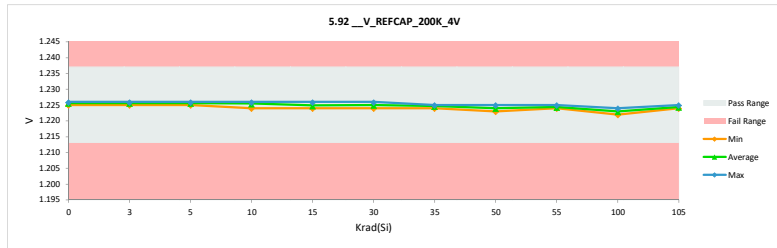
LDR TID Report  
TPS7H5001-SP

5.92_V_REFCAP_200K_4V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.224	-0.001
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.224	-0.001
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.225	1.224	-0.001
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.225	1.223	-0.002
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.223	-0.002
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.225	0.000
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001

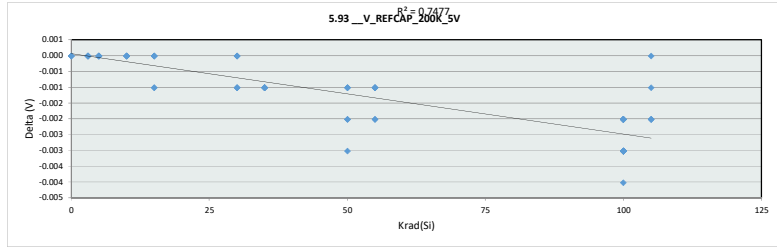


5.92_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

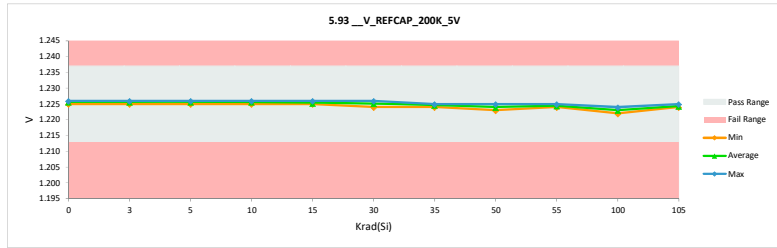


LDR TID Report  
TPS7H5001-SP

5.93_V_REFCAP_200K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.225	0.000
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.226	0.000
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.226	0.000
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.226	1.224	-0.002
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.226	1.224	-0.002
105	374	1.225	1.225	0.000
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



5.93_V_REFCAP												
Test Site												
Tester												
Test Number												
Max Limit	1.237	V										
Min Limit	1.213	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.222	1.224	
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225	1.224	
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225	
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	

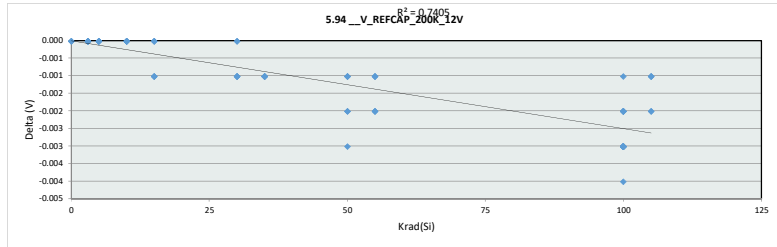




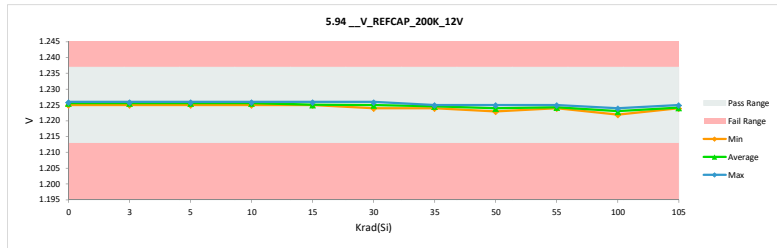
LDR TID Report  
TPS7H5001-SP

5.94_V_REFCAP_200K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.225	0.000
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.226	1.223	-0.003
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



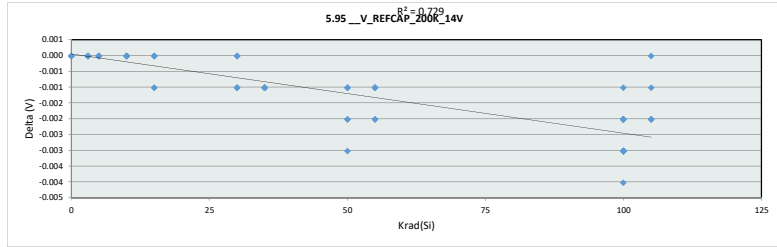
5.94_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



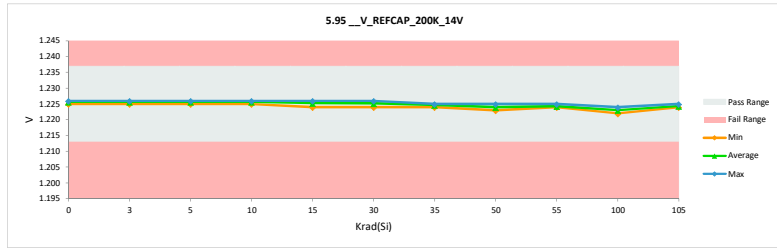
LDR TID Report  
TPS7H5001-SP

5.95_V_REFCAP_200K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.226	0.000
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.226	1.224	-0.002
105	374	1.225	1.225	0.000
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001

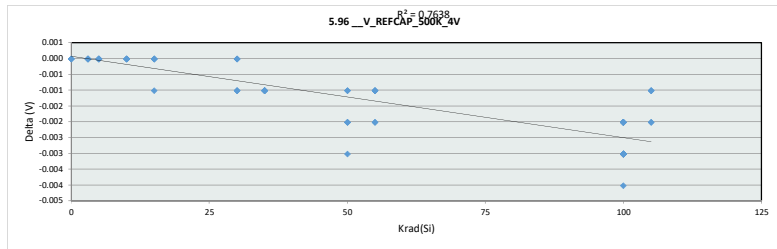


5.95_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

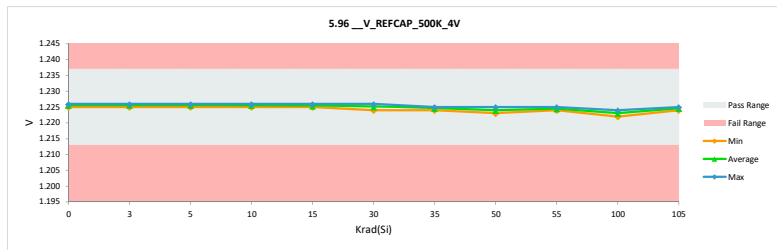


LDR TID Report  
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5.96_V_REFCAP_500K_4V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	117	1.225	1.225	0.000	
0	118	1.226	1.226	0.000	
0	122	1.226	1.226	0.000	
3	61	1.225	1.225	0.000	
3	62	1.226	1.226	0.000	
3	63	1.226	1.226	0.000	
3	64	1.225	1.225	0.000	
3	65	1.226	1.226	0.000	
3	66	1.226	1.226	0.000	
5	67	1.225	1.225	0.000	
5	68	1.225	1.225	0.000	
5	69	1.226	1.226	0.000	
5	70	1.226	1.226	0.000	
5	71	1.226	1.226	0.000	
5	72	1.226	1.226	0.000	
10	73	1.225	1.225	0.000	
10	74	1.226	1.226	0.000	
10	75	1.226	1.226	0.000	
10	76	1.225	1.225	0.000	
10	77	1.226	1.226	0.000	
10	78	1.226	1.226	0.000	
15	79	1.225	1.225	0.000	
15	80	1.225	1.225	0.000	
15	81	1.226	1.226	0.000	
15	82	1.226	1.225	-0.001	
15	83	1.226	1.226	0.000	
15	84	1.226	1.226	0.000	
30	86	1.225	1.224	-0.001	
30	89	1.226	1.225	-0.001	
30	91	1.226	1.226	0.000	
30	92	1.225	1.225	0.000	
30	94	1.226	1.225	-0.001	
30	95	1.226	1.226	0.000	
35	100	1.225	1.224	-0.001	
35	101	1.225	1.224	-0.001	
35	102	1.226	1.225	-0.001	
35	103	1.226	1.225	-0.001	
35	104	1.226	1.225	-0.001	
35	105	1.226	1.225	-0.001	
50	106	1.226	1.224	-0.002	
50	107	1.226	1.224	-0.002	
50	108	1.226	1.223	-0.003	
50	109	1.226	1.224	-0.002	
50	110	1.226	1.225	-0.001	
50	111	1.225	1.224	-0.001	
55	113	1.225	1.224	-0.001	
55	114	1.226	1.224	-0.002	
55	115	1.226	1.224	-0.002	
55	116	1.226	1.225	-0.001	
55	126	1.226	1.225	-0.001	
55	128	1.226	1.225	-0.001	
100	129	1.225	1.222	-0.003	
100	131	1.226	1.223	-0.003	
100	132	1.226	1.223	-0.003	
100	133	1.226	1.224	-0.002	
100	134	1.226	1.223	-0.003	
100	135	1.226	1.222	-0.004	
100	138	1.226	1.223	-0.003	
100	139	1.226	1.224	-0.002	
100	140	1.226	1.223	-0.003	
100	141	1.225	1.222	-0.003	
100	142	1.226	1.223	-0.003	
100	143	1.225	1.223	-0.002	
100	144	1.226	1.224	-0.002	
100	220	1.225	1.222	-0.003	
100	221	1.226	1.223	-0.003	
100	224	1.225	1.222	-0.003	
100	225	1.226	1.224	-0.002	
100	226	1.226	1.224	-0.002	
100	228	1.226	1.223	-0.003	
100	232	1.226	1.223	-0.003	
100	233	1.226	1.224	-0.002	
100	236	1.226	1.224	-0.002	
100	238	1.226	1.223	-0.003	
100	243	1.226	1.223	-0.003	
105	244	1.226	1.225	-0.001	
105	246	1.226	1.225	-0.001	
105	248	1.226	1.224	-0.002	
105	374	1.226	1.225	-0.001	
105	375	1.226	1.224	-0.002	
105	Max	1.226	1.226	0.000	
105	Average	1.226	1.224	-0.001	
105	Min	1.225	1.222	-0.004	
105	Std Dev	0.000	0.001	0.001	



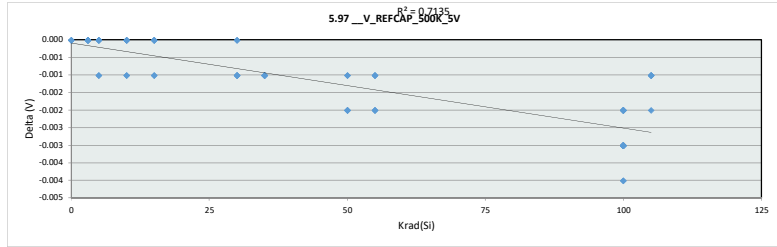
5.96_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225	1.223	1.225
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



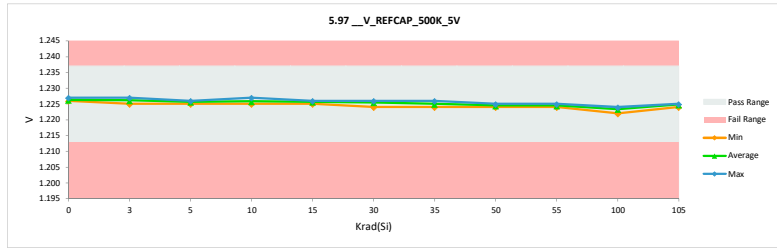
LDR TID Report  
TPS7H5001-SP

5.97_V_REFCAP_500K_5V			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.226	1.225	-0.001
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.224	-0.002
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.224	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.226	1.225	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.001	0.001	0.001



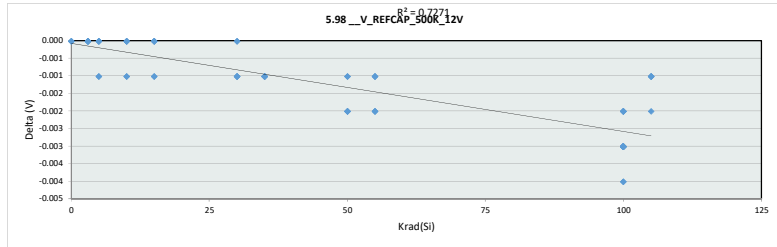
5.97_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237	V								
Min Limit	1.213	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



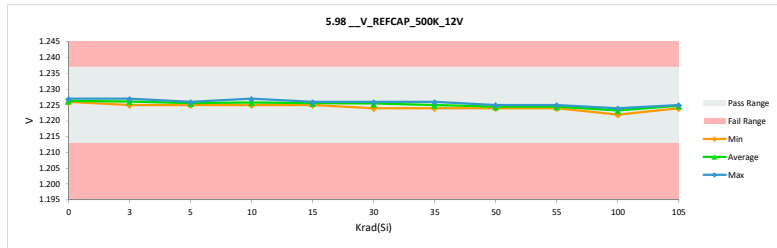
LDR TID Report  
TPS7H5001-SP

5.98_V_REFCAP_500K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.226	1.225	-0.001
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.224	-0.003
100	226	1.226	1.224	-0.002
100	228	1.227	1.224	-0.003
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.226	1.225	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.001	0.001	0.001



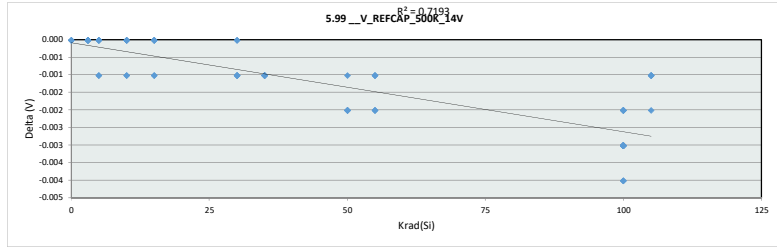
5.98_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.223
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



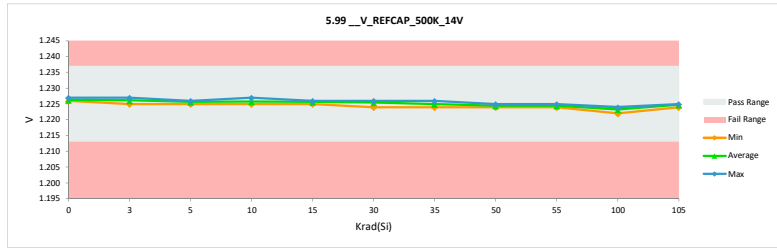
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5.99_V_REFCAP_500K_14V			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.226	1.225	-0.002
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.224	-0.003
100	226	1.226	1.224	-0.002
100	228	1.227	1.224	-0.003
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.224	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.226	1.225	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.001	0.001	0.001



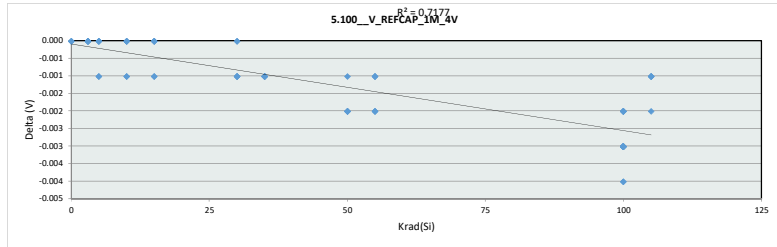
5.99_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237	V								
Min Limit	1.213	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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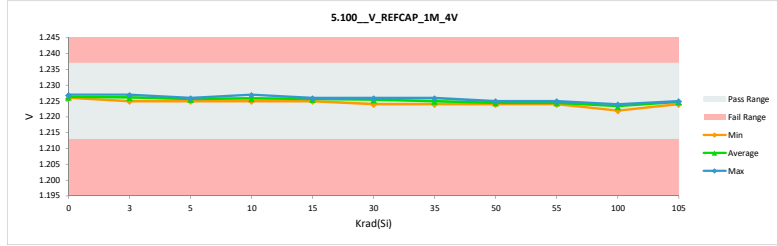
		5.100_V_REFCAP_1M_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.226	1.223	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.224	-0.003
100	226	1.226	1.224	-0.002
100	228	1.226	1.224	-0.002
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.226	1.225	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.001	0.001	0.001



		5.100_V_REFCAP	
Test Site			
Tester			
Test Number			
Max Limit	1.237	V	
Min Limit	1.213	V	

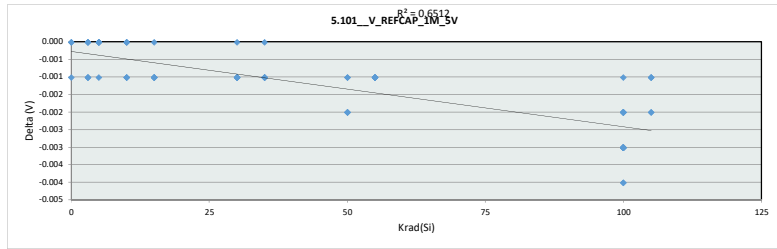
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.223	1.225
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.226	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



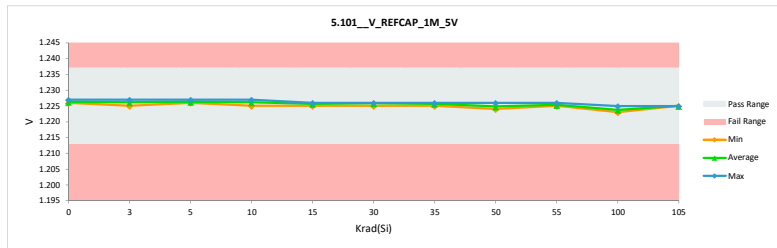
LDR TID Report  
TPS7H5001-SP

		5.101_V_REFCAP_1M_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.226	-0.001
3	61	1.226	1.225	-0.001
3	62	1.227	1.227	0.000
3	63	1.227	1.226	-0.001
3	64	1.226	1.226	0.000
3	65	1.227	1.226	-0.001
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.226	-0.001
5	72	1.226	1.226	0.000
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.226	-0.001
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.226	-0.001
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.226	1.226	0.000
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.226	1.226	0.000
50	106	1.226	1.225	-0.001
50	107	1.227	1.225	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.226	1.225	-0.001
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.223	-0.004
100	135	1.227	1.223	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.226	1.225	-0.001
100	220	1.226	1.223	-0.003
100	221	1.226	1.224	-0.002
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.226	1.224	-0.002
100	233	1.227	1.224	-0.003
100	236	1.227	1.225	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.225	-0.001
105	Max	1.227	1.227	0.000
105	Average	1.227	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.001	0.001	0.001



		5.101_V_REFCAP												
Test Site														
Tester														
Test Number		1.237	V											
Min Limit		1.213	V											
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105		
LL		1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min		1.226	1.225	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223	1.225	1.225	1.225
Average		1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.224	1.225	1.225
Max		1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225
UL		1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

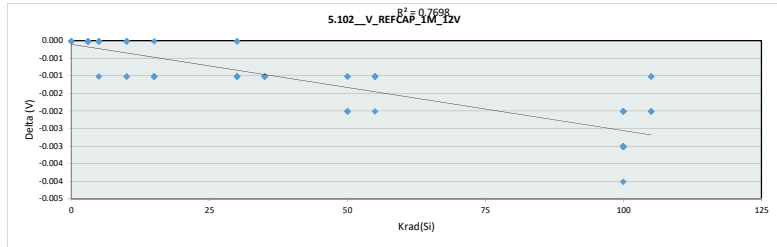




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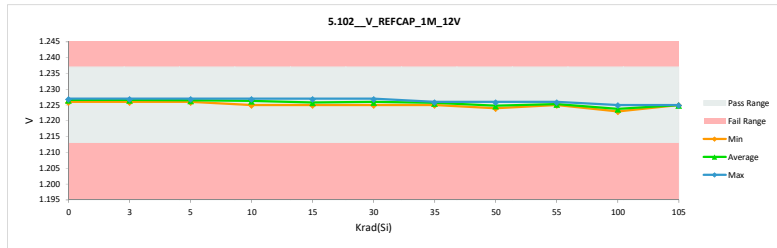
5.102_V_REFCAP_1M_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.227	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.226	-0.001
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.227	1.226	-0.001
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.226	1.225	-0.001
50	107	1.227	1.225	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.225	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	375	1.227	1.225	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.227	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001



5.102_V_REFCAP	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

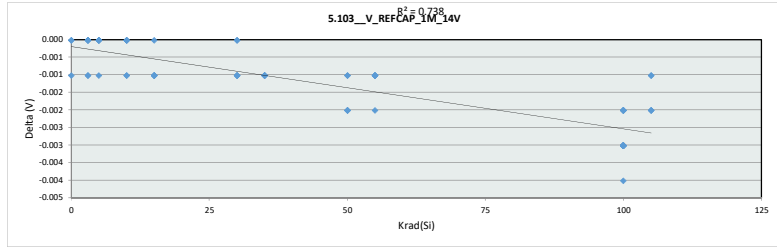
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.225
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



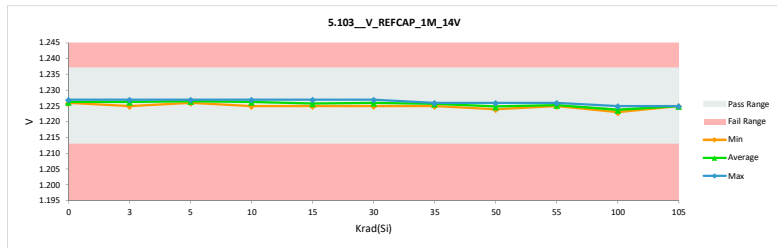
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5.103_V_REFCAP_1M_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.226	-0.001
3	61	1.226	1.225	-0.001
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.226	-0.001
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.226	-0.001
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.227	1.226	-0.001
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.226	1.225	-0.001
50	107	1.227	1.225	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.225	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.227	1.225	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.227	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001



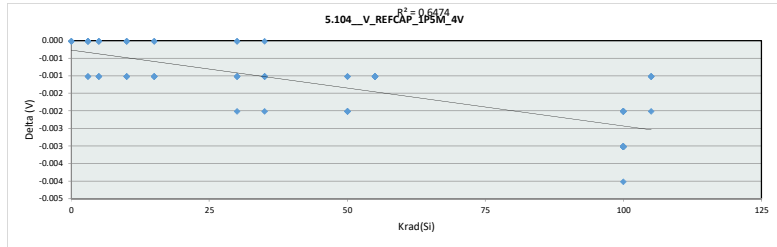
5.103_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223
Average	1.226	1.226	1.227	1.226	1.226	1.226	1.226	1.225	1.225	1.224
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



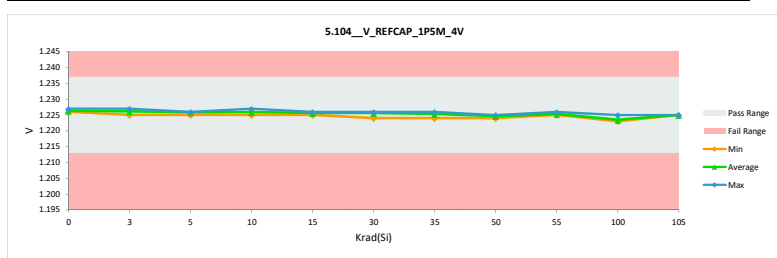
LDR TID Report  
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5.104_V_REFCAP_1P5M_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.226	1.225	-0.001
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.226	-0.001
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.225	-0.001
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.227	1.226	-0.001
5	72	1.226	1.226	0.000
10	73	1.226	1.225	-0.001
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.227	1.226	-0.001
15	84	1.226	1.226	0.000
30	86	1.226	1.224	-0.002
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.226	1.224	-0.002
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.227	1.226	-0.001
35	105	1.226	1.226	0.000
50	106	1.226	1.225	-0.001
50	107	1.226	1.225	-0.001
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.226	1.225	-0.001
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.225	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.226	1.224	-0.002
100	220	1.226	1.223	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	375	1.226	1.225	-0.001
105	Max	1.227	1.227	0.000
105	Average	1.226	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001

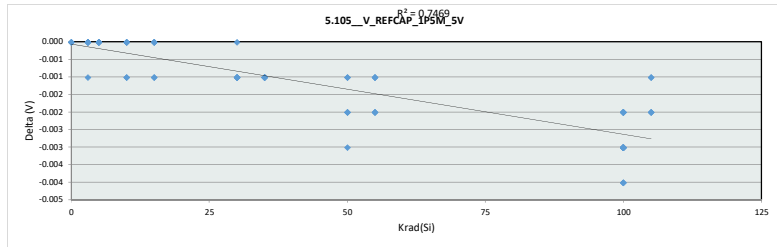


5.104_V_REFCAP	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

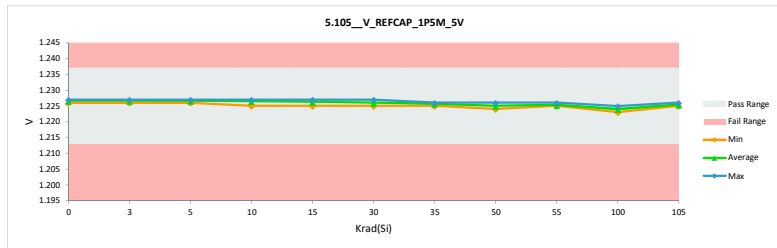


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		5.105_V_REFCAP_1P5M_5V		
Test Site				
Tester				
Test Number				
Unit		V	V	
Max Limit		1.237	1.237	
Min Limit		1.213	1.213	
Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.227	1.226	-0.001
3	65	1.227	1.227	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	0.000
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.227	1.226	-0.001
10	77	1.227	1.227	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.226	0.000
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.227	0.000
15	84	1.227	1.227	0.000
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.227	1.226	-0.001
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.225	-0.002
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.225	-0.001
55	113	1.226	1.225	-0.001
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.224	-0.003
100	140	1.227	1.224	-0.003
100	141	1.227	1.223	-0.004
100	142	1.227	1.224	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.227	1.223	-0.004
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.225	-0.002
100	236	1.227	1.225	-0.002
100	238	1.227	1.224	-0.003
100	243	1.226	1.224	-0.002
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.225	-0.002
105	374	1.227	1.226	-0.001
105	376	1.227	1.225	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.227	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001



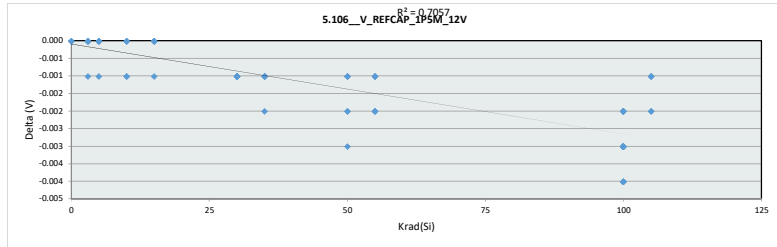
		5.105_V_REFCAP								
Test Site										
Tester										
Test Number										
Max Limit		1.237	V							
Min Limit		1.213	V							
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223
Average	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.225	1.224
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



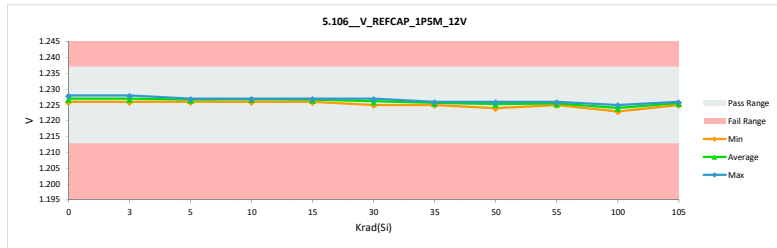
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5.106_V_REFCAP_1P5M_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.228	1.228	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.228	1.228	0.000
3	63	1.227	1.227	0.000
3	64	1.227	1.227	0.000
3	65	1.227	1.227	0.000
3	66	1.228	1.227	-0.001
5	67	1.227	1.226	-0.001
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	0.000
10	73	1.226	1.226	0.000
10	74	1.228	1.227	-0.001
10	75	1.227	1.227	0.000
10	76	1.227	1.227	0.000
10	77	1.227	1.227	0.000
10	78	1.228	1.227	-0.001
15	79	1.227	1.226	-0.001
15	80	1.226	1.226	0.000
15	81	1.227	1.227	0.000
15	82	1.227	1.227	0.000
15	83	1.227	1.227	0.000
15	84	1.227	1.227	0.000
30	86	1.226	1.225	-0.001
30	89	1.228	1.227	-0.001
30	91	1.227	1.226	-0.001
30	92	1.227	1.226	-0.001
30	94	1.227	1.226	-0.001
30	95	1.228	1.227	-0.001
35	100	1.227	1.225	-0.002
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.226	-0.001
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.226	-0.001
50	110	1.228	1.226	-0.002
50	111	1.226	1.225	-0.001
55	113	1.227	1.225	-0.002
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.226	-0.001
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.227	1.224	-0.003
100	131	1.227	1.224	-0.003
100	132	1.228	1.224	-0.004
100	133	1.228	1.225	-0.003
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.225	-0.002
100	140	1.227	1.224	-0.003
100	141	1.227	1.223	-0.004
100	142	1.227	1.224	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.227	1.223	-0.004
100	221	1.227	1.224	-0.003
100	224	1.227	1.223	-0.004
100	225	1.227	1.225	-0.002
100	226	1.227	1.225	-0.002
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.225	-0.002
100	236	1.227	1.225	-0.002
100	238	1.227	1.224	-0.003
100	243	1.227	1.224	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.226	-0.001
105	374	1.227	1.226	-0.001
105	375	1.227	1.225	-0.002
105	Max	1.228	1.228	0.000
105	Average	1.227	1.226	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001



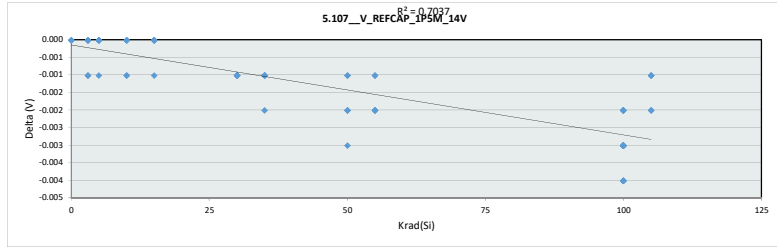
5.106_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225	1.223
Average	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.224	1.225
Max	1.228	1.228	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



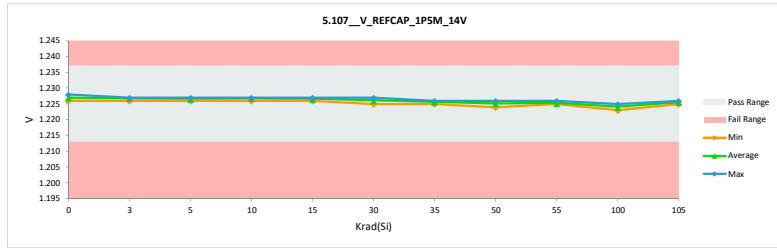
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5.107_V_REFCAP_1P5M_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.228	1.228	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.228	1.227	-0.001
3	63	1.227	1.227	0.000
3	64	1.227	1.227	0.000
3	65	1.227	1.227	0.000
3	66	1.228	1.227	-0.001
5	67	1.227	1.226	-0.001
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	0.000
10	73	1.226	1.226	0.000
10	74	1.228	1.227	-0.001
10	75	1.227	1.227	0.000
10	76	1.227	1.227	0.000
10	77	1.227	1.227	0.000
10	78	1.228	1.227	-0.001
15	79	1.227	1.226	-0.001
15	80	1.226	1.226	0.000
15	81	1.227	1.227	0.000
15	82	1.227	1.227	0.000
15	83	1.227	1.227	0.000
15	84	1.227	1.227	0.000
30	86	1.226	1.225	-0.001
30	89	1.228	1.227	-0.001
30	91	1.227	1.226	-0.001
30	92	1.227	1.226	-0.001
30	94	1.227	1.226	-0.001
30	95	1.228	1.227	-0.001
35	100	1.227	1.225	-0.002
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.226	-0.001
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.225	-0.002
50	110	1.228	1.226	-0.002
50	111	1.226	1.225	-0.001
55	113	1.227	1.225	-0.002
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.227	1.224	-0.003
100	131	1.227	1.224	-0.003
100	132	1.228	1.224	-0.004
100	133	1.228	1.225	-0.003
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.225	-0.002
100	140	1.227	1.224	-0.003
100	141	1.227	1.223	-0.004
100	142	1.227	1.224	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.227	1.223	-0.004
100	221	1.227	1.224	-0.003
100	224	1.227	1.223	-0.004
100	225	1.227	1.225	-0.002
100	226	1.227	1.225	-0.002
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.228	1.225	-0.003
100	236	1.227	1.225	-0.002
100	238	1.227	1.224	-0.003
100	243	1.227	1.224	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.226	-0.001
105	374	1.227	1.226	-0.001
105	376	1.227	1.225	-0.002
105	Max	1.228	1.228	0.000
105	Average	1.227	1.226	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.001	0.001	0.001



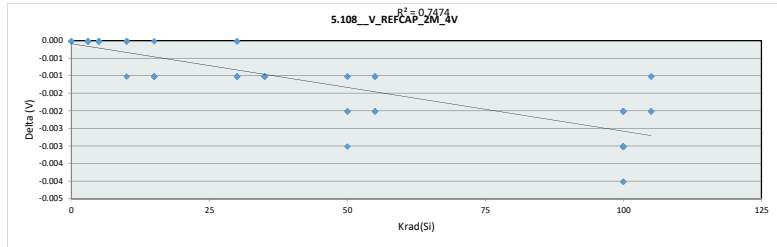
5.107_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225	1.223
Average	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.225	1.225	1.224
Max	1.228	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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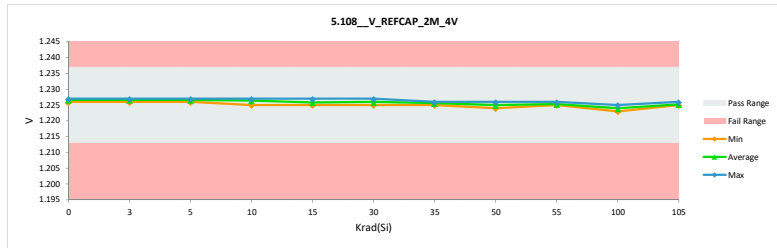
5.108_V_REFCAP_2M_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.227	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	-0.001
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.227	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.227	1.226	-0.001
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.225	-0.002
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.225	-0.001
55	113	1.226	1.225	-0.001
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.224	-0.003
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.224	-0.002
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.225	-0.002
100	236	1.227	1.225	-0.002
100	238	1.227	1.223	-0.004
100	243	1.226	1.224	-0.002
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.225	-0.002
105	374	1.226	1.225	-0.001
105	375	1.227	1.225	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.227	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001



5.108_V_REFCAP	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

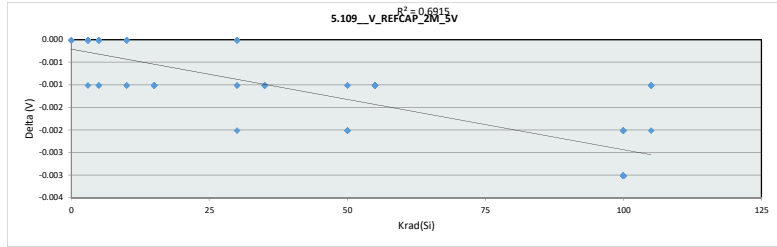
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.225	1.224	1.225
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



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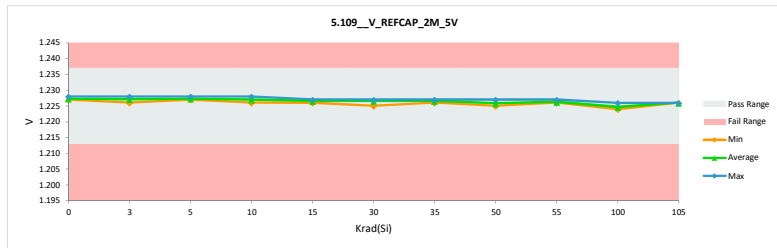
5.109_V_REFCAP_2M_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.228	0.000
0	122	1.227	1.227	0.000
3	61	1.227	1.226	-0.001
3	62	1.228	1.228	0.000
3	63	1.227	1.227	0.000
3	64	1.227	1.227	0.000
3	65	1.227	1.227	0.000
3	66	1.228	1.228	0.000
5	67	1.227	1.227	0.000
5	68	1.227	1.227	0.000
5	69	1.228	1.228	0.000
5	70	1.228	1.228	0.000
5	71	1.228	1.227	-0.001
5	72	1.228	1.227	-0.001
10	73	1.227	1.226	-0.001
10	74	1.228	1.227	-0.001
10	75	1.227	1.227	0.000
10	76	1.227	1.227	0.000
10	77	1.227	1.227	0.000
10	78	1.228	1.228	0.000
15	79	1.227	1.226	-0.001
15	80	1.227	1.226	-0.001
15	81	1.228	1.227	-0.001
15	82	1.228	1.227	-0.001
15	83	1.228	1.227	-0.001
15	84	1.228	1.227	-0.001
30	86	1.227	1.225	-0.002
30	89	1.228	1.227	-0.001
30	91	1.227	1.227	0.000
30	92	1.227	1.227	0.000
30	94	1.227	1.227	0.000
30	95	1.228	1.227	-0.001
35	100	1.227	1.226	-0.001
35	101	1.227	1.226	-0.001
35	102	1.228	1.227	-0.001
35	103	1.228	1.227	-0.001
35	104	1.228	1.227	-0.001
35	105	1.228	1.227	-0.001
50	106	1.227	1.226	-0.001
50	107	1.228	1.226	-0.002
50	108	1.227	1.225	-0.002
50	109	1.228	1.226	-0.002
50	110	1.228	1.227	-0.001
50	111	1.227	1.225	-0.002
55	113	1.227	1.226	-0.001
55	114	1.227	1.226	-0.001
55	115	1.227	1.226	-0.001
55	116	1.227	1.226	-0.001
55	126	1.228	1.227	-0.001
55	128	1.228	1.227	-0.001
100	129	1.227	1.224	-0.003
100	131	1.227	1.225	-0.002
100	132	1.228	1.225	-0.003
100	133	1.228	1.226	-0.002
100	134	1.228	1.225	-0.003
100	135	1.227	1.224	-0.003
100	138	1.227	1.224	-0.003
100	139	1.227	1.225	-0.002
100	140	1.228	1.225	-0.003
100	141	1.227	1.224	-0.003
100	142	1.227	1.224	-0.003
100	143	1.227	1.225	-0.002
100	144	1.228	1.226	-0.002
100	220	1.227	1.224	-0.003
100	221	1.227	1.225	-0.002
100	224	1.227	1.224	-0.003
100	225	1.228	1.225	-0.003
100	226	1.228	1.225	-0.003
100	228	1.228	1.225	-0.003
100	232	1.228	1.225	-0.003
100	233	1.228	1.225	-0.003
100	236	1.228	1.225	-0.003
100	238	1.227	1.224	-0.003
100	243	1.227	1.224	-0.003
105	244	1.227	1.226	-0.001
105	246	1.228	1.226	-0.002
105	248	1.227	1.226	-0.001
105	374	1.227	1.226	-0.001
105	376	1.227	1.226	-0.001
105	Max	1.228	1.228	0.000
105	Average	1.227	1.226	-0.001
105	Min	1.227	1.224	-0.003
105	Std Dev	0.001	0.001	0.001



5.109_V_REFCAP	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.227	1.226	1.227	1.226	1.226	1.225	1.226	1.225	1.226	1.224	1.226
Average	1.227	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.225	1.226
Max	1.228	1.228	1.228	1.228	1.227	1.227	1.227	1.227	1.227	1.226	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

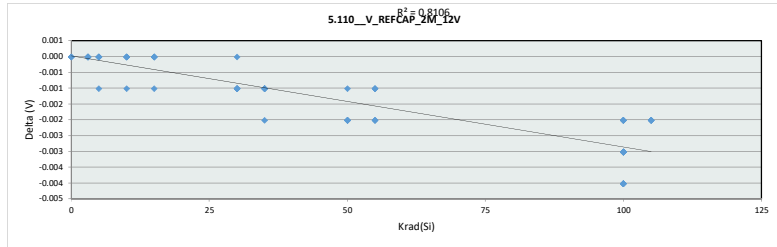




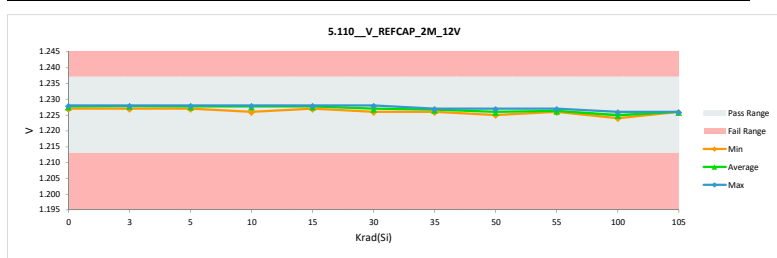
LDR TID Report  
TPS7H5001-SP

5.110_V_REFCAP_2M_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.228	0.000
0	122	1.228	1.228	0.000
3	61	1.227	1.227	0.000
3	62	1.228	1.228	0.000
3	63	1.228	1.228	0.000
3	64	1.228	1.228	0.000
3	65	1.228	1.228	0.000
3	66	1.228	1.228	0.000
5	67	1.228	1.227	-0.001
5	68	1.227	1.227	0.000
5	69	1.228	1.228	0.000
5	70	1.228	1.228	0.000
5	71	1.228	1.228	0.000
5	72	1.228	1.228	0.000
10	73	1.227	1.226	-0.001
10	74	1.228	1.228	0.000
10	75	1.228	1.228	0.000
10	76	1.228	1.228	0.000
10	77	1.228	1.228	0.000
10	78	1.228	1.228	0.000
15	79	1.228	1.227	-0.001
15	80	1.227	1.227	0.000
15	81	1.228	1.228	0.000
15	82	1.228	1.228	0.000
15	83	1.228	1.228	0.000
15	84	1.228	1.228	0.000
30	86	1.227	1.226	-0.001
30	89	1.228	1.227	-0.001
30	91	1.228	1.227	-0.001
30	92	1.228	1.227	-0.001
30	94	1.228	1.227	-0.001
30	95	1.228	1.228	0.000
35	100	1.228	1.226	-0.002
35	101	1.227	1.226	-0.001
35	102	1.228	1.227	-0.001
35	103	1.228	1.227	-0.001
35	104	1.228	1.227	-0.001
35	105	1.228	1.227	-0.001
50	106	1.228	1.226	-0.002
50	107	1.228	1.226	-0.002
50	108	1.227	1.225	-0.002
50	109	1.228	1.226	-0.002
50	110	1.229	1.227	-0.002
50	111	1.227	1.226	-0.001
55	113	1.227	1.226	-0.001
55	114	1.228	1.226	-0.002
55	115	1.228	1.226	-0.002
55	116	1.228	1.226	-0.002
55	126	1.228	1.227	-0.001
55	128	1.228	1.227	-0.001
100	129	1.228	1.225	-0.003
100	131	1.228	1.225	-0.003
100	132	1.228	1.225	-0.003
100	133	1.229	1.226	-0.003
100	134	1.228	1.225	-0.003
100	135	1.228	1.224	-0.004
100	138	1.228	1.225	-0.003
100	139	1.228	1.225	-0.003
100	140	1.228	1.225	-0.003
100	141	1.228	1.224	-0.004
100	142	1.228	1.225	-0.003
100	143	1.227	1.225	-0.002
100	144	1.228	1.226	-0.002
100	220	1.228	1.224	-0.004
100	221	1.228	1.225	-0.003
100	224	1.228	1.224	-0.004
100	225	1.228	1.226	-0.002
100	226	1.228	1.225	-0.003
100	228	1.228	1.225	-0.003
100	232	1.228	1.225	-0.003
100	233	1.228	1.226	-0.002
100	236	1.228	1.226	-0.002
100	238	1.228	1.224	-0.004
100	243	1.227	1.224	-0.003
105	244	1.228	1.226	-0.002
105	246	1.228	1.226	-0.002
105	248	1.228	1.226	-0.002
105	374	1.228	1.226	-0.002
105	376	1.228	1.226	-0.002
105	Max	1.229	1.228	0.000
105	Average	1.228	1.226	-0.001
105	Min	1.227	1.224	-0.004
105	Std Dev	0.000	0.001	0.001



5.110_V_REFCAP	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

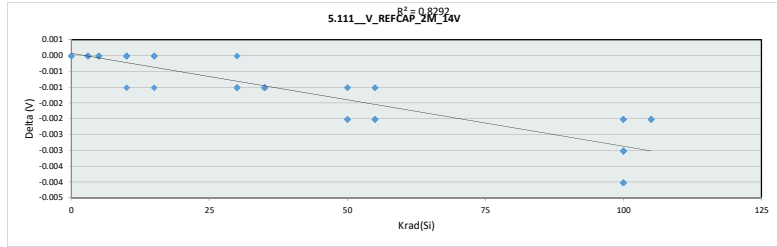


Krad(Si)	LL	Min	Average	Max	UL
0	1.213	1.228	1.228	1.228	1.237
3	1.213	1.228	1.228	1.228	1.237
5	1.213	1.228	1.228	1.228	1.237
10	1.213	1.228	1.228	1.228	1.237
15	1.213	1.228	1.228	1.228	1.237
30	1.213	1.228	1.227	1.227	1.237
35	1.213	1.228	1.227	1.227	1.237
50	1.213	1.228	1.226	1.226	1.237
55	1.213	1.228	1.226	1.226	1.237
100	1.213	1.228	1.226	1.226	1.237
105	1.213	1.228	1.226	1.226	1.237

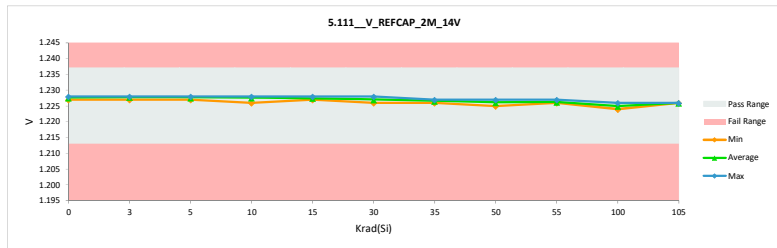
LDR TID Report  
TPS7H5001-SP

5.111_V_REFCAP_2M_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.228	0.000
0	122	1.228	1.228	0.000
3	61	1.227	1.227	0.000
3	62	1.228	1.228	0.000
3	63	1.228	1.228	0.000
3	64	1.228	1.228	0.000
3	65	1.228	1.228	0.000
3	66	1.228	1.228	0.000
5	67	1.227	1.227	0.000
5	68	1.227	1.227	0.000
5	69	1.228	1.228	0.000
5	70	1.228	1.228	0.000
5	71	1.228	1.228	0.000
5	72	1.228	1.228	0.000
10	73	1.227	1.226	-0.001
10	74	1.228	1.228	0.000
10	75	1.228	1.228	0.000
10	76	1.228	1.228	0.000
10	77	1.228	1.228	0.000
10	78	1.228	1.228	0.000
15	79	1.227	1.227	0.000
15	80	1.227	1.227	0.000
15	81	1.228	1.228	0.000
15	82	1.228	1.228	0.000
15	83	1.228	1.227	-0.001
15	84	1.228	1.228	0.000
30	86	1.227	1.226	-0.001
30	89	1.228	1.227	-0.001
30	91	1.228	1.227	-0.001
30	92	1.228	1.227	-0.001
30	94	1.228	1.227	-0.001
30	95	1.228	1.228	0.000
35	100	1.227	1.226	-0.001
35	101	1.227	1.226	-0.001
35	102	1.228	1.227	-0.001
35	103	1.228	1.227	-0.001
35	104	1.228	1.227	-0.001
35	105	1.228	1.227	-0.001
50	106	1.228	1.227	-0.001
50	107	1.228	1.226	-0.002
50	108	1.227	1.225	-0.002
50	109	1.228	1.226	-0.002
50	110	1.229	1.227	-0.002
50	111	1.227	1.226	-0.001
55	113	1.228	1.226	-0.002
55	114	1.228	1.226	-0.002
55	115	1.228	1.226	-0.002
55	116	1.228	1.226	-0.002
55	126	1.228	1.227	-0.001
55	128	1.228	1.227	-0.001
100	129	1.228	1.225	-0.003
100	131	1.228	1.225	-0.003
100	132	1.229	1.225	-0.004
100	133	1.229	1.226	-0.003
100	134	1.228	1.225	-0.003
100	135	1.228	1.224	-0.004
100	138	1.228	1.225	-0.003
100	139	1.228	1.225	-0.003
100	140	1.228	1.225	-0.003
100	141	1.228	1.224	-0.004
100	142	1.228	1.225	-0.003
100	143	1.227	1.225	-0.002
100	144	1.228	1.226	-0.002
100	220	1.228	1.224	-0.004
100	221	1.228	1.225	-0.003
100	224	1.228	1.224	-0.004
100	225	1.228	1.226	-0.002
100	226	1.228	1.225	-0.003
100	228	1.228	1.225	-0.003
100	232	1.228	1.225	-0.003
100	233	1.228	1.226	-0.002
100	236	1.228	1.226	-0.002
100	238	1.228	1.224	-0.004
100	243	1.227	1.224	-0.003
105	244	1.228	1.226	-0.002
105	246	1.228	1.226	-0.002
105	248	1.228	1.226	-0.002
105	374	1.228	1.226	-0.002
105	376	1.228	1.226	-0.002
105	Max	1.229	1.228	0.000
105	Average	1.228	1.226	-0.001
105	Min	1.227	1.224	-0.004
105	Std Dev	0.000	0.001	0.001



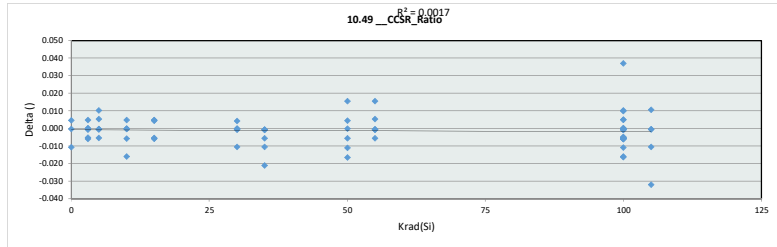
5.111_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237 V										
Min Limit	1.213 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.227	1.227	1.227	1.226	1.227	1.226	1.226	1.225	1.226	1.224	1.226
Average	1.228	1.228	1.228	1.228	1.228	1.227	1.227	1.226	1.226	1.225	1.226
Max	1.228	1.228	1.228	1.228	1.228	1.228	1.227	1.227	1.227	1.226	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



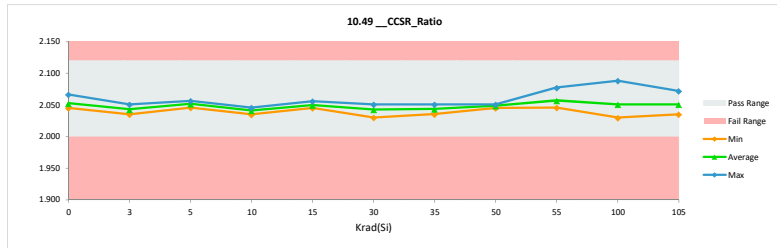
LDR TID Report  
TPS7H5001-SP

		10.49_CCSR_Ratio	
Test Site			
Tester			
Test Number			
Unit			
Max Limit	2.12	2.12	
Min Limit	2	2	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.041	2.046	0.005
0	118	2.067	2.067	0.000
0	122	2.056	2.046	-0.010
3	61	2.041	2.046	0.005
3	62	2.046	2.041	-0.006
3	63	2.046	2.046	0.000
3	64	2.051	2.051	0.000
3	65	2.040	2.036	-0.005
3	66	2.041	2.041	0.000
5	67	2.041	2.046	0.006
5	68	2.056	2.056	0.000
5	69	2.056	2.051	-0.005
5	70	2.046	2.057	0.011
5	71	2.051	2.051	0.000
5	72	2.051	2.051	0.000
10	73	2.041	2.046	0.005
10	74	2.046	2.041	-0.005
10	75	2.046	2.046	0.000
10	76	2.051	2.035	-0.016
10	77	2.040	2.041	0.000
10	78	2.041	2.041	0.000
15	79	2.041	2.046	0.005
15	80	2.056	2.051	-0.005
15	81	2.056	2.051	-0.005
15	82	2.046	2.051	0.005
15	83	2.051	2.046	-0.005
15	84	2.051	2.056	0.005
30	86	2.041	2.041	0.000
30	89	2.046	2.046	0.000
30	91	2.046	2.046	0.000
30	92	2.051	2.051	0.000
30	94	2.040	2.030	-0.010
30	95	2.041	2.046	0.005
35	100	2.041	2.040	0.000
35	101	2.056	2.036	-0.021
35	102	2.056	2.046	-0.010
35	103	2.046	2.046	0.000
35	104	2.051	2.046	-0.005
35	105	2.051	2.051	0.000
50	106	2.062	2.051	-0.011
50	107	2.041	2.046	0.005
50	108	2.051	2.046	-0.005
50	109	2.035	2.051	0.016
50	110	2.067	2.051	-0.016
50	111	2.051	2.051	0.000
55	113	2.051	2.051	0.000
55	114	2.062	2.062	0.000
55	115	2.051	2.046	-0.005
55	116	2.051	2.051	0.000
55	126	2.062	2.078	0.016
55	128	2.051	2.057	0.006
100	129	2.035	2.046	0.010
100	131	2.062	2.056	-0.006
100	132	2.051	2.046	-0.005
100	133	2.041	2.046	0.005
100	134	2.041	2.041	0.000
100	135	2.046	2.030	-0.016
100	138	2.056	2.057	0.000
100	139	2.062	2.046	-0.016
100	140	2.041	2.040	0.000
100	141	2.072	2.067	-0.005
100	142	2.067	2.061	-0.006
100	143	2.041	2.041	0.000
100	144	2.088	2.078	-0.010
100	220	2.046	2.046	0.000
100	221	2.051	2.046	-0.005
100	224	2.056	2.057	0.001
100	225	2.035	2.041	0.005
100	226	2.067	2.062	-0.005
100	228	2.051	2.046	-0.004
100	232	2.051	2.089	0.037
100	233	2.046	2.041	-0.005
100	236	2.046	2.041	-0.005
100	238	2.051	2.062	0.011
100	243	2.046	2.046	0.000
105	244	2.072	2.072	0.000
105	246	2.078	2.046	-0.032
105	248	2.046	2.036	-0.010
105	374	2.051	2.062	0.011
105	376	2.041	2.041	0.000
105	Max	2.088	2.089	0.037
105	Average	2.050	2.049	-0.001
105	Min	2.035	2.030	-0.032
105	Std Dev	0.010	0.010	0.009



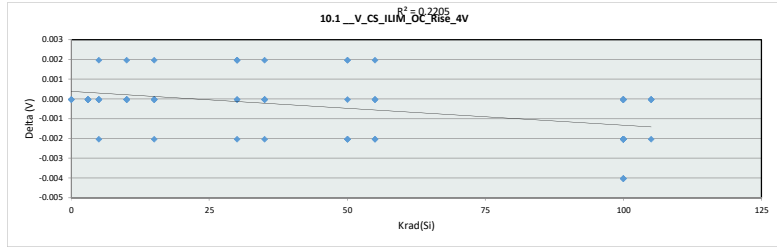
		10.49_CCSR_Ratio											
Test Site													
Tester													
Test Number													
Max Limit	2.12												
Min Limit	2												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000		
Min	2.046	2.036	2.046	2.035	2.046	2.030	2.036	2.046	2.046	2.030	2.036		
Average	2.053	2.043	2.052	2.042	2.050	2.043	2.044	2.049	2.057	2.051	2.051		
Max	2.067	2.051	2.057	2.046	2.056	2.051	2.051	2.051	2.078	2.089	2.072		
UL	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120		



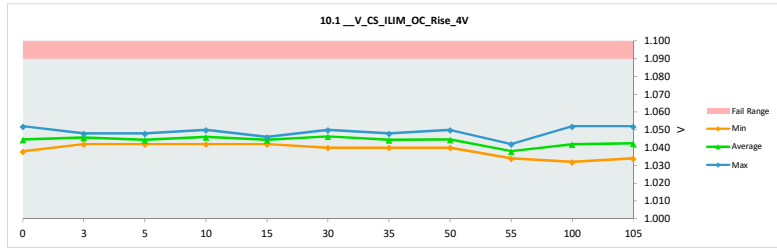
LDR TID Report  
TPS7H5001-SP

10.1 V_CS_ILIM_OC_Rise_4V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	1.09	1.09
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.052	1.052	0.000
0	118	1.038	1.038	0.000
0	122	1.044	1.044	0.000
3	61	1.048	1.048	0.000
3	62	1.046	1.046	0.000
3	63	1.046	1.046	0.000
3	64	1.044	1.044	0.000
3	65	1.042	1.042	0.000
3	66	1.048	1.048	0.000
5	67	1.048	1.048	0.000
5	68	1.046	1.046	0.000
5	69	1.042	1.044	0.002
5	70	1.044	1.044	0.000
5	71	1.042	1.042	0.000
5	72	1.044	1.042	-0.002
10	73	1.048	1.050	0.002
10	74	1.046	1.046	0.000
10	75	1.046	1.046	0.000
10	76	1.044	1.044	0.000
10	77	1.042	1.042	0.000
10	78	1.048	1.048	0.000
15	79	1.048	1.046	-0.002
15	80	1.046	1.046	0.000
15	81	1.042	1.044	0.002
15	82	1.044	1.044	0.000
15	83	1.042	1.042	0.000
15	84	1.044	1.044	0.000
30	86	1.048	1.050	0.002
30	89	1.046	1.046	0.000
30	91	1.046	1.046	0.000
30	92	1.044	1.046	0.002
30	94	1.042	1.040	-0.002
30	95	1.048	1.050	0.002
35	100	1.048	1.048	0.000
35	101	1.046	1.046	0.000
35	102	1.042	1.044	0.002
35	103	1.044	1.044	0.000
35	104	1.042	1.040	-0.002
35	105	1.044	1.044	0.000
50	106	1.050	1.050	0.000
50	107	1.046	1.044	-0.002
50	108	1.046	1.044	-0.002
50	109	1.044	1.042	-0.002
50	110	1.046	1.048	0.002
50	111	1.038	1.040	0.002
55	113	1.036	1.034	-0.002
55	114	1.036	1.036	0.000
55	115	1.038	1.036	-0.002
55	116	1.040	1.040	0.000
55	126	1.042	1.042	0.000
55	128	1.038	1.040	0.002
100	129	1.046	1.044	-0.002
100	131	1.034	1.034	0.000
100	132	1.038	1.036	-0.002
100	133	1.038	1.038	0.000
100	134	1.044	1.044	0.000
100	135	1.046	1.042	-0.004
100	138	1.052	1.050	-0.002
100	139	1.040	1.036	-0.004
100	140	1.040	1.040	0.000
100	141	1.044	1.040	-0.004
100	142	1.034	1.032	-0.002
100	143	1.046	1.044	-0.002
100	144	1.052	1.052	0.000
100	220	1.048	1.046	-0.002
100	221	1.050	1.048	-0.002
100	224	1.042	1.042	0.000
100	225	1.042	1.040	-0.002
100	226	1.042	1.040	-0.002
100	228	1.048	1.044	-0.004
100	232	1.042	1.042	0.000
100	233	1.050	1.048	-0.002
100	236	1.040	1.038	-0.002
100	238	1.042	1.042	0.000
100	243	1.046	1.044	-0.002
105	244	1.038	1.038	0.000
105	246	1.034	1.034	0.000
105	248	1.052	1.052	0.000
105	374	1.040	1.040	0.000
105	376	1.050	1.048	-0.002
105	Max	1.052	1.052	0.002
105	Average	1.044	1.043	-0.001
105	Min	1.034	1.032	-0.004
105	Std Dev	0.004	0.004	0.001



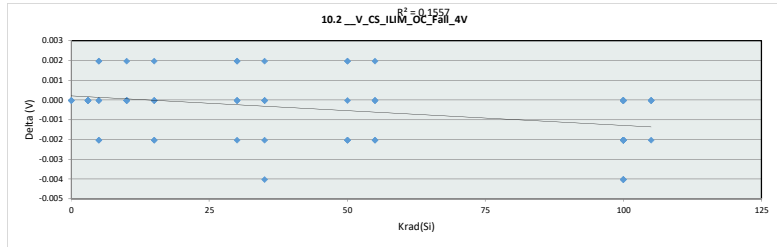
10.1 V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.038	1.042	1.042	1.042	1.042	1.040	1.040	1.040	1.034	1.032	1.034
Average	1.045	1.046	1.044	1.044	1.044	1.046	1.044	1.044	1.045	1.038	1.042
Max	1.052	1.048	1.048	1.050	1.046	1.050	1.048	1.050	1.042	1.052	1.052
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



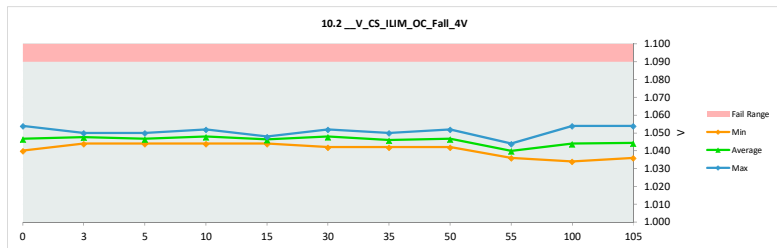
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10.2 V_CS_ILIM_OC_Fail_4V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.09 1.09
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.054	1.054	0.000
0	118	1.040	1.040	0.000
0	122	1.046	1.046	0.000
3	61	1.050	1.050	0.000
3	62	1.048	1.048	0.000
3	63	1.048	1.048	0.000
3	64	1.046	1.046	0.000
3	65	1.044	1.044	0.000
3	66	1.050	1.050	0.000
5	67	1.050	1.050	0.000
5	68	1.050	1.048	-0.002
5	69	1.044	1.046	0.002
5	70	1.046	1.048	0.002
5	71	1.044	1.044	0.000
5	72	1.046	1.044	-0.002
10	73	1.050	1.052	0.002
10	74	1.048	1.048	0.000
10	75	1.048	1.048	0.000
10	76	1.046	1.046	0.000
10	77	1.044	1.044	0.000
10	78	1.050	1.050	0.000
15	79	1.050	1.048	-0.002
15	80	1.050	1.048	-0.002
15	81	1.044	1.046	0.002
15	82	1.046	1.046	0.000
15	83	1.044	1.044	0.000
15	84	1.046	1.046	0.000
30	86	1.050	1.052	0.002
30	89	1.048	1.048	0.000
30	91	1.048	1.048	0.000
30	92	1.046	1.048	0.002
30	94	1.044	1.042	-0.002
30	95	1.050	1.050	0.000
35	100	1.050	1.050	0.000
35	101	1.050	1.046	-0.004
35	102	1.044	1.046	0.002
35	103	1.046	1.046	0.000
35	104	1.044	1.042	-0.002
35	105	1.046	1.046	0.000
50	106	1.052	1.052	0.000
50	107	1.048	1.046	-0.002
50	108	1.048	1.046	-0.002
50	109	1.046	1.044	-0.002
50	110	1.048	1.050	0.002
50	111	1.040	1.042	0.002
55	113	1.038	1.036	-0.002
55	114	1.038	1.038	0.000
55	115	1.040	1.038	-0.002
55	116	1.042	1.042	0.000
55	126	1.044	1.044	0.000
55	128	1.040	1.042	0.002
100	129	1.048	1.046	-0.002
100	131	1.036	1.036	0.000
100	132	1.038	1.038	0.000
100	133	1.040	1.040	0.000
100	134	1.046	1.046	0.000
100	135	1.048	1.044	-0.004
100	138	1.054	1.052	-0.002
100	139	1.042	1.040	-0.002
100	140	1.042	1.040	-0.002
100	141	1.046	1.042	-0.004
100	142	1.038	1.034	-0.004
100	143	1.048	1.046	-0.002
100	144	1.054	1.054	0.000
100	220	1.050	1.050	0.000
100	221	1.052	1.050	-0.002
100	224	1.044	1.044	0.000
100	225	1.044	1.042	-0.002
100	226	1.044	1.042	-0.002
100	228	1.050	1.046	-0.004
100	232	1.044	1.044	0.000
100	233	1.052	1.050	-0.002
100	236	1.042	1.040	-0.002
100	238	1.044	1.044	0.000
100	243	1.048	1.046	-0.002
105	244	1.040	1.040	0.000
105	246	1.036	1.036	0.000
105	248	1.054	1.054	0.000
105	374	1.042	1.042	0.000
105	376	1.052	1.050	-0.002
105	Max	1.054	1.054	0.002
105	Average	1.046	1.045	-0.001
105	Min	1.036	1.034	-0.004
105	Std Dev	0.004	0.004	0.002



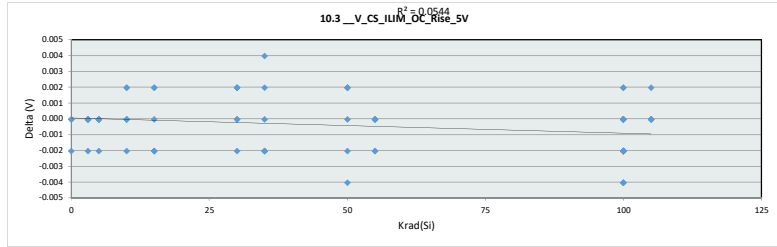
10.2 V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09 V										
Min Limit	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.040	1.044	1.044	1.044	1.044	1.042	1.042	1.042	1.036	1.034	1.036
Average	1.047	1.048	1.047	1.048	1.046	1.048	1.046	1.047	1.040	1.044	1.044
Max	1.054	1.050	1.050	1.052	1.048	1.052	1.050	1.052	1.044	1.054	1.054
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



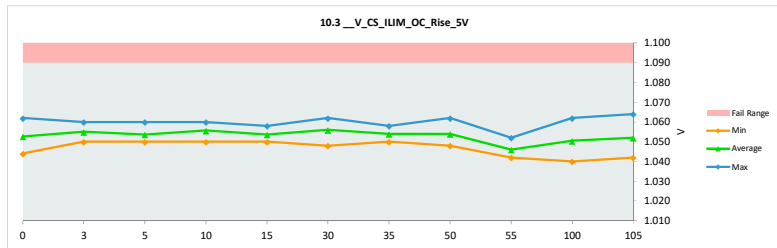
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10.3 V_CS_ILIM_OC_Rise_5V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	1.09	1.09
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.062	1.062	0.000
0	118	1.046	1.044	-0.002
0	122	1.052	1.052	0.000
3	61	1.058	1.058	0.000
3	62	1.056	1.054	-0.002
3	63	1.054	1.054	0.000
3	64	1.054	1.054	0.000
3	65	1.050	1.050	0.000
3	66	1.060	1.060	0.000
5	67	1.060	1.060	0.000
5	68	1.054	1.054	0.000
5	69	1.052	1.052	0.000
5	70	1.056	1.056	0.000
5	71	1.052	1.050	-0.002
5	72	1.050	1.050	0.000
10	73	1.058	1.060	0.002
10	74	1.056	1.054	-0.002
10	75	1.054	1.054	0.000
10	76	1.054	1.056	0.002
10	77	1.050	1.050	0.000
10	78	1.060	1.060	0.000
15	79	1.060	1.058	-0.002
15	80	1.054	1.052	-0.002
15	81	1.052	1.054	0.002
15	82	1.056	1.058	0.002
15	83	1.052	1.050	-0.002
15	84	1.050	1.050	0.000
30	86	1.058	1.060	0.002
30	89	1.056	1.056	0.000
30	91	1.054	1.054	0.000
30	92	1.054	1.056	0.002
30	94	1.050	1.048	-0.002
30	95	1.060	1.062	0.002
35	100	1.060	1.058	-0.002
35	101	1.054	1.052	-0.002
35	102	1.052	1.054	0.002
35	103	1.056	1.056	0.000
35	104	1.052	1.050	-0.002
35	105	1.050	1.054	0.004
50	106	1.060	1.062	0.002
50	107	1.054	1.054	0.000
50	108	1.054	1.042	-0.012
50	109	1.056	1.052	-0.004
50	110	1.054	1.056	0.002
50	111	1.046	1.048	0.002
55	113	1.044	1.042	-0.002
55	114	1.042	1.042	0.000
55	115	1.048	1.046	-0.002
55	116	1.048	1.048	0.000
55	126	1.052	1.052	0.000
55	128	1.046	1.046	0.000
100	129	1.054	1.052	-0.002
100	131	1.044	1.042	-0.002
100	132	1.044	1.042	-0.002
100	133	1.046	1.046	0.000
100	134	1.050	1.052	0.002
100	135	1.054	1.052	-0.002
100	138	1.060	1.058	-0.002
100	139	1.048	1.044	-0.004
100	140	1.050	1.048	-0.002
100	141	1.050	1.048	-0.002
100	142	1.044	1.040	-0.004
100	143	1.054	1.054	0.000
100	144	1.062	1.062	0.000
100	220	1.058	1.058	0.000
100	221	1.062	1.060	-0.002
100	224	1.048	1.048	0.000
100	225	1.050	1.048	-0.002
100	226	1.050	1.048	-0.002
100	228	1.058	1.054	-0.004
100	232	1.050	1.052	0.002
100	233	1.058	1.056	-0.002
100	236	1.046	1.046	0.000
100	238	1.050	1.050	0.000
100	243	1.054	1.052	-0.002
105	244	1.046	1.048	0.002
105	246	1.042	1.042	0.000
105	248	1.064	1.064	0.000
105	374	1.048	1.048	0.000
105	376	1.058	1.058	0.000
105	378	1.064	1.064	0.004
	Max	1.053	1.052	0.000
	Average	1.053	1.052	0.000
	Min	1.042	1.040	-0.004
	Std Dev	0.005	0.006	0.002



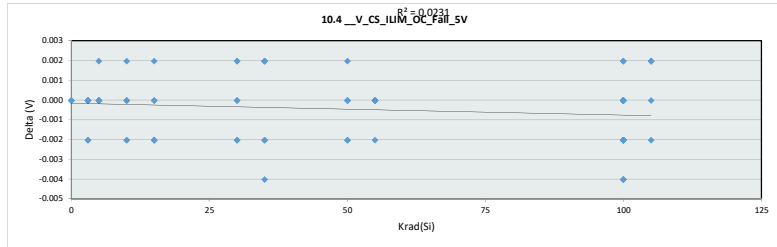
10.3 V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.050	1.050	1.050	1.050	1.048	1.050	1.048	1.042	1.040	1.042
Average	1.053	1.055	1.054	1.054	1.054	1.056	1.054	1.054	1.046	1.051	1.052
Max	1.062	1.060	1.060	1.060	1.058	1.062	1.058	1.062	1.052	1.062	1.064
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



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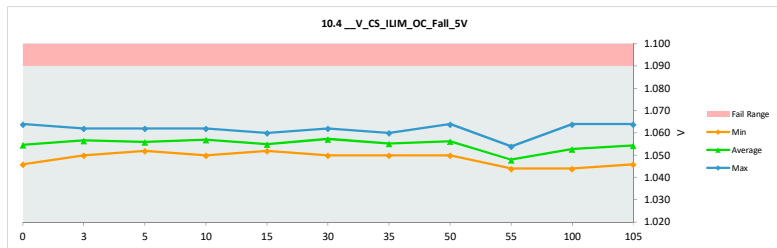
10.4 V_CS_ILIM_OC_Fail_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.064	1.064	0.000
0	118	1.046	1.046	0.000
0	122	1.054	1.054	0.000
3	61	1.060	1.060	0.000
3	62	1.058	1.056	-0.002
3	63	1.056	1.056	0.000
3	64	1.056	1.056	0.000
3	65	1.052	1.050	-0.002
3	66	1.062	1.062	0.000
5	67	1.062	1.062	0.000
5	68	1.056	1.056	0.000
5	69	1.054	1.054	0.000
5	70	1.056	1.058	0.002
5	71	1.054	1.054	0.000
5	72	1.052	1.052	0.000
10	73	1.060	1.060	0.000
10	74	1.058	1.056	-0.002
10	75	1.056	1.056	0.000
10	76	1.056	1.058	0.002
10	77	1.052	1.050	-0.002
10	78	1.062	1.062	0.000
15	79	1.062	1.060	-0.002
15	80	1.056	1.054	-0.002
15	81	1.054	1.054	0.000
15	82	1.056	1.058	0.002
15	83	1.054	1.052	-0.002
15	84	1.052	1.052	0.000
30	86	1.060	1.062	0.002
30	89	1.058	1.056	-0.002
30	91	1.056	1.056	0.000
30	92	1.056	1.058	0.002
30	94	1.052	1.050	-0.002
30	95	1.062	1.062	0.000
35	100	1.062	1.060	-0.002
35	101	1.056	1.054	-0.002
35	102	1.054	1.056	0.002
35	103	1.056	1.058	0.002
35	104	1.054	1.050	-0.004
35	105	1.052	1.054	0.002
50	106	1.064	1.064	0.000
50	107	1.058	1.056	-0.002
50	108	1.056	1.056	0.000
50	109	1.056	1.054	-0.002
50	110	1.058	1.058	0.000
50	111	1.048	1.050	0.002
55	113	1.046	1.044	-0.002
55	114	1.044	1.044	0.000
55	115	1.048	1.048	0.000
55	116	1.050	1.050	0.000
55	126	1.054	1.054	0.000
55	128	1.048	1.048	0.000
100	129	1.056	1.056	0.000
100	131	1.044	1.044	0.000
100	132	1.046	1.046	0.000
100	133	1.048	1.046	-0.002
100	134	1.054	1.054	0.000
100	135	1.056	1.054	-0.002
100	138	1.064	1.060	-0.004
100	139	1.050	1.046	-0.004
100	140	1.052	1.050	-0.002
100	141	1.052	1.050	-0.002
100	142	1.046	1.044	-0.002
100	143	1.056	1.056	0.000
100	144	1.064	1.064	0.000
100	220	1.058	1.060	0.002
100	221	1.064	1.062	-0.002
100	224	1.052	1.050	-0.002
100	225	1.050	1.052	0.002
100	226	1.052	1.050	-0.002
100	228	1.060	1.056	-0.004
100	232	1.054	1.054	0.000
100	233	1.060	1.058	-0.002
100	236	1.050	1.048	-0.002
100	238	1.052	1.052	0.000
100	243	1.056	1.056	0.000
105	244	1.048	1.050	0.002
105	246	1.044	1.046	0.002
105	248	1.066	1.064	-0.002
105	374	1.050	1.052	0.002
105	375	1.060	1.060	0.000
105	376	1.066	1.064	0.002
	Max	1.055	1.054	0.000
	Average	1.055	1.054	0.000
	Min	1.044	1.044	-0.004
	Std Dev	0.005	0.005	0.002



10.4 V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09
Min Limit	V
Krad(Si)	0
LL	0
Min	1.046
Average	1.055
Max	1.064
UL	1.090

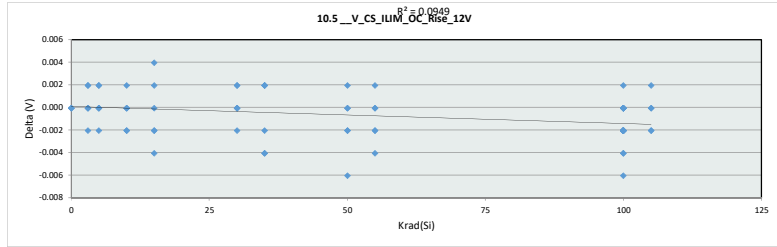
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.046	1.050	1.052	1.050	1.052	1.050	1.050	1.050	1.044	1.044	1.046
Average	1.055	1.057	1.056	1.057	1.055	1.057	1.055	1.056	1.048	1.053	1.054
Max	1.064	1.062	1.062	1.062	1.060	1.062	1.060	1.064	1.054	1.064	1.064
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



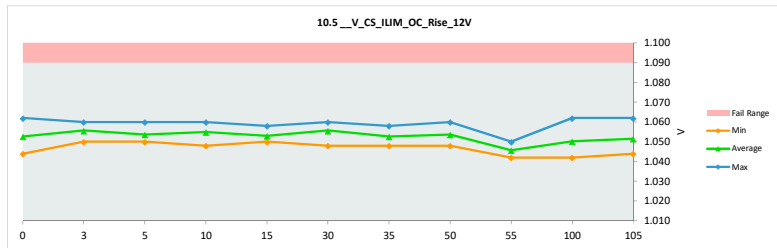
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10.5 V_CS_ILIM_OC_Rise_12V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	1.09	1.09
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.062	1.062	0.000
0	118	1.044	1.044	0.000
0	122	1.052	1.052	0.000
3	61	1.058	1.060	0.002
3	62	1.056	1.054	-0.002
3	63	1.054	1.054	0.000
3	64	1.056	1.056	0.000
3	65	1.048	1.050	0.002
3	66	1.060	1.060	0.000
5	67	1.060	1.060	0.000
5	68	1.054	1.052	-0.002
5	69	1.050	1.052	0.002
5	70	1.054	1.056	0.002
5	71	1.052	1.052	0.000
5	72	1.050	1.050	0.000
10	73	1.058	1.060	0.002
10	74	1.056	1.054	-0.002
10	75	1.054	1.054	0.000
10	76	1.056	1.054	-0.002
10	77	1.048	1.048	0.000
10	78	1.060	1.060	0.000
15	79	1.060	1.056	-0.004
15	80	1.054	1.052	-0.002
15	81	1.050	1.052	0.002
15	82	1.054	1.058	0.004
15	83	1.052	1.050	-0.002
15	84	1.050	1.050	0.000
30	86	1.058	1.060	0.002
30	89	1.056	1.054	-0.002
30	91	1.054	1.054	0.000
30	92	1.056	1.058	0.002
30	94	1.048	1.048	0.000
30	95	1.060	1.060	0.000
35	100	1.060	1.058	-0.002
35	101	1.054	1.050	-0.004
35	102	1.050	1.052	0.002
35	103	1.054	1.056	0.002
35	104	1.052	1.048	-0.004
35	105	1.050	1.052	0.002
50	106	1.062	1.060	-0.002
50	107	1.056	1.054	-0.002
50	108	1.054	1.054	0.000
50	109	1.056	1.050	-0.006
50	110	1.056	1.056	0.000
50	111	1.046	1.048	0.002
55	113	1.046	1.042	-0.004
55	114	1.040	1.042	0.002
55	115	1.048	1.046	-0.002
55	116	1.048	1.048	0.000
55	126	1.052	1.050	-0.002
55	128	1.046	1.046	0.000
100	129	1.054	1.054	0.000
100	131	1.042	1.042	0.000
100	132	1.044	1.042	-0.002
100	133	1.046	1.044	-0.002
100	134	1.052	1.050	-0.002
100	135	1.054	1.052	-0.002
100	138	1.060	1.058	-0.002
100	139	1.048	1.044	-0.004
100	140	1.050	1.048	-0.002
100	141	1.052	1.048	-0.004
100	142	1.042	1.042	0.000
100	143	1.056	1.054	-0.002
100	144	1.060	1.062	0.002
100	220	1.056	1.056	0.000
100	221	1.060	1.058	-0.002
100	224	1.048	1.046	-0.002
100	225	1.050	1.050	0.000
100	226	1.050	1.048	-0.002
100	228	1.058	1.052	-0.006
100	232	1.052	1.052	0.000
100	233	1.058	1.056	-0.002
100	236	1.048	1.046	-0.002
100	238	1.050	1.048	-0.002
100	243	1.054	1.054	0.000
105	244	1.048	1.046	-0.002
105	246	1.042	1.044	0.002
105	248	1.064	1.062	-0.002
105	374	1.048	1.048	0.000
105	376	1.058	1.058	0.000
105	375	1.064	1.062	-0.002
105	Max	1.053	1.052	-0.001
105	Average	1.053	1.052	-0.001
105	Min	1.040	1.042	-0.006
105	Std Dev	0.005	0.005	0.002



10.5 V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.050	1.050	1.048	1.050	1.048	1.048	1.048	1.042	1.042	1.044
Average	1.053	1.056	1.054	1.055	1.053	1.056	1.053	1.054	1.046	1.050	1.052
Max	1.062	1.060	1.060	1.060	1.058	1.060	1.058	1.060	1.050	1.062	1.062
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

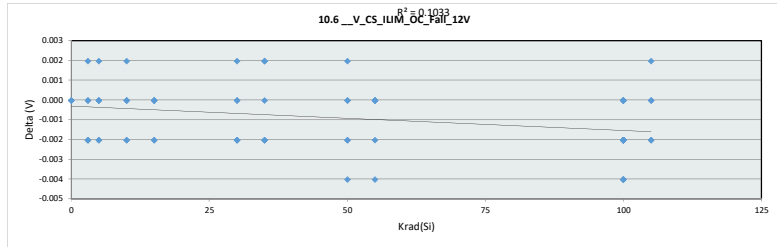




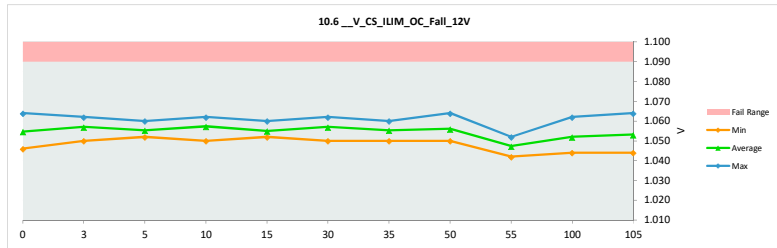
LDR TID Report  
TPS7H5001-SP

10.6 V_CS_ILIM_OC_Fall_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.064	1.064	0.000
0	118	1.046	1.046	0.000
0	122	1.054	1.054	0.000
3	61	1.060	1.062	0.002
3	62	1.058	1.056	-0.002
3	63	1.056	1.056	0.000
3	64	1.058	1.056	-0.002
3	65	1.052	1.050	-0.002
3	66	1.062	1.062	0.000
5	67	1.062	1.060	-0.002
5	68	1.056	1.054	-0.002
5	69	1.054	1.054	0.000
5	70	1.058	1.058	0.000
5	71	1.052	1.054	0.002
5	72	1.052	1.052	0.000
10	73	1.060	1.062	0.002
10	74	1.058	1.056	-0.002
10	75	1.056	1.056	0.000
10	76	1.058	1.058	0.000
10	77	1.052	1.050	-0.002
10	78	1.062	1.062	0.000
15	79	1.062	1.060	-0.002
15	80	1.056	1.054	-0.002
15	81	1.054	1.054	0.000
15	82	1.058	1.058	0.000
15	83	1.052	1.052	0.000
15	84	1.052	1.052	0.000
30	86	1.060	1.062	0.002
30	89	1.058	1.056	-0.002
30	91	1.056	1.054	-0.002
30	92	1.058	1.058	0.000
30	94	1.052	1.050	-0.002
30	95	1.062	1.062	0.000
35	100	1.062	1.060	-0.002
35	101	1.056	1.054	-0.002
35	102	1.054	1.056	0.002
35	103	1.058	1.058	0.000
35	104	1.052	1.050	-0.002
35	105	1.052	1.054	0.002
50	106	1.064	1.064	0.000
50	107	1.058	1.056	-0.002
50	108	1.056	1.054	-0.002
50	109	1.058	1.054	-0.004
50	110	1.058	1.058	0.000
50	111	1.048	1.050	0.002
55	113	1.048	1.044	-0.004
55	114	1.042	1.042	0.000
55	115	1.048	1.048	0.000
55	116	1.050	1.050	0.000
55	126	1.054	1.052	-0.002
55	128	1.048	1.048	0.000
100	129	1.056	1.054	-0.002
100	131	1.044	1.044	0.000
100	132	1.046	1.044	-0.002
100	133	1.048	1.046	-0.002
100	134	1.054	1.054	0.000
100	135	1.056	1.054	-0.002
100	138	1.062	1.060	-0.002
100	139	1.050	1.046	-0.004
100	140	1.052	1.050	-0.002
100	141	1.052	1.048	-0.004
100	142	1.046	1.044	-0.002
100	143	1.056	1.056	0.000
100	144	1.064	1.062	-0.002
100	220	1.058	1.058	0.000
100	221	1.064	1.060	-0.004
100	224	1.050	1.048	-0.002
100	225	1.052	1.050	-0.002
100	226	1.052	1.050	-0.002
100	228	1.060	1.056	-0.004
100	232	1.054	1.054	0.000
100	233	1.060	1.058	-0.002
100	236	1.048	1.046	-0.002
100	238	1.052	1.052	0.000
100	243	1.056	1.054	-0.002
105	244	1.048	1.048	0.000
105	246	1.044	1.044	0.000
105	248	1.066	1.064	-0.002
105	374	1.050	1.052	0.002
105	376	1.066	1.058	-0.002
105	Max	1.066	1.064	0.002
105	Average	1.055	1.054	-0.001
105	Min	1.042	1.042	-0.004
105	Std Dev	0.005	0.005	0.002



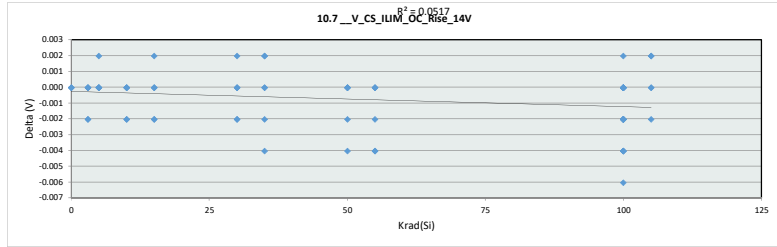
10.6 V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09
Min Limit	V
Krad(Si)	0
LL	0
Min	1.046
Average	1.055
Max	1.064
UL	1.090



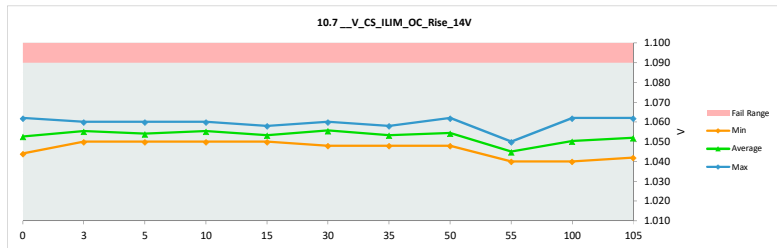
LDR TID Report  
TPS7H5001-SP

10.7_V_CS_ILIM_OC_Rise_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.062	1.062	0.000
0	118	1.044	1.044	0.000
0	122	1.052	1.052	0.000
3	61	1.060	1.060	0.000
3	62	1.056	1.054	-0.002
3	63	1.054	1.054	0.000
3	64	1.056	1.054	-0.002
3	65	1.050	1.050	0.000
3	66	1.060	1.060	0.000
5	67	1.060	1.060	0.000
5	68	1.052	1.054	0.002
5	69	1.052	1.052	0.000
5	70	1.056	1.056	0.000
5	71	1.052	1.052	0.000
5	72	1.050	1.050	0.000
10	73	1.060	1.060	0.000
10	74	1.056	1.054	-0.002
10	75	1.054	1.054	0.000
10	76	1.056	1.054	-0.002
10	77	1.050	1.050	0.000
10	78	1.060	1.060	0.000
15	79	1.060	1.058	-0.002
15	80	1.052	1.052	0.000
15	81	1.052	1.052	0.000
15	82	1.056	1.056	0.000
15	83	1.052	1.050	-0.002
15	84	1.050	1.052	0.002
30	86	1.060	1.060	0.000
30	89	1.056	1.054	-0.002
30	91	1.054	1.054	0.000
30	92	1.056	1.058	0.002
30	94	1.050	1.048	-0.002
30	95	1.060	1.060	0.000
35	100	1.060	1.058	-0.002
35	101	1.052	1.052	0.000
35	102	1.052	1.054	0.002
35	103	1.056	1.056	0.000
35	104	1.052	1.048	-0.004
35	105	1.050	1.052	0.002
50	106	1.062	1.062	0.000
50	107	1.056	1.054	-0.002
50	108	1.054	1.054	0.000
50	109	1.056	1.052	-0.004
50	110	1.056	1.056	0.000
50	111	1.048	1.048	0.000
55	113	1.046	1.042	-0.004
55	114	1.040	1.040	0.000
55	115	1.048	1.044	-0.004
55	116	1.048	1.048	0.000
55	126	1.052	1.050	-0.002
55	128	1.046	1.046	0.000
100	129	1.054	1.052	-0.002
100	131	1.042	1.042	0.000
100	132	1.044	1.044	0.000
100	133	1.044	1.044	0.000
100	134	1.052	1.050	-0.002
100	135	1.054	1.052	-0.002
100	138	1.060	1.058	-0.002
100	139	1.048	1.044	-0.004
100	140	1.048	1.048	0.000
100	141	1.052	1.046	-0.006
100	142	1.044	1.040	-0.004
100	143	1.054	1.054	0.000
100	144	1.062	1.062	0.000
100	220	1.056	1.056	0.000
100	221	1.062	1.060	-0.002
100	224	1.048	1.048	0.000
100	225	1.050	1.050	0.000
100	226	1.050	1.048	-0.002
100	228	1.058	1.054	-0.004
100	232	1.052	1.052	0.000
100	233	1.058	1.056	-0.002
100	236	1.048	1.044	-0.004
100	238	1.048	1.050	0.002
100	243	1.056	1.054	-0.002
105	244	1.046	1.048	0.002
105	246	1.042	1.042	0.000
105	248	1.064	1.062	-0.002
105	374	1.048	1.050	0.002
105	376	1.058	1.058	0.000
105	Max	1.064	1.062	0.002
105	Average	1.053	1.052	-0.001
105	Min	1.040	1.040	-0.006
105	Std Dev	0.005	0.006	0.002



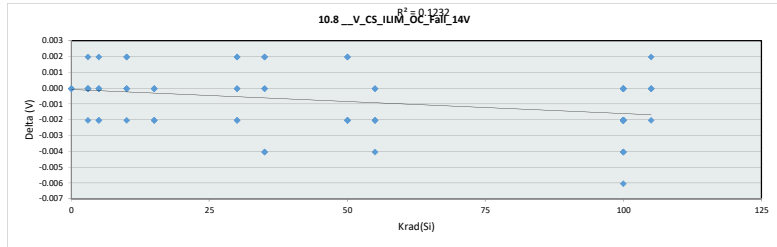
10.7_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09 V										
Min Limit	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.050	1.050	1.050	1.050	1.048	1.048	1.048	1.040	1.040	1.042
Average	1.053	1.055	1.054	1.054	1.053	1.056	1.053	1.054	1.045	1.050	1.052
Max	1.062	1.060	1.060	1.060	1.058	1.060	1.058	1.062	1.050	1.062	1.062
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



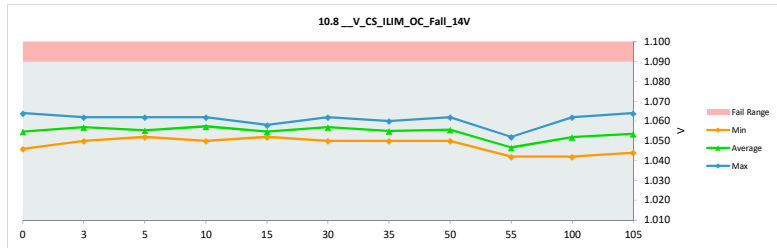
LDR TID Report  
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10.8 V_CS_ILIM_OC_Fall_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.064	1.064	0.000
0	118	1.046	1.046	0.000
0	122	1.054	1.054	0.000
3	61	1.060	1.062	0.002
3	62	1.056	1.056	0.000
3	63	1.056	1.056	0.000
3	64	1.056	1.056	0.000
3	65	1.052	1.050	-0.002
3	66	1.062	1.062	0.000
5	67	1.060	1.062	0.002
5	68	1.056	1.054	-0.002
5	69	1.054	1.054	0.000
5	70	1.058	1.058	0.000
5	71	1.054	1.052	-0.002
5	72	1.052	1.052	0.000
10	73	1.060	1.062	0.002
10	74	1.056	1.056	0.000
10	75	1.056	1.056	0.000
10	76	1.056	1.058	0.002
10	77	1.052	1.050	-0.002
10	78	1.062	1.062	0.000
15	79	1.060	1.058	-0.002
15	80	1.056	1.054	-0.002
15	81	1.054	1.054	0.000
15	82	1.058	1.058	0.000
15	83	1.054	1.052	-0.002
15	84	1.052	1.052	0.000
30	86	1.060	1.062	0.002
30	89	1.056	1.056	0.000
30	91	1.056	1.054	-0.002
30	92	1.056	1.058	0.002
30	94	1.052	1.050	-0.002
30	95	1.062	1.062	0.000
35	100	1.060	1.060	0.000
35	101	1.056	1.052	-0.004
35	102	1.054	1.056	0.002
35	103	1.058	1.058	0.000
35	104	1.054	1.050	-0.004
35	105	1.052	1.054	0.002
50	106	1.064	1.062	-0.002
50	107	1.058	1.056	-0.002
50	108	1.056	1.054	-0.002
50	109	1.056	1.054	-0.002
50	110	1.056	1.058	0.002
50	111	1.048	1.050	0.002
55	113	1.048	1.044	-0.004
55	114	1.042	1.042	0.000
55	115	1.048	1.046	-0.002
55	116	1.050	1.048	-0.002
55	126	1.054	1.052	-0.002
55	128	1.048	1.048	0.000
100	129	1.056	1.054	-0.002
100	131	1.044	1.044	0.000
100	132	1.046	1.044	-0.002
100	133	1.048	1.046	-0.002
100	134	1.054	1.054	0.000
100	135	1.056	1.054	-0.002
100	138	1.062	1.060	-0.002
100	139	1.050	1.046	-0.004
100	140	1.052	1.050	-0.002
100	141	1.054	1.048	-0.006
100	142	1.044	1.042	-0.002
100	143	1.058	1.056	-0.002
100	144	1.064	1.062	-0.002
100	220	1.058	1.058	0.000
100	221	1.064	1.062	-0.002
100	224	1.050	1.048	-0.002
100	225	1.052	1.050	-0.002
100	226	1.052	1.048	-0.004
100	228	1.060	1.056	-0.004
100	232	1.054	1.054	0.000
100	233	1.060	1.058	-0.002
100	236	1.048	1.046	-0.002
100	238	1.052	1.052	0.000
100	243	1.056	1.056	0.000
105	244	1.048	1.048	0.000
105	246	1.044	1.044	0.000
105	248	1.066	1.064	-0.002
105	374	1.050	1.052	0.002
105	375	1.060	1.060	0.000
105	Max	1.066	1.064	0.002
105	Average	1.055	1.054	-0.001
105	Min	1.042	1.042	-0.006
105	Std Dev	0.005	0.006	0.002



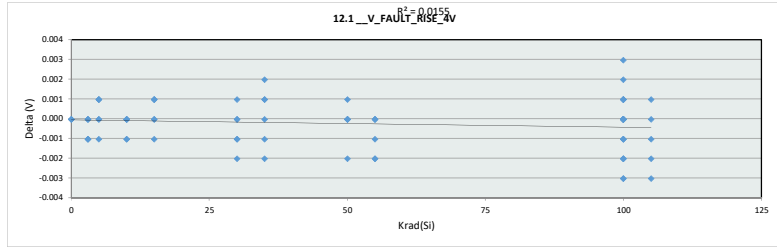
10.8 V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09
Min Limit	V
Krad(Si)	0
LL	0
Min	1.046
Average	1.055
Max	1.064
UL	1.090



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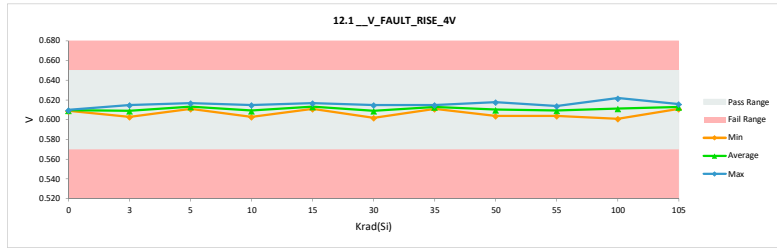
12.1_V_FAULT_RISE_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.609	0.609	0.000
0	118	0.610	0.610	0.000
0	122	0.610	0.610	0.000
3	61	0.605	0.604	-0.001
3	62	0.614	0.614	0.000
3	63	0.603	0.603	0.000
3	64	0.615	0.614	-0.001
3	65	0.606	0.605	-0.001
3	66	0.615	0.615	0.000
5	67	0.616	0.615	-0.001
5	68	0.610	0.611	0.001
5	69	0.610	0.611	0.001
5	70	0.616	0.617	0.001
5	71	0.611	0.611	0.000
5	72	0.615	0.615	0.000
10	73	0.605	0.605	0.000
10	74	0.614	0.614	0.000
10	75	0.603	0.603	0.000
10	76	0.615	0.614	-0.001
10	77	0.606	0.605	-0.001
10	78	0.615	0.615	0.000
15	79	0.616	0.615	-0.001
15	80	0.610	0.611	0.001
15	81	0.610	0.611	0.001
15	82	0.616	0.617	0.001
15	83	0.611	0.611	0.000
15	84	0.615	0.615	0.000
30	86	0.605	0.604	-0.001
30	89	0.614	0.615	0.001
30	91	0.603	0.602	-0.001
30	92	0.615	0.613	-0.002
30	94	0.606	0.606	0.000
30	95	0.615	0.615	0.000
35	100	0.616	0.614	-0.002
35	101	0.610	0.612	0.002
35	102	0.610	0.611	0.001
35	103	0.616	0.615	-0.001
35	104	0.611	0.612	0.001
35	105	0.615	0.615	0.000
50	106	0.614	0.614	0.000
50	107	0.612	0.613	0.001
50	108	0.606	0.606	0.000
50	109	0.618	0.618	0.000
50	110	0.606	0.604	-0.002
50	111	0.607	0.607	0.000
55	113	0.606	0.606	0.000
55	114	0.606	0.604	-0.002
55	115	0.612	0.611	-0.001
55	116	0.616	0.614	-0.002
55	126	0.613	0.613	0.000
55	128	0.609	0.609	0.000
100	129	0.610	0.611	0.001
100	131	0.604	0.603	-0.001
100	132	0.606	0.606	0.000
100	133	0.611	0.611	0.000
100	134	0.609	0.606	-0.003
100	135	0.614	0.613	-0.001
100	138	0.611	0.611	0.000
100	139	0.603	0.601	-0.002
100	140	0.607	0.608	0.001
100	141	0.617	0.614	-0.003
100	142	0.616	0.616	0.000
100	143	0.609	0.609	0.000
100	144	0.616	0.614	-0.002
100	220	0.613	0.613	0.000
100	221	0.616	0.616	0.000
100	224	0.602	0.605	0.003
100	225	0.610	0.610	0.000
100	226	0.611	0.610	-0.001
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.614	0.615	0.001
100	238	0.612	0.610	-0.002
100	243	0.617	0.617	0.000
105	244	0.612	0.611	-0.001
105	246	0.610	0.611	0.001
105	248	0.617	0.615	-0.002
105	374	0.613	0.613	0.000
105	376	0.619	0.616	-0.003
105	Max	0.622	0.622	0.003
105	Average	0.611	0.611	0.000
105	Min	0.602	0.601	-0.003
105	Std Dev	0.005	0.005	0.001



12.1_V_FAULT_RISE_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

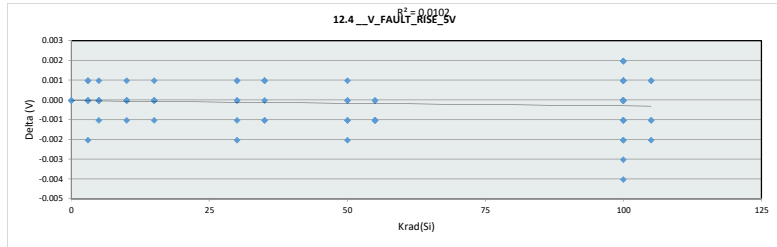
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.609	0.603	0.611	0.603	0.611	0.602	0.611	0.604	0.604	0.601	0.611
Average	0.610	0.609	0.613	0.609	0.613	0.609	0.613	0.610	0.610	0.612	0.613
Max	0.610	0.615	0.617	0.615	0.617	0.615	0.615	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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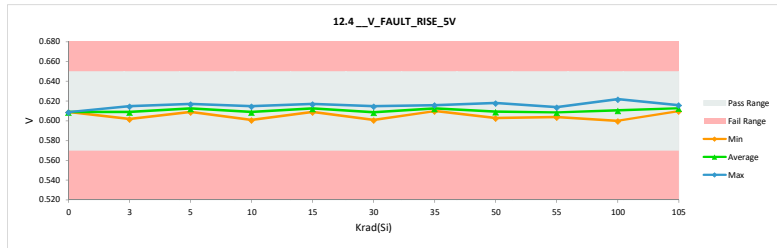
12.4_V_FAULT_RISE_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.609	0.609	0.000
0	118	0.609	0.609	0.000
0	122	0.609	0.609	0.000
3	61	0.606	0.604	-0.002
3	62	0.613	0.613	0.000
3	63	0.601	0.602	0.001
3	64	0.615	0.615	0.000
3	65	0.605	0.605	0.000
3	66	0.614	0.615	0.001
5	67	0.616	0.615	-0.001
5	68	0.609	0.609	0.000
5	69	0.609	0.610	0.001
5	70	0.617	0.617	0.000
5	71	0.610	0.610	0.000
5	72	0.614	0.614	-0.000
10	73	0.606	0.605	-0.001
10	74	0.613	0.613	0.000
10	75	0.601	0.601	0.000
10	76	0.615	0.615	0.000
10	77	0.605	0.605	0.000
10	78	0.614	0.615	0.001
15	79	0.616	0.616	0.000
15	80	0.609	0.609	0.000
15	81	0.609	0.610	0.001
15	82	0.617	0.617	0.000
15	83	0.610	0.610	0.000
15	84	0.614	0.613	-0.001
30	86	0.606	0.604	-0.002
30	89	0.613	0.614	0.001
30	91	0.601	0.601	0.000
30	92	0.615	0.614	-0.001
30	94	0.605	0.605	0.000
30	95	0.614	0.615	0.001
35	100	0.616	0.615	-0.001
35	101	0.609	0.610	0.001
35	102	0.609	0.610	0.001
35	103	0.617	0.616	-0.001
35	104	0.610	0.611	0.001
35	105	0.614	0.614	0.000
50	106	0.615	0.614	-0.001
50	107	0.612	0.613	0.001
50	108	0.604	0.604	0.000
50	109	0.619	0.618	-0.001
50	110	0.605	0.603	-0.002
50	111	0.605	0.605	0.000
55	113	0.605	0.604	-0.001
55	114	0.605	0.604	-0.001
55	115	0.611	0.610	-0.001
55	116	0.615	0.614	-0.001
55	126	0.612	0.612	0.000
55	128	0.608	0.608	0.000
100	129	0.609	0.610	0.001
100	131	0.601	0.600	-0.001
100	132	0.604	0.604	0.000
100	133	0.611	0.611	0.000
100	134	0.608	0.605	-0.003
100	135	0.613	0.612	-0.001
100	138	0.610	0.610	0.000
100	139	0.602	0.600	-0.002
100	140	0.605	0.606	0.001
100	141	0.617	0.613	-0.004
100	142	0.614	0.614	0.000
100	143	0.608	0.608	0.000
100	144	0.615	0.613	-0.002
100	220	0.613	0.614	0.001
100	221	0.615	0.615	0.000
100	224	0.601	0.603	0.002
100	225	0.610	0.609	-0.001
100	226	0.610	0.610	0.000
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.612	0.614	0.002
100	238	0.611	0.610	-0.001
100	243	0.616	0.616	0.000
105	244	0.611	0.610	-0.001
105	246	0.609	0.610	0.001
105	248	0.616	0.615	-0.001
105	374	0.612	0.613	0.001
105	376	0.618	0.616	-0.002
105	Max	0.622	0.622	0.000
105	Average	0.611	0.611	0.000
105	Min	0.601	0.600	-0.004
105	Std Dev	0.005	0.005	0.001



12.4_V_FAULT_R	
Test Site	
Tester	
Test Number	
Max Limit	0.65 V
Min Limit	0.57 V

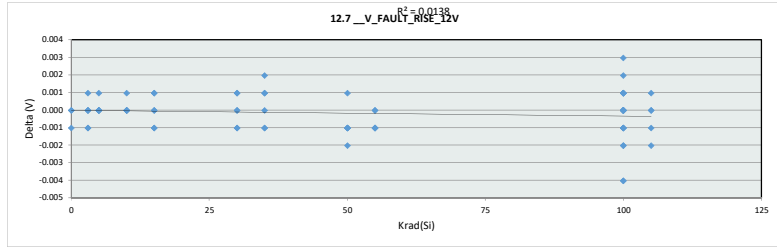
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.609	0.602	0.609	0.601	0.609	0.601	0.610	0.603	0.604	0.600	0.610
Average	0.609	0.609	0.613	0.609	0.613	0.609	0.613	0.610	0.609	0.611	0.613
Max	0.609	0.615	0.617	0.615	0.617	0.615	0.616	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



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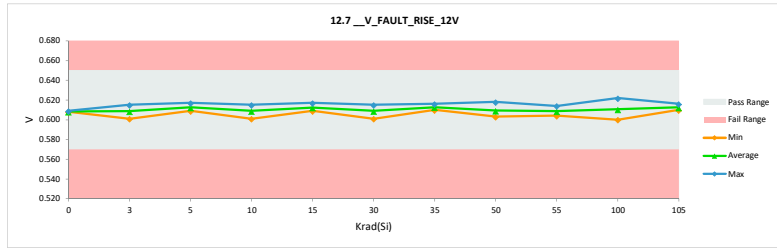
12.7_V_FAULT_RISE_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.609	0.609	0.000
0	118	0.609	0.608	-0.001
0	122	0.608	0.608	0.000
3	61	0.605	0.604	-0.001
3	62	0.613	0.613	0.000
3	63	0.601	0.601	0.000
3	64	0.615	0.615	0.000
3	65	0.605	0.604	-0.001
3	66	0.614	0.615	0.001
5	67	0.616	0.616	0.000
5	68	0.608	0.609	0.001
5	69	0.609	0.609	0.000
5	70	0.617	0.617	0.000
5	71	0.610	0.610	0.000
5	72	0.614	0.614	0.000
10	73	0.605	0.605	0.000
10	74	0.613	0.613	0.000
10	75	0.601	0.601	0.000
10	76	0.615	0.615	0.000
10	77	0.605	0.605	0.000
10	78	0.614	0.615	0.001
15	79	0.616	0.615	-0.001
15	80	0.608	0.609	0.001
15	81	0.609	0.610	0.001
15	82	0.617	0.617	0.000
15	83	0.610	0.610	0.000
15	84	0.614	0.613	-0.001
30	86	0.605	0.604	-0.001
30	89	0.613	0.614	0.001
30	91	0.601	0.601	0.000
30	92	0.615	0.614	-0.001
30	94	0.605	0.605	0.000
30	95	0.614	0.615	0.001
35	100	0.616	0.615	-0.001
35	101	0.608	0.610	0.002
35	102	0.609	0.610	0.001
35	103	0.617	0.616	-0.001
35	104	0.610	0.611	0.001
35	105	0.614	0.614	0.000
50	106	0.615	0.614	-0.001
50	107	0.612	0.613	0.001
50	108	0.604	0.603	-0.001
50	109	0.619	0.618	-0.001
50	110	0.605	0.603	-0.002
50	111	0.605	0.604	-0.001
55	113	0.605	0.604	-0.001
55	114	0.605	0.604	-0.001
55	115	0.611	0.610	-0.001
55	116	0.615	0.614	-0.001
55	126	0.612	0.612	0.000
55	128	0.608	0.608	0.000
100	129	0.609	0.610	0.001
100	131	0.601	0.600	-0.001
100	132	0.604	0.604	0.000
100	133	0.611	0.611	0.000
100	134	0.608	0.604	-0.004
100	135	0.613	0.612	-0.001
100	138	0.610	0.609	-0.001
100	139	0.601	0.600	-0.001
100	140	0.605	0.606	0.001
100	141	0.617	0.613	-0.004
100	142	0.614	0.614	0.000
100	143	0.608	0.608	0.000
100	144	0.615	0.613	-0.002
100	220	0.614	0.615	0.001
100	221	0.615	0.615	0.000
100	224	0.600	0.603	0.003
100	225	0.609	0.609	0.000
100	226	0.610	0.610	0.000
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.612	0.613	0.001
100	238	0.611	0.609	-0.002
100	243	0.616	0.616	0.000
105	244	0.610	0.610	0.000
105	246	0.609	0.610	0.001
105	248	0.616	0.615	-0.001
105	374	0.612	0.612	0.000
105	376	0.618	0.616	-0.002
105	Max	0.622	0.622	0.003
105	Average	0.611	0.610	0.000
105	Min	0.600	0.600	-0.004
105	Std Dev	0.005	0.005	0.001



12.7_V_FAULT_R	
Test Site	
Tester	
Test Number	
Max Limit	0.65 V
Min Limit	0.57 V

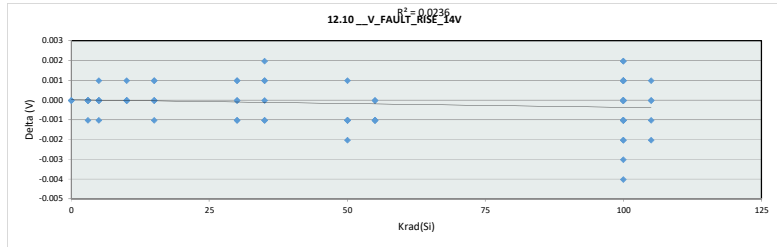
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.608	0.601	0.609	0.601	0.609	0.601	0.610	0.603	0.604	0.600	0.610
Average	0.608	0.609	0.613	0.609	0.612	0.609	0.613	0.609	0.609	0.611	0.613
Max	0.609	0.615	0.617	0.615	0.617	0.615	0.616	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



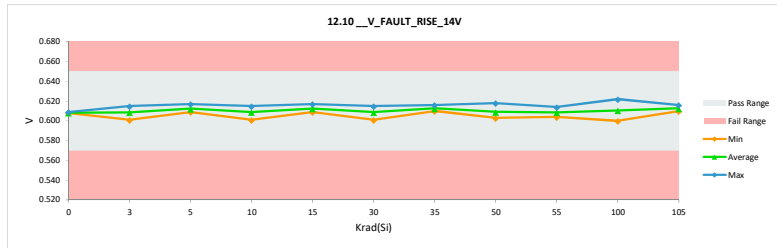
LDR TID Report  
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12.10_V_FAULT_RISE_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.609	0.609	0.000
0	118	0.608	0.608	0.000
0	122	0.608	0.608	0.000
3	61	0.605	0.604	-0.001
3	62	0.613	0.613	0.000
3	63	0.601	0.601	0.000
3	64	0.615	0.615	0.000
3	65	0.605	0.605	0.000
3	66	0.614	0.614	0.000
5	67	0.616	0.615	-0.001
5	68	0.608	0.609	0.001
5	69	0.609	0.609	0.000
5	70	0.617	0.617	0.000
5	71	0.610	0.610	0.000
5	72	0.614	0.614	0.000
10	73	0.605	0.605	0.000
10	74	0.613	0.613	0.000
10	75	0.601	0.601	0.000
10	76	0.615	0.615	0.000
10	77	0.605	0.605	0.000
10	78	0.614	0.615	0.001
15	79	0.616	0.616	0.000
15	80	0.608	0.609	0.001
15	81	0.609	0.610	0.001
15	82	0.617	0.617	0.000
15	83	0.610	0.610	0.000
15	84	0.614	0.613	-0.001
30	86	0.605	0.604	-0.001
30	89	0.613	0.614	0.001
30	91	0.601	0.601	0.000
30	92	0.615	0.614	-0.001
30	94	0.605	0.605	0.000
30	95	0.614	0.615	0.001
35	100	0.616	0.615	-0.001
35	101	0.608	0.610	0.002
35	102	0.609	0.610	0.001
35	103	0.617	0.616	-0.001
35	104	0.610	0.611	0.001
35	105	0.614	0.614	0.000
50	106	0.615	0.614	-0.001
50	107	0.612	0.613	0.001
50	108	0.604	0.603	-0.001
50	109	0.619	0.618	-0.001
50	110	0.605	0.603	-0.002
50	111	0.605	0.604	-0.001
55	113	0.605	0.604	-0.001
55	114	0.605	0.604	-0.001
55	115	0.611	0.610	-0.001
55	116	0.615	0.614	-0.001
55	126	0.612	0.612	0.000
55	128	0.608	0.608	0.000
100	129	0.609	0.610	0.001
100	131	0.601	0.600	-0.001
100	132	0.604	0.604	0.000
100	133	0.611	0.611	0.000
100	134	0.608	0.604	-0.004
100	135	0.613	0.612	-0.001
100	138	0.610	0.609	-0.001
100	139	0.602	0.600	-0.002
100	140	0.605	0.606	0.001
100	141	0.617	0.614	-0.003
100	142	0.614	0.614	0.000
100	143	0.608	0.608	0.000
100	144	0.615	0.613	-0.002
100	220	0.614	0.615	0.001
100	221	0.615	0.615	0.000
100	224	0.601	0.603	0.002
100	225	0.609	0.608	-0.001
100	226	0.610	0.609	-0.001
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.612	0.613	0.001
100	238	0.610	0.610	0.000
100	243	0.616	0.616	0.000
105	244	0.610	0.610	0.000
105	246	0.609	0.610	0.001
105	248	0.616	0.615	-0.001
105	374	0.612	0.612	0.000
105	376	0.618	0.616	-0.002
105	Max	0.622	0.622	0.000
105	Average	0.611	0.610	0.000
105	Min	0.601	0.600	-0.004
105	Std Dev	0.005	0.005	0.001



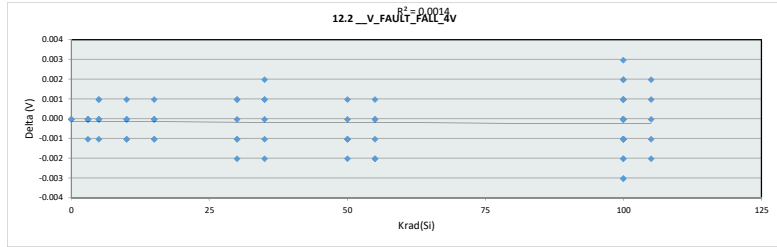
12.10_V_FAULT											
Test Site											
Tester											
Test Number											
Max Limit	0.65 V										
Min Limit	0.57 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.608	0.601	0.609	0.601	0.609	0.601	0.610	0.603	0.604	0.600	0.610
Average	0.608	0.609	0.612	0.609	0.613	0.609	0.613	0.609	0.609	0.611	0.613
Max	0.609	0.615	0.617	0.615	0.617	0.615	0.616	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



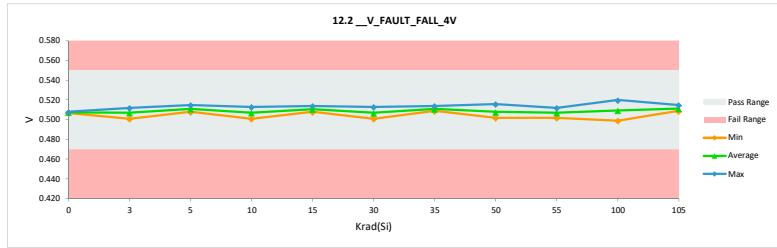
LDR TID Report  
TPS7H5001-SP

12.2_V_FAULT_FALL_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.507	0.507	0.000
0	118	0.508	0.508	0.000
0	122	0.508	0.508	0.000
3	61	0.503	0.503	0.000
3	62	0.512	0.512	0.000
3	63	0.501	0.501	0.000
3	64	0.512	0.512	0.000
3	65	0.504	0.503	-0.001
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.508	0.509	0.001
5	69	0.508	0.508	0.000
5	70	0.514	0.515	0.001
5	71	0.509	0.509	0.000
5	72	0.513	0.513	0.000
10	73	0.503	0.503	0.000
10	74	0.512	0.511	-0.001
10	75	0.501	0.501	0.000
10	76	0.512	0.512	0.000
10	77	0.504	0.503	-0.001
10	78	0.512	0.513	0.001
15	79	0.514	0.513	-0.001
15	80	0.508	0.509	0.001
15	81	0.508	0.508	0.000
15	82	0.514	0.514	0.000
15	83	0.509	0.509	0.000
15	84	0.513	0.512	-0.001
30	86	0.503	0.501	-0.002
30	89	0.512	0.513	0.001
30	91	0.501	0.501	0.000
30	92	0.512	0.511	-0.001
30	94	0.504	0.503	-0.001
30	95	0.512	0.513	0.001
35	100	0.514	0.512	-0.002
35	101	0.508	0.510	0.002
35	102	0.508	0.509	0.001
35	103	0.514	0.514	0.000
35	104	0.509	0.510	0.001
35	105	0.513	0.512	-0.001
50	106	0.512	0.512	0.000
50	107	0.510	0.511	0.001
50	108	0.504	0.503	-0.001
50	109	0.517	0.516	-0.001
50	110	0.504	0.502	-0.002
50	111	0.505	0.504	-0.001
55	113	0.504	0.503	-0.001
55	114	0.504	0.502	-0.002
55	115	0.509	0.509	0.000
55	116	0.514	0.512	-0.002
55	126	0.511	0.511	0.000
55	128	0.506	0.507	0.001
100	129	0.507	0.509	0.002
100	131	0.502	0.501	-0.001
100	132	0.504	0.504	0.000
100	133	0.509	0.509	0.000
100	134	0.507	0.504	-0.003
100	135	0.512	0.511	-0.001
100	138	0.510	0.509	-0.001
100	139	0.501	0.499	-0.002
100	140	0.504	0.505	0.001
100	141	0.515	0.512	-0.003
100	142	0.514	0.513	-0.001
100	143	0.507	0.507	0.000
100	144	0.514	0.512	-0.002
100	220	0.511	0.512	0.001
100	221	0.514	0.514	0.000
100	224	0.500	0.503	0.003
100	225	0.508	0.507	-0.001
100	226	0.508	0.508	0.000
100	228	0.512	0.513	0.001
100	232	0.517	0.519	0.002
100	233	0.520	0.520	0.000
100	236	0.512	0.513	0.001
100	238	0.510	0.509	-0.001
100	243	0.515	0.515	0.000
105	244	0.509	0.509	0.000
105	246	0.507	0.509	0.002
105	248	0.514	0.513	-0.001
105	374	0.510	0.511	0.001
105	376	0.517	0.515	-0.002
105	Max	0.520	0.520	0.003
105	Average	0.509	0.509	0.000
105	Min	0.500	0.499	-0.003
105	Std Dev	0.005	0.005	0.001



12.2_V_FAULT_F											
Test Site											
Tester											
Test Number											
Max Limit	0.55 V										
Min Limit	0.47 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.507	0.501	0.508	0.501	0.508	0.501	0.509	0.502	0.502	0.499	0.509
Average	0.508	0.507	0.511	0.507	0.511	0.507	0.511	0.508	0.507	0.510	0.511
Max	0.508	0.512	0.515	0.513	0.514	0.513	0.514	0.516	0.512	0.520	0.515
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

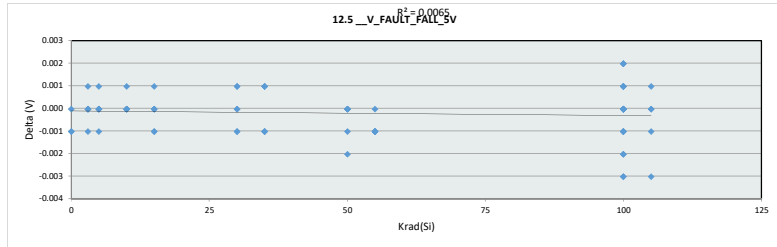




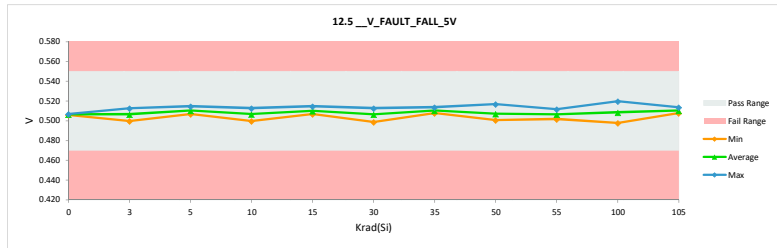
LDR TID Report  
TPS7H5001-SP

12.5_V_FAULT_FALL_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.508	0.507	-0.001
0	118	0.507	0.506	-0.001
0	122	0.507	0.507	0.000
3	61	0.503	0.502	-0.001
3	62	0.511	0.511	0.000
3	63	0.499	0.500	0.001
3	64	0.513	0.513	0.000
3	65	0.503	0.503	0.000
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.507	0.507	0.000
5	69	0.507	0.508	0.001
5	70	0.515	0.515	0.000
5	71	0.508	0.508	0.000
5	72	0.512	0.512	0.000
10	73	0.503	0.503	0.000
10	74	0.511	0.511	0.000
10	75	0.499	0.500	0.001
10	76	0.513	0.513	0.000
10	77	0.503	0.503	0.000
10	78	0.512	0.512	0.000
15	79	0.514	0.513	-0.001
15	80	0.507	0.507	0.000
15	81	0.507	0.507	0.000
15	82	0.515	0.515	0.000
15	83	0.508	0.509	0.001
15	84	0.512	0.511	-0.001
30	86	0.503	0.502	-0.001
30	89	0.511	0.512	0.001
30	91	0.499	0.499	0.000
30	92	0.513	0.512	-0.001
30	94	0.503	0.503	0.000
30	95	0.512	0.513	0.001
35	100	0.514	0.513	-0.001
35	101	0.507	0.508	0.001
35	102	0.507	0.508	0.001
35	103	0.515	0.514	-0.001
35	104	0.508	0.509	0.001
35	105	0.512	0.511	-0.001
50	106	0.512	0.512	0.000
50	107	0.511	0.511	0.000
50	108	0.502	0.502	0.000
50	109	0.517	0.517	0.000
50	110	0.503	0.501	-0.002
50	111	0.503	0.502	-0.001
55	113	0.503	0.502	-0.001
55	114	0.503	0.502	-0.001
55	115	0.509	0.508	-0.001
55	116	0.513	0.512	-0.001
55	126	0.511	0.510	-0.001
55	128	0.506	0.506	0.000
100	129	0.508	0.509	0.001
100	131	0.499	0.498	-0.001
100	132	0.502	0.502	0.000
100	133	0.509	0.509	0.000
100	134	0.506	0.503	-0.003
100	135	0.511	0.510	-0.001
100	138	0.508	0.508	0.000
100	139	0.500	0.498	-0.002
100	140	0.503	0.504	0.001
100	141	0.515	0.512	-0.003
100	142	0.512	0.512	0.000
100	143	0.507	0.506	-0.001
100	144	0.513	0.511	-0.002
100	220	0.512	0.513	0.001
100	221	0.513	0.513	0.000
100	224	0.499	0.501	0.002
100	225	0.507	0.507	0.000
100	226	0.508	0.508	0.000
100	228	0.512	0.514	0.002
100	232	0.517	0.519	0.002
100	233	0.520	0.520	0.000
100	236	0.510	0.511	0.001
100	238	0.509	0.508	-0.001
100	243	0.514	0.514	0.000
105	244	0.509	0.508	-0.001
105	246	0.507	0.508	0.001
105	248	0.513	0.513	0.000
105	374	0.510	0.510	0.000
105	376	0.517	0.514	-0.003
105	Max	0.520	0.520	0.002
105	Average	0.509	0.509	0.000
105	Min	0.499	0.498	-0.003
105	Std Dev	0.005	0.005	0.001



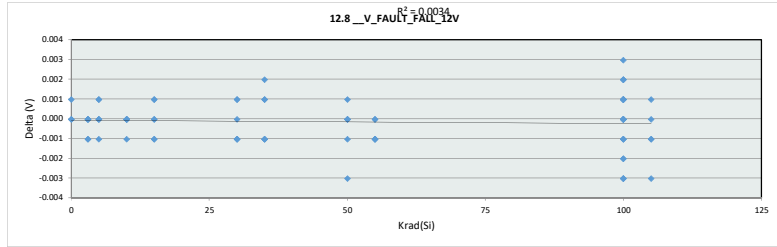
12.5_V_FAULT_F											
Test Site											
Tester											
Test Number											
Max Limit	0.55 V										
Min Limit	0.47 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.506	0.500	0.507	0.500	0.507	0.499	0.508	0.501	0.502	0.498	0.508
Average	0.507	0.507	0.511	0.507	0.510	0.507	0.511	0.508	0.507	0.509	0.511
Max	0.507	0.513	0.515	0.513	0.515	0.513	0.514	0.517	0.512	0.520	0.514
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



LDR TID Report  
TPS7H5001-SP

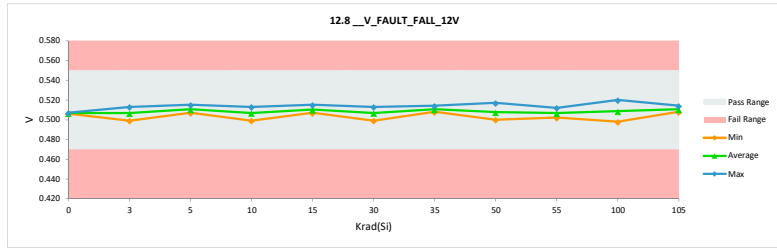
12.8_V_FAULT_FALL_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.507	0.507	0.000
0	118	0.506	0.506	0.000
0	122	0.506	0.507	0.001
3	61	0.503	0.502	-0.001
3	62	0.511	0.511	0.000
3	63	0.500	0.499	-0.001
3	64	0.513	0.513	0.000
3	65	0.503	0.503	0.000
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.506	0.507	0.001
5	69	0.507	0.508	0.001
5	70	0.515	0.515	0.000
5	71	0.508	0.508	0.000
5	72	0.512	0.512	0.000
10	73	0.503	0.503	0.000
10	74	0.511	0.511	0.000
10	75	0.500	0.499	-0.001
10	76	0.513	0.513	0.000
10	77	0.503	0.503	0.000
10	78	0.512	0.512	0.000
15	79	0.514	0.513	-0.001
15	80	0.506	0.507	0.001
15	81	0.507	0.508	0.001
15	82	0.515	0.515	0.000
15	83	0.508	0.508	0.000
15	84	0.512	0.511	-0.001
30	86	0.503	0.502	-0.001
30	89	0.511	0.512	0.001
30	91	0.500	0.499	-0.001
30	92	0.513	0.512	-0.001
30	94	0.503	0.503	0.000
30	95	0.512	0.513	0.001
35	100	0.514	0.513	-0.001
35	101	0.506	0.508	0.002
35	102	0.507	0.508	0.001
35	103	0.515	0.514	-0.001
35	104	0.508	0.509	0.001
35	105	0.512	0.511	-0.001
50	106	0.512	0.512	0.000
50	107	0.511	0.512	0.001
50	108	0.502	0.501	-0.001
50	109	0.517	0.517	0.000
50	110	0.503	0.500	-0.003
50	111	0.503	0.503	0.000
55	113	0.503	0.502	-0.001
55	114	0.503	0.502	-0.001
55	115	0.509	0.508	-0.001
55	116	0.513	0.512	-0.001
55	126	0.510	0.510	0.000
55	128	0.506	0.506	0.000
100	129	0.507	0.509	0.002
100	131	0.499	0.498	-0.001
100	132	0.502	0.502	0.000
100	133	0.509	0.510	0.001
100	134	0.506	0.503	-0.003
100	135	0.511	0.510	-0.001
100	138	0.508	0.508	0.000
100	139	0.500	0.498	-0.002
100	140	0.503	0.504	0.001
100	141	0.515	0.512	-0.003
100	142	0.511	0.512	0.001
100	143	0.507	0.506	-0.001
100	144	0.513	0.510	-0.003
100	220	0.512	0.513	0.001
100	221	0.513	0.513	0.000
100	224	0.498	0.501	0.003
100	225	0.508	0.506	-0.002
100	226	0.508	0.508	0.000
100	228	0.512	0.513	0.001
100	232	0.517	0.519	0.002
100	233	0.519	0.520	0.001
100	236	0.509	0.511	0.002
100	238	0.508	0.508	0.000
100	243	0.514	0.514	0.000
105	244	0.508	0.508	0.000
105	246	0.507	0.508	0.001
105	248	0.514	0.513	-0.001
105	374	0.511	0.510	-0.001
105	376	0.517	0.514	-0.003
105	Max	0.519	0.520	0.003
105	Average	0.509	0.508	0.000
105	Min	0.498	0.498	-0.003
105	Std Dev	0.005	0.005	0.001



12.8_V_FAULT_F	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

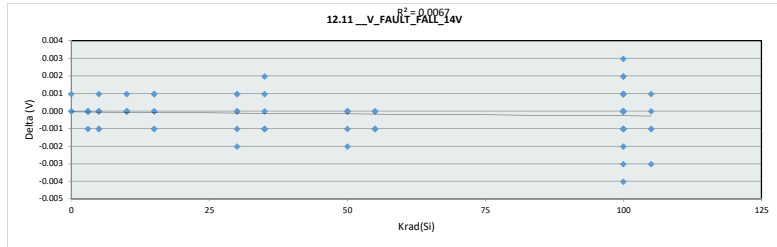
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.506	0.499	0.507	0.499	0.507	0.499	0.508	0.500	0.502	0.498	0.508
Average	0.507	0.507	0.511	0.507	0.510	0.507	0.511	0.508	0.507	0.509	0.511
Max	0.507	0.513	0.515	0.513	0.515	0.513	0.514	0.517	0.512	0.520	0.514
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



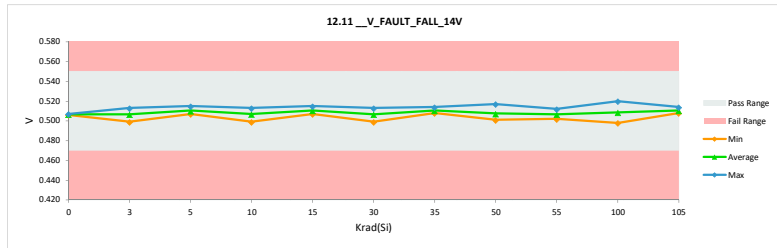
LDR TID Report  
TPS7H5001-SP

12.11_V_FAULT_FALL_14V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	0.55 0.55
Min Limit	0.47 0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.507	0.507	0.000
0	118	0.506	0.507	0.001
0	122	0.506	0.506	0.000
3	61	0.503	0.502	-0.001
3	62	0.511	0.511	0.000
3	63	0.499	0.499	0.000
3	64	0.513	0.513	0.000
3	65	0.503	0.503	0.000
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.506	0.507	0.001
5	69	0.507	0.507	0.000
5	70	0.515	0.515	0.000
5	71	0.509	0.508	-0.001
5	72	0.512	0.512	0.000
10	73	0.503	0.503	0.000
10	74	0.511	0.511	0.000
10	75	0.499	0.499	0.000
10	76	0.513	0.513	0.000
10	77	0.503	0.503	0.000
10	78	0.512	0.513	0.001
15	79	0.514	0.513	-0.001
15	80	0.506	0.507	0.001
15	81	0.507	0.508	0.001
15	82	0.515	0.515	0.000
15	83	0.509	0.509	0.000
15	84	0.512	0.511	-0.001
30	86	0.503	0.501	-0.002
30	89	0.511	0.512	0.001
30	91	0.499	0.499	0.000
30	92	0.513	0.512	-0.001
30	94	0.503	0.503	0.000
30	95	0.512	0.513	0.001
35	100	0.514	0.513	-0.001
35	101	0.506	0.508	0.002
35	102	0.507	0.508	0.001
35	103	0.515	0.514	-0.001
35	104	0.509	0.509	0.000
35	105	0.512	0.511	-0.001
50	106	0.512	0.512	0.000
50	107	0.511	0.511	0.000
50	108	0.502	0.502	0.000
50	109	0.517	0.517	0.000
50	110	0.503	0.501	-0.002
50	111	0.503	0.502	-0.001
55	113	0.503	0.502	-0.001
55	114	0.503	0.502	-0.001
55	115	0.509	0.508	-0.001
55	116	0.513	0.512	-0.001
55	126	0.510	0.510	0.000
55	128	0.506	0.506	0.000
100	129	0.507	0.508	0.001
100	131	0.499	0.498	-0.001
100	132	0.502	0.501	-0.001
100	133	0.508	0.509	0.001
100	134	0.506	0.502	-0.004
100	135	0.511	0.510	-0.001
100	138	0.508	0.508	0.000
100	139	0.500	0.498	-0.002
100	140	0.503	0.504	0.001
100	141	0.515	0.512	-0.003
100	142	0.512	0.512	0.000
100	143	0.506	0.506	0.000
100	144	0.512	0.511	-0.001
100	220	0.512	0.513	0.001
100	221	0.513	0.513	0.000
100	224	0.498	0.501	0.003
100	225	0.507	0.507	0.000
100	226	0.508	0.508	0.000
100	228	0.512	0.514	0.002
100	232	0.517	0.519	0.002
100	233	0.520	0.520	0.000
100	236	0.510	0.511	0.001
100	238	0.509	0.508	-0.001
100	243	0.514	0.514	0.000
105	244	0.509	0.508	-0.001
105	246	0.507	0.508	0.001
105	248	0.514	0.513	-0.001
105	374	0.510	0.510	0.000
105	376	0.517	0.514	-0.003
105	Max	0.520	0.520	0.003
105	Average	0.509	0.508	0.000
105	Min	0.498	0.498	-0.004
105	Std Dev	0.005	0.005	0.001



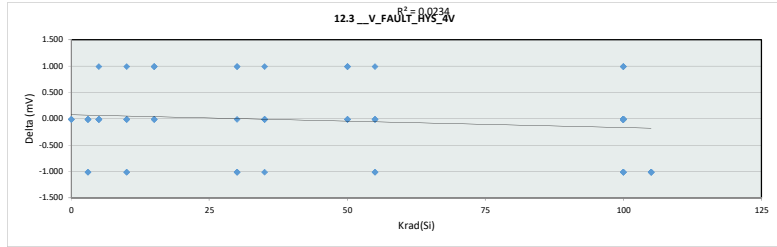
12.11_V_FAULT										
Test Site										
Tester										
Test Number										
Max Limit	0.55 V									
Min Limit	0.47 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.506	0.499	0.507	0.499	0.507	0.499	0.508	0.501	0.502	0.498
Average	0.507	0.507	0.510	0.507	0.511	0.507	0.511	0.508	0.507	0.509
Max	0.507	0.513	0.515	0.513	0.515	0.513	0.514	0.517	0.512	0.520
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



LDR TID Report  
TPS7H5001-SP

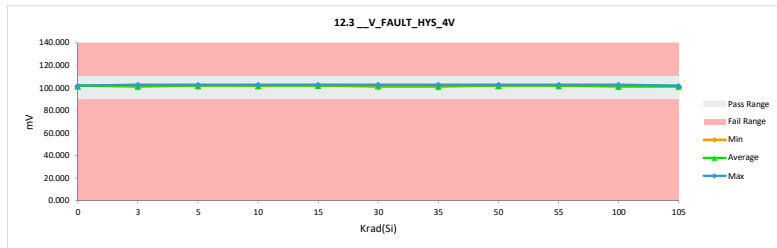
12.3_V_FAULT_HYS_4V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	110
Min Limit	90

Krad(Si)	Serial #	PRE	POST	Delta
0	117	102.000	102.000	0.000
0	118	102.000	102.000	0.000
0	122	102.000	102.000	0.000
3	61	102.000	101.000	-1.000
3	62	102.000	102.000	0.000
3	63	102.000	102.000	0.000
3	64	103.000	102.000	-1.000
3	65	102.000	102.000	0.000
3	66	103.000	103.000	0.000
5	67	102.000	102.000	0.000
5	68	102.000	102.000	0.000
5	69	102.000	103.000	1.000
5	70	102.000	102.000	0.000
5	71	102.000	102.000	0.000
5	72	102.000	102.000	0.000
10	73	102.000	102.000	0.000
10	74	102.000	103.000	1.000
10	75	102.000	102.000	0.000
10	76	103.000	102.000	-1.000
10	77	102.000	102.000	0.000
10	78	103.000	102.000	-1.000
15	79	102.000	102.000	0.000
15	80	102.000	102.000	0.000
15	81	102.000	103.000	1.000
15	82	102.000	103.000	1.000
15	83	102.000	102.000	0.000
15	84	102.000	103.000	1.000
30	86	102.000	103.000	1.000
30	89	102.000	102.000	0.000
30	91	102.000	101.000	-1.000
30	92	103.000	102.000	-1.000
30	94	102.000	103.000	1.000
30	95	103.000	102.000	-1.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	101.000	-1.000
35	104	102.000	102.000	0.000
35	105	102.000	103.000	1.000
50	106	102.000	102.000	0.000
50	107	102.000	102.000	0.000
50	108	102.000	103.000	1.000
50	109	101.000	102.000	1.000
50	110	102.000	102.000	0.000
50	111	102.000	103.000	1.000
55	113	102.000	103.000	1.000
55	114	102.000	102.000	0.000
55	115	103.000	102.000	-1.000
55	116	102.000	102.000	0.000
55	126	102.000	102.000	0.000
55	128	103.000	102.000	-1.000
100	129	103.000	102.000	-1.000
100	131	102.000	102.000	0.000
100	132	102.000	102.000	0.000
100	133	102.000	102.000	0.000
100	134	102.000	102.000	0.000
100	135	102.000	102.000	0.000
100	138	101.000	102.000	1.000
100	139	102.000	102.000	0.000
100	140	103.000	103.000	0.000
100	141	102.000	102.000	0.000
100	142	102.000	103.000	1.000
100	143	102.000	102.000	0.000
100	144	102.000	102.000	0.000
100	220	102.000	101.000	-1.000
100	221	102.000	102.000	0.000
100	224	102.000	102.000	0.000
100	225	102.000	103.000	1.000
100	226	103.000	102.000	-1.000
100	228	102.000	102.000	0.000
100	232	102.000	102.000	0.000
100	233	102.000	102.000	0.000
100	236	102.000	102.000	0.000
100	238	102.000	101.000	-1.000
100	243	102.000	102.000	0.000
105	244	103.000	102.000	-1.000
105	246	103.000	102.000	-1.000
105	248	103.000	102.000	-1.000
105	374	103.000	102.000	-1.000
105	376	102.000	101.000	-1.000
105	Max	103.000	103.000	1.000
105	Average	102.163	102.113	-0.050
105	Min	101.000	101.000	-1.000
105	Std Dev	0.434	0.503	0.654



12.3_V_FAULT_HYS_4V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	110
Min Limit	90

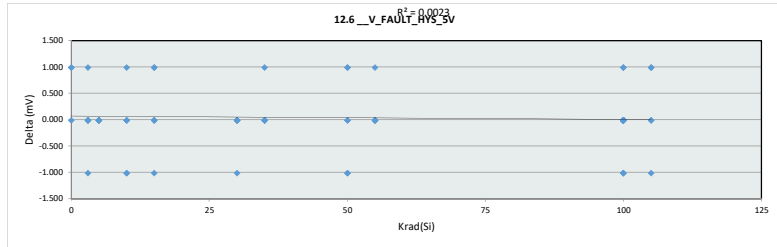
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	101.000	102.000	102.000	102.000	101.000	101.000	102.000	102.000	101.000	101.000
Average	102.000	102.000	102.167	102.167	102.500	102.167	102.000	102.333	102.167	102.042	101.800
Max	102.000	103.000	103.000	103.000	103.000	103.000	103.000	103.000	103.000	103.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



LDR TID Report  
TPS7H5001-SP

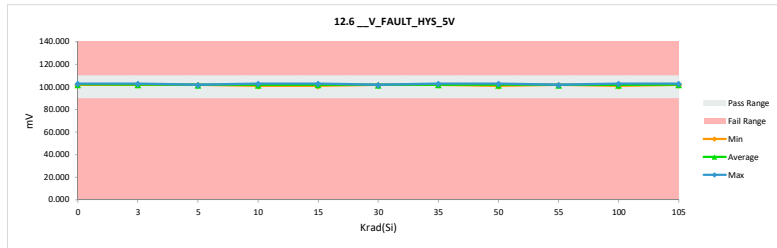
12.6_V_FAULT_HYS_5V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	110
Min Limit	90

Krad(Si)	Serial #	PRE	POST	Delta
0	117	101.000	102.000	1.000
0	118	102.000	103.000	1.000
0	122	102.000	102.000	0.000
3	61	103.000	102.000	-1.000
3	62	102.000	102.000	0.000
3	63	102.000	102.000	0.000
3	64	102.000	102.000	0.000
3	65	102.000	102.000	0.000
3	66	102.000	103.000	1.000
5	67	102.000	102.000	0.000
5	68	102.000	102.000	0.000
5	69	102.000	102.000	0.000
5	70	102.000	102.000	0.000
5	71	102.000	102.000	0.000
5	72	102.000	102.000	0.000
10	73	103.000	102.000	-1.000
10	74	102.000	102.000	0.000
10	75	102.000	101.000	-1.000
10	76	102.000	102.000	0.000
10	77	102.000	102.000	0.000
10	78	102.000	103.000	1.000
15	79	102.000	103.000	1.000
15	80	102.000	102.000	0.000
15	81	102.000	103.000	1.000
15	82	102.000	102.000	0.000
15	83	102.000	101.000	-1.000
15	84	102.000	102.000	0.000
30	86	103.000	102.000	-1.000
30	89	102.000	102.000	0.000
30	91	102.000	102.000	0.000
30	92	102.000	102.000	0.000
30	94	102.000	102.000	0.000
30	95	102.000	102.000	0.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	102.000	0.000
35	104	102.000	102.000	0.000
35	105	102.000	103.000	1.000
50	106	103.000	102.000	-1.000
50	107	101.000	102.000	1.000
50	108	102.000	102.000	0.000
50	109	102.000	101.000	-1.000
50	110	102.000	102.000	0.000
50	111	102.000	103.000	1.000
55	113	102.000	102.000	0.000
55	114	102.000	102.000	0.000
55	115	102.000	102.000	0.000
55	116	102.000	102.000	0.000
55	126	101.000	102.000	1.000
55	128	102.000	102.000	0.000
100	129	101.000	101.000	0.000
100	131	102.000	102.000	0.000
100	132	102.000	102.000	0.000
100	133	102.000	102.000	0.000
100	134	102.000	102.000	0.000
100	135	102.000	102.000	0.000
100	138	102.000	102.000	0.000
100	139	102.000	102.000	0.000
100	140	102.000	102.000	0.000
100	141	102.000	101.000	-1.000
100	142	102.000	102.000	0.000
100	143	101.000	102.000	1.000
100	144	102.000	102.000	0.000
100	220	101.000	101.000	0.000
100	221	102.000	102.000	0.000
100	224	102.000	102.000	0.000
100	225	103.000	102.000	-1.000
100	226	102.000	102.000	0.000
100	228	102.000	101.000	-1.000
100	232	102.000	102.000	0.000
100	233	102.000	102.000	0.000
100	236	102.000	103.000	1.000
100	238	102.000	102.000	0.000
100	243	102.000	102.000	0.000
105	244	102.000	102.000	0.000
105	246	102.000	102.000	0.000
105	248	103.000	102.000	-1.000
105	374	102.000	103.000	1.000
105	375	101.000	102.000	1.000
105	Max	103.000	103.000	1.000
105	Average	101.988	102.025	0.038
105	Min	101.000	101.000	-1.000
105	Std Dev	0.405	0.449	0.561



12.6_V_FAULT_HYS_5V	
Test Site	
Tester	
Test Number	
Max Limit	110 mV
Min Limit	90 mV

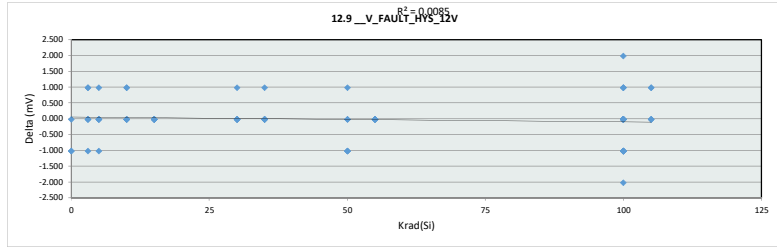
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	102.000	102.000	101.000	101.000	102.000	102.000	101.000	102.000	101.000	102.000
Average	102.333	102.167	102.000	102.000	102.167	102.000	102.167	102.000	102.000	101.875	102.200
Max	103.000	103.000	102.000	103.000	103.000	102.000	103.000	103.000	102.000	103.000	103.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



LDR TID Report  
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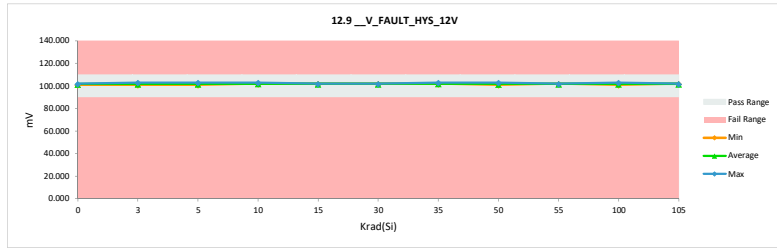
12.9_V_FAULT_HYS_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	110
Min Limit	90

Krad(Si)	Serial #	PRE	POST	Delta
0	117	102.000	102.000	0.000
0	118	103.000	102.000	-1.000
0	122	102.000	101.000	-1.000
3	61	102.000	102.000	0.000
3	62	102.000	102.000	0.000
3	63	101.000	102.000	1.000
3	64	102.000	102.000	0.000
3	65	102.000	101.000	-1.000
3	66	102.000	103.000	1.000
5	67	102.000	103.000	1.000
5	68	102.000	102.000	0.000
5	69	102.000	101.000	-1.000
5	70	102.000	102.000	0.000
5	71	102.000	102.000	0.000
5	72	102.000	102.000	0.000
10	73	102.000	102.000	0.000
10	74	102.000	102.000	0.000
10	75	101.000	102.000	1.000
10	76	102.000	102.000	0.000
10	77	102.000	102.000	0.000
10	78	102.000	103.000	1.000
15	79	102.000	102.000	0.000
15	80	102.000	102.000	0.000
15	81	102.000	102.000	0.000
15	82	102.000	102.000	0.000
15	83	102.000	102.000	0.000
15	84	102.000	102.000	0.000
30	86	102.000	102.000	0.000
30	89	102.000	102.000	0.000
30	91	101.000	102.000	1.000
30	92	102.000	102.000	0.000
30	94	102.000	102.000	0.000
30	95	102.000	102.000	0.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	102.000	0.000
35	104	102.000	102.000	0.000
35	105	102.000	103.000	1.000
50	106	103.000	102.000	-1.000
50	107	101.000	101.000	0.000
50	108	102.000	102.000	0.000
50	109	102.000	101.000	-1.000
50	110	102.000	103.000	1.000
50	111	102.000	101.000	-1.000
55	113	102.000	102.000	0.000
55	114	102.000	102.000	0.000
55	115	102.000	102.000	0.000
55	116	102.000	102.000	0.000
55	126	102.000	102.000	0.000
55	128	102.000	102.000	0.000
100	129	102.000	101.000	-1.000
100	131	102.000	102.000	0.000
100	132	102.000	102.000	0.000
100	133	102.000	101.000	-1.000
100	134	102.000	101.000	-1.000
100	135	102.000	102.000	0.000
100	138	102.000	101.000	-1.000
100	139	101.000	102.000	1.000
100	140	102.000	102.000	0.000
100	141	102.000	101.000	-1.000
100	142	103.000	102.000	-1.000
100	143	101.000	102.000	1.000
100	144	102.000	103.000	1.000
100	220	102.000	102.000	0.000
100	221	102.000	102.000	0.000
100	224	102.000	102.000	0.000
100	225	101.000	103.000	2.000
100	226	102.000	102.000	0.000
100	228	102.000	102.000	0.000
100	232	102.000	102.000	0.000
100	233	103.000	102.000	-1.000
100	236	103.000	102.000	-1.000
100	238	103.000	101.000	-2.000
100	243	102.000	102.000	0.000
105	244	102.000	102.000	0.000
105	246	102.000	102.000	0.000
105	248	102.000	102.000	0.000
105	374	101.000	102.000	1.000
105	376	101.000	102.000	1.000
105	Max	103.000	103.000	2.000
105	Average	101.963	101.938	-0.025
105	Min	101.000	101.000	-2.000
105	Std Dev	0.434	0.486	0.675



12.9_V_FAULT_HYS_12V	
Test Site	
Tester	
Test Number	
Max Limit	110 mV
Min Limit	90 mV

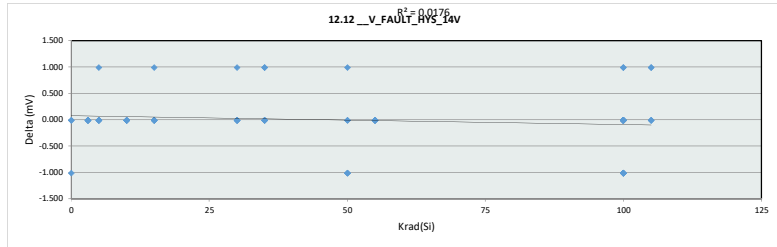
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	101.000	101.000	101.000	102.000	102.000	102.000	102.000	101.000	102.000	101.000	102.000
Average	101.667	102.000	102.000	102.167	102.000	102.000	102.167	101.667	102.000	101.833	102.000
Max	102.000	103.000	103.000	103.000	102.000	102.000	103.000	103.000	102.000	103.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



LDR TID Report  
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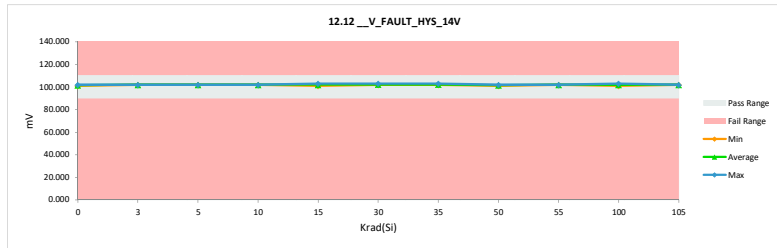
12.12 V_FAULT_HYS_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	110
Min Limit	90

Krad(Si)	Serial #	PRE	POST	Delta
0	117	102.000	102.000	0.000
0	118	102.000	101.000	-1.000
0	122	102.000	102.000	0.000
3	61	102.000	102.000	0.000
3	62	102.000	102.000	0.000
3	63	102.000	102.000	0.000
3	64	102.000	102.000	0.000
3	65	102.000	102.000	0.000
3	66	102.000	102.000	0.000
5	67	102.000	102.000	0.000
5	68	102.000	102.000	0.000
5	69	102.000	102.000	0.000
5	70	102.000	102.000	0.000
5	71	101.000	102.000	1.000
5	72	102.000	102.000	0.000
10	73	102.000	102.000	0.000
10	74	102.000	102.000	0.000
10	75	102.000	102.000	0.000
10	76	102.000	102.000	0.000
10	77	102.000	102.000	0.000
10	78	102.000	102.000	0.000
15	79	102.000	103.000	1.000
15	80	102.000	102.000	0.000
15	81	102.000	102.000	0.000
15	82	102.000	102.000	0.000
15	83	101.000	101.000	0.000
15	84	102.000	102.000	0.000
30	86	102.000	103.000	1.000
30	89	102.000	102.000	0.000
30	91	102.000	102.000	0.000
30	92	102.000	102.000	0.000
30	94	102.000	102.000	0.000
30	95	102.000	102.000	0.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	102.000	0.000
35	104	101.000	102.000	1.000
35	105	102.000	103.000	1.000
50	106	103.000	102.000	-1.000
50	107	101.000	102.000	1.000
50	108	102.000	101.000	-1.000
50	109	102.000	101.000	-1.000
50	110	102.000	102.000	0.000
50	111	102.000	102.000	0.000
55	113	102.000	102.000	0.000
55	114	102.000	102.000	0.000
55	115	102.000	102.000	0.000
55	116	102.000	102.000	0.000
55	126	102.000	102.000	0.000
55	128	102.000	102.000	0.000
100	129	102.000	102.000	0.000
100	131	102.000	102.000	0.000
100	132	102.000	103.000	1.000
100	133	103.000	102.000	-1.000
100	134	102.000	102.000	0.000
100	135	102.000	102.000	0.000
100	138	102.000	101.000	-1.000
100	139	102.000	102.000	0.000
100	140	102.000	102.000	0.000
100	141	102.000	102.000	0.000
100	142	102.000	102.000	0.000
100	143	102.000	102.000	0.000
100	144	103.000	102.000	-1.000
100	220	102.000	102.000	0.000
100	221	102.000	102.000	0.000
100	224	103.000	102.000	-1.000
100	225	102.000	101.000	-1.000
100	226	102.000	101.000	-1.000
100	228	102.000	101.000	-1.000
100	232	102.000	102.000	0.000
100	233	102.000	102.000	0.000
100	236	102.000	102.000	0.000
100	238	101.000	102.000	1.000
100	243	102.000	102.000	0.000
105	244	101.000	102.000	1.000
105	246	102.000	102.000	0.000
105	248	102.000	102.000	0.000
105	374	102.000	102.000	0.000
105	375	101.000	102.000	1.000
105	Max	103.000	103.000	1.000
105	Average	101.963	101.950	-0.013
105	Min	101.000	101.000	-1.000
105	Std Dev	0.371	0.386	0.515



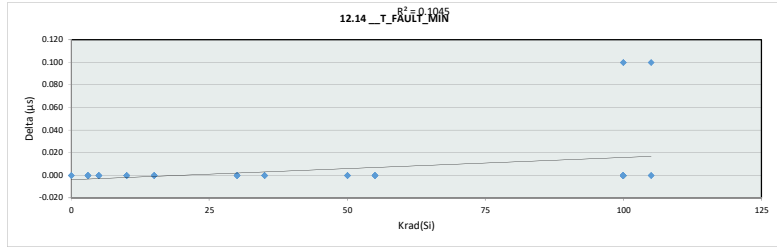
12.12 V_FAULT	
Test Site	
Tester	
Test Number	
Max Limit	110 mV
Min Limit	90 mV

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	101.000	102.000	102.000	102.000	101.000	102.000	102.000	101.000	102.000	101.000	102.000
Average	101.667	102.000	102.000	102.000	102.000	102.167	102.167	101.667	102.000	101.875	102.000
Max	102.000	102.000	102.000	102.000	103.000	103.000	103.000	102.000	102.000	103.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

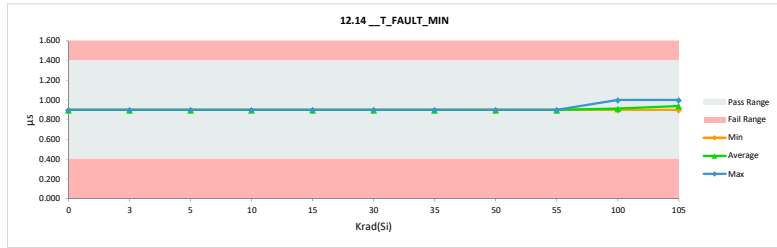


LDR TID Report  
TPS7H5001-SP

12.14 T_FAULT_MIN				
Test Site	Tester	Test Number	Unit	Max Limit
			µs	1.4
			µs	1.4
			Min Limit	0.4
			Min Limit	0.4
Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.900	0.900	0.000
0	118	0.900	0.900	0.000
0	122	0.900	0.900	0.000
3	61	0.900	0.900	0.000
3	62	0.900	0.900	0.000
3	63	0.900	0.900	0.000
3	64	0.900	0.900	0.000
3	65	0.900	0.900	0.000
3	66	0.900	0.900	0.000
5	67	0.900	0.900	0.000
5	68	0.900	0.900	0.000
5	69	0.900	0.900	0.000
5	70	0.900	0.900	0.000
5	71	0.900	0.900	0.000
5	72	0.900	0.900	0.000
10	73	0.900	0.900	0.000
10	74	0.900	0.900	0.000
10	75	0.900	0.900	0.000
10	76	0.900	0.900	0.000
10	77	0.900	0.900	0.000
10	78	0.900	0.900	0.000
15	79	0.900	0.900	0.000
15	80	0.900	0.900	0.000
15	81	0.900	0.900	0.000
15	82	0.900	0.900	0.000
15	83	0.900	0.900	0.000
15	84	0.900	0.900	0.000
30	86	0.900	0.900	0.000
30	89	0.900	0.900	0.000
30	91	0.900	0.900	0.000
30	92	0.900	0.900	0.000
30	94	0.900	0.900	0.000
30	95	0.900	0.900	0.000
35	100	0.900	0.900	0.000
35	101	0.900	0.900	0.000
35	102	0.900	0.900	0.000
35	103	0.900	0.900	0.000
35	104	0.900	0.900	0.000
35	105	0.900	0.900	0.000
50	106	0.900	0.900	0.000
50	107	0.900	0.900	0.000
50	108	0.900	0.900	0.000
50	109	0.900	0.900	0.000
50	110	0.900	0.900	0.000
50	111	0.900	0.900	0.000
55	113	0.900	0.900	0.000
55	114	0.900	0.900	0.000
55	115	0.900	0.900	0.000
55	116	0.900	0.900	0.000
55	126	0.900	0.900	0.000
55	128	0.900	0.900	0.000
100	129	0.900	0.900	0.000
100	131	0.900	0.900	0.000
100	132	0.900	0.900	0.000
100	133	0.900	1.000	0.100
100	134	0.900	0.900	0.000
100	135	0.900	0.900	0.000
100	138	0.900	1.000	0.100
100	139	0.900	0.900	0.000
100	140	0.900	0.900	0.000
100	141	0.900	0.900	0.000
100	142	0.900	0.900	0.000
100	143	0.900	0.900	0.000
100	144	0.900	0.900	0.000
100	220	0.900	0.900	0.000
100	221	0.900	0.900	0.000
100	224	0.900	0.900	0.000
100	225	0.900	1.000	0.100
100	226	0.900	0.900	0.000
100	228	0.900	0.900	0.000
100	232	0.900	0.900	0.000
100	233	0.900	0.900	0.000
100	236	0.900	0.900	0.000
100	238	0.900	0.900	0.000
100	243	0.900	0.900	0.000
105	244	0.900	0.900	0.000
105	246	0.900	1.000	0.100
105	248	0.900	1.000	0.100
105	374	0.900	0.900	0.000
105	376	0.900	0.900	0.000
105	Max	0.900	1.000	0.100
	Average	0.900	0.906	0.006
	Min	0.900	0.900	0.000
	Std Dev	0.000	0.024	0.024



12.14 T_FAULT										
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit					
			µs	1.4	0.4					
			µs	1.4	0.4					
Krad(Si)	LL	Min	Average	Max	UL					
0	0.400	0.900	0.900	0.900	1.400					
3	0.400	0.900	0.900	0.900	1.400					
5	0.400	0.900	0.900	0.900	1.400					
10	0.400	0.900	0.900	0.900	1.400					
15	0.400	0.900	0.900	0.900	1.400					
30	0.400	0.900	0.900	0.900	1.400					
35	0.400	0.900	0.900	0.900	1.400					
50	0.400	0.900	0.900	0.900	1.400					
55	0.400	0.900	0.900	0.900	1.400					
100	0.400	0.900	0.913	1.000	1.400					
105	0.400	0.900	0.940	1.000	1.400					

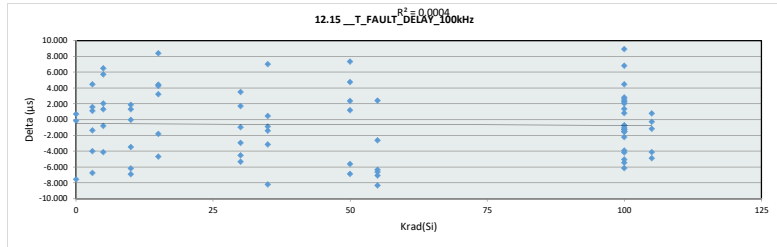




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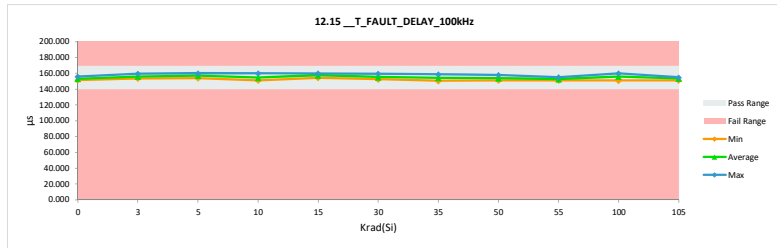
		12.15 T_FAULT_DELAY_100kHz	
Test Site			
Tester			
Test Number			
Unit	µs	µs	
Max Limit	169	169	
Min Limit	140	140	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	156.143	156.056	-0.087
0	118	159.074	151.573	-7.501
0	122	151.484	152.229	0.745
3	61	156.932	155.615	-1.317
3	62	158.789	159.947	1.158
3	63	157.986	154.051	-3.935
3	64	151.448	155.983	4.535
3	65	156.182	157.851	1.669
3	66	160.282	153.618	-6.664
5	67	151.792	158.349	6.557
5	68	154.968	154.237	-0.731
5	69	158.714	160.092	1.378
5	70	154.510	160.288	5.778
5	71	159.183	155.113	-4.070
5	72	153.705	155.822	2.117
10	73	156.932	156.955	0.023
10	74	158.789	160.172	1.383
10	75	157.986	151.153	-6.833
10	76	151.448	153.366	1.918
10	77	156.182	152.786	-3.396
10	78	160.282	154.164	-6.118
15	79	151.792	160.258	8.466
15	80	154.968	159.295	4.327
15	81	158.714	156.952	-1.762
15	82	154.510	157.764	3.254
15	83	159.183	154.551	-4.632
15	84	153.705	158.177	4.472
30	86	156.932	154.070	-2.862
30	89	158.789	154.344	-4.445
30	91	157.986	152.738	-5.248
30	92	151.448	153.204	1.756
30	94	156.182	159.721	3.539
30	95	160.282	159.368	-0.914
35	100	151.792	158.873	7.081
35	101	154.968	155.493	0.525
35	102	158.714	157.898	-0.816
35	103	154.510	153.189	-1.321
35	104	159.183	151.038	-8.145
35	105	153.705	150.633	-3.072
50	106	158.671	151.863	-6.808
50	107	150.844	155.662	4.818
50	108	156.731	151.185	-5.546
50	109	151.456	153.882	2.426
50	110	150.688	158.091	7.403
50	111	152.919	154.157	1.238
55	113	157.832	151.572	-6.260
55	114	158.852	151.858	-6.994
55	115	157.847	155.276	-2.571
55	116	152.416	154.863	2.447
55	126	159.618	153.048	-6.570
55	128	159.685	151.431	-8.254
100	129	159.530	158.555	-0.975
100	131	158.720	154.857	-3.863
100	132	156.479	155.007	-1.472
100	133	152.354	151.192	-1.162
100	134	153.883	152.446	-1.437
100	135	153.011	157.528	4.517
100	138	151.345	158.222	6.877
100	139	157.626	156.962	-0.664
100	140	155.256	157.550	2.294
100	141	153.392	154.290	0.898
100	142	151.035	160.028	8.993
100	143	155.673	154.489	-1.184
100	144	153.909	152.443	-1.466
100	220	160.301	154.222	-6.079
100	221	158.395	153.020	-5.375
100	224	157.600	160.016	2.416
100	225	158.006	160.123	2.117
100	226	156.717	159.604	2.887
100	228	159.064	156.919	-2.145
100	232	150.810	153.462	2.652
100	233	158.394	159.793	1.399
100	236	156.087	151.104	-4.983
100	238	158.605	157.912	-0.693
100	243	159.534	155.424	-4.110
105	244	154.325	155.179	0.854
105	246	156.249	151.434	-4.815
105	248	154.956	154.761	-0.195
105	374	159.035	154.997	-4.038
105	376	154.127	153.017	-1.110
105	375	160.301	160.288	8.993
Average		156.027	155.430	-0.597
Min		150.688	150.633	-8.254
Std Dev		2.934	2.885	4.207



		12.15 T_FAULT	
Test Site			
Tester			
Test Number			
Max Limit	169	µs	
Min Limit	140	µs	

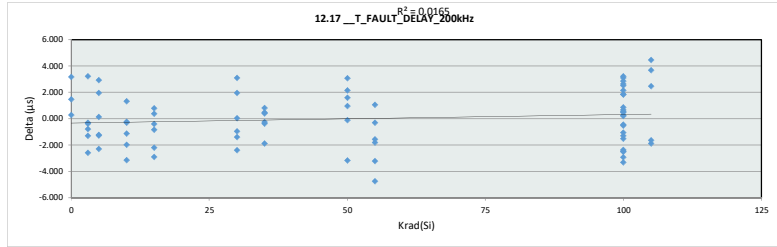
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000
Min	151.573	153.618	154.237	151.153	154.551	152.738	150.633	151.185	151.431	151.104	151.434
Average	153.286	156.178	157.317	154.766	157.833	155.574	154.521	154.140	153.008	156.049	153.878
Max	156.056	159.947	160.288	160.172	160.258	159.721	158.873	158.091	155.276	160.123	155.179
UL	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000



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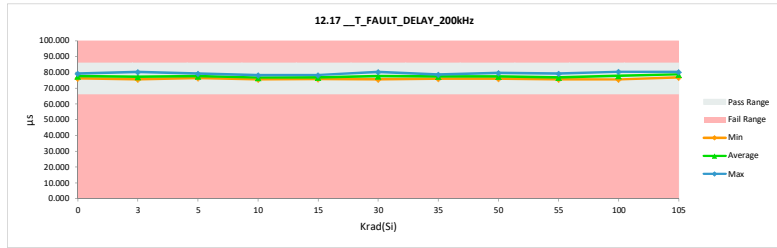
12.17 T_FAULT_DELAY_200kHz	
Test Site	
Tester	
Test Number	
Unit	µs
Max Limit	86
Min Limit	66

Krad(Si)	Serial #	PRE	POST	Delta
0	117	76.024	79.236	3.212
0	118	75.738	77.243	1.505
0	122	75.955	76.263	0.308
3	61	77.634	76.356	-1.278
3	62	78.253	75.702	-2.551
3	63	76.991	80.247	3.256
3	64	76.031	75.684	-0.347
3	65	76.345	75.597	-0.748
3	66	80.256	79.979	-0.277
5	67	76.314	79.272	2.958
5	68	77.920	78.091	0.171
5	69	76.563	78.563	2.000
5	70	79.071	76.803	-2.268
5	71	77.758	76.512	-1.246
5	72	77.981	76.787	-1.194
10	73	77.634	75.681	-1.953
10	74	78.253	77.150	-1.103
10	75	76.991	78.357	1.366
10	76	76.031	75.760	-0.271
10	77	76.345	76.143	-0.202
10	78	80.256	77.139	-3.117
15	79	76.314	77.136	0.822
15	80	77.920	78.333	0.413
15	81	76.563	76.186	-0.377
15	82	79.071	76.213	-2.858
15	83	77.758	76.956	-0.802
15	84	77.981	75.813	-2.168
30	86	77.634	76.696	-0.938
30	89	78.253	80.229	1.976
30	91	76.991	75.628	-1.363
30	92	76.031	79.157	3.126
30	94	76.345	76.422	0.077
30	95	80.256	77.898	-2.358
35	100	76.314	76.103	-0.211
35	101	77.920	76.067	-1.853
35	102	76.563	77.412	0.849
35	103	79.071	78.736	-0.335
35	104	77.758	78.275	0.517
35	105	77.981	78.407	0.426
50	106	76.126	78.320	2.194
50	107	76.178	76.112	-0.066
50	108	75.739	76.721	0.982
50	109	76.659	79.763	3.104
50	110	75.674	77.292	1.618
50	111	79.271	76.130	-3.141
55	113	78.894	75.700	-3.194
55	114	79.548	79.285	-0.263
55	115	78.432	76.903	-1.529
55	116	78.081	76.295	-1.786
55	126	80.422	75.700	-4.722
55	128	76.158	77.250	1.092
100	129	79.056	77.581	-1.475
100	131	76.518	79.780	3.262
100	132	77.120	75.847	-1.273
100	133	75.773	76.469	0.696
100	134	78.090	78.372	0.282
100	135	76.534	78.726	2.192
100	138	78.036	77.535	-0.501
100	139	76.750	76.330	-0.420
100	140	78.159	80.060	1.901
100	141	80.463	77.176	-3.287
100	142	76.838	75.807	-1.031
100	143	77.498	78.078	0.580
100	144	79.988	77.558	-2.430
100	220	78.921	79.423	0.502
100	221	77.287	79.146	1.859
100	224	77.185	79.720	2.535
100	225	79.691	77.212	-2.479
100	226	76.238	77.144	0.906
100	228	77.318	80.453	3.135
100	232	75.636	75.884	0.248
100	233	78.075	75.746	-2.329
100	236	78.451	75.569	-2.882
100	238	76.571	79.237	2.666
100	243	75.494	78.388	2.894
105	244	77.119	79.611	2.492
105	246	78.618	77.010	-1.608
105	248	76.175	79.889	3.714
105	374	75.807	80.286	4.479
105	376	78.693	76.840	-1.853
105	Max	80.463	80.453	4.479
105	Average	77.504	77.507	0.003
105	Min	75.494	75.569	-4.722
105	Std Dev	1.335	1.475	2.020



12.17 T_FAULT	
Test Site	
Tester	
Test Number	
Max Limit	86 µs
Min Limit	66 µs

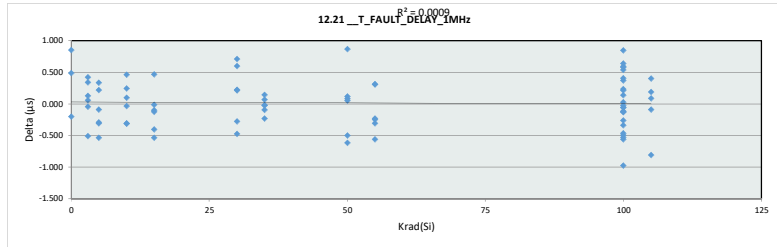
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
Min	76.263	75.597	76.512	75.681	75.813	75.628	76.067	76.112	75.700	75.569	76.840
Average	77.581	77.261	77.671	76.705	76.773	77.672	77.500	77.390	76.856	77.802	78.727
Max	79.236	80.247	79.272	78.357	78.333	80.229	78.736	79.763	79.285	80.453	80.286
UL	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000



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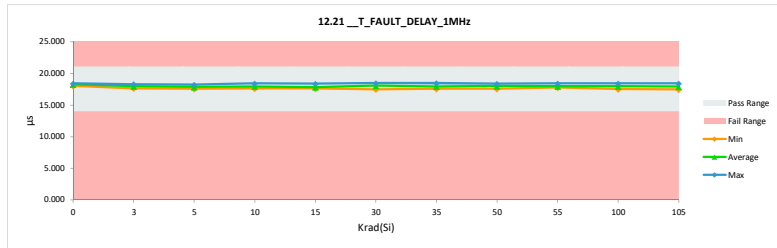
		12.21 T_FAULT_DELAY_1MHz	
Test Site			
Tester			
Test Number			
Unit		µs	µs
Max Limit		21	21
Min Limit		14	14

Krad(Si)	Serial #	PRE	POST	Delta
0	117	17.570	18.429	0.859
0	118	17.881	18.375	0.494
0	122	18.212	18.019	-0.193
3	61	17.751	17.713	-0.038
3	62	17.738	18.166	0.428
3	63	17.654	18.002	0.348
3	64	17.996	18.061	0.065
3	65	18.145	17.640	-0.505
3	66	18.164	18.297	0.133
5	67	17.579	17.921	0.342
5	68	17.825	17.739	-0.086
5	69	17.998	18.223	0.225
5	70	18.392	18.106	-0.286
5	71	18.123	17.593	-0.530
5	72	17.856	17.557	-0.299
10	73	17.738	17.857	0.106
10	74	17.738	18.205	0.467
10	75	17.654	17.628	-0.026
10	76	17.996	17.688	-0.308
10	77	18.145	17.843	-0.302
10	78	18.164	18.416	0.252
15	79	17.579	18.054	0.475
15	80	17.825	17.729	-0.096
15	81	17.998	17.599	-0.399
15	82	18.392	18.384	-0.008
15	83	18.123	17.595	-0.528
15	84	17.856	17.739	-0.117
30	86	17.751	17.481	-0.270
30	89	17.738	18.454	0.716
30	91	17.654	18.257	0.603
30	92	17.996	18.217	0.221
30	94	18.145	17.675	-0.470
30	95	18.164	18.393	0.229
35	100	17.579	17.565	-0.014
35	101	17.825	17.734	-0.091
35	102	17.998	18.146	0.148
35	103	18.392	18.463	0.071
35	104	18.123	18.105	-0.018
35	105	17.856	17.629	-0.227
50	106	18.470	17.859	-0.611
50	107	18.134	18.224	0.090
50	108	18.057	18.179	0.122
50	109	17.500	18.370	0.870
50	110	17.508	17.564	0.056
50	111	18.435	17.943	-0.492
55	113	18.317	17.762	-0.555
55	114	18.096	18.411	0.315
55	115	18.127	17.884	-0.243
55	116	18.319	18.092	-0.227
55	126	18.170	17.868	-0.302
55	128	17.556	17.873	0.317
100	129	18.391	18.423	0.032
100	131	17.500	18.351	0.851
100	132	17.567	18.152	0.585
100	133	18.306	17.780	-0.526
100	134	18.439	18.328	-0.111
100	135	18.111	17.988	-0.123
100	138	17.550	17.792	0.242
100	139	18.393	18.062	-0.331
100	140	18.322	17.767	-0.555
100	141	17.554	17.499	-0.055
100	142	18.088	17.831	-0.257
100	143	17.568	17.982	0.414
100	144	17.745	17.726	-0.019
100	220	18.369	18.323	-0.046
100	221	17.642	18.187	0.545
100	224	17.564	17.708	0.144
100	225	17.507	18.154	0.647
100	226	18.195	17.739	-0.456
100	228	17.810	18.408	0.598
100	232	18.209	17.719	-0.490
100	233	17.746	18.125	0.379
100	236	18.464	17.494	-0.970
100	238	17.821	17.703	-0.118
100	243	17.658	17.877	0.219
105	244	17.861	17.776	-0.085
105	246	17.844	18.038	0.194
105	248	18.276	17.475	-0.801
105	374	17.625	17.919	0.094
105	376	18.001	18.409	0.408
105	Max	18.470	18.463	0.870
105	Average	17.954	17.968	0.014
105	Min	17.500	17.475	-0.970
105	Std Dev	0.292	0.293	0.398



		12.21 T_FAULT	
Test Site			
Tester			
Test Number			
Max Limit		21	µs
Min Limit		14	µs

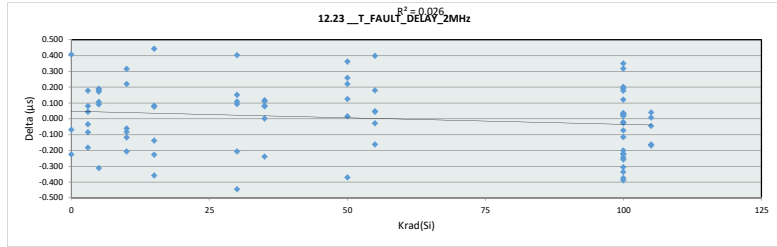
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000
Min	18.019	17.640	17.557	17.628	17.595	17.481	17.565	17.564	17.762	17.494	17.475
Average	18.274	17.980	17.857	17.940	17.850	18.080	17.940	18.023	17.982	17.963	17.923
Max	18.429	18.297	18.223	18.416	18.384	18.454	18.463	18.370	18.411	18.423	18.409
UL	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000



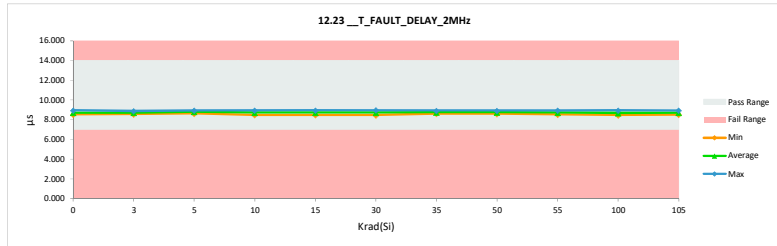
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12.23 T_FAULT_DELAY_2MHz	
Test Site	
Tester	
Test Number	
Unit	µs      µs
Max Limit	14      14
Min Limit	7      7

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.722	8.654	-0.068
0	118	8.560	8.969	0.409
0	122	8.566	8.543	-0.223
3	61	8.587	8.634	0.047
3	62	8.569	8.652	0.083
3	63	8.923	8.891	-0.032
3	64	8.916	8.832	-0.084
3	65	8.565	8.745	0.180
3	66	8.746	8.566	-0.180
5	67	8.511	8.703	0.192
5	68	8.939	8.630	-0.309
5	69	8.740	8.913	0.173
5	70	8.677	8.863	0.186
5	71	8.853	8.945	0.092
5	72	8.758	8.866	0.108
10	73	8.587	8.528	-0.059
10	74	8.569	8.487	-0.082
10	75	8.923	8.808	-0.115
10	76	8.916	8.711	-0.205
10	77	8.565	8.882	0.317
10	78	8.746	8.969	0.223
15	79	8.511	8.955	0.444
15	80	8.939	8.714	-0.225
15	81	8.740	8.604	-0.136
15	82	8.677	8.762	0.085
15	83	8.853	8.497	-0.356
15	84	8.758	8.837	0.079
30	86	8.587	8.699	0.112
30	89	8.569	8.974	0.405
30	91	8.923	8.718	-0.205
30	92	8.916	8.473	-0.443
30	94	8.565	8.660	0.095
30	95	8.746	8.899	0.153
35	100	8.511	8.631	0.120
35	101	8.939	8.702	-0.237
35	102	8.740	8.854	0.114
35	103	8.677	8.680	0.003
35	104	8.853	8.936	0.083
35	105	8.758	8.840	0.082
50	106	8.529	8.894	0.365
50	107	8.971	8.602	-0.369
50	108	8.701	8.923	0.222
50	109	8.496	8.756	0.260
50	110	8.725	8.742	0.017
50	111	8.513	8.641	0.128
55	113	8.801	8.641	-0.160
55	114	8.561	8.962	0.401
55	115	8.485	8.534	0.049
55	116	8.548	8.596	0.048
55	126	8.779	8.753	-0.026
55	128	8.708	8.890	0.182
100	129	8.872	8.759	-0.113
100	131	8.891	8.693	-0.198
100	132	8.910	8.885	-0.025
100	133	8.619	8.652	0.033
100	134	8.833	8.498	-0.335
100	135	8.902	8.646	-0.256
100	138	8.934	8.631	-0.303
100	139	8.964	8.745	-0.219
100	140	8.737	8.775	0.038
100	141	8.599	8.918	0.319
100	142	8.865	8.621	-0.244
100	143	8.564	8.601	0.037
100	144	8.534	8.560	0.026
100	220	8.577	8.756	0.179
100	221	8.558	8.762	0.204
100	224	8.846	8.474	-0.372
100	225	8.492	8.844	0.352
100	226	8.706	8.634	-0.072
100	228	8.976	8.958	-0.018
100	232	8.480	8.603	0.123
100	233	8.830	8.607	-0.223
100	236	8.687	8.707	0.020
100	238	8.942	8.556	-0.386
100	243	8.674	8.867	0.193
105	244	8.953	8.786	-0.167
105	246	8.681	8.639	-0.042
105	248	8.882	8.722	-0.160
105	374	8.897	8.940	0.043
105	376	8.517	8.527	0.010
105	Max	8.976	8.974	0.444
105	Average	8.727	8.732	0.005
105	Min	8.480	8.473	-0.443
105	Std Dev	0.156	0.143	0.210



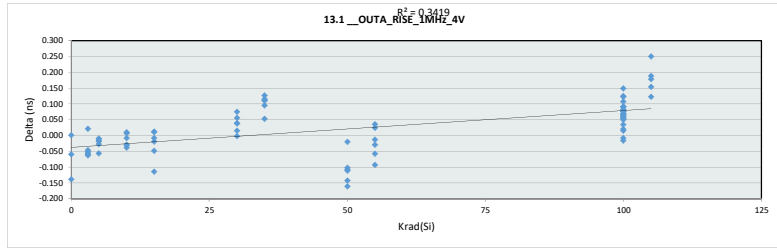
12.23 T_FAULT											
Test Site											
Tester											
Test Number											
Max Limit	14 µs										
Min Limit	7 µs										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000
Min	8.543	8.566	8.630	8.487	8.497	8.473	8.631	8.602	8.534	8.474	8.527
Average	8.722	8.720	8.820	8.731	8.728	8.737	8.774	8.760	8.729	8.698	8.723
Max	8.969	8.891	8.945	8.969	8.955	8.974	8.936	8.923	8.962	8.958	8.940
UL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000



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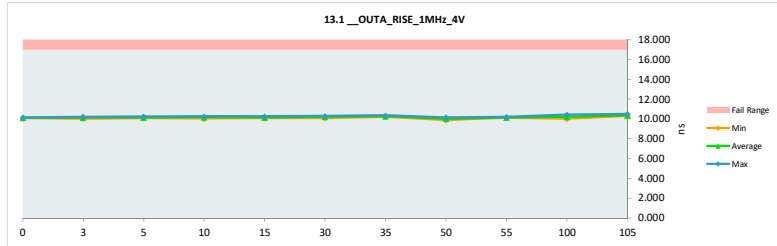
13.1 OUTA_RISE_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.240	10.103	-0.137
0	118	10.173	10.175	0.002
0	122	10.176	10.118	-0.058
3	61	10.269	10.224	-0.045
3	62	10.279	10.230	-0.049
3	63	10.245	10.184	-0.061
3	64	10.098	10.039	-0.059
3	65	10.220	10.166	-0.054
3	66	10.139	10.162	0.023
5	67	10.162	10.145	-0.017
5	68	10.274	10.248	-0.026
5	69	10.210	10.194	-0.016
5	70	10.183	10.128	-0.055
5	71	10.280	10.271	-0.009
5	72	10.109	10.100	-0.009
10	73	10.269	10.280	0.011
10	74	10.279	10.287	0.008
10	75	10.245	10.216	-0.029
10	76	10.098	10.061	-0.037
10	77	10.220	10.192	-0.028
10	78	10.139	10.132	-0.007
15	79	10.162	10.155	-0.007
15	80	10.274	10.287	0.013
15	81	10.210	10.097	-0.113
15	82	10.183	10.136	-0.047
15	83	10.280	10.262	-0.018
15	84	10.109	10.121	0.012
30	86	10.269	10.309	0.040
30	89	10.279	10.278	-0.001
30	91	10.245	10.321	0.076
30	92	10.098	10.114	0.016
30	94	10.220	10.260	0.040
30	95	10.139	10.196	0.057
35	100	10.162	10.290	0.128
35	101	10.274	10.386	0.112
35	102	10.210	10.323	0.113
35	103	10.183	10.237	0.054
35	104	10.280	10.376	0.096
35	105	10.109	10.226	0.117
50	106	10.152	10.011	-0.141
50	107	10.014	9.904	-0.110
50	108	10.197	10.178	-0.019
50	109	10.195	10.035	-0.160
50	110	10.230	10.129	-0.101
50	111	10.142	10.035	-0.107
55	113	10.188	10.176	-0.012
55	114	10.275	10.219	-0.056
55	115	10.229	10.201	-0.028
55	116	10.233	10.141	-0.092
55	126	10.155	10.181	0.026
55	128	10.140	10.177	0.037
100	129	10.101	10.184	0.083
100	131	10.120	10.197	0.077
100	132	10.098	10.175	0.077
100	133	10.179	10.261	0.082
100	134	10.068	10.053	-0.015
100	135	10.200	10.236	0.036
100	138	10.108	10.175	0.067
100	139	10.087	10.179	0.092
100	140	10.218	10.269	0.051
100	141	10.223	10.216	-0.007
100	142	10.242	10.303	0.061
100	143	10.313	10.463	0.150
100	144	10.115	10.241	0.126
100	220	10.183	10.292	0.109
100	221	10.151	10.221	0.070
100	224	10.207	10.331	0.124
100	225	10.157	10.173	0.016
100	226	10.262	10.318	0.056
100	228	10.227	10.249	0.022
100	232	10.057	10.149	0.092
100	233	10.209	10.273	0.064
100	236	10.262	10.319	0.057
100	238	10.114	10.184	0.070
100	243	10.195	10.287	0.092
105	244	10.297	10.476	0.179
105	246	10.159	10.314	0.155
105	248	10.190	10.380	0.190
105	374	10.270	10.521	0.251
105	376	10.203	10.327	0.124
105	375	10.313	10.521	0.251
Max				
Average		10.192	10.215	0.023
Min		10.014	9.904	-0.160
Std Dev		0.066	0.106	0.081



13.1 OUTA_RISE	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns
Krad(Si)	0
LL	3
Min	10.103
Average	10.132
Max	10.175
UL	17.000

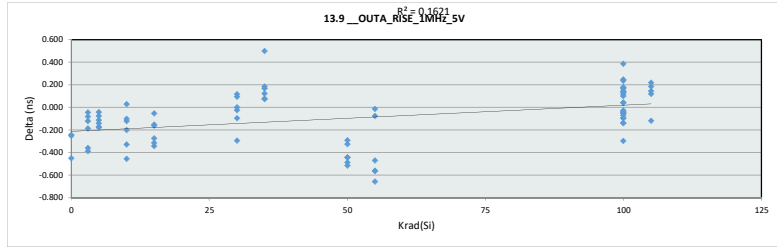
	3	5	10	15	30	35	50	55	100	105	
Min	10.103	10.039	10.100	10.061	10.097	10.114	10.226	9.904	10.141	10.053	10.314
Average	10.132	10.168	10.181	10.195	10.176	10.246	10.306	10.049	10.183	10.240	10.404
Max	10.175	10.230	10.271	10.287	10.287	10.321	10.386	10.178	10.219	10.463	10.521
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



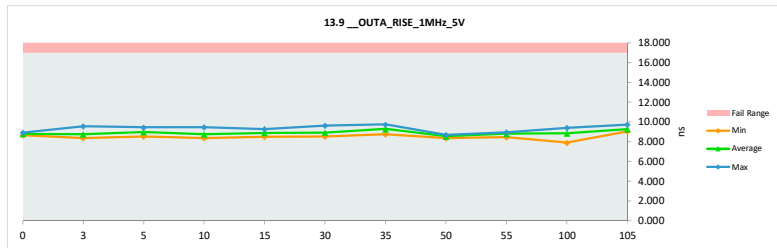
LDR TID Report  
TPS7H5001-SP

13.9_OUTA_RISE_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.283	8.837	-0.446
0	118	8.909	8.660	-0.249
0	122	9.163	8.923	-0.240
3	61	9.934	9.580	-0.354
3	62	8.427	8.385	-0.042
3	63	8.843	8.660	-0.183
3	64	8.801	8.683	-0.118
3	65	8.463	8.386	-0.077
3	66	9.369	8.986	-0.383
5	67	8.772	8.664	-0.108
5	68	9.638	9.465	-0.173
5	69	9.347	9.276	-0.071
5	70	8.585	8.546	-0.039
5	71	9.246	9.109	-0.137
5	72	9.069	8.902	-0.167
10	73	9.934	9.482	-0.452
10	74	8.427	8.459	0.032
10	75	8.843	8.645	-0.198
10	76	8.801	8.685	-0.116
10	77	8.463	8.365	-0.098
10	78	9.369	9.044	-0.325
15	79	8.772	8.501	-0.271
15	80	9.638	9.300	-0.338
15	81	9.347	9.198	-0.149
15	82	8.585	8.537	-0.048
15	83	9.246	8.935	-0.311
15	84	9.069	8.912	-0.157
30	86	9.934	9.645	-0.289
30	89	8.427	8.547	0.120
30	91	8.843	8.821	-0.022
30	92	8.801	8.805	0.004
30	94	8.463	8.560	0.097
30	95	9.369	9.277	-0.092
35	100	8.772	8.853	0.081
35	101	9.638	9.715	0.077
35	102	9.347	9.474	0.127
35	103	8.585	8.758	0.173
35	104	9.246	9.748	0.502
35	105	9.069	9.259	0.190
50	106	9.213	8.703	-0.510
50	107	8.664	8.376	-0.288
50	108	9.095	8.612	-0.483
50	109	8.945	8.624	-0.321
50	110	8.829	8.390	-0.439
50	111	9.037	8.600	-0.437
55	113	9.250	8.784	-0.466
55	114	9.565	8.912	-0.653
55	115	9.386	8.830	-0.556
55	116	9.505	8.945	-0.560
55	126	8.482	8.473	-0.009
55	128	9.036	8.965	-0.071
100	129	8.367	8.617	0.250
100	131	8.990	8.897	-0.093
100	132	8.790	8.765	-0.025
100	133	9.114	8.980	-0.134
100	134	8.798	8.752	-0.046
100	135	9.175	9.127	-0.048
100	138	8.850	8.955	0.105
100	139	8.010	8.249	0.239
100	140	9.030	8.964	-0.066
100	141	9.452	9.158	-0.294
100	142	8.807	8.989	0.182
100	143	9.060	9.228	0.168
100	144	7.856	7.900	0.044
100	220	8.953	9.075	0.122
100	221	8.794	8.705	-0.089
100	224	8.576	8.965	0.389
100	225	9.199	9.156	-0.043
100	226	9.282	9.424	0.142
100	228	9.113	9.252	0.139
100	232	8.589	8.636	0.047
100	233	8.901	8.872	-0.029
100	236	8.882	9.027	0.145
100	238	9.132	8.997	-0.135
100	243	8.216	8.392	0.176
105	244	9.545	9.735	0.190
105	246	9.038	9.187	0.149
105	248	8.833	9.057	0.224
105	374	9.229	9.115	-0.114
105	376	9.249	9.372	0.123
105	Max	9.934	9.748	0.502
105	Average	8.996	8.904	-0.092
105	Min	7.856	7.900	-0.653
105	Std Dev	0.415	0.372	0.234



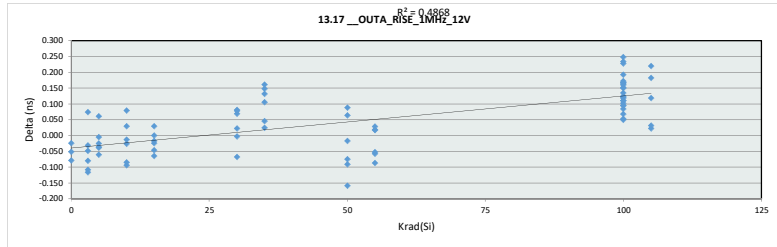
13.9_OUTA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.660	8.385	8.546	8.365	8.501	8.547	8.758	8.376	8.473	7.900	9.057
Average	8.807	8.780	8.994	8.780	8.897	8.943	9.301	8.551	8.818	8.878	9.293
Max	8.923	9.580	9.465	9.482	9.300	9.645	9.748	8.703	8.965	9.424	9.735
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



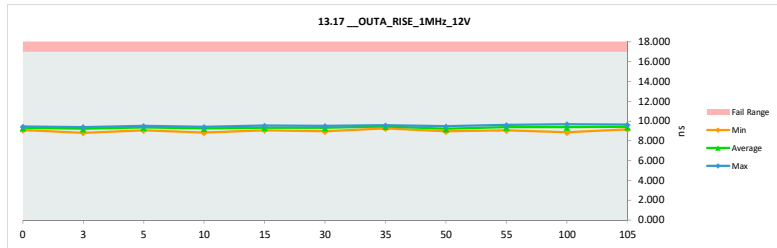
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13.17_OUTA_RISE_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.494	9.444	-0.050
0	118	9.091	9.068	-0.023
0	122	9.464	9.386	-0.078
3	61	9.508	9.393	-0.115
3	62	9.106	9.181	0.075
3	63	9.372	9.342	-0.030
3	64	9.253	9.174	-0.079
3	65	8.858	8.811	-0.047
3	66	9.501	9.394	-0.107
5	67	9.183	9.245	0.062
5	68	9.489	9.454	-0.035
5	69	9.544	9.507	-0.037
5	70	9.109	9.050	-0.059
5	71	9.257	9.233	-0.024
5	72	9.471	9.467	-0.004
10	73	9.508	9.415	-0.093
10	74	9.106	9.186	0.080
10	75	9.372	9.360	-0.012
10	76	9.253	9.284	0.031
10	77	8.858	8.833	-0.025
10	78	9.501	9.417	-0.084
15	79	9.183	9.214	0.031
15	80	9.489	9.473	-0.016
15	81	9.544	9.521	-0.023
15	82	9.109	9.064	-0.045
15	83	9.257	9.194	-0.063
15	84	9.471	9.472	0.001
30	86	9.508	9.442	-0.066
30	89	9.106	9.185	0.079
30	91	9.372	9.396	0.024
30	92	9.253	9.323	0.070
30	94	8.858	8.940	0.082
30	95	9.501	9.499	-0.002
35	100	9.183	9.316	0.133
35	101	9.489	9.536	0.047
35	102	9.544	9.570	0.026
35	103	9.109	9.258	0.149
35	104	9.257	9.419	0.162
35	105	9.471	9.578	0.107
50	106	9.433	9.343	-0.090
50	107	9.175	9.101	-0.074
50	108	9.122	9.187	0.065
50	109	9.375	9.464	0.089
50	110	9.108	8.950	-0.158
50	111	9.300	9.284	-0.016
55	113	9.236	9.185	-0.051
55	114	9.539	9.483	-0.056
55	115	9.487	9.401	-0.086
55	116	9.417	9.435	0.018
55	126	9.032	9.052	0.020
55	128	9.569	9.599	0.030
100	129	8.840	9.089	0.249
100	131	9.486	9.622	0.136
100	132	9.399	9.495	0.096
100	133	9.159	9.263	0.104
100	134	9.336	9.448	0.112
100	135	9.466	9.660	0.194
100	138	9.433	9.487	0.054
100	139	8.827	9.000	0.173
100	140	9.341	9.392	0.051
100	141	9.466	9.630	0.164
100	142	9.197	9.365	0.168
100	143	9.304	9.424	0.120
100	144	8.764	8.859	0.095
100	220	9.322	9.551	0.229
100	221	9.314	9.399	0.085
100	224	9.111	9.279	0.168
100	225	9.165	9.234	0.069
100	226	9.192	9.318	0.126
100	228	9.221	9.374	0.153
100	232	9.148	9.309	0.161
100	233	9.136	9.287	0.151
100	236	9.133	9.368	0.235
100	238	9.556	9.679	0.123
100	243	8.945	9.057	0.112
105	244	9.438	9.462	0.024
105	246	9.436	9.619	0.183
105	248	9.127	9.247	0.120
105	374	9.103	9.136	0.033
105	376	9.396	9.617	0.221
105	Max	9.569	9.679	0.249
105	Average	9.282	9.327	0.046
105	Min	8.764	8.811	-0.158
105	Std Dev	0.204	0.199	0.096



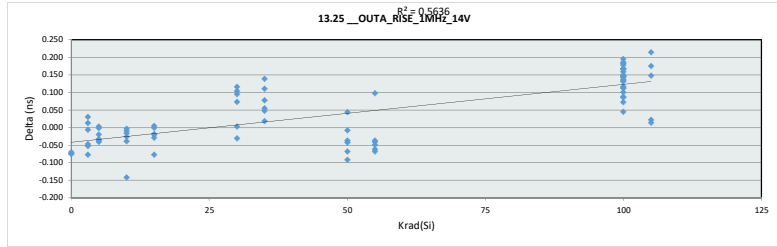
13.17_OUTA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.068	8.811	9.050	8.833	9.064	8.940	9.258	8.950	9.052	8.859	9.136
Average	9.299	9.216	9.326	9.249	9.323	9.298	9.446	9.222	9.359	9.358	9.416
Max	9.444	9.394	9.507	9.417	9.521	9.499	9.578	9.464	9.599	9.679	9.619
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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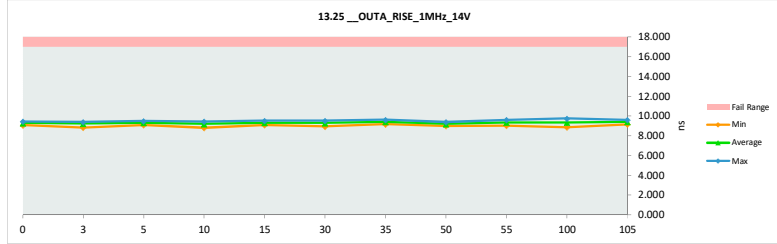
13.25_OUTA_RISE_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.513	9.440	-0.073
0	118	9.146	9.072	-0.074
0	122	9.462	9.393	-0.069
3	61	9.446	9.394	-0.052
3	62	9.160	9.191	0.031
3	63	9.397	9.321	-0.076
3	64	9.203	9.217	0.014
3	65	8.831	8.826	-0.005
3	66	9.417	9.371	-0.046
5	67	9.200	9.199	-0.001
5	68	9.495	9.455	-0.040
5	69	9.516	9.497	-0.019
5	70	9.117	9.085	-0.032
5	71	9.243	9.247	0.004
5	72	9.448	9.411	-0.037
10	73	9.446	9.444	-0.002
10	74	9.160	9.145	-0.015
10	75	9.397	9.256	-0.141
10	76	9.203	9.194	-0.009
10	77	8.831	8.806	-0.025
10	78	9.417	9.379	-0.038
15	79	9.200	9.200	0.000
15	80	9.495	9.419	-0.076
15	81	9.516	9.522	0.006
15	82	9.117	9.089	-0.028
15	83	9.243	9.224	-0.019
15	84	9.448	9.430	-0.018
30	86	9.446	9.416	-0.030
30	89	9.160	9.265	0.105
30	91	9.397	9.401	0.004
30	92	9.203	9.277	0.074
30	94	8.831	8.948	0.117
30	95	9.417	9.513	0.096
35	100	9.200	9.256	0.056
35	101	9.495	9.514	0.019
35	102	9.516	9.627	0.111
35	103	9.117	9.166	0.049
35	104	9.243	9.383	0.140
35	105	9.448	9.527	0.079
50	106	9.368	9.301	-0.067
50	107	9.179	9.088	-0.091
50	108	9.175	9.133	-0.042
50	109	9.345	9.390	0.045
50	110	9.018	8.982	-0.036
50	111	9.292	9.285	-0.007
55	113	9.206	9.139	-0.067
55	114	9.516	9.468	-0.048
55	115	9.446	9.410	-0.036
55	116	9.476	9.436	-0.040
55	126	9.074	9.013	-0.061
55	128	9.499	9.598	0.099
100	129	8.896	9.043	0.147
100	131	9.493	9.566	0.073
100	132	9.464	9.510	0.046
100	133	9.129	9.298	0.169
100	134	9.303	9.416	0.113
100	135	9.532	9.681	0.149
100	138	9.383	9.472	0.089
100	139	8.787	8.934	0.147
100	140	9.262	9.380	0.118
100	141	9.467	9.618	0.151
100	142	9.199	9.365	0.166
100	143	9.217	9.348	0.131
100	144	8.776	8.863	0.087
100	220	9.335	9.518	0.183
100	221	9.290	9.391	0.101
100	224	9.045	9.224	0.179
100	225	9.100	9.243	0.143
100	226	9.201	9.338	0.137
100	228	9.208	9.344	0.136
100	232	9.110	9.297	0.187
100	233	9.105	9.219	0.114
100	236	9.125	9.294	0.169
100	238	9.558	9.754	0.196
100	243	8.881	9.041	0.160
105	244	9.413	9.436	0.023
105	246	9.424	9.600	0.176
105	248	9.089	9.238	0.149
105	374	9.130	9.145	0.015
105	376	9.376	9.591	0.215
105	Max	9.558	9.754	0.215
105	Average	9.268	9.312	0.044
105	Min	8.776	8.806	-0.141
105	Std Dev	0.200	0.200	0.089



13.25_OUTA_RISE	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.072	8.826	9.085	8.806	9.089	8.948	9.166	8.982	9.013	8.863	9.145
Average	9.302	9.220	9.316	9.204	9.314	9.303	9.412	9.197	9.344	9.340	9.402
Max	9.440	9.394	9.497	9.444	9.522	9.513	9.627	9.390	9.598	9.754	9.600
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

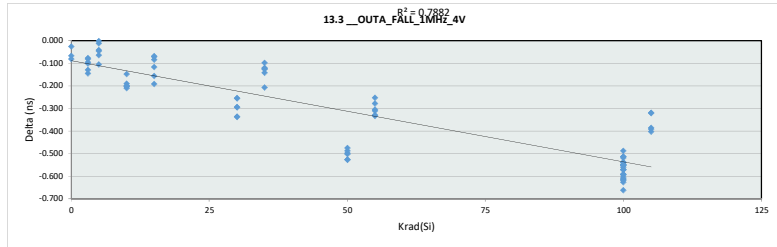




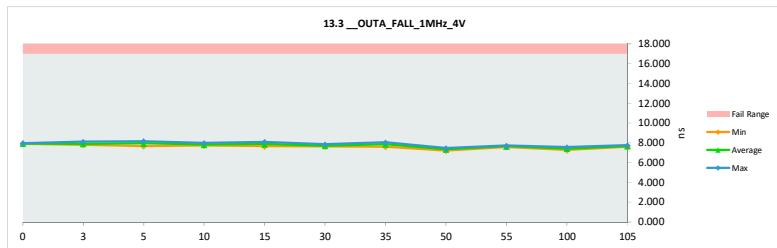
LDR TID Report  
TPS7H5001-SP

13.3 OUTA_FALL_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.042	7.977	-0.065
0	118	7.930	7.905	-0.025
0	122	8.023	7.943	-0.080
3	61	8.004	7.925	-0.079
3	62	8.194	8.119	-0.075
3	63	7.950	7.850	-0.100
3	64	7.904	7.810	-0.094
3	65	7.934	7.806	-0.128
3	66	8.103	7.960	-0.143
5	67	7.728	7.684	-0.044
5	68	8.174	8.164	-0.010
5	69	7.995	7.932	-0.063
5	70	7.878	7.838	-0.040
5	71	7.968	7.968	0.000
5	72	8.256	8.152	-0.104
10	73	8.004	7.796	-0.208
10	74	8.194	7.993	-0.201
10	75	7.950	7.751	-0.199
10	76	7.904	7.758	-0.146
10	77	7.934	7.746	-0.188
10	78	8.103	7.905	-0.198
15	79	7.728	7.661	-0.067
15	80	8.174	8.105	-0.069
15	81	7.995	7.805	-0.190
15	82	7.878	7.763	-0.115
15	83	7.968	7.886	-0.082
15	84	8.256	8.101	-0.155
30	86	8.004	7.710	-0.294
30	89	8.194	7.860	-0.334
30	91	7.950	7.696	-0.254
30	92	7.904	7.651	-0.253
30	94	7.934	7.642	-0.292
30	95	8.103	7.767	-0.336
35	100	7.728	7.609	-0.119
35	101	8.174	8.049	-0.125
35	102	7.995	7.898	-0.097
35	103	7.878	7.737	-0.141
35	104	7.968	7.847	-0.121
35	105	8.256	8.050	-0.206
50	106	7.888	7.388	-0.500
50	107	7.783	7.287	-0.496
50	108	7.944	7.470	-0.474
50	109	7.723	7.237	-0.486
50	110	7.955	7.429	-0.526
50	111	7.889	7.365	-0.524
55	113	8.039	7.707	-0.332
55	114	7.933	7.624	-0.309
55	115	7.949	7.621	-0.328
55	116	8.032	7.729	-0.303
55	126	7.864	7.588	-0.276
55	128	7.934	7.683	-0.251
100	129	7.963	7.404	-0.559
100	131	8.051	7.515	-0.536
100	132	8.135	7.565	-0.570
100	133	8.110	7.562	-0.548
100	134	8.038	7.429	-0.609
100	135	8.099	7.507	-0.592
100	138	7.927	7.382	-0.545
100	139	7.845	7.295	-0.550
100	140	8.058	7.488	-0.570
100	141	7.934	7.397	-0.537
100	142	8.046	7.457	-0.589
100	143	7.886	7.375	-0.511
100	144	7.792	7.275	-0.517
100	220	8.039	7.414	-0.625
100	221	7.861	7.314	-0.547
100	224	7.985	7.499	-0.486
100	225	7.989	7.420	-0.569
100	226	7.931	7.380	-0.551
100	228	8.170	7.557	-0.613
100	232	7.890	7.378	-0.512
100	233	7.977	7.387	-0.590
100	236	7.920	7.317	-0.603
100	238	8.062	7.445	-0.617
100	243	7.997	7.336	-0.661
105	244	8.084	7.683	-0.401
105	246	8.038	7.721	-0.317
105	248	7.974	7.655	-0.319
105	374	7.983	7.599	-0.384
105	376	8.155	7.765	-0.390
	Max	8.256	8.164	0.000
	Average	7.989	7.668	-0.321
	Min	7.723	7.237	-0.661
	Std Dev	0.123	0.243	0.204



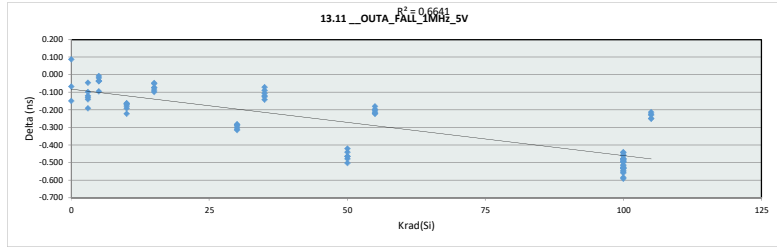
13.3 OUTA_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.905	7.806	7.684	7.746	7.661	7.642	7.609	7.237	7.588	7.275	7.599
Average	7.942	7.912	7.956	7.825	7.887	7.721	7.865	7.363	7.659	7.421	7.685
Max	7.977	8.119	8.164	7.993	8.105	7.860	8.050	7.470	7.729	7.565	7.765
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



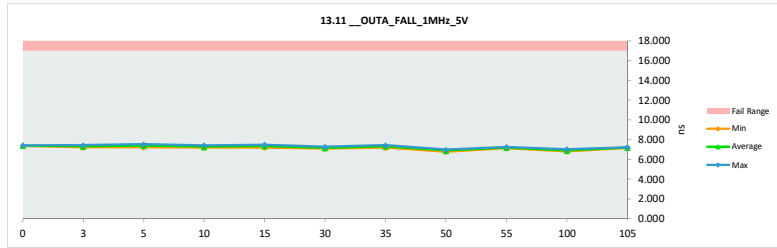
LDR TID Report  
TPS7H5001-SP

13.11_OUTA_FALL_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.498	7.434	-0.064
0	118	7.334	7.425	-0.091
0	122	7.508	7.361	-0.147
3	61	7.468	7.341	-0.127
3	62	7.591	7.453	-0.138
3	63	7.432	7.314	-0.118
3	64	7.417	7.229	-0.188
3	65	7.404	7.361	-0.043
3	66	7.514	7.419	-0.095
5	67	7.285	7.251	-0.034
5	68	7.562	7.471	-0.091
5	69	7.432	7.401	-0.031
5	70	7.343	7.327	-0.016
5	71	7.442	7.409	-0.033
5	72	7.558	7.553	-0.005
10	73	7.468	7.308	-0.160
10	74	7.591	7.428	-0.163
10	75	7.432	7.212	-0.220
10	76	7.417	7.229	-0.188
10	77	7.404	7.240	-0.164
10	78	7.514	7.339	-0.175
15	79	7.285	7.215	-0.070
15	80	7.562	7.466	-0.096
15	81	7.432	7.387	-0.045
15	82	7.343	7.258	-0.085
15	83	7.442	7.371	-0.071
15	84	7.558	7.510	-0.048
30	86	7.468	7.156	-0.312
30	89	7.591	7.295	-0.296
30	91	7.432	7.147	-0.285
30	92	7.417	7.111	-0.306
30	94	7.404	7.122	-0.282
30	95	7.514	7.235	-0.279
35	100	7.285	7.198	-0.087
35	101	7.562	7.445	-0.117
35	102	7.432	7.309	-0.123
35	103	7.343	7.204	-0.139
35	104	7.442	7.374	-0.068
35	105	7.558	7.455	-0.103
50	106	7.379	6.903	-0.476
50	107	7.362	6.862	-0.500
50	108	7.419	7.002	-0.417
50	109	7.555	6.794	-0.461
50	110	7.369	6.931	-0.438
50	111	7.374	6.913	-0.461
55	113	7.426	7.207	-0.219
55	114	7.376	7.199	-0.177
55	115	7.406	7.189	-0.217
55	116	7.438	7.239	-0.199
55	126	7.358	7.148	-0.210
55	128	7.459	7.266	-0.193
100	129	7.419	6.926	-0.493
100	131	7.453	6.999	-0.454
100	132	7.533	7.059	-0.474
100	133	7.539	6.983	-0.556
100	134	7.412	6.954	-0.458
100	135	7.480	6.951	-0.529
100	138	7.402	6.907	-0.495
100	139	7.316	6.874	-0.442
100	140	7.454	6.976	-0.478
100	141	7.477	6.950	-0.527
100	142	7.515	6.926	-0.589
100	143	7.430	6.904	-0.526
100	144	7.348	6.856	-0.492
100	220	7.426	6.910	-0.516
100	221	7.302	6.827	-0.475
100	224	7.420	6.983	-0.437
100	225	7.542	6.961	-0.581
100	226	7.387	6.855	-0.532
100	228	7.528	6.999	-0.529
100	232	7.344	6.861	-0.483
100	233	7.395	6.885	-0.510
100	236	7.387	6.844	-0.543
100	238	7.454	6.973	-0.481
100	243	7.485	6.935	-0.550
105	244	7.461	7.235	-0.226
105	246	7.445	7.223	-0.222
105	248	7.425	7.215	-0.210
105	374	7.424	7.178	-0.246
105	376	7.479	7.232	-0.247
105	Max	7.591	7.553	0.038
105	Average	7.437	7.160	-0.277
105	Min	7.255	6.794	-0.589
105	Std Dev	0.078	0.208	0.187



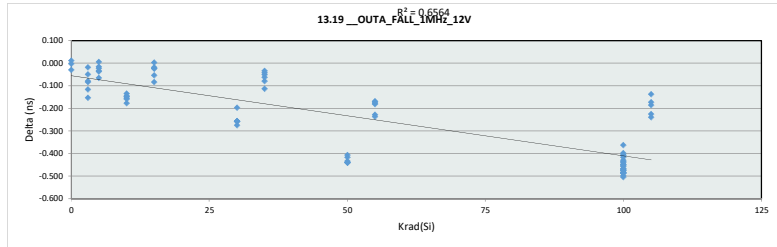
13.11_OUTA_FALL_1MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.361	7.229	7.251	7.212	7.215	7.111	7.198	6.794	7.148	6.827	7.178
Average	7.407	7.353	7.402	7.293	7.368	7.178	7.331	6.901	7.208	6.929	7.217
Max	7.434	7.453	7.553	7.428	7.510	7.295	7.455	7.002	7.266	7.059	7.235
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



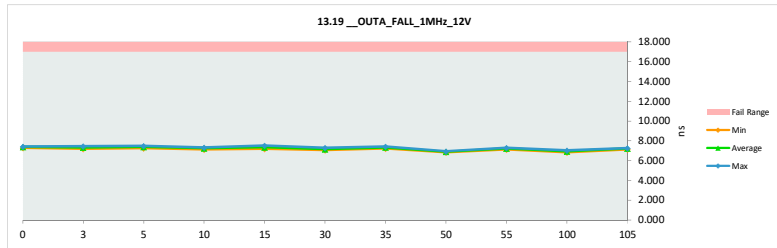
LDR TID Report  
TPS7H5001-SP

13.19_OUTA_FALL_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.428	7.427	-0.001
0	118	7.312	7.284	-0.028
0	122	7.420	7.433	0.013
3	61	7.372	7.295	-0.077
3	62	7.486	7.469	-0.017
3	63	7.446	7.365	-0.081
3	64	7.343	7.296	-0.047
3	65	7.325	7.211	-0.114
3	66	7.478	7.327	-0.151
5	67	7.289	7.254	-0.035
5	68	7.502	7.489	-0.013
5	69	7.385	7.365	-0.020
5	70	7.341	7.309	-0.032
5	71	7.393	7.400	0.007
5	72	7.543	7.480	-0.063
10	73	7.372	7.239	-0.133
10	74	7.486	7.334	-0.152
10	75	7.446	7.302	-0.144
10	76	7.343	7.187	-0.156
10	77	7.325	7.150	-0.175
10	78	7.478	7.334	-0.144
15	79	7.289	7.207	-0.082
15	80	7.502	7.450	-0.052
15	81	7.385	7.390	0.005
15	82	7.341	7.319	-0.022
15	83	7.393	7.372	-0.021
15	84	7.543	7.526	-0.017
30	86	7.372	7.116	-0.256
30	89	7.486	7.291	-0.195
30	91	7.446	7.173	-0.273
30	92	7.343	7.088	-0.255
30	94	7.325	7.069	-0.256
30	95	7.478	7.224	-0.254
35	100	7.289	7.240	-0.049
35	101	7.502	7.425	-0.077
35	102	7.385	7.353	-0.032
35	103	7.341	7.281	-0.060
35	104	7.393	7.352	-0.041
35	105	7.543	7.431	-0.112
50	106	7.322	6.918	-0.404
50	107	7.270	6.833	-0.437
50	108	7.390	6.958	-0.432
50	109	7.309	6.894	-0.415
50	110	7.332	6.894	-0.438
50	111	7.376	6.937	-0.439
55	113	7.411	7.185	-0.226
55	114	7.419	7.244	-0.175
55	115	7.415	7.245	-0.170
55	116	7.448	7.214	-0.234
55	126	7.301	7.135	-0.166
55	128	7.476	7.297	-0.179
100	129	7.377	6.912	-0.465
100	131	7.404	6.990	-0.414
100	132	7.453	7.048	-0.405
100	133	7.398	6.932	-0.466
100	134	7.432	6.949	-0.483
100	135	7.384	7.024	-0.360
100	138	7.415	6.930	-0.485
100	139	7.298	6.892	-0.406
100	140	7.383	6.937	-0.446
100	141	7.432	6.951	-0.481
100	142	7.430	6.934	-0.496
100	143	7.322	6.926	-0.396
100	144	7.297	6.860	-0.437
100	220	7.413	6.962	-0.451
100	221	7.344	6.889	-0.455
100	224	7.369	6.924	-0.445
100	225	7.378	6.906	-0.472
100	226	7.367	6.865	-0.502
100	228	7.440	6.966	-0.474
100	232	7.338	6.905	-0.433
100	233	7.341	6.914	-0.427
100	236	7.352	6.870	-0.482
100	238	7.453	6.967	-0.486
100	243	7.404	6.934	-0.470
105	244	7.456	7.219	-0.237
105	246	7.415	7.279	-0.136
105	248	7.396	7.212	-0.184
105	374	7.362	7.139	-0.223
105	376	7.444	7.273	-0.171
	Max	7.543	7.526	0.013
	Average	7.395	7.156	-0.239
	Min	7.270	6.833	-0.502
	Std Dev	0.066	0.200	0.177



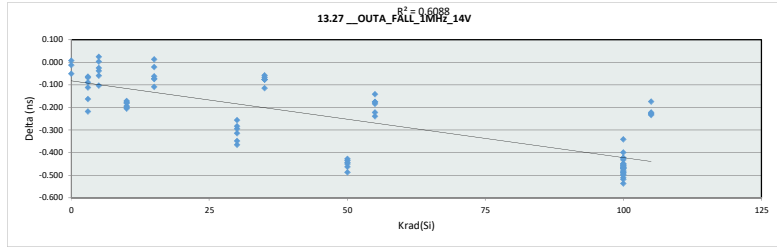
13.19_OUTA_FALL_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.284	7.211	7.254	7.150	7.207	7.069	7.240	6.833	7.135	6.860	7.139
Average	7.381	7.327	7.383	7.258	7.377	7.160	7.347	6.906	7.220	6.933	7.224
Max	7.433	7.469	7.489	7.334	7.526	7.291	7.431	6.958	7.297	7.048	7.279
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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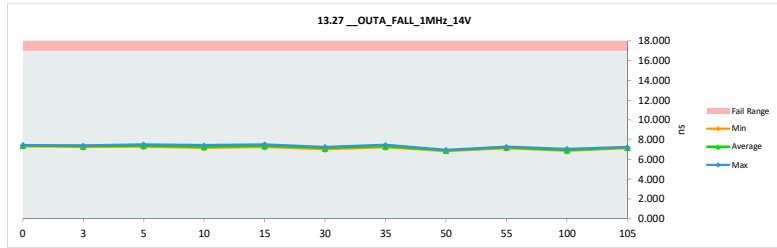
13.27_OUTA_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.454	7.463	0.009
0	118	7.340	7.329	-0.011
0	122	7.436	7.387	-0.049
3	61	7.450	7.388	-0.062
3	62	7.630	7.414	-0.216
3	63	7.447	7.360	-0.087
3	64	7.412	7.251	-0.161
3	65	7.346	7.280	-0.066
3	66	7.508	7.398	-0.110
5	67	7.271	7.276	0.005
5	68	7.533	7.497	-0.036
5	69	7.441	7.340	-0.101
5	70	7.350	7.326	-0.024
5	71	7.448	7.390	-0.058
5	72	7.509	7.535	0.026
10	73	7.450	7.246	-0.204
10	74	7.630	7.438	-0.192
10	75	7.447	7.278	-0.169
10	76	7.412	7.216	-0.196
10	77	7.346	7.171	-0.175
10	78	7.508	7.328	-0.180
15	79	7.271	7.252	-0.019
15	80	7.533	7.473	-0.060
15	81	7.441	7.370	-0.071
15	82	7.350	7.279	-0.071
15	83	7.448	7.341	-0.107
15	84	7.509	7.524	0.015
30	86	7.450	7.158	-0.292
30	89	7.630	7.266	-0.364
30	91	7.447	7.193	-0.254
30	92	7.412	7.131	-0.281
30	94	7.346	7.033	-0.313
30	95	7.508	7.161	-0.347
35	100	7.271	7.215	-0.056
35	101	7.533	7.468	-0.065
35	102	7.441	7.366	-0.075
35	103	7.350	7.275	-0.075
35	104	7.448	7.335	-0.113
35	105	7.509	7.435	-0.074
50	106	7.381	6.920	-0.461
50	107	7.333	6.847	-0.486
50	108	7.406	6.973	-0.433
50	109	7.315	6.867	-0.448
50	110	7.323	6.897	-0.426
50	111	7.390	6.948	-0.442
55	113	7.382	7.208	-0.174
55	114	7.437	7.255	-0.182
55	115	7.404	7.264	-0.140
55	116	7.447	7.273	-0.174
55	126	7.375	7.138	-0.237
55	128	7.498	7.278	-0.220
100	129	7.352	6.905	-0.447
100	131	7.466	7.036	-0.430
100	132	7.464	7.042	-0.422
100	133	7.437	6.962	-0.475
100	134	7.435	6.968	-0.467
100	135	7.411	7.071	-0.340
100	138	7.362	6.941	-0.421
100	139	7.360	6.875	-0.485
100	140	7.435	6.926	-0.509
100	141	7.433	6.978	-0.455
100	142	7.478	6.943	-0.535
100	143	7.414	6.946	-0.468
100	144	7.329	6.871	-0.458
100	220	7.456	6.940	-0.516
100	221	7.376	6.878	-0.498
100	224	7.388	6.922	-0.466
100	225	7.405	6.917	-0.488
100	226	7.390	6.896	-0.494
100	228	7.475	7.013	-0.462
100	232	7.323	6.925	-0.398
100	233	7.334	6.881	-0.453
100	236	7.394	6.929	-0.465
100	238	7.460	7.012	-0.448
100	243	7.400	6.917	-0.483
105	244	7.444	7.216	-0.228
105	246	7.486	7.262	-0.224
105	248	7.411	7.239	-0.172
105	374	7.372	7.140	-0.232
105	376	7.452	7.232	-0.220
105	Max	7.630	7.535	0.026
105	Average	7.424	7.165	-0.258
105	Min	7.271	6.847	-0.535
105	Std Dev	0.074	0.200	0.176



13.27_OUTA_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

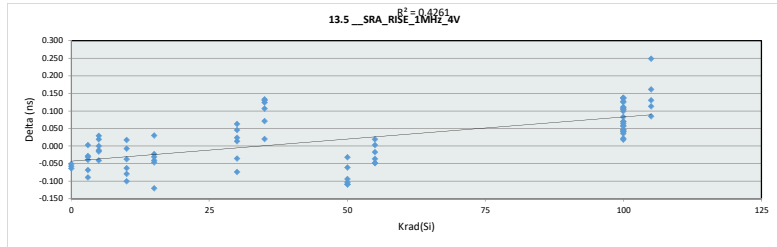
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.329	7.251	7.276	7.171	7.252	7.033	7.215	6.847	7.138	6.871	7.140
Average	7.393	7.349	7.394	7.280	7.373	7.157	7.349	6.909	7.236	6.946	7.218
Max	7.463	7.414	7.535	7.438	7.524	7.266	7.468	6.973	7.278	7.071	7.262
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



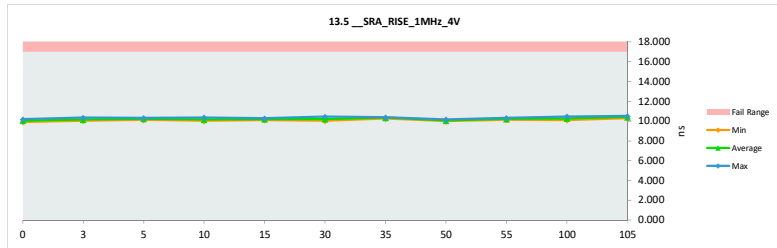
LDR TID Report  
TPS7H5001-SP

13.5_SRA_RISE_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.090	10.034	-0.056
0	118	9.973	9.923	-0.050
0	122	10.245	10.182	-0.063
3	61	10.370	10.340	-0.030
3	62	10.067	10.040	-0.027
3	63	10.107	10.040	-0.067
3	64	10.160	10.122	-0.038
3	65	10.224	10.228	0.004
3	66	10.226	10.138	-0.088
5	67	10.212	10.242	0.030
5	68	10.148	10.168	0.020
5	69	10.359	10.319	-0.040
5	70	10.256	10.257	0.001
5	71	10.309	10.295	-0.014
5	72	10.139	10.128	-0.011
10	73	10.370	10.364	-0.006
10	74	10.067	10.031	-0.036
10	75	10.107	10.045	-0.062
10	76	10.160	10.082	-0.078
10	77	10.224	10.242	0.018
10	78	10.226	10.127	-0.099
15	79	10.212	10.167	-0.045
15	80	10.148	10.118	-0.030
15	81	10.359	10.240	-0.119
15	82	10.256	10.287	0.031
15	83	10.309	10.287	-0.022
15	84	10.139	10.099	-0.040
30	86	10.370	10.434	0.064
30	89	10.067	10.082	0.015
30	91	10.107	10.034	-0.073
30	92	10.160	10.185	0.025
30	94	10.224	10.271	0.047
30	95	10.226	10.192	-0.034
35	100	10.212	10.346	0.134
35	101	10.148	10.279	0.131
35	102	10.359	10.380	0.021
35	103	10.256	10.381	0.125
35	104	10.309	10.381	0.072
35	105	10.139	10.247	0.108
50	106	10.134	10.026	-0.108
50	107	10.203	10.096	-0.107
50	108	10.932	10.001	-0.931
50	109	10.229	10.169	-0.060
50	110	10.121	10.019	-0.102
50	111	10.073	9.980	-0.093
55	113	10.180	10.133	-0.047
55	114	10.308	10.261	-0.047
55	115	10.180	10.164	-0.016
55	116	10.219	10.184	-0.035
55	126	10.174	10.194	0.020
55	128	10.305	10.309	0.004
100	129	10.204	10.263	0.059
100	131	10.100	10.229	0.129
100	132	10.109	10.179	0.070
100	133	10.150	10.260	0.110
100	134	10.044	10.182	0.138
100	135	10.276	10.334	0.058
100	138	10.185	10.297	0.112
100	139	9.968	10.106	0.138
100	140	10.146	10.217	0.071
100	141	10.260	10.308	0.048
100	142	10.131	10.257	0.126
100	143	10.355	10.461	0.106
100	144	10.091	10.133	0.042
100	220	10.326	10.370	0.044
100	221	10.121	10.169	0.048
100	224	10.163	10.228	0.065
100	225	10.235	10.258	0.023
100	226	10.111	10.250	0.139
100	228	10.298	10.398	0.100
100	232	10.343	10.385	0.042
100	233	10.257	10.276	0.019
100	236	10.259	10.296	0.037
100	238	10.248	10.355	0.107
100	243	10.099	10.183	0.084
105	244	10.246	10.408	0.162
105	246	10.047	10.297	0.250
105	248	10.400	10.514	0.114
105	374	10.308	10.439	0.131
105	375	10.311	10.397	0.086
105	Max	10.400	10.514	0.250
105	Average	10.200	10.222	0.022
105	Min	9.968	9.923	-0.119
105	Std Dev	0.100	0.126	0.078



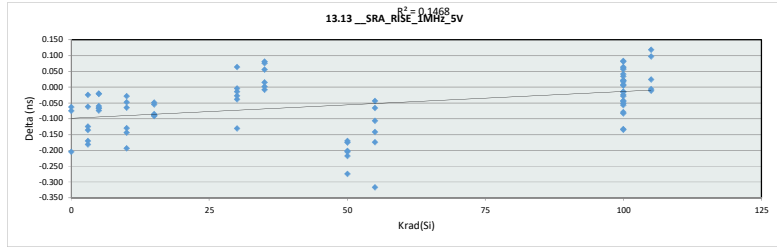
13.5_SRA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.923	10.040	10.128	10.031	10.099	10.034	10.247	9.980	10.133	10.106	10.297
Average	10.046	10.151	10.235	10.149	10.200	10.200	10.336	10.049	10.208	10.266	10.411
Max	10.182	10.340	10.319	10.364	10.287	10.434	10.381	10.169	10.309	10.461	10.514
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



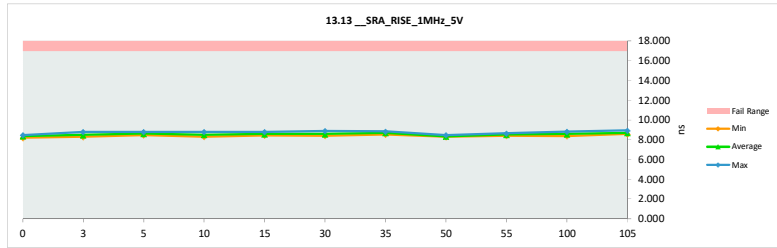
LDR TID Report  
TPS7H5001-SP

13.13_SRA_RISE_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.524	8.462	-0.062
0	118	8.271	8.197	-0.074
0	122	8.680	8.477	-0.203
3	61	8.923	8.788	-0.135
3	62	8.336	8.313	-0.023
3	63	8.421	8.298	-0.123
3	64	8.679	8.499	-0.180
3	65	8.666	8.605	-0.061
3	66	8.601	8.432	-0.169
5	67	8.648	8.629	-0.019
5	68	8.540	8.520	-0.020
5	69	8.850	8.791	-0.059
5	70	8.723	8.650	-0.073
5	71	8.671	8.605	-0.066
5	72	8.528	8.463	-0.065
10	73	8.923	8.795	-0.128
10	74	8.336	8.309	-0.027
10	75	8.421	8.357	-0.064
10	76	8.679	8.487	-0.192
10	77	8.666	8.523	-0.143
10	78	8.601	8.554	-0.047
15	79	8.648	8.600	-0.048
15	80	8.540	8.452	-0.088
15	81	8.850	8.796	-0.054
15	82	8.723	8.633	-0.090
15	83	8.671	8.587	-0.084
15	84	8.528	8.440	-0.088
30	86	8.923	8.897	-0.026
30	89	8.336	8.401	0.065
30	91	8.421	8.418	-0.003
30	92	8.679	8.550	-0.129
30	94	8.666	8.629	-0.037
30	95	8.601	8.588	-0.013
35	100	8.648	8.725	0.077
35	101	8.540	8.543	0.003
35	102	8.850	8.843	-0.007
35	103	8.723	8.805	0.082
35	104	8.671	8.687	0.016
35	105	8.528	8.585	0.057
50	106	8.490	8.316	-0.174
50	107	8.675	8.402	-0.273
50	108	8.512	8.296	-0.216
50	109	8.677	8.474	-0.203
50	110	8.514	8.314	-0.200
50	111	8.486	8.317	-0.169
55	113	8.551	8.410	-0.141
55	114	8.970	8.654	-0.316
55	115	8.550	8.508	-0.042
55	116	8.607	8.434	-0.173
55	126	8.511	8.446	-0.065
55	128	8.749	8.644	-0.105
100	129	8.582	8.541	-0.041
100	131	8.488	8.510	0.022
100	132	8.480	8.538	0.058
100	133	8.484	8.470	-0.014
100	134	8.423	8.432	0.009
100	135	8.736	8.713	-0.023
100	138	8.678	8.696	0.018
100	139	8.311	8.373	0.062
100	140	8.527	8.514	-0.013
100	141	8.683	8.632	-0.051
100	142	8.536	8.620	0.084
100	143	8.757	8.822	0.065
100	144	8.292	8.374	0.082
100	220	8.791	8.834	0.043
100	221	8.551	8.469	-0.082
100	224	8.544	8.550	0.006
100	225	8.709	8.653	-0.056
100	226	8.523	8.558	0.035
100	228	8.716	8.739	0.023
100	232	8.736	8.605	-0.131
100	233	8.645	8.567	-0.078
100	236	8.633	8.606	-0.027
100	238	8.857	8.723	-0.134
100	243	8.498	8.454	-0.044
105	244	8.596	8.586	-0.010
105	246	8.498	8.617	0.119
105	248	8.839	8.937	0.098
105	374	8.567	8.592	0.025
105	376	8.747	8.742	-0.005
105	Max	8.970	8.937	0.119
105	Average	8.612	8.558	-0.054
105	Min	8.271	8.197	-0.316
105	Std Dev	0.153	0.157	0.089



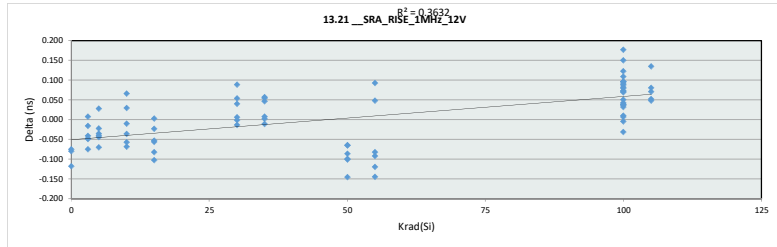
13.13_SRA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.197	8.298	8.463	8.309	8.440	8.401	8.543	8.296	8.410	8.373	8.586
Average	8.379	8.489	8.610	8.504	8.585	8.581	8.698	8.353	8.516	8.583	8.695
Max	8.477	8.788	8.791	8.795	8.796	8.897	8.843	8.474	8.654	8.834	8.937
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



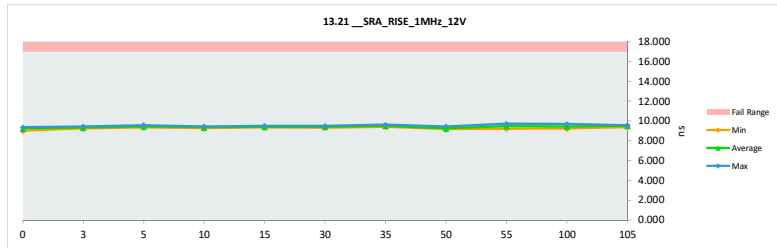
LDR TID Report  
TPS7H5001-SP

		13.21_SRA_RISE_1MHz_12V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.450	9.333	-0.117
0	118	9.100	9.026	-0.074
0	122	9.454	9.375	-0.079
3	61	9.520	9.446	-0.074
3	62	9.303	9.288	-0.015
3	63	9.368	9.377	0.009
3	64	9.370	9.322	-0.048
3	65	9.294	9.248	-0.046
3	66	9.492	9.452	-0.040
5	67	9.511	9.540	0.029
5	68	9.429	9.408	-0.021
5	69	9.632	9.593	-0.039
5	70	9.444	9.375	-0.069
5	71	9.507	9.464	-0.043
5	72	9.434	9.400	-0.034
10	73	9.520	9.464	-0.056
10	74	9.303	9.334	0.031
10	75	9.368	9.435	0.067
10	76	9.370	9.335	-0.035
10	77	9.294	9.285	-0.009
10	78	9.492	9.425	-0.067
15	79	9.511	9.515	0.004
15	80	9.429	9.407	-0.022
15	81	9.632	9.531	-0.101
15	82	9.444	9.363	-0.081
15	83	9.507	9.455	-0.052
15	84	9.434	9.379	-0.055
30	86	9.520	9.508	-0.012
30	89	9.303	9.393	0.090
30	91	9.368	9.423	0.055
30	92	9.370	9.377	0.007
30	94	9.294	9.335	0.041
30	95	9.492	9.492	0.000
35	100	9.511	9.559	0.048
35	101	9.429	9.419	-0.010
35	102	9.632	9.641	0.009
35	103	9.444	9.502	0.058
35	104	9.507	9.510	0.003
35	105	9.434	9.490	0.056
50	106	9.342	9.244	-0.098
50	107	9.329	9.244	-0.085
50	108	9.288	9.188	-0.100
50	109	9.605	9.461	-0.144
50	110	9.253	9.190	-0.063
50	111	9.398	9.334	-0.064
55	113	9.379	9.236	-0.143
55	114	9.716	9.598	-0.118
55	115	9.591	9.500	-0.091
55	116	9.528	9.447	-0.081
55	126	9.385	9.434	0.049
55	128	9.635	9.729	0.094
100	129	9.325	9.365	0.040
100	131	9.433	9.503	0.070
100	132	9.420	9.544	0.124
100	133	9.176	9.249	0.073
100	134	9.337	9.370	0.033
100	135	9.547	9.657	0.110
100	138	9.475	9.513	0.038
100	139	9.244	9.283	0.039
100	140	9.391	9.402	0.011
100	141	9.512	9.556	0.044
100	142	9.441	9.532	0.091
100	143	9.553	9.650	0.097
100	144	9.304	9.393	0.089
100	220	9.578	9.630	0.052
100	221	9.515	9.485	-0.030
100	224	9.355	9.452	0.097
100	225	9.269	9.343	0.074
100	226	9.249	9.427	0.178
100	228	9.425	9.522	0.097
100	232	9.317	9.398	0.081
100	233	9.395	9.403	0.008
100	236	9.442	9.439	-0.003
100	238	9.556	9.707	0.151
100	243	9.326	9.409	0.083
105	244	9.388	9.460	0.072
105	246	9.455	9.591	0.136
105	248	9.542	9.591	0.049
105	374	9.332	9.385	0.053
105	376	9.504	9.586	0.082
105	Max	9.716	9.729	0.178
105	Average	9.427	9.433	0.006
105	Min	9.100	9.026	-0.144
105	Std Dev	0.114	0.125	0.073



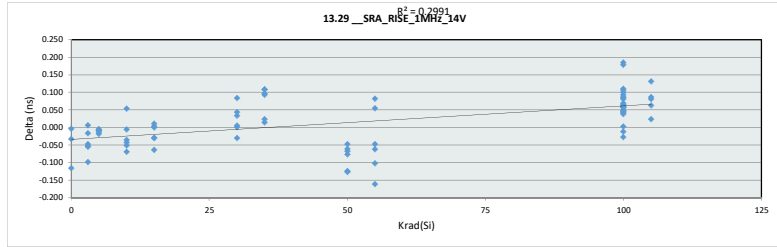
		13.21_SRA_RISE	
Test Site			
Tester			
Test Number			
Max Limit	17	ns	
Min Limit		ns	
Krad(Si)	0	3	5
LL			
Min	9.026	9.248	9.375
Average	9.245	9.356	9.463
Max	9.375	9.452	9.593
UL	17.000	17.000	17.000



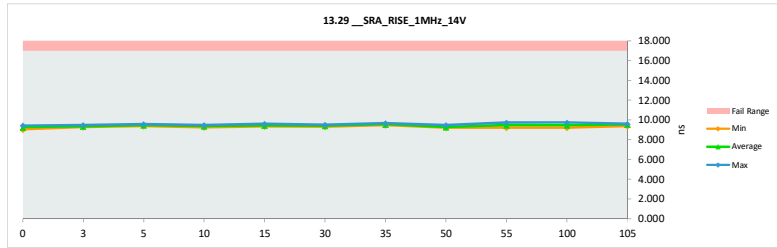
LDR TID Report  
TPS7H5001-SP

13.29_SRA_RISE_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.444	9.330	-0.114
0	118	9.071	9.039	-0.032
0	122	9.441	9.439	-0.002
3	61	9.520	9.467	-0.053
3	62	9.293	9.278	-0.015
3	63	9.408	9.356	-0.052
3	64	9.374	9.328	-0.046
3	65	9.281	9.289	0.008
3	66	9.512	9.415	-0.097
5	67	9.521	9.515	-0.006
5	68	9.461	9.455	-0.006
5	69	9.593	9.581	-0.012
5	70	9.397	9.394	-0.003
5	71	9.511	9.494	-0.017
5	72	9.396	9.388	-0.008
10	73	9.520	9.470	-0.050
10	74	9.293	9.348	0.055
10	75	9.408	9.404	-0.004
10	76	9.374	9.332	-0.042
10	77	9.281	9.247	-0.034
10	78	9.512	9.444	-0.068
15	79	9.521	9.533	0.012
15	80	9.461	9.398	-0.063
15	81	9.593	9.598	0.005
15	82	9.397	9.398	0.001
15	83	9.511	9.482	-0.029
15	84	9.396	9.368	-0.028
30	86	9.520	9.522	0.002
30	89	9.293	9.328	0.035
30	91	9.408	9.453	0.045
30	92	9.374	9.345	-0.029
30	94	9.281	9.366	0.085
30	95	9.512	9.519	0.007
35	100	9.521	9.631	0.110
35	101	9.461	9.486	0.025
35	102	9.593	9.687	0.094
35	103	9.397	9.507	0.110
35	104	9.511	9.527	0.016
35	105	9.396	9.494	0.098
50	106	9.337	9.271	-0.066
50	107	9.310	9.235	-0.075
50	108	9.353	9.230	-0.123
50	109	9.602	9.477	-0.125
50	110	9.292	9.232	-0.060
50	111	9.389	9.343	-0.046
55	113	9.335	9.234	-0.101
55	114	9.730	9.570	-0.160
55	115	9.565	9.519	-0.046
55	116	9.533	9.472	-0.061
55	126	9.287	9.343	0.056
55	128	9.659	9.742	0.083
100	129	9.293	9.353	0.060
100	131	9.403	9.504	0.101
100	132	9.387	9.475	0.088
100	133	9.249	9.238	-0.011
100	134	9.324	9.406	0.082
100	135	9.563	9.749	0.186
100	138	9.510	9.549	0.039
100	139	9.278	9.344	0.066
100	140	9.369	9.412	0.043
100	141	9.543	9.606	0.063
100	142	9.464	9.513	0.049
100	143	9.595	9.641	0.046
100	144	9.389	9.363	-0.026
100	220	9.576	9.635	0.059
100	221	9.457	9.543	0.086
100	224	9.375	9.425	0.050
100	225	9.244	9.355	0.111
100	226	9.261	9.441	0.180
100	228	9.464	9.534	0.070
100	232	9.355	9.414	0.059
100	233	9.388	9.392	0.004
100	236	9.397	9.491	0.094
100	238	9.630	9.738	0.108
100	243	9.375	9.434	0.059
105	244	9.418	9.500	0.082
105	246	9.451	9.583	0.132
105	248	9.520	9.608	0.088
105	374	9.356	9.381	0.025
105	376	9.523	9.587	0.064
105	Max	9.730	9.749	0.186
105	Average	9.429	9.444	0.015
105	Min	9.071	9.039	-0.160
105	Std Dev	0.115	0.131	0.070



13.29_SRA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.039	9.278	9.388	9.247	9.368	9.328	9.486	9.230	9.234	9.238	9.381
Average	9.269	9.356	9.471	9.374	9.463	9.422	9.555	9.298	9.480	9.481	9.532
Max	9.439	9.467	9.581	9.470	9.598	9.522	9.687	9.477	9.742	9.749	9.608
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

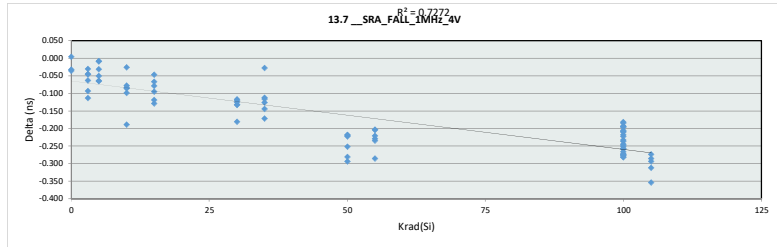




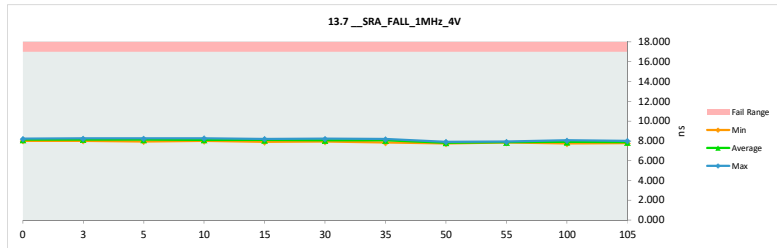
LDR TID Report  
TPS7H5001-SP

13.7_SRA_FALL_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.199	8.204	0.005
0	118	8.025	7.994	-0.031
0	122	8.190	8.155	-0.035
3	61	8.158	8.129	-0.029
3	62	8.339	8.247	-0.092
3	63	8.075	8.030	-0.045
3	64	8.160	8.098	-0.062
3	65	8.051	8.007	-0.044
3	66	8.353	8.241	-0.112
5	67	7.942	7.934	-0.008
5	68	8.287	8.223	-0.064
5	69	8.125	8.095	-0.030
5	70	8.115	8.107	-0.008
5	71	8.187	8.124	-0.063
5	72	8.297	8.248	-0.049
10	73	8.158	8.076	-0.082
10	74	8.339	8.254	-0.085
10	75	8.075	7.977	-0.098
10	76	8.160	8.135	-0.025
10	77	8.051	7.974	-0.077
10	78	8.353	8.165	-0.188
15	79	7.942	7.896	-0.046
15	80	8.287	8.193	-0.094
15	81	8.125	8.047	-0.078
15	82	8.115	8.049	-0.066
15	83	8.187	8.069	-0.118
15	84	8.297	8.169	-0.128
30	86	8.158	8.034	-0.124
30	89	8.339	8.208	-0.131
30	91	8.075	7.956	-0.119
30	92	8.160	8.044	-0.116
30	94	8.051	7.920	-0.131
30	95	8.353	8.173	-0.180
35	100	7.942	7.831	-0.111
35	101	8.287	8.172	-0.115
35	102	8.125	8.098	-0.027
35	103	8.115	7.990	-0.125
35	104	8.187	8.016	-0.171
35	105	8.297	8.154	-0.143
50	106	8.083	7.803	-0.280
50	107	8.053	7.831	-0.222
50	108	8.129	7.912	-0.217
50	109	7.947	7.729	-0.218
50	110	8.134	7.841	-0.293
50	111	8.022	7.771	-0.251
55	113	8.050	7.830	-0.220
55	114	8.044	7.811	-0.233
55	115	8.042	7.814	-0.228
55	116	8.197	7.913	-0.284
55	126	8.069	7.867	-0.202
55	128	8.084	7.882	-0.202
100	129	8.087	7.852	-0.235
100	131	8.154	7.903	-0.251
100	132	8.245	8.036	-0.209
100	133	8.214	8.006	-0.208
100	134	8.027	7.835	-0.192
100	135	8.210	7.938	-0.272
100	138	8.073	7.816	-0.257
100	139	7.944	7.727	-0.217
100	140	8.162	7.887	-0.275
100	141	8.116	7.844	-0.272
100	142	8.135	7.931	-0.204
100	143	8.084	7.892	-0.192
100	144	7.954	7.759	-0.195
100	220	8.156	7.934	-0.222
100	221	8.057	7.825	-0.232
100	224	8.104	7.830	-0.274
100	225	8.139	7.874	-0.265
100	226	8.077	7.821	-0.256
100	228	8.279	8.036	-0.243
100	232	8.084	7.903	-0.181
100	233	8.182	7.999	-0.183
100	236	8.069	7.788	-0.281
100	238	8.150	7.904	-0.246
100	243	8.129	7.849	-0.280
105	244	8.199	7.846	-0.353
105	246	8.069	7.796	-0.273
105	248	8.145	7.834	-0.311
105	374	8.052	7.768	-0.284
105	376	8.288	7.995	-0.293
	Max	8.353	8.254	0.005
	Average	8.139	7.973	-0.166
	Min	7.942	7.727	-0.353
	Std Dev	0.105	0.146	0.092



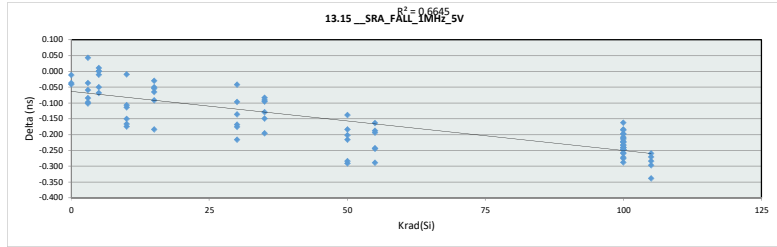
13.7_SRA_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.994	8.007	7.934	7.974	7.896	7.920	7.831	7.729	7.811	7.727	7.768
Average	8.118	8.125	8.122	8.097	8.071	8.056	8.044	7.815	7.853	7.883	7.848
Max	8.204	8.247	8.248	8.254	8.193	8.208	8.172	7.912	7.913	8.036	7.995
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



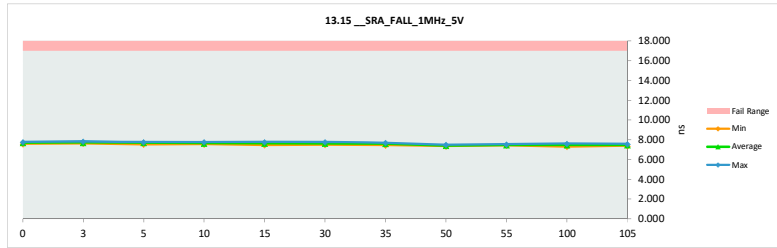
LDR TID Report  
TPS7H5001-SP

13.15_SRA_FALL_1MHz_5V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	17	17
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.795	7.755	-0.040
0	118	7.609	7.573	-0.036
0	122	7.775	7.765	-0.010
3	61	7.802	7.701	-0.101
3	62	7.893	7.835	-0.058
3	63	7.720	7.623	-0.097
3	64	7.642	7.686	0.044
3	65	7.657	7.621	-0.036
3	66	7.873	7.790	-0.083
5	67	7.524	7.526	0.002
5	68	7.754	7.756	0.002
5	69	7.738	7.671	-0.067
5	70	7.672	7.684	0.012
5	71	7.755	7.746	-0.009
5	72	7.818	7.769	-0.049
10	73	7.802	7.629	-0.173
10	74	7.893	7.728	-0.165
10	75	7.720	7.571	-0.149
10	76	7.642	7.634	-0.008
10	77	7.657	7.551	-0.106
10	78	7.873	7.760	-0.113
15	79	7.524	7.460	-0.064
15	80	7.754	7.725	-0.029
15	81	7.738	7.555	-0.183
15	82	7.672	7.623	-0.049
15	83	7.755	7.665	-0.090
15	84	7.818	7.765	-0.053
30	86	7.802	7.667	-0.135
30	89	7.893	7.719	-0.174
30	91	7.720	7.505	-0.215
30	92	7.642	7.601	-0.041
30	94	7.657	7.490	-0.167
30	95	7.873	7.777	-0.096
35	100	7.524	7.442	-0.082
35	101	7.754	7.660	-0.094
35	102	7.738	7.543	-0.195
35	103	7.672	7.583	-0.089
35	104	7.755	7.607	-0.148
35	105	7.818	7.690	-0.128
50	106	7.567	7.384	-0.183
50	107	7.663	7.380	-0.283
50	108	7.694	7.493	-0.201
50	109	7.563	7.348	-0.215
50	110	7.608	7.471	-0.137
50	111	7.650	7.360	-0.290
55	113	7.680	7.438	-0.242
55	114	7.618	7.425	-0.193
55	115	7.638	7.451	-0.187
55	116	7.770	7.482	-0.288
55	126	7.696	7.453	-0.243
55	128	7.705	7.543	-0.162
100	129	7.714	7.475	-0.239
100	131	7.687	7.465	-0.222
100	132	7.790	7.605	-0.185
100	133	7.788	7.548	-0.240
100	134	7.660	7.415	-0.245
100	135	7.738	7.505	-0.233
100	138	7.656	7.473	-0.183
100	139	7.578	7.303	-0.275
100	140	7.690	7.483	-0.207
100	141	7.702	7.456	-0.246
100	142	7.670	7.509	-0.161
100	143	7.700	7.469	-0.231
100	144	7.570	7.347	-0.223
100	220	7.719	7.499	-0.220
100	221	7.662	7.404	-0.258
100	224	7.628	7.431	-0.197
100	225	7.717	7.507	-0.210
100	226	7.726	7.439	-0.287
100	228	7.853	7.582	-0.271
100	232	7.636	7.439	-0.197
100	233	7.773	7.533	-0.240
100	236	7.656	7.410	-0.246
100	238	7.735	7.479	-0.256
100	243	7.675	7.462	-0.213
105	244	7.795	7.458	-0.337
105	246	7.647	7.377	-0.270
105	248	7.785	7.489	-0.296
105	374	7.721	7.438	-0.283
105	376	7.840	7.582	-0.258
105	Max	7.893	7.835	0.044
105	Average	7.714	7.553	-0.160
105	Min	7.524	7.303	-0.337
105	Std Dev	0.089	0.128	0.092



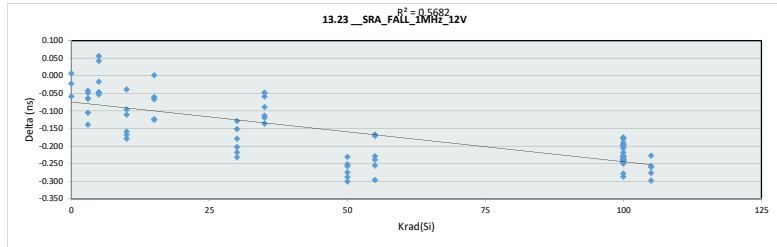
13.15_SRA_FALL											
Test Site											
Tester											
Test Number											
Min Limit	17	ns									
Max Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.573	7.621	7.526	7.551	7.460	7.490	7.442	7.348	7.425	7.303	7.377
Average	7.698	7.709	7.692	7.646	7.632	7.627	7.588	7.406	7.465	7.468	7.469
Max	7.765	7.835	7.769	7.760	7.765	7.777	7.690	7.493	7.543	7.605	7.582
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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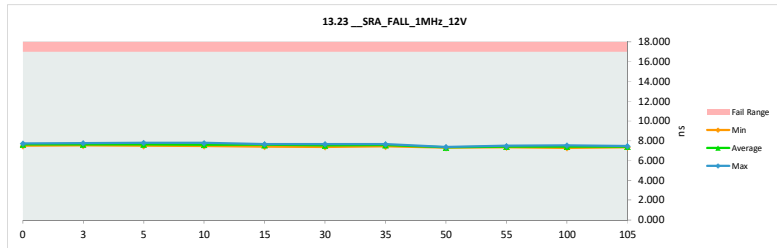
13.23_SRA_FALL_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.776	7.755	-0.021
0	118	7.495	7.503	0.008
0	122	7.749	7.691	-0.058
3	61	7.741	7.603	-0.138
3	62	7.830	7.767	-0.063
3	63	7.670	7.629	-0.041
3	64	7.739	7.635	-0.104
3	65	7.598	7.534	-0.064
3	66	7.823	7.775	-0.048
5	67	7.554	7.508	-0.046
5	68	7.725	7.673	-0.052
5	69	7.649	7.692	0.043
5	70	7.607	7.591	-0.016
5	71	7.724	7.678	-0.046
5	72	7.748	7.805	0.057
10	73	7.741	7.574	-0.167
10	74	7.830	7.792	-0.038
10	75	7.670	7.575	-0.095
10	76	7.739	7.561	-0.178
10	77	7.598	7.488	-0.110
10	78	7.823	7.665	-0.158
15	79	7.554	7.430	-0.124
15	80	7.725	7.665	-0.060
15	81	7.649	7.651	0.002
15	82	7.607	7.546	-0.061
15	83	7.724	7.602	-0.122
15	84	7.748	7.682	-0.066
30	86	7.741	7.510	-0.231
30	89	7.830	7.679	-0.151
30	91	7.670	7.542	-0.128
30	92	7.739	7.561	-0.178
30	94	7.598	7.381	-0.217
30	95	7.823	7.621	-0.202
35	100	7.554	7.442	-0.112
35	101	7.725	7.590	-0.135
35	102	7.649	7.602	-0.047
35	103	7.607	7.519	-0.088
35	104	7.724	7.605	-0.119
35	105	7.748	7.690	-0.058
50	106	7.607	7.307	-0.300
50	107	7.620	7.364	-0.256
50	108	7.621	7.370	-0.251
50	109	7.584	7.310	-0.274
50	110	7.609	7.322	-0.287
50	111	7.611	7.381	-0.230
55	113	7.598	7.370	-0.228
55	114	7.679	7.384	-0.295
55	115	7.637	7.467	-0.170
55	116	7.761	7.507	-0.254
55	126	7.534	7.368	-0.166
55	128	7.705	7.467	-0.238
100	129	7.641	7.414	-0.227
100	131	7.653	7.479	-0.174
100	132	7.747	7.507	-0.240
100	133	7.707	7.512	-0.195
100	134	7.577	7.335	-0.242
100	135	7.757	7.521	-0.236
100	138	7.616	7.415	-0.201
100	139	7.513	7.287	-0.226
100	140	7.612	7.363	-0.249
100	141	7.678	7.473	-0.205
100	142	7.679	7.446	-0.233
100	143	7.666	7.477	-0.189
100	144	7.534	7.337	-0.197
100	220	7.659	7.465	-0.194
100	221	7.603	7.386	-0.217
100	224	7.530	7.354	-0.176
100	225	7.710	7.424	-0.286
100	226	7.635	7.358	-0.277
100	228	7.730	7.552	-0.178
100	232	7.606	7.377	-0.229
100	233	7.758	7.514	-0.244
100	236	7.647	7.408	-0.239
100	238	7.694	7.456	-0.238
100	243	7.659	7.431	-0.228
105	244	7.694	7.468	-0.226
105	246	7.645	7.370	-0.275
105	248	7.649	7.393	-0.256
105	374	7.662	7.365	-0.297
105	376	7.751	7.492	-0.259
	Max	7.830	7.805	0.025
	Average	7.673	7.510	-0.163
	Min	7.495	7.287	-0.208
	Std Dev	0.081	0.130	0.091



13.23_SRA_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

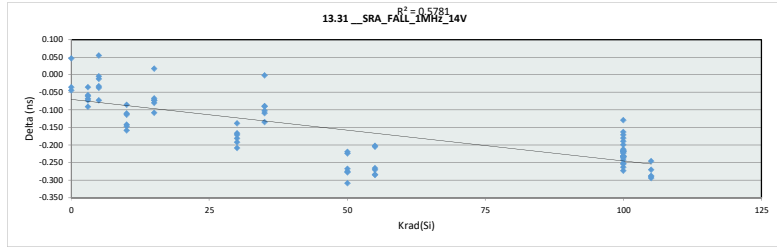
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.503	7.534	7.508	7.488	7.430	7.381	7.442	7.307	7.368	7.287	7.365
Average	7.650	7.657	7.658	7.609	7.596	7.549	7.575	7.342	7.427	7.429	7.418
Max	7.755	7.775	7.805	7.792	7.682	7.679	7.690	7.381	7.507	7.552	7.492
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



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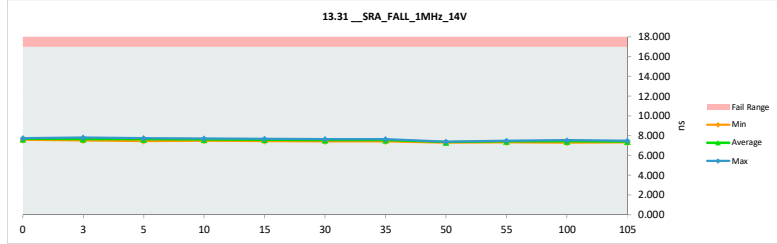
13.31_SRA_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.797	7.763	-0.034
0	118	7.528	7.576	0.048
0	122	7.751	7.708	-0.043
3	61	7.719	7.652	-0.067
3	62	7.874	7.814	-0.060
3	63	7.685	7.595	-0.090
3	64	7.733	7.662	-0.071
3	65	7.575	7.541	-0.034
3	66	7.816	7.759	-0.057
5	67	7.535	7.464	-0.071
5	68	7.662	7.718	0.056
5	69	7.680	7.670	-0.010
5	70	7.624	7.588	-0.036
5	71	7.717	7.686	-0.031
5	72	7.725	7.772	-0.003
10	73	7.719	7.578	-0.141
10	74	7.874	7.729	-0.145
10	75	7.685	7.576	-0.109
10	76	7.733	7.576	-0.157
10	77	7.575	7.491	-0.084
10	78	7.816	7.704	-0.112
15	79	7.535	7.464	-0.071
15	80	7.662	7.681	0.019
15	81	7.680	7.601	-0.079
15	82	7.624	7.517	-0.107
15	83	7.717	7.651	-0.066
15	84	7.775	7.704	-0.071
30	86	7.719	7.528	-0.191
30	89	7.874	7.667	-0.207
30	91	7.685	7.520	-0.165
30	92	7.733	7.553	-0.180
30	94	7.575	7.438	-0.137
30	95	7.816	7.646	-0.170
35	100	7.535	7.434	-0.101
35	101	7.662	7.662	0.000
35	102	7.680	7.592	-0.088
35	103	7.624	7.535	-0.089
35	104	7.717	7.584	-0.133
35	105	7.725	7.667	-0.108
50	106	7.548	7.325	-0.223
50	107	7.630	7.356	-0.274
50	108	7.667	7.360	-0.307
50	109	7.572	7.296	-0.276
50	110	7.628	7.362	-0.266
50	111	7.621	7.403	-0.218
55	113	7.586	7.385	-0.201
55	114	7.694	7.411	-0.283
55	115	7.708	7.439	-0.269
55	116	7.747	7.483	-0.264
55	126	7.626	7.342	-0.284
55	128	7.718	7.515	-0.203
100	129	7.648	7.415	-0.233
100	131	7.653	7.438	-0.215
100	132	7.732	7.553	-0.179
100	133	7.706	7.444	-0.262
100	134	7.583	7.311	-0.272
100	135	7.762	7.520	-0.242
100	138	7.612	7.394	-0.218
100	139	7.496	7.298	-0.198
100	140	7.587	7.369	-0.218
100	141	7.711	7.476	-0.235
100	142	7.657	7.427	-0.230
100	143	7.693	7.462	-0.231
100	144	7.541	7.353	-0.188
100	220	7.695	7.484	-0.211
100	221	7.613	7.383	-0.230
100	224	7.567	7.350	-0.217
100	225	7.670	7.417	-0.253
100	226	7.641	7.390	-0.251
100	228	7.740	7.526	-0.214
100	232	7.601	7.431	-0.170
100	233	7.706	7.474	-0.232
100	236	7.654	7.432	-0.222
100	238	7.631	7.503	-0.128
100	243	7.626	7.464	-0.162
105	244	7.710	7.466	-0.244
105	246	7.638	7.369	-0.269
105	248	7.666	7.377	-0.289
105	374	7.647	7.354	-0.293
105	376	7.787	7.501	-0.286
	Max	7.874	7.814	0.056
	Average	7.675	7.514	-0.161
	Min	7.496	7.296	-0.307
	Std Dev	0.084	0.130	0.093



13.31_SRA_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	

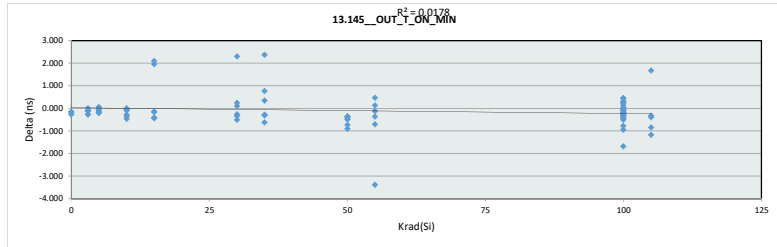
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.576	7.541	7.464	7.491	7.464	7.438	7.434	7.296	7.342	7.298	7.354
Average	7.682	7.671	7.650	7.609	7.603	7.559	7.579	7.350	7.429	7.430	7.413
Max	7.763	7.814	7.772	7.729	7.704	7.667	7.667	7.403	7.515	7.553	7.501
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



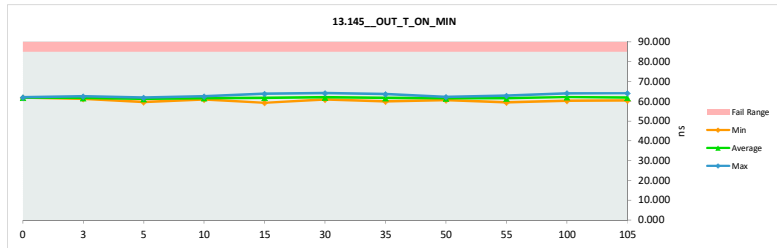
LDR TID Report  
TPS7H5001-SP

13.145_OUT_T_ON_MIN	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	85 85
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	62.194	62.046	-0.148
0	118	61.936	61.809	-0.127
0	122	62.069	61.827	-0.242
3	61	62.508	62.529	0.021
3	62	61.780	61.681	-0.099
3	63	61.428	61.182	-0.246
3	64	61.685	61.628	-0.057
3	65	61.842	61.745	-0.097
3	66	61.761	61.518	-0.243
5	67	61.840	61.664	-0.176
5	68	61.378	61.311	-0.067
5	69	62.081	61.906	-0.175
5	70	61.278	61.357	0.079
5	71	61.598	61.470	-0.128
5	72	59.635	59.636	0.001
10	73	62.508	62.471	-0.037
10	74	61.780	61.808	0.028
10	75	61.428	60.976	-0.452
10	76	61.685	61.609	-0.076
10	77	61.842	61.582	-0.260
10	78	61.761	61.435	-0.326
15	79	61.840	63.812	1.972
15	80	61.378	61.261	-0.117
15	81	62.081	61.928	-0.153
15	82	61.278	63.394	2.116
15	83	61.598	61.210	-0.388
15	84	59.635	59.219	-0.416
30	86	62.508	62.266	-0.242
30	89	61.780	64.094	2.314
30	91	61.428	60.934	-0.494
30	92	61.685	61.803	0.118
30	94	61.842	62.100	0.258
30	95	61.761	61.448	-0.313
35	100	61.840	61.554	-0.286
35	101	61.378	60.774	-0.604
35	102	62.081	61.814	-0.267
35	103	61.278	63.660	2.382
35	104	61.598	62.384	0.786
35	105	59.635	60.000	0.365
50	106	61.004	60.652	-0.352
50	107	62.561	62.156	-0.405
50	108	62.694	61.975	-0.719
50	109	62.422	61.540	-0.882
50	110	61.625	61.146	-0.479
50	111	61.945	61.610	-0.335
55	113	60.862	61.016	0.154
55	114	63.003	62.902	-0.101
55	115	60.109	59.429	-0.680
55	116	65.227	61.862	-3.365
55	126	62.586	62.236	-0.350
55	128	61.675	62.166	0.491
100	129	60.544	60.286	-0.258
100	131	62.657	63.113	0.456
100	132	60.800	60.752	-0.048
100	133	61.969	61.653	-0.316
100	134	62.743	62.261	-0.482
100	135	63.668	63.525	-0.143
100	138	61.265	60.930	-0.335
100	139	63.211	63.268	0.057
100	140	61.700	60.938	-0.762
100	141	63.559	63.870	0.311
100	142	61.470	61.061	-0.409
100	143	62.422	62.462	0.040
100	144	62.144	61.917	-0.227
100	220	60.954	60.887	-0.067
100	221	63.467	63.614	0.147
100	224	62.128	62.069	-0.059
100	225	63.476	63.709	0.233
100	226	63.103	61.443	-1.660
100	228	61.497	60.573	-0.924
100	232	62.399	61.964	-0.435
100	233	63.745	64.080	0.335
100	236	63.672	63.499	-0.173
100	238	61.054	61.532	0.478
100	243	62.175	61.998	-0.177
105	244	62.614	62.244	-0.370
105	246	62.715	61.565	-1.150
105	248	60.732	60.429	-0.303
105	374	61.925	61.092	-0.833
105	376	62.411	64.104	1.693
105	375	65.227	64.104	2.382
105	Max	61.932	61.830	-0.102
105	Average	61.932	61.830	-0.102
105	Min	59.635	59.219	-3.365
105	Std Dev	0.941	1.060	0.767



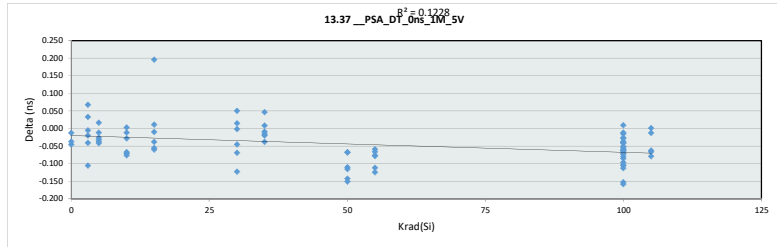
13.145_OUT_T_ON_MIN											
Test Site											
Tester											
Test Number											
Max Limit	85 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	61.809	61.182	59.636	60.976	59.219	60.934	60.000	60.652	59.429	60.286	60.429
Average	61.894	61.714	61.224	61.647	61.804	62.108	61.698	61.513	61.602	62.142	61.887
Max	62.046	62.529	61.906	62.471	63.812	64.094	63.660	62.156	62.902	64.080	64.104
UL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000



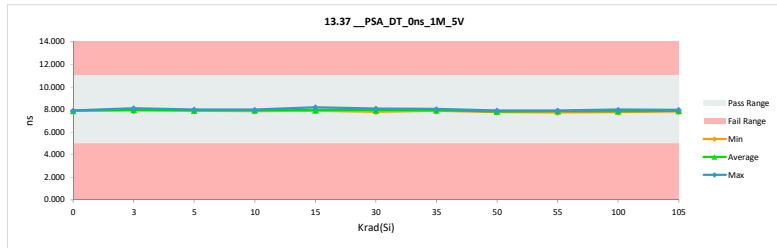
LDR TID Report  
TPS7H5001-SP

		13.37 PSA_DT_0ns_1M_5V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	11	11	
Min Limit	5	5	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.941	7.897	-0.044
0	118	7.950	7.914	-0.036
0	122	7.927	7.915	-0.012
3	61	7.986	8.020	0.034
3	62	8.052	7.948	-0.104
3	63	7.991	7.951	-0.040
3	64	7.934	7.915	-0.019
3	65	8.047	8.116	0.069
3	66	7.983	7.979	-0.004
5	67	7.911	7.900	-0.011
5	68	7.959	7.934	-0.025
5	69	7.932	7.901	-0.031
5	70	8.005	7.964	-0.041
5	71	8.044	8.008	-0.036
5	72	7.890	7.908	0.018
10	73	7.986	7.990	0.004
10	74	8.052	7.986	-0.066
10	75	7.991	7.964	-0.027
10	76	7.934	7.864	-0.070
10	77	8.047	7.972	-0.075
10	78	7.983	7.972	-0.011
15	79	7.911	7.902	-0.009
15	80	7.959	7.905	-0.054
15	81	7.932	7.895	-0.037
15	82	8.005	8.202	0.197
15	83	8.044	7.985	-0.059
15	84	7.890	7.902	0.012
30	86	7.986	7.985	-0.001
30	89	8.052	7.984	-0.068
30	91	7.991	8.007	0.016
30	92	7.934	7.812	-0.122
30	94	8.047	8.098	0.051
30	95	7.983	7.939	-0.044
35	100	7.911	7.897	-0.014
35	101	7.959	7.940	-0.019
35	102	7.932	7.941	0.009
35	103	8.005	8.053	0.048
35	104	8.044	8.007	-0.037
35	105	7.890	7.882	-0.008
50	106	7.955	7.805	-0.150
50	107	8.036	7.894	-0.142
50	108	7.947	7.881	-0.066
50	109	7.886	7.777	-0.109
50	110	7.916	7.802	-0.114
50	111	7.996	7.929	-0.067
55	113	7.973	7.862	-0.111
55	114	7.961	7.896	-0.065
55	115	8.006	7.929	-0.077
55	116	7.958	7.900	-0.058
55	126	7.885	7.761	-0.124
55	128	7.966	7.891	-0.075
100	129	7.927	7.913	-0.014
100	131	7.989	7.885	-0.104
100	132	7.872	7.812	-0.060
100	133	7.954	7.859	-0.095
100	134	7.881	7.822	-0.059
100	135	7.935	7.851	-0.084
100	138	7.975	7.914	-0.061
100	139	7.944	7.832	-0.112
100	140	7.859	7.780	-0.079
100	141	8.034	7.877	-0.157
100	142	7.903	7.878	-0.025
100	143	7.984	7.931	-0.053
100	144	7.960	7.890	-0.070
100	220	7.934	7.869	-0.065
100	221	7.804	7.814	0.010
100	224	7.872	7.800	-0.072
100	225	7.893	7.790	-0.103
100	226	8.001	7.965	-0.036
100	228	7.936	7.925	-0.011
100	232	7.937	7.910	-0.027
100	233	7.895	7.854	-0.041
100	236	8.049	7.999	-0.050
100	238	7.958	7.806	-0.152
100	243	7.952	7.913	-0.039
105	244	7.932	7.870	-0.062
105	246	7.884	7.886	0.002
105	248	7.931	7.919	-0.012
105	374	8.046	7.981	-0.065
105	376	7.900	7.822	-0.078
105	Max	8.052	8.202	0.197
105	Average	7.958	7.913	-0.045
105	Min	7.804	7.761	-0.157
105	Std Dev	0.055	0.078	0.055



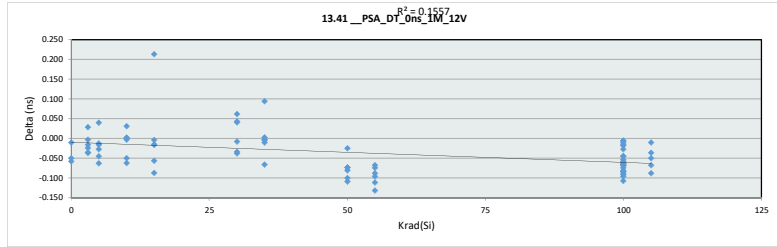
		13.37 PSA_DT_0									
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.897	7.915	7.900	7.864	7.895	7.812	7.882	7.777	7.761	7.780	7.822
Average	7.909	7.988	7.936	7.958	7.965	7.971	7.953	7.848	7.873	7.870	7.896
Max	7.915	8.116	8.008	7.990	8.202	8.098	8.053	7.929	7.929	7.999	7.981
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000



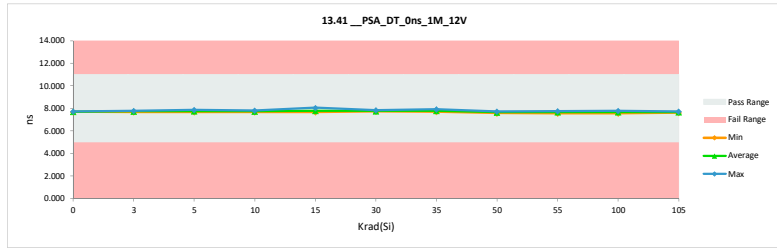
LDR TID Report  
TPS7H5001-SP

13.41 PSA_DT_Ons_1M_12V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	11	11
Min Limit	5	5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.784	7.735	-0.049
0	118	7.711	7.702	-0.009
0	122	7.774	7.717	-0.057
3	61	7.748	7.778	0.030
3	62	7.810	7.776	-0.034
3	63	7.818	7.803	-0.015
3	64	7.699	7.697	-0.002
3	65	7.770	7.735	-0.035
3	66	7.797	7.774	-0.023
5	67	7.746	7.684	-0.062
5	68	7.777	7.762	-0.015
5	69	7.776	7.764	-0.012
5	70	7.853	7.894	0.041
5	71	7.892	7.848	-0.044
5	72	7.710	7.684	-0.026
10	73	7.748	7.780	0.032
10	74	7.810	7.813	0.003
10	75	7.818	7.821	0.003
10	76	7.699	7.697	-0.002
10	77	7.770	7.709	-0.061
10	78	7.797	7.748	-0.049
15	79	7.746	7.732	-0.014
15	80	7.777	7.774	-0.003
15	81	7.776	7.721	-0.055
15	82	7.853	8.067	0.214
15	83	7.892	7.806	-0.086
15	84	7.710	7.695	-0.015
30	86	7.748	7.811	0.063
30	89	7.810	7.777	-0.033
30	91	7.818	7.860	0.042
30	92	7.699	7.743	0.044
30	94	7.770	7.763	-0.007
30	95	7.797	7.760	-0.037
35	100	7.746	7.737	-0.009
35	101	7.777	7.781	0.004
35	102	7.776	7.773	-0.003
35	103	7.853	7.948	0.095
35	104	7.892	7.827	-0.065
35	105	7.710	7.708	-0.002
50	106	7.677	7.653	-0.024
50	107	7.783	7.703	-0.080
50	108	7.736	7.664	-0.072
50	109	7.757	7.658	-0.099
50	110	7.700	7.592	-0.108
50	111	7.823	7.751	-0.072
55	113	7.760	7.665	-0.095
55	114	7.782	7.651	-0.131
55	115	7.852	7.785	-0.067
55	116	7.777	7.704	-0.073
55	126	7.691	7.581	-0.110
55	128	7.822	7.735	-0.087
100	129	7.726	7.642	-0.084
100	131	7.783	7.730	-0.053
100	132	7.716	7.649	-0.067
100	133	7.679	7.584	-0.095
100	134	7.692	7.634	-0.058
100	135	7.721	7.716	-0.005
100	138	7.789	7.728	-0.061
100	139	7.732	7.643	-0.089
100	140	7.670	7.655	-0.015
100	141	7.780	7.700	-0.080
100	142	7.712	7.702	-0.010
100	143	7.809	7.742	-0.067
100	144	7.766	7.676	-0.090
100	220	7.779	7.696	-0.083
100	221	7.741	7.635	-0.106
100	224	7.638	7.593	-0.045
100	225	7.668	7.642	-0.026
100	226	7.762	7.689	-0.073
100	228	7.717	7.713	-0.004
100	232	7.770	7.703	-0.067
100	233	7.709	7.692	-0.017
100	236	7.847	7.804	-0.043
100	238	7.780	7.716	-0.064
100	243	7.791	7.728	-0.063
105	244	7.721	7.686	-0.035
105	246	7.728	7.641	-0.087
105	248	7.750	7.701	-0.049
105	374	7.809	7.742	-0.067
105	376	7.683	7.674	-0.009
105	Max	7.892	8.067	0.214
105	Average	7.764	7.728	-0.036
105	Min	7.638	7.581	-0.131
105	Std Dev	0.055	0.079	0.052



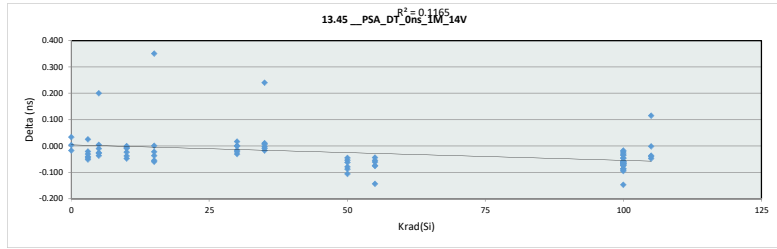
13.41 PSA_DT											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.702	7.697	7.684	7.697	7.695	7.743	7.708	7.592	7.581	7.584	7.641
Average	7.718	7.761	7.773	7.761	7.799	7.786	7.796	7.670	7.687	7.684	7.689
Max	7.735	7.803	7.894	7.821	8.067	7.860	7.948	7.751	7.785	7.804	7.742
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000



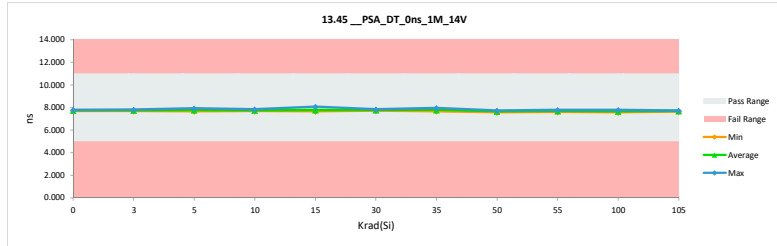
LDR TID Report  
TPS7H5001-SP

13.45 PSA_DT_0ns_1M_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	11 11
Min Limit	5 5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.758	7.743	-0.015
0	118	7.714	7.719	0.005
0	122	7.762	7.798	0.036
3	61	7.782	7.763	-0.019
3	62	7.861	7.822	-0.039
3	63	7.823	7.794	-0.029
3	64	7.752	7.702	-0.050
3	65	7.762	7.718	-0.044
3	66	7.772	7.799	0.027
5	67	7.743	7.735	-0.008
5	68	7.759	7.765	0.006
5	69	7.767	7.732	-0.035
5	70	7.729	7.931	0.202
5	71	7.857	7.833	-0.024
5	72	7.706	7.681	-0.025
10	73	7.782	7.746	-0.036
10	74	7.861	7.839	-0.022
10	75	7.823	7.824	0.001
10	76	7.752	7.706	-0.046
10	77	7.762	7.755	-0.007
10	78	7.772	7.768	-0.004
15	79	7.743	7.685	-0.058
15	80	7.759	7.739	-0.020
15	81	7.767	7.732	-0.035
15	82	7.729	8.081	0.352
15	83	7.857	7.804	-0.053
15	84	7.706	7.709	0.003
30	86	7.782	7.785	0.003
30	89	7.861	7.847	-0.014
30	91	7.823	7.824	0.001
30	92	7.752	7.723	-0.029
30	94	7.762	7.742	-0.020
30	95	7.772	7.791	0.019
35	100	7.743	7.736	-0.007
35	101	7.759	7.768	0.009
35	102	7.767	7.766	-0.001
35	103	7.729	7.971	0.242
35	104	7.857	7.869	0.012
35	105	7.706	7.691	-0.015
50	106	7.686	7.634	-0.052
50	107	7.758	7.681	-0.077
50	108	7.758	7.654	-0.104
50	109	7.734	7.691	-0.043
50	110	7.670	7.584	-0.086
50	111	7.804	7.743	-0.061
55	113	7.743	7.669	-0.074
55	114	7.832	7.690	-0.142
55	115	7.844	7.785	-0.059
55	116	7.777	7.705	-0.072
55	126	7.665	7.612	-0.053
55	128	7.785	7.743	-0.042
100	129	7.754	7.669	-0.085
100	131	7.784	7.699	-0.085
100	132	7.721	7.659	-0.062
100	133	7.662	7.589	-0.073
100	134	7.711	7.638	-0.073
100	135	7.738	7.693	-0.045
100	138	7.801	7.708	-0.093
100	139	7.704	7.671	-0.033
100	140	7.650	7.583	-0.067
100	141	7.786	7.696	-0.090
100	142	7.677	7.657	-0.020
100	143	7.802	7.745	-0.057
100	144	7.702	7.669	-0.033
100	220	7.772	7.707	-0.065
100	221	7.716	7.659	-0.057
100	224	7.685	7.621	-0.064
100	225	7.665	7.600	-0.065
100	226	7.819	7.673	-0.146
100	228	7.746	7.721	-0.025
100	232	7.788	7.719	-0.069
100	233	7.690	7.630	-0.060
100	236	7.852	7.796	-0.056
100	238	7.743	7.727	-0.016
100	243	7.789	7.746	-0.043
105	244	7.728	7.690	-0.038
105	246	7.596	7.713	0.117
105	248	7.711	7.711	0.000
105	374	7.791	7.745	-0.046
105	376	7.685	7.650	-0.035
105	378	7.861	8.081	0.352
105	Average	7.756	7.730	-0.026
105	Min	7.596	7.583	-0.146
105	Std Dev	0.055	0.083	0.071



13.45 PSA_DT_0											
Test Site											
Tester											
Test Number											
Max Limit	11 ns										
Min Limit	5 ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.719	7.702	7.681	7.706	7.685	7.723	7.691	7.584	7.612	7.583	7.650
Average	7.753	7.766	7.780	7.773	7.792	7.785	7.800	7.665	7.701	7.678	7.702
Max	7.798	7.822	7.931	7.839	8.081	7.847	7.971	7.743	7.785	7.796	7.745
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

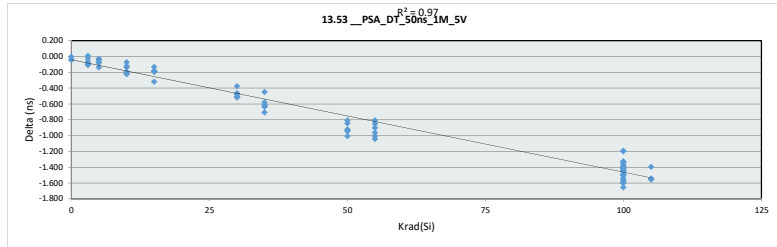




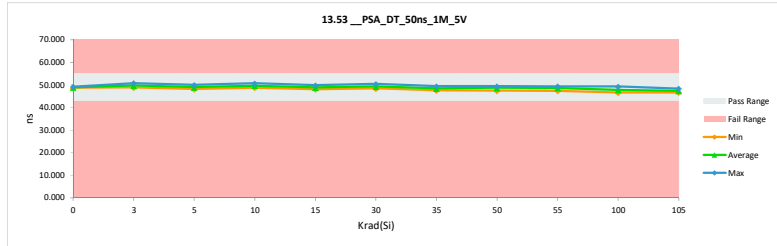
LDR TID Report  
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13.53 PSA_DT_50ns_1M_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	55 55
Min Limit	43 43

Krad(Si)	Serial #	PRE	POST	Delta
0	117	48.763	48.724	-0.039
0	118	49.284	49.284	0.000
0	122	48.872	48.836	-0.036
3	61	49.443	49.424	-0.019
3	62	50.945	50.842	-0.103
3	63	48.950	48.891	-0.059
3	64	50.024	49.929	-0.095
3	65	49.871	49.778	-0.093
3	66	49.743	49.757	0.014
5	67	49.259	49.186	-0.073
5	68	48.543	48.500	-0.043
5	69	48.434	48.300	-0.134
5	70	48.232	48.204	-0.028
5	71	50.067	49.994	-0.073
5	72	49.989	49.961	-0.028
10	73	49.443	49.374	-0.069
10	74	50.945	50.726	-0.219
10	75	48.950	48.817	-0.133
10	76	50.024	49.810	-0.214
10	77	49.871	49.681	-0.190
10	78	49.743	49.627	-0.116
15	79	49.259	49.078	-0.181
15	80	48.543	48.358	-0.185
15	81	48.434	48.120	-0.314
15	82	48.232	48.103	-0.129
15	83	50.067	49.888	-0.179
15	84	49.989	49.799	-0.190
30	86	49.443	49.070	-0.373
30	89	50.945	50.431	-0.514
30	91	48.950	48.489	-0.461
30	92	50.024	49.553	-0.471
30	94	49.871	49.366	-0.505
30	95	49.743	49.241	-0.502
35	100	49.259	48.634	-0.625
35	101	48.543	48.097	-0.446
35	102	48.434	47.731	-0.703
35	103	48.232	47.659	-0.573
35	104	50.067	49.437	-0.630
35	105	49.989	49.384	-0.605
50	106	50.389	49.473	-0.916
50	107	49.696	48.891	-0.805
50	108	48.355	47.515	-0.840
50	109	49.291	48.347	-0.944
50	110	49.935	49.011	-0.924
50	111	50.165	49.161	-1.004
55	113	50.175	49.218	-0.957
55	114	48.263	47.412	-0.851
55	115	50.220	49.218	-1.002
55	116	50.225	49.421	-0.804
55	126	49.167	48.268	-0.899
55	128	49.720	48.679	-1.041
100	129	49.798	48.433	-1.365
100	131	48.555	46.986	-1.569
100	132	49.133	47.644	-1.489
100	133	48.490	46.988	-1.502
100	134	50.073	48.483	-1.590
100	135	49.518	48.070	-1.448
100	138	49.690	48.286	-1.404
100	139	48.114	46.710	-1.404
100	140	48.309	46.899	-1.410
100	141	50.030	48.378	-1.652
100	142	48.311	46.927	-1.384
100	143	48.209	46.726	-1.483
100	144	49.693	48.361	-1.332
100	220	49.301	47.769	-1.532
100	221	49.776	48.588	-1.188
100	224	48.483	47.153	-1.330
100	225	49.930	48.522	-1.408
100	226	50.892	49.334	-1.558
100	228	50.023	48.427	-1.596
100	232	47.972	46.651	-1.321
100	233	49.639	48.447	-1.192
100	236	48.348	46.961	-1.387
100	238	49.646	48.207	-1.439
100	243	50.112	48.681	-1.431
105	244	48.519	46.978	-1.541
105	246	48.229	46.680	-1.549
105	248	49.386	47.844	-1.542
105	374	48.517	47.127	-1.390
105	376	49.925	48.377	-1.548
105	377	50.945	50.842	0.014
105	Average	49.371	48.592	-0.779
105	Min	47.972	46.651	-1.652
105	Std Dev	0.777	1.011	0.582



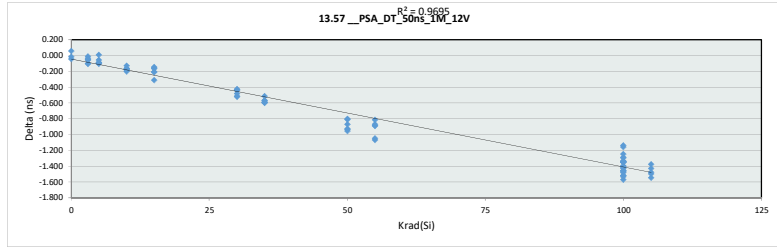
13.53 PSA DT		5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.724	48.891	48.204	48.817	48.103	48.489	47.659	47.515	47.412	46.651
Average	48.948	49.770	49.024	49.673	48.891	49.358	48.490	48.733	48.703	47.818
Max	49.284	50.842	49.994	50.726	49.888	50.431	49.437	49.473	49.421	49.334
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000



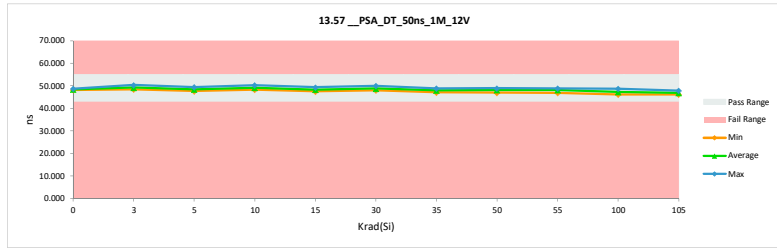
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13.57 PSA_DT 50ns_1M_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	55 55
Min Limit	43 43

Krad(Si)	Serial #	PRE	POST	Delta
0	117	48.273	48.261	-0.012
0	118	48.721	48.784	0.063
0	122	48.415	48.377	-0.038
3	61	48.966	48.935	-0.031
3	62	50.477	50.425	-0.052
3	63	48.527	48.480	-0.047
3	64	49.510	49.412	-0.098
3	65	49.314	49.212	-0.102
3	66	49.286	49.279	-0.007
5	67	48.809	48.726	-0.083
5	68	48.146	48.093	-0.053
5	69	47.914	47.828	-0.086
5	70	47.744	47.755	0.011
5	71	49.572	49.479	-0.093
5	72	49.506	49.405	-0.101
10	73	48.966	48.809	-0.157
10	74	50.477	50.314	-0.163
10	75	48.527	48.350	-0.177
10	76	49.510	49.314	-0.196
10	77	49.314	49.139	-0.175
10	78	49.286	49.161	-0.125
15	79	48.809	48.605	-0.204
15	80	48.146	47.945	-0.201
15	81	47.914	47.609	-0.305
15	82	47.744	47.593	-0.151
15	83	49.572	49.431	-0.141
15	84	49.506	49.346	-0.160
30	86	48.966	48.547	-0.419
30	89	50.477	50.041	-0.436
30	91	48.527	48.098	-0.429
30	92	49.510	48.992	-0.518
30	94	49.314	48.806	-0.508
30	95	49.286	48.811	-0.475
35	100	48.809	48.249	-0.560
35	101	48.146	47.635	-0.511
35	102	47.914	47.343	-0.571
35	103	47.744	47.157	-0.587
35	104	49.572	48.981	-0.591
35	105	49.506	48.913	-0.593
50	106	49.884	49.018	-0.866
50	107	49.194	48.391	-0.803
50	108	47.820	47.022	-0.798
50	109	48.911	47.960	-0.951
50	110	49.352	48.423	-0.929
50	111	49.667	48.745	-0.922
55	113	49.577	48.691	-0.886
55	114	47.864	46.997	-0.867
55	115	49.865	48.803	-1.062
55	116	49.793	48.984	-0.809
55	126	48.695	47.815	-0.880
55	128	49.323	48.281	-1.042
100	129	49.261	47.924	-1.337
100	131	48.160	46.595	-1.565
100	132	48.702	47.188	-1.514
100	133	47.939	46.488	-1.451
100	134	49.528	47.998	-1.530
100	135	49.087	47.666	-1.421
100	138	49.230	47.889	-1.341
100	139	47.602	46.261	-1.341
100	140	47.844	46.447	-1.397
100	141	49.451	47.987	-1.464
100	142	47.823	46.494	-1.329
100	143	47.753	46.305	-1.448
100	144	49.214	47.923	-1.291
100	220	48.804	47.389	-1.415
100	221	49.342	48.188	-1.154
100	224	48.013	46.683	-1.330
100	225	49.348	48.008	-1.340
100	226	50.336	48.824	-1.512
100	228	49.438	47.963	-1.475
100	232	47.494	46.213	-1.281
100	233	49.072	47.938	-1.134
100	236	47.775	46.532	-1.243
100	238	49.134	47.741	-1.393
100	243	49.549	48.204	-1.345
105	244	47.960	46.535	-1.425
105	246	47.796	46.255	-1.541
105	248	48.905	47.433	-1.472
105	374	48.016	46.645	-1.371
105	376	49.418	47.924	-1.494
	Max	50.477	50.425	0.063
	Average	48.883	48.130	-0.753
	Min	47.494	46.213	-1.565
	Std Dev	0.767	0.998	0.559



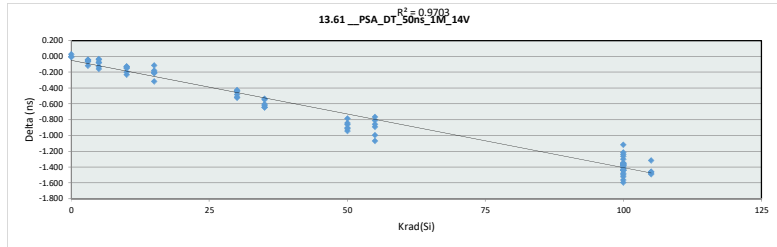
13.57 PSA_DT		5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.261	48.480	47.755	48.350	47.593	48.098	47.157	47.022	46.997	46.213
Average	48.474	49.291	48.548	49.181	48.422	48.883	48.046	48.260	48.262	47.369
Max	48.784	50.425	49.479	50.314	49.431	50.041	48.981	49.018	48.984	48.824
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000



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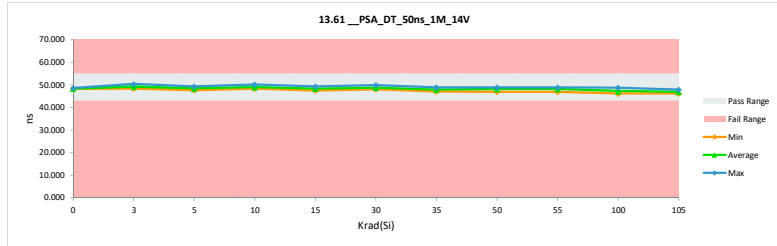
13.61_PSA_DT_50ns_1M_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	55 55
Min Limit	43 43

Krad(Si)	Serial #	PRE	POST	Delta
0	117	48.245	48.240	-0.005
0	118	48.741	48.747	0.006
0	122	48.361	48.395	0.034
3	61	48.972	48.933	-0.039
3	62	50.482	50.446	-0.036
3	63	48.514	48.454	-0.060
3	64	49.532	49.421	-0.111
3	65	49.271	49.217	-0.054
3	66	49.307	49.268	-0.039
5	67	48.792	48.725	-0.067
5	68	48.115	48.081	-0.034
5	69	47.934	47.810	-0.124
5	70	47.753	47.724	-0.029
5	71	49.596	49.445	-0.151
5	72	49.517	49.442	-0.075
10	73	48.972	48.852	-0.120
10	74	50.482	50.290	-0.192
10	75	48.514	48.388	-0.126
10	76	49.532	49.307	-0.225
10	77	49.271	49.134	-0.137
10	78	49.307	49.164	-0.143
15	79	48.792	48.620	-0.172
15	80	48.115	47.922	-0.193
15	81	47.934	47.625	-0.309
15	82	47.753	47.551	-0.202
15	83	49.596	49.390	-0.206
15	84	49.517	49.407	-0.110
30	86	48.972	48.545	-0.427
30	89	50.482	50.043	-0.439
30	91	48.514	48.092	-0.422
30	92	49.532	49.054	-0.478
30	94	49.271	48.763	-0.508
30	95	49.307	48.791	-0.516
35	100	48.792	48.192	-0.600
35	101	48.115	47.587	-0.528
35	102	47.934	47.295	-0.639
35	103	47.753	47.212	-0.541
35	104	49.596	48.977	-0.619
35	105	49.517	48.880	-0.637
50	106	49.865	49.028	-0.837
50	107	49.211	48.354	-0.857
50	108	47.838	47.056	-0.782
50	109	48.894	47.957	-0.937
50	110	49.370	48.460	-0.910
50	111	49.640	48.744	-0.896
55	113	49.584	48.698	-0.886
55	114	47.784	46.978	-0.806
55	115	49.868	48.804	-1.064
55	116	49.744	48.983	-0.761
55	126	48.680	47.826	-0.854
55	128	49.329	48.338	-0.991
100	129	49.245	47.904	-1.341
100	131	48.179	46.591	-1.588
100	132	48.677	47.243	-1.434
100	133	47.942	46.506	-1.436
100	134	49.587	48.028	-1.559
100	135	49.076	47.644	-1.432
100	138	49.270	47.907	-1.363
100	139	47.626	46.250	-1.376
100	140	47.810	46.439	-1.371
100	141	49.477	47.985	-1.492
100	142	47.851	46.496	-1.355
100	143	47.692	46.301	-1.391
100	144	49.192	47.850	-1.342
100	220	48.790	47.421	-1.369
100	221	49.308	48.194	-1.114
100	224	47.959	46.727	-1.232
100	225	49.329	47.979	-1.350
100	226	50.329	48.814	-1.515
100	228	49.455	47.984	-1.471
100	232	47.476	46.219	-1.257
100	233	49.150	47.940	-1.210
100	236	47.812	46.519	-1.293
100	238	49.198	47.770	-1.428
100	243	49.559	48.157	-1.402
105	244	48.002	46.551	-1.451
105	246	47.802	46.345	-1.457
105	248	48.894	47.410	-1.484
105	374	47.975	46.666	-1.309
105	376	49.467	47.993	-1.474
105	378	50.482	50.446	0.034
105	Average	48.883	48.131	-0.752
105	Min	47.476	46.219	-1.588
105	Std Dev	0.773	0.995	0.556



13.61_PSA_DT_50ns_1M_14V	
Test Site	
Tester	
Test Number	
Max Limit	55 ns
Min Limit	43 ns

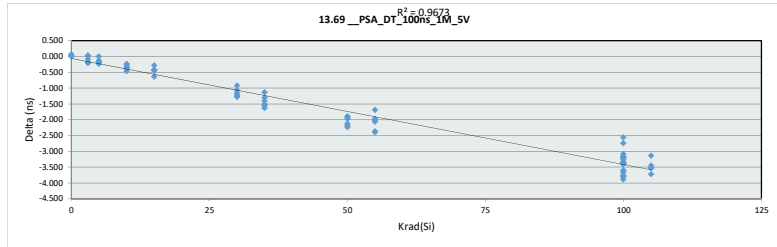
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	48.240	48.454	47.724	48.388	47.551	48.092	47.212	47.056	46.978	46.219	46.345
Average	48.461	49.290	48.538	49.189	48.419	48.881	48.024	48.267	48.271	47.370	46.993
Max	48.747	50.446	49.445	50.290	49.407	50.043	48.977	49.028	48.983	48.814	47.993
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000



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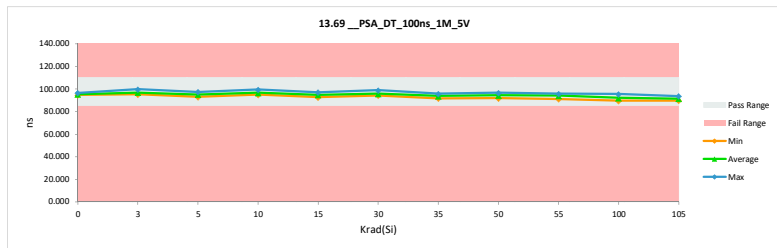
13.69_PSA_DT_100ns_1M_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	110 110
Min Limit	85 85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	94.675	94.691	0.016
0	118	96.440	96.478	0.038
0	122	94.897	94.981	0.084
3	61	95.431	95.475	0.044
3	62	99.889	99.824	-0.065
3	63	95.486	95.312	-0.174
3	64	97.108	96.930	-0.178
3	65	96.827	96.638	-0.189
3	66	96.803	96.838	0.035
5	67	95.613	95.497	-0.116
5	68	94.254	94.103	-0.151
5	69	93.862	93.707	-0.155
5	70	92.964	92.980	0.016
5	71	97.333	97.115	-0.218
5	72	97.490	97.332	-0.158
10	73	95.431	95.181	-0.250
10	74	99.889	99.516	-0.373
10	75	95.486	95.034	-0.452
10	76	97.108	96.787	-0.321
10	77	96.827	96.501	-0.326
10	78	96.803	96.581	-0.222
15	79	95.613	95.197	-0.416
15	80	94.254	93.820	-0.434
15	81	93.862	93.235	-0.627
15	82	92.964	92.697	-0.267
15	83	97.333	96.908	-0.425
15	84	97.490	97.075	-0.415
30	86	95.431	94.522	-0.909
30	89	99.889	98.833	-1.056
30	91	95.486	94.221	-1.265
30	92	97.108	95.961	-1.147
30	94	96.827	95.579	-1.248
30	95	96.803	95.592	-1.211
35	100	95.613	94.215	-1.398
35	101	94.254	93.136	-1.118
35	102	93.862	92.323	-1.539
35	103	92.964	91.672	-1.292
35	104	97.333	95.838	-1.495
35	105	97.490	95.875	-1.615
50	106	98.563	96.599	-1.964
50	107	95.935	94.033	-1.902
50	108	93.747	91.867	-1.880
50	109	95.555	93.441	-2.114
50	110	97.445	95.281	-2.164
50	111	97.593	95.364	-2.229
55	113	97.404	95.355	-2.049
55	114	93.056	91.098	-1.958
55	115	97.653	95.294	-2.359
55	116	97.544	95.870	-1.674
55	126	95.591	93.592	-1.999
55	128	96.250	93.866	-2.384
100	129	96.687	93.537	-3.150
100	131	94.159	90.379	-3.780
100	132	95.526	91.763	-3.763
100	133	94.345	90.750	-3.595
100	134	97.561	93.687	-3.874
100	135	96.111	92.744	-3.367
100	138	96.131	92.969	-3.162
100	139	93.285	90.051	-3.234
100	140	93.428	90.078	-3.350
100	141	97.040	93.383	-3.657
100	142	93.719	90.412	-3.307
100	143	93.255	89.670	-3.585
100	144	96.747	93.573	-3.174
100	220	95.186	91.820	-3.366
100	221	96.989	94.261	-2.728
100	224	93.877	90.691	-3.186
100	225	97.042	93.643	-3.399
100	226	99.320	95.582	-3.738
100	228	97.229	93.447	-3.782
100	232	92.657	89.580	-3.077
100	233	96.405	93.854	-2.551
100	236	93.372	90.207	-3.165
100	238	95.907	92.533	-3.374
100	243	97.518	94.142	-3.376
105	244	93.927	90.413	-3.514
105	246	93.466	89.758	-3.708
105	248	95.644	92.122	-3.522
105	374	94.169	91.047	-3.122
105	376	97.084	93.642	-3.442
	Max	99.889	99.824	0.084
	Average	95.866	94.070	-1.796
	Min	92.657	89.580	-3.874
	Std Dev	1.762	2.360	1.375



13.69_PSA_DT	
Test Site	
Tester	
Test Number	
Max Limit	110 ns
Min Limit	85 ns

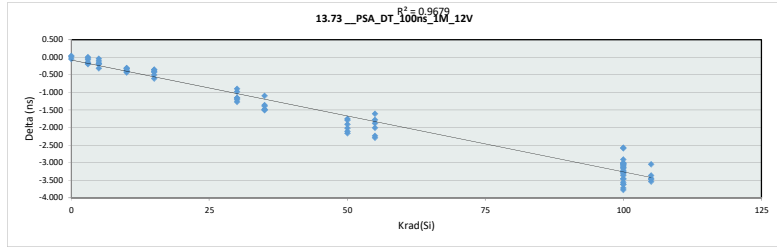
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.691	95.312	92.980	95.034	92.697	94.221	91.672	91.867	91.098	89.580	89.758
Average	95.383	96.836	95.122	96.600	94.822	95.785	93.843	94.431	94.179	92.198	91.396
Max	96.478	99.824	97.332	99.516	97.075	98.833	95.875	96.599	95.870	95.582	93.642
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



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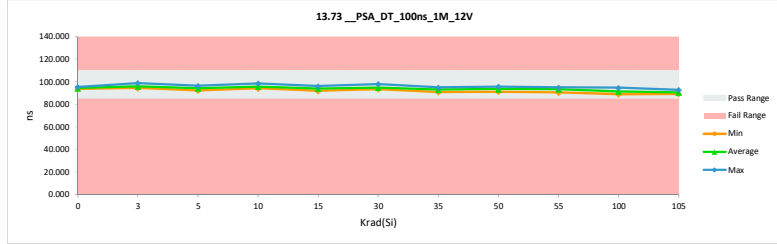
13.73_PSA_DT_100ns_1M_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	110 110
Min Limit	85 85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	93.956	93.978	0.022
0	118	95.586	95.551	-0.035
0	122	94.171	94.221	0.050
3	61	94.782	94.797	0.015
3	62	99.041	99.030	-0.011
3	63	94.714	94.586	-0.128
3	64	96.399	96.211	-0.188
3	65	95.940	95.773	-0.167
3	66	96.152	96.093	-0.059
5	67	94.878	94.785	-0.093
5	68	93.547	93.383	-0.164
5	69	93.207	93.019	-0.188
5	70	92.442	92.413	-0.029
5	71	96.595	96.296	-0.299
5	72	96.708	96.555	-0.153
10	73	94.782	94.492	-0.290
10	74	99.041	98.640	-0.401
10	75	94.714	94.289	-0.425
10	76	96.399	96.029	-0.370
10	77	95.940	95.633	-0.307
10	78	96.152	95.831	-0.321
15	79	94.878	94.469	-0.409
15	80	93.547	93.156	-0.391
15	81	93.207	92.610	-0.597
15	82	92.442	92.102	-0.340
15	83	96.595	96.104	-0.491
15	84	96.708	96.360	-0.348
30	86	94.782	93.897	-0.885
30	89	99.041	98.077	-0.964
30	91	94.714	93.574	-1.140
30	92	96.399	95.253	-1.146
30	94	95.940	94.744	-1.196
30	95	96.152	94.897	-1.255
35	100	94.878	93.526	-1.352
35	101	93.547	92.463	-1.084
35	102	93.207	91.742	-1.465
35	103	92.442	91.061	-1.381
35	104	96.595	95.121	-1.474
35	105	96.708	95.211	-1.497
50	106	97.735	95.839	-1.896
50	107	95.230	93.455	-1.775
50	108	93.007	91.268	-1.739
50	109	94.888	92.882	-2.006
50	110	96.603	94.451	-2.152
50	111	96.784	94.683	-2.101
55	113	96.579	94.581	-1.998
55	114	92.434	90.663	-1.771
55	115	96.899	94.673	-2.226
55	116	96.792	95.192	-1.600
55	126	94.816	92.946	-1.870
55	128	95.684	93.407	-2.277
100	129	95.887	92.831	-3.056
100	131	93.569	89.856	-3.713
100	132	94.802	91.198	-3.604
100	133	93.432	90.080	-3.352
100	134	96.817	93.050	-3.767
100	135	95.488	92.219	-3.269
100	138	95.435	92.434	-3.001
100	139	92.544	89.466	-3.078
100	140	92.707	89.587	-3.120
100	141	96.267	92.811	-3.456
100	142	92.950	89.784	-3.166
100	143	92.585	89.139	-3.446
100	144	95.893	92.874	-3.019
100	220	94.587	91.274	-3.313
100	221	96.211	93.632	-2.579
100	224	93.134	90.131	-3.003
100	225	96.147	93.026	-3.121
100	226	98.453	94.840	-3.613
100	228	96.444	92.895	-3.549
100	232	92.069	89.166	-2.903
100	233	95.653	93.089	-2.564
100	236	92.667	89.635	-3.032
100	238	95.256	92.040	-3.216
100	243	96.684	93.440	-3.244
105	244	93.241	89.781	-3.460
105	246	92.890	89.363	-3.527
105	248	94.875	91.439	-3.436
105	374	93.401	90.363	-3.038
105	376	96.362	93.008	-3.354
105	Max	99.041	99.030	0.050
105	Average	95.135	93.406	-1.730
105	Min	92.069	89.139	-3.767
105	Std Dev	1.709	2.277	1.307



13.73_PSA_DT	
Test Site	
Tester	
Test Number	
Max Limit	110 ns
Min Limit	85 ns

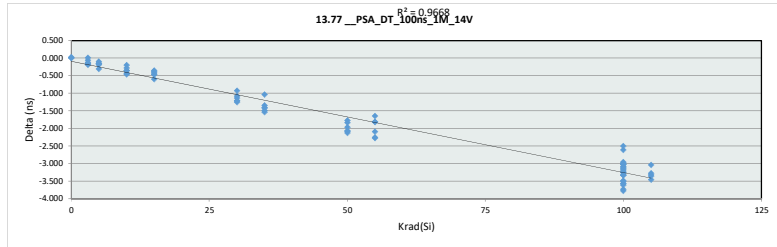
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.978	94.586	92.413	94.289	92.102	93.574	91.061	91.268	90.663	89.139	89.363
Average	94.583	96.082	94.409	95.819	94.134	95.074	93.187	93.763	93.577	91.604	90.791
Max	95.551	99.030	96.555	98.640	96.360	98.077	95.211	95.839	95.192	94.840	93.008
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



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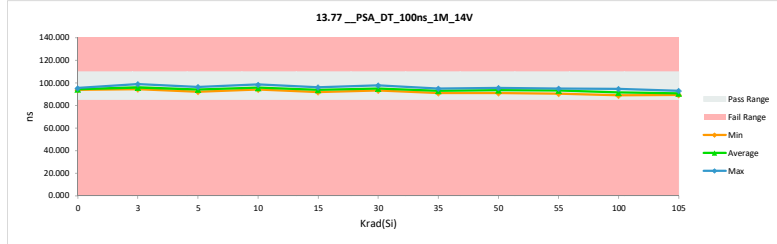
13.77 PSA_DT_100ns_1M_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	110 110
Min Limit	85 85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	93.955	93.979	0.024
0	118	95.575	95.590	0.015
0	122	94.179	94.225	0.046
3	61	94.745	94.766	0.021
3	62	99.120	99.006	-0.114
3	63	94.728	94.594	-0.134
3	64	96.375	96.217	-0.158
3	65	95.940	95.760	-0.180
3	66	96.129	96.082	-0.047
5	67	94.902	94.774	-0.128
5	68	93.536	93.360	-0.176
5	69	93.195	93.043	-0.152
5	70	92.466	92.382	-0.084
5	71	96.536	96.245	-0.291
5	72	96.722	96.577	-0.145
10	73	94.745	94.564	-0.181
10	74	99.120	98.673	-0.447
10	75	94.728	94.310	-0.418
10	76	96.375	96.034	-0.341
10	77	95.940	95.596	-0.344
10	78	96.129	95.861	-0.268
15	79	94.902	94.442	-0.460
15	80	93.536	93.134	-0.402
15	81	93.195	92.611	-0.584
15	82	92.466	92.128	-0.338
15	83	96.536	96.099	-0.437
15	84	96.722	96.353	-0.369
30	86	94.745	93.830	-0.915
30	89	99.120	98.048	-1.072
30	91	94.728	93.528	-1.200
30	92	96.375	95.252	-1.123
30	94	95.940	94.735	-1.205
30	95	96.129	94.888	-1.241
35	100	94.902	93.499	-1.403
35	101	93.536	92.514	-1.022
35	102	93.195	91.715	-1.480
35	103	92.466	91.135	-1.331
35	104	96.536	95.138	-1.398
35	105	96.722	95.202	-1.520
50	106	97.764	95.814	-1.950
50	107	95.234	93.410	-1.824
50	108	93.002	91.242	-1.760
50	109	94.909	92.839	-2.070
50	110	96.512	94.464	-2.048
50	111	96.757	94.640	-2.117
55	113	96.626	94.551	-2.075
55	114	92.446	90.646	-1.800
55	115	96.882	94.643	-2.239
55	116	96.775	95.144	-1.631
55	126	94.796	92.992	-1.804
55	128	95.643	93.381	-2.262
100	129	95.882	92.814	-3.068
100	131	93.571	89.804	-3.767
100	132	94.767	91.211	-3.556
100	133	93.436	90.114	-3.322
100	134	96.747	93.029	-3.718
100	135	95.479	92.169	-3.310
100	138	95.413	92.440	-2.973
100	139	92.521	89.433	-3.088
100	140	92.700	89.575	-3.125
100	141	96.247	92.776	-3.471
100	142	93.007	89.842	-3.165
100	143	92.587	89.084	-3.503
100	144	95.889	92.879	-3.010
100	220	94.576	91.256	-3.320
100	221	96.228	93.628	-2.600
100	224	93.160	90.161	-2.999
100	225	96.225	92.986	-3.239
100	226	98.438	94.841	-3.597
100	228	96.456	92.889	-3.567
100	232	92.028	89.087	-2.941
100	233	95.596	93.107	-2.489
100	236	92.633	89.619	-3.014
100	238	95.267	92.025	-3.242
100	243	96.682	93.479	-3.203
105	244	93.115	89.804	-3.311
105	246	92.842	89.394	-3.448
105	248	94.923	91.582	-3.341
105	374	93.382	90.357	-3.025
105	375	96.341	93.077	-3.264
Max		99.120	99.006	0.046
Average		95.130	93.401	-1.728
Min		92.028	89.084	-3.767
Std Dev		1.715	2.275	1.302



13.77 PSA_DT	
Test Site	
Tester	
Test Number	
Max Limit	110 ns
Min Limit	85 ns

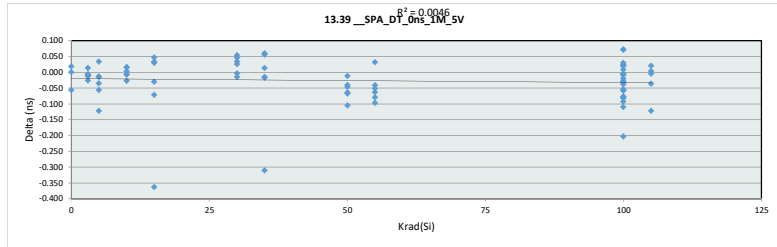
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	93.979	94.594	92.382	94.310	92.128	93.528	91.135	91.242	90.646	89.084	89.394
Average	94.598	96.071	94.397	95.840	94.128	95.047	93.201	93.735	93.560	91.594	90.843
Max	95.590	99.006	96.577	98.673	96.353	98.048	95.202	95.814	95.144	94.841	93.077
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



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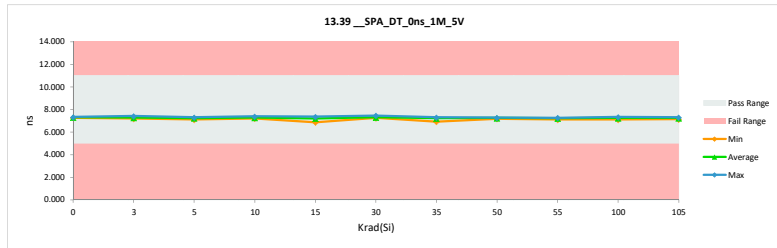
13.39 SPA_DT_0ns_1M_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	11 11
Min Limit	5 5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.326	7.328	0.002
0	118	7.390	7.335	-0.055
0	122	7.256	7.276	0.020
3	61	7.256	7.252	-0.004
3	62	7.281	7.256	-0.025
3	63	7.253	7.268	0.015
3	64	7.214	7.204	-0.010
3	65	7.430	7.423	-0.007
3	66	7.253	7.243	-0.010
5	67	7.243	7.278	0.035
5	68	7.221	7.208	-0.013
5	69	7.286	7.252	-0.034
5	70	7.240	7.119	-0.121
5	71	7.328	7.315	-0.013
5	72	7.306	7.251	-0.055
10	73	7.256	7.273	0.017
10	74	7.281	7.276	-0.005
10	75	7.253	7.271	0.018
10	76	7.214	7.209	-0.005
10	77	7.430	7.404	-0.026
10	78	7.253	7.258	0.005
15	79	7.243	7.291	0.048
15	80	7.221	7.256	0.035
15	81	7.286	7.216	-0.070
15	82	7.240	6.878	-0.362
15	83	7.328	7.359	0.031
15	84	7.306	7.277	-0.029
30	86	7.256	7.283	0.027
30	89	7.281	7.267	-0.014
30	91	7.253	7.251	-0.002
30	92	7.214	7.269	0.055
30	94	7.430	7.477	0.047
30	95	7.253	7.288	0.035
35	100	7.243	7.300	0.057
35	101	7.221	7.282	0.061
35	102	7.286	7.272	-0.014
35	103	7.240	6.931	-0.309
35	104	7.328	7.312	-0.016
35	105	7.306	7.321	0.015
50	106	7.316	7.250	-0.066
50	107	7.288	7.184	-0.104
50	108	7.308	7.263	-0.045
50	109	7.235	7.225	-0.010
50	110	7.317	7.278	-0.039
50	111	7.340	7.278	-0.062
55	113	7.308	7.258	-0.050
55	114	7.296	7.256	-0.040
55	115	7.241	7.274	0.033
55	116	7.238	7.160	-0.078
55	126	7.183	7.121	-0.062
55	128	7.360	7.264	-0.096
100	129	7.357	7.282	-0.075
100	131	7.248	7.321	0.073
100	132	7.249	7.172	-0.077
100	133	7.199	7.271	0.072
100	134	7.310	7.306	-0.004
100	135	7.283	7.253	-0.030
100	138	7.323	7.266	-0.057
100	139	7.298	7.279	-0.019
100	140	7.250	7.275	0.025
100	141	7.393	7.302	-0.091
100	142	7.291	7.264	-0.027
100	143	7.272	7.235	-0.037
100	144	7.240	7.233	-0.007
100	220	7.363	7.254	-0.109
100	221	7.377	7.175	-0.202
100	224	7.265	7.234	-0.031
100	225	7.292	7.212	-0.080
100	226	7.319	7.350	0.031
100	228	7.273	7.270	-0.003
100	232	7.157	7.104	-0.053
100	233	7.208	7.232	0.024
100	236	7.370	7.315	-0.055
100	238	7.259	7.269	0.010
100	243	7.312	7.333	0.021
105	244	7.245	7.267	0.022
105	246	7.273	7.270	-0.003
105	248	7.245	7.250	0.005
105	374	7.353	7.318	-0.035
105	376	7.257	7.136	-0.121
105	Max	7.430	7.477	0.073
105	Average	7.283	7.256	-0.027
105	Min	7.157	6.878	-0.362
105	Std Dev	0.056	0.083	0.070



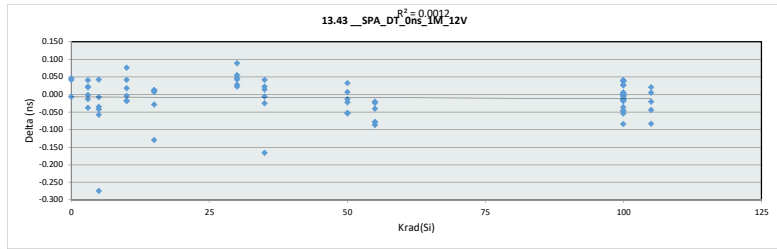
13.39 SPA_DT_0	
Test Site	
Tester	
Test Number	
Max Limit	11 ns
Min Limit	5 ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5,000	5,000	5,000	5,000	5,000	5,000	5,000	5,000	5,000	5,000	5,000
Min	7.276	7.204	7.119	7.209	6.878	7.251	6.931	7.184	7.121	7.104	7.136
Average	7.313	7.274	7.237	7.282	7.213	7.306	7.236	7.246	7.222	7.259	7.248
Max	7.335	7.423	7.315	7.404	7.359	7.477	7.321	7.278	7.274	7.350	7.318
UL	11,000	11,000	11,000	11,000	11,000	11,000	11,000	11,000	11,000	11,000	11,000

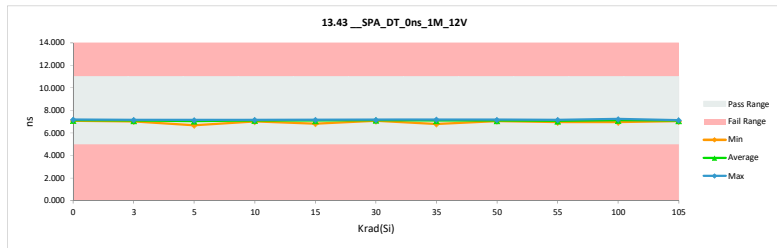


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13.43 SPA_DT_Ons_1M_12V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	11	11		
Min Limit	5	5		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.151	7.198	0.047
0	118	7.100	7.094	-0.006
0	122	7.069	7.111	0.042
3	61	7.063	7.051	-0.012
3	62	7.093	7.114	0.021
3	63	7.124	7.147	0.023
3	64	7.038	7.038	0.000
3	65	7.135	7.176	0.041
3	66	7.134	7.097	-0.037
5	67	7.145	7.088	-0.057
5	68	7.179	7.145	-0.034
5	69	7.103	7.146	0.043
5	70	6.968	6.694	-0.274
5	71	7.182	7.141	-0.041
5	72	7.136	7.129	-0.007
10	73	7.063	7.105	0.042
10	74	7.093	7.170	0.077
10	75	7.124	7.143	0.019
10	76	7.038	7.021	-0.017
10	77	7.135	7.132	-0.003
10	78	7.134	7.116	-0.018
15	79	7.145	7.117	-0.028
15	80	7.179	7.192	0.013
15	81	7.103	7.116	0.013
15	82	6.968	6.839	-0.129
15	83	7.182	7.194	0.012
15	84	7.136	7.144	0.008
30	86	7.063	7.092	0.029
30	89	7.093	7.183	0.090
30	91	7.124	7.180	0.056
30	92	7.038	7.081	0.043
30	94	7.135	7.183	0.048
30	95	7.134	7.157	0.023
35	100	7.145	7.140	-0.005
35	101	7.179	7.202	0.023
35	102	7.103	7.118	0.015
35	103	6.968	6.803	-0.165
35	104	7.182	7.158	-0.024
35	105	7.136	7.178	0.042
50	106	7.156	7.105	-0.051
50	107	7.051	7.059	0.008
50	108	7.096	7.129	0.033
50	109	7.111	7.099	-0.012
50	110	7.134	7.081	-0.053
50	111	7.194	7.173	-0.021
55	113	7.095	7.075	-0.020
55	114	7.107	7.067	-0.040
55	115	7.181	7.158	-0.023
55	116	7.124	7.047	-0.077
55	126	7.044	6.958	-0.086
55	128	7.255	7.176	-0.079
100	129	7.156	7.112	-0.044
100	131	7.161	7.157	-0.004
100	132	7.123	7.076	-0.047
100	133	7.105	7.094	-0.011
100	134	7.188	7.171	-0.017
100	135	7.186	7.188	0.002
100	138	7.199	7.181	-0.018
100	139	7.132	7.132	0.000
100	140	7.131	7.136	0.005
100	141	7.205	7.246	0.041
100	142	7.088	7.087	-0.001
100	143	7.081	7.108	0.027
100	144	7.075	7.104	0.029
100	220	7.127	7.123	-0.004
100	221	7.103	7.110	0.007
100	224	7.181	7.098	-0.083
100	225	7.134	7.120	-0.014
100	226	7.135	7.100	-0.035
100	228	7.140	7.128	-0.012
100	232	7.010	6.957	-0.053
100	233	7.064	7.063	-0.001
100	236	7.146	7.187	0.041
100	238	7.140	7.179	0.039
100	243	7.172	7.212	0.040
105	244	7.130	7.048	-0.082
105	246	7.142	7.148	0.006
105	248	7.119	7.076	-0.043
105	374	7.146	7.126	-0.020
105	376	7.095	7.116	0.021
	Max	7.255	7.246	0.090
	Average	7.120	7.111	-0.009
	Min	6.968	6.694	-0.274
	Std Dev	0.054	0.085	0.053



13.43 SPA_DT											
Test Site											
Tester											
Test Number											
Max Limit	11	ns									
Min Limit	5	ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.094	7.038	6.694	7.021	6.839	7.081	6.803	7.059	6.958	6.957	7.048
Average	7.134	7.104	7.057	7.115	7.100	7.146	7.100	7.108	7.080	7.128	7.103
Max	7.198	7.176	7.146	7.170	7.194	7.183	7.202	7.173	7.176	7.246	7.148
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000

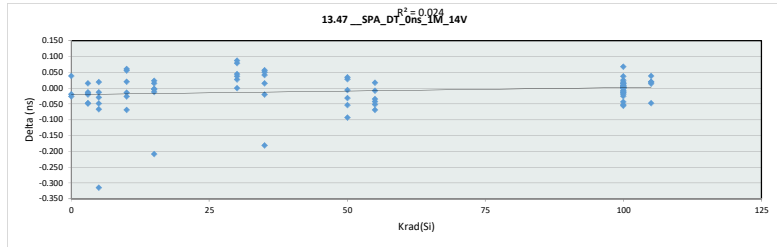




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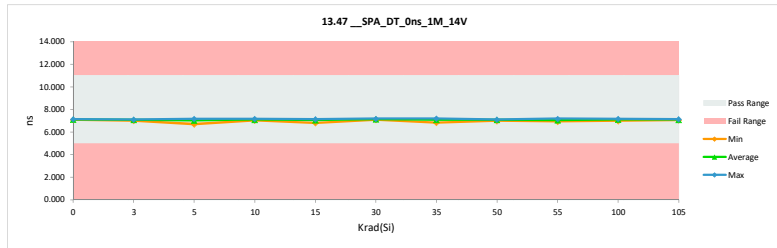
13.47 SPA_DT_Ons_1M_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	11 11
Min Limit	5 5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.125	7.165	0.040
0	118	7.145	7.127	-0.018
0	122	7.128	7.103	-0.025
3	61	7.083	7.099	0.016
3	62	7.124	7.112	-0.012
3	63	7.156	7.141	-0.015
3	64	7.070	7.023	-0.047
3	65	7.186	7.138	-0.048
3	66	7.084	7.065	-0.019
5	67	7.096	7.048	-0.048
5	68	7.131	7.119	-0.012
5	69	7.140	7.074	-0.066
5	70	7.028	6.714	-0.314
5	71	7.170	7.190	0.020
5	72	7.139	7.111	-0.028
10	73	7.083	7.104	0.021
10	74	7.124	7.186	0.062
10	75	7.156	7.143	-0.013
10	76	7.070	7.045	-0.025
10	77	7.186	7.118	-0.068
10	78	7.084	7.141	0.057
15	79	7.096	7.093	-0.003
15	80	7.131	7.147	0.016
15	81	7.140	7.129	-0.011
15	82	7.028	6.821	-0.207
15	83	7.170	7.169	-0.001
15	84	7.139	7.163	0.024
30	86	7.083	7.121	0.038
30	89	7.124	7.204	0.080
30	91	7.156	7.202	0.046
30	92	7.070	7.098	0.028
30	94	7.186	7.187	0.001
30	95	7.084	7.172	0.088
35	100	7.096	7.154	0.058
35	101	7.131	7.174	0.043
35	102	7.140	7.121	-0.019
35	103	7.028	6.848	-0.180
35	104	7.170	7.222	0.052
35	105	7.139	7.155	0.016
50	106	7.160	7.107	-0.053
50	107	7.100	7.008	-0.092
50	108	7.113	7.108	-0.005
50	109	7.083	7.118	0.035
50	110	7.091	7.120	0.029
50	111	7.158	7.128	-0.030
55	113	7.043	7.061	0.018
55	114	7.129	7.061	-0.068
55	115	7.183	7.150	-0.033
55	116	7.124	7.083	-0.041
55	126	7.004	6.953	-0.051
55	128	7.210	7.203	-0.007
100	129	7.113	7.114	0.001
100	131	7.169	7.156	-0.013
100	132	7.077	7.085	0.008
100	133	7.138	7.086	-0.052
100	134	7.180	7.166	-0.014
100	135	7.144	7.151	0.007
100	138	7.170	7.146	-0.024
100	139	7.172	7.130	-0.042
100	140	7.116	7.185	0.069
100	141	7.243	7.188	-0.055
100	142	7.096	7.096	0.000
100	143	7.125	7.119	-0.006
100	144	7.098	7.089	-0.009
100	220	7.116	7.127	0.011
100	221	7.118	7.138	0.020
100	224	7.117	7.120	0.003
100	225	7.116	7.124	0.008
100	226	7.083	7.086	0.003
100	228	7.134	7.116	-0.018
100	232	7.001	7.005	0.004
100	233	7.080	7.096	0.016
100	236	7.147	7.173	0.026
100	238	7.140	7.178	0.038
100	243	7.167	7.183	0.016
105	244	7.131	7.084	-0.047
105	246	7.151	7.172	0.021
105	248	7.086	7.101	0.015
105	374	7.105	7.144	0.039
105	376	7.076	7.096	0.020
105	Max	7.243	7.222	0.088
105	Average	7.120	7.111	-0.009
105	Min	7.001	6.714	-0.314
105	Std Dev	0.045	0.080	0.059



13.47 SPA_DT	
Test Site	
Tester	
Test Number	
Max Limit	11 ns
Min Limit	5 ns

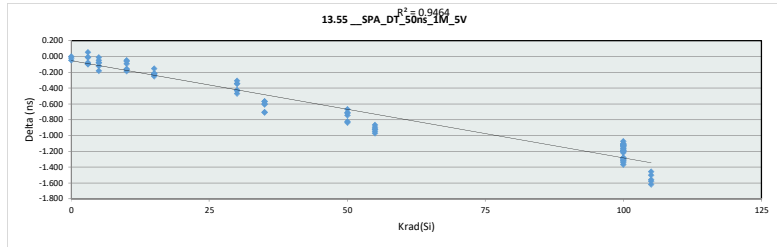
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000	5.000
Min	7.103	7.023	6.714	7.045	6.821	7.098	6.848	7.008	6.953	7.005	7.084
Average	7.132	7.096	7.043	7.123	7.087	7.164	7.112	7.098	7.085	7.127	7.119
Max	7.165	7.141	7.190	7.186	7.169	7.204	7.222	7.128	7.203	7.188	7.172
UL	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000	11.000



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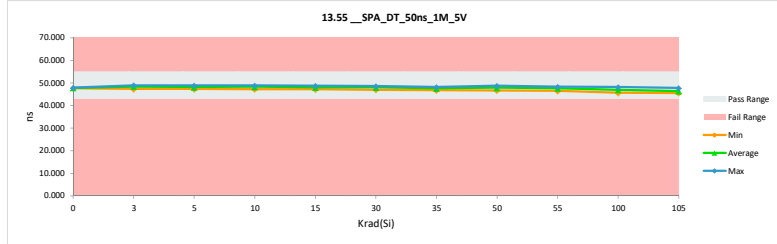
13.55 SPA_DT_50ns_1M_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	55 55
Min Limit	43 43

Krad(Si)	Serial #	PRE	POST	Delta
0	117	47.893	47.899	0.006
0	118	47.754	47.710	-0.044
0	122	47.819	47.805	-0.014
3	61	48.655	48.646	-0.009
3	62	48.469	48.467	-0.002
3	63	47.420	47.348	-0.072
3	64	49.042	48.950	-0.092
3	65	48.257	48.164	-0.093
3	66	48.862	48.919	0.057
5	67	48.211	48.141	-0.070
5	68	47.341	47.334	-0.007
5	69	48.016	47.838	-0.178
5	70	48.195	48.117	-0.078
5	71	48.726	48.610	-0.116
5	72	48.926	48.883	-0.043
10	73	48.655	48.613	-0.042
10	74	48.469	48.412	-0.057
10	75	47.420	47.239	-0.181
10	76	49.042	48.890	-0.152
10	77	48.257	48.100	-0.157
10	78	48.862	48.774	-0.088
15	79	48.211	47.998	-0.213
15	80	47.341	47.126	-0.215
15	81	48.016	47.768	-0.248
15	82	48.195	48.046	-0.149
15	83	48.726	48.496	-0.230
15	84	48.926	48.718	-0.208
30	86	48.655	48.353	-0.302
30	89	48.469	48.129	-0.340
30	91	47.420	47.957	-0.463
30	92	49.042	48.615	-0.427
30	94	48.257	47.836	-0.421
30	95	48.862	48.523	-0.339
35	100	48.211	47.649	-0.562
35	101	47.341	46.777	-0.564
35	102	48.016	47.315	-0.701
35	103	48.195	47.601	-0.594
35	104	48.726	48.124	-0.602
35	105	48.926	48.224	-0.702
50	106	49.088	48.386	-0.702
50	107	49.551	48.734	-0.817
50	108	47.382	46.671	-0.711
50	109	48.480	47.819	-0.661
50	110	48.283	47.450	-0.833
50	111	49.009	48.271	-0.738
55	113	49.241	48.299	-0.942
55	114	47.412	46.493	-0.919
55	115	48.808	47.919	-0.889
55	116	49.146	48.283	-0.863
55	126	47.934	47.021	-0.913
55	128	49.053	48.085	-0.968
100	129	48.927	47.617	-1.310
100	131	47.592	46.412	-1.180
100	132	48.045	46.762	-1.283
100	133	47.457	46.245	-1.212
100	134	49.182	47.992	-1.190
100	135	48.625	47.465	-1.160
100	138	48.989	47.625	-1.364
100	139	46.830	45.631	-1.199
100	140	47.406	46.107	-1.299
100	141	48.906	47.587	-1.319
100	142	47.363	46.267	-1.096
100	143	47.241	45.904	-1.337
100	144	47.928	46.764	-1.164
100	220	48.252	47.134	-1.118
100	221	48.067	46.936	-1.131
100	224	47.191	46.068	-1.123
100	225	48.845	47.567	-1.278
100	226	48.761	47.651	-1.110
100	228	49.082	47.963	-1.119
100	232	47.867	46.725	-1.142
100	233	48.410	47.344	-1.066
100	236	47.250	46.146	-1.104
100	238	49.379	48.189	-1.190
100	243	48.155	46.882	-1.273
105	244	47.318	45.746	-1.572
105	246	47.407	45.955	-1.452
105	248	48.510	47.013	-1.497
105	374	47.211	45.596	-1.615
105	376	49.334	47.779	-1.555
	Max	49.551	48.950	0.057
	Average	48.284	47.595	-0.689
	Min	46.830	45.596	-1.615
	Std Dev	0.668	0.878	0.510



13.55 SPA_DT_5	
Test Site	
Tester	
Test Number	
Max Limit	55 ns
Min Limit	43 ns

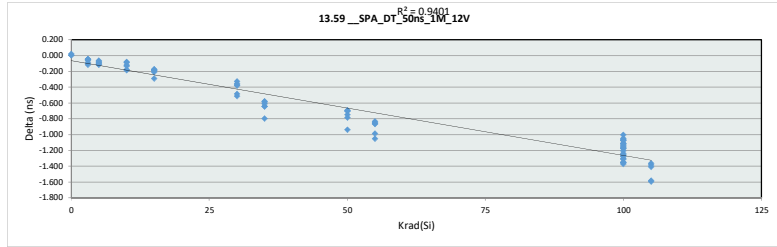
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	47.710	47.348	47.334	47.239	47.126	46.957	46.777	46.671	46.493	45.631	45.596
Average	47.805	48.416	48.154	48.338	48.025	48.069	47.615	47.889	47.683	46.958	46.418
Max	47.899	48.950	48.883	48.890	48.718	48.615	48.224	48.734	48.299	48.189	47.779
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000



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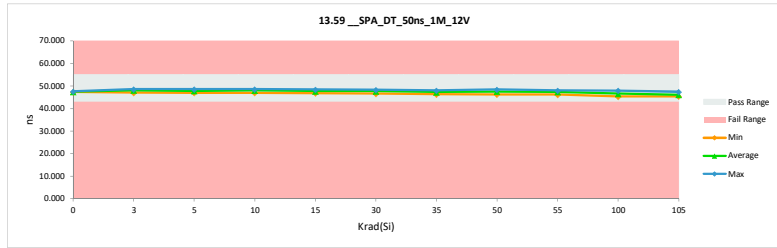
13.59 SPA_DT_50ns_1M_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	55 55
Min Limit	43 43

Krad(Si)	Serial #	PRE	POST	Delta
0	117	47.521	47.545	0.024
0	118	47.312	47.320	0.008
0	122	47.465	47.474	0.009
3	61	48.331	48.286	-0.045
3	62	48.201	48.163	-0.038
3	63	47.102	47.014	-0.088
3	64	48.661	48.603	-0.058
3	65	47.886	47.773	-0.113
3	66	48.586	48.541	-0.045
5	67	47.888	47.776	-0.112
5	68	46.973	46.915	-0.058
5	69	47.668	47.562	-0.106
5	70	47.847	47.761	-0.086
5	71	48.389	48.298	-0.091
5	72	48.641	48.567	-0.074
10	73	48.331	48.248	-0.083
10	74	48.201	48.083	-0.118
10	75	47.102	46.919	-0.183
10	76	48.661	48.583	-0.078
10	77	47.886	47.711	-0.175
10	78	48.586	48.459	-0.127
15	79	47.888	47.713	-0.175
15	80	46.973	46.766	-0.207
15	81	47.668	47.381	-0.287
15	82	47.847	47.677	-0.170
15	83	48.389	48.203	-0.186
15	84	48.641	48.455	-0.186
30	86	48.331	47.964	-0.367
30	89	48.201	47.826	-0.375
30	91	47.102	46.593	-0.509
30	92	48.661	48.311	-0.350
30	94	47.886	47.405	-0.481
30	95	48.586	48.262	-0.324
35	100	47.888	47.280	-0.608
35	101	46.973	46.333	-0.640
35	102	47.668	46.877	-0.791
35	103	47.847	47.264	-0.583
35	104	48.389	47.816	-0.573
35	105	48.641	48.002	-0.639
50	106	48.764	48.020	-0.744
50	107	49.168	48.386	-0.782
50	108	46.944	46.246	-0.698
50	109	48.194	47.500	-0.694
50	110	47.927	46.994	-0.933
50	111	48.657	47.970	-0.687
55	113	48.806	47.947	-0.859
55	114	47.019	46.176	-0.843
55	115	48.536	47.676	-0.860
55	116	48.806	47.976	-0.830
55	126	47.651	46.604	-1.047
55	128	48.795	47.812	-0.983
100	129	48.557	47.251	-1.306
100	131	47.280	46.029	-1.251
100	132	47.746	46.379	-1.367
100	133	47.015	45.844	-1.171
100	134	48.870	47.670	-1.200
100	135	48.329	47.221	-1.108
100	138	48.642	47.298	-1.344
100	139	46.408	45.336	-1.072
100	140	47.075	45.778	-1.297
100	141	48.610	47.261	-1.349
100	142	46.924	45.924	-1.000
100	143	46.864	45.631	-1.233
100	144	47.623	46.347	-1.276
100	220	47.938	46.789	-1.149
100	221	47.720	46.614	-1.106
100	224	46.795	45.740	-1.055
100	225	48.452	47.185	-1.267
100	226	48.402	47.284	-1.118
100	228	48.756	47.597	-1.159
100	232	47.453	46.387	-1.066
100	233	48.181	47.012	-1.169
100	236	46.843	45.800	-1.043
100	238	49.082	47.948	-1.134
100	243	47.801	46.451	-1.350
105	244	46.877	45.498	-1.379
105	246	47.059	45.701	-1.358
105	248	48.177	46.601	-1.576
105	374	46.757	45.352	-1.405
105	376	49.003	47.412	-1.591
	Max	49.168	48.603	0.024
	Average	47.942	47.255	-0.687
	Min	46.408	45.336	-1.591
	Std Dev	0.691	0.883	0.499



13.59 SPA_DT_50ns_1M_12V	
Test Site	
Tester	
Test Number	
Max Limit	55 ns
Min Limit	43 ns

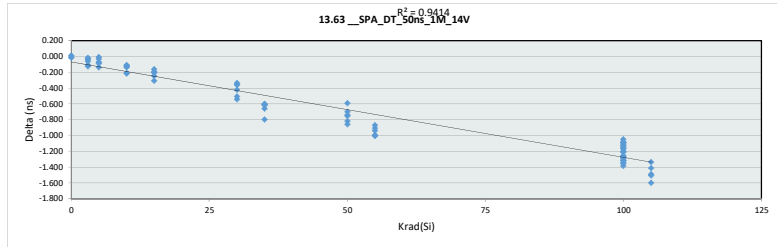
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	47.320	47.014	46.915	46.919	46.766	46.593	46.333	46.246	46.176	45.336	45.352
Average	47.446	48.063	47.813	48.001	47.699	47.727	47.262	47.519	47.365	46.616	46.113
Max	47.545	48.603	48.567	48.583	48.455	48.311	48.002	48.386	47.976	47.948	47.412
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000



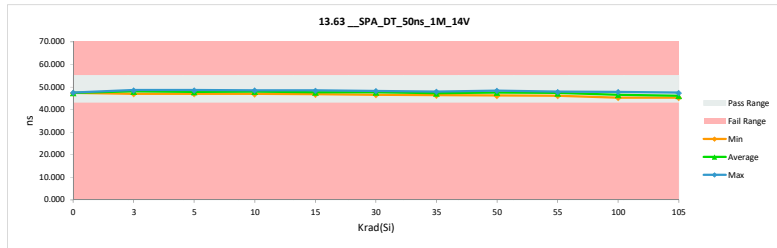
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13.63 SPA_DT_50ns_1M_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	55 55
Min Limit	43 43

Krad(Si)	Serial #	PRE	POST	Delta
0	117	47.549	47.539	-0.010
0	118	47.316	47.335	0.019
0	122	47.483	47.481	-0.002
3	61	48.331	48.303	-0.028
3	62	48.227	48.107	-0.120
3	63	47.117	47.017	-0.100
3	64	48.661	48.651	-0.010
3	65	47.891	47.834	-0.057
3	66	48.570	48.534	-0.036
5	67	47.889	47.821	-0.068
5	68	46.979	46.975	-0.004
5	69	47.698	47.565	-0.133
5	70	47.819	47.748	-0.071
5	71	48.419	48.335	-0.084
5	72	48.628	48.602	-0.026
10	73	48.331	48.211	-0.120
10	74	48.227	48.095	-0.132
10	75	47.117	46.911	-0.206
10	76	48.661	48.551	-0.110
10	77	47.891	47.683	-0.208
10	78	48.570	48.465	-0.105
15	79	47.889	47.701	-0.188
15	80	46.979	46.786	-0.193
15	81	47.698	47.397	-0.301
15	82	47.819	47.619	-0.200
15	83	48.419	48.177	-0.242
15	84	48.628	48.471	-0.157
30	86	48.331	47.983	-0.348
30	89	48.227	47.809	-0.418
30	91	47.117	46.580	-0.537
30	92	48.661	48.306	-0.355
30	94	47.891	47.391	-0.500
30	95	48.570	48.237	-0.333
35	100	47.889	47.282	-0.607
35	101	46.979	46.383	-0.596
35	102	47.698	46.907	-0.791
35	103	47.819	47.217	-0.602
35	104	48.419	47.809	-0.610
35	105	48.628	47.973	-0.655
50	106	48.742	48.047	-0.695
50	107	49.181	48.369	-0.812
50	108	46.952	46.203	-0.749
50	109	48.150	47.563	-0.587
50	110	47.906	47.053	-0.853
50	111	48.713	47.979	-0.734
55	113	48.787	47.923	-0.864
55	114	47.051	46.149	-0.902
55	115	48.598	47.609	-0.989
55	116	48.839	47.904	-0.935
55	126	47.620	46.626	-0.994
55	128	48.793	47.796	-0.997
100	129	48.574	47.290	-1.284
100	131	47.248	46.041	-1.207
100	132	47.708	46.396	-1.312
100	133	47.014	45.860	-1.154
100	134	48.828	47.635	-1.193
100	135	48.333	47.230	-1.103
100	138	48.685	47.332	-1.353
100	139	46.382	45.301	-1.081
100	140	47.048	45.736	-1.312
100	141	48.631	47.252	-1.379
100	142	46.944	45.901	-1.043
100	143	46.900	45.592	-1.308
100	144	47.597	46.339	-1.258
100	220	47.948	46.789	-1.159
100	221	47.776	46.621	-1.155
100	224	46.812	45.734	-1.078
100	225	48.453	47.203	-1.250
100	226	48.412	47.286	-1.126
100	228	48.732	47.605	-1.127
100	232	47.449	46.370	-1.079
100	233	48.197	46.995	-1.202
100	236	46.857	45.769	-1.088
100	238	49.160	47.886	-1.274
100	243	47.750	46.417	-1.333
105	244	46.833	45.502	-1.331
105	246	47.069	45.660	-1.409
105	248	48.193	46.598	-1.595
105	374	46.787	45.303	-1.484
105	376	48.973	47.473	-1.500
105	375	49.181	48.651	0.019
Average		47.946	47.252	-0.694
Min		46.382	45.301	-1.595
Std Dev		0.691	0.888	0.502



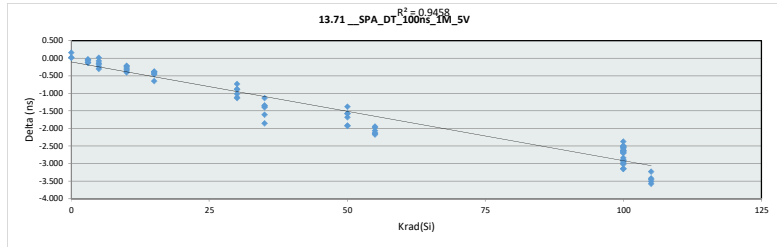
13.63 SPA_DT_50ns_1M_14V		5	10	15	30	35	50	55	100	105
LL	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000	43.000
Min	47.335	47.017	46.975	46.911	46.786	46.580	46.383	46.203	46.149	45.301
Average	47.452	48.074	47.841	47.986	47.692	47.718	47.262	47.536	47.335	46.608
Max	47.539	48.651	48.602	48.551	48.471	48.306	47.973	48.369	47.923	47.886
UL	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000	55.000



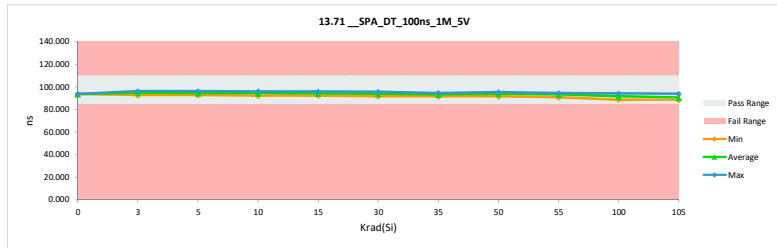
LDR TID Report  
TPS7H5001-SP

13.71 SPA_DT_100ns_1M_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	110 110
Min Limit	85 85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	93.821	93.849	0.028
0	118	93.899	93.936	0.037
0	122	93.624	93.795	0.171
3	61	95.004	94.981	-0.023
3	62	95.541	95.427	-0.114
3	63	92.877	92.822	-0.055
3	64	96.085	95.970	-0.115
3	65	94.002	93.887	-0.115
3	66	96.410	96.384	-0.026
5	67	94.306	94.180	-0.126
5	68	92.868	92.892	0.024
5	69	95.000	94.709	-0.291
5	70	94.775	94.557	-0.218
5	71	95.523	95.363	-0.160
5	72	96.353	96.285	-0.068
10	73	95.004	94.800	-0.204
10	74	95.541	95.271	-0.270
10	75	92.877	92.483	-0.394
10	76	96.085	95.786	-0.299
10	77	94.002	93.654	-0.348
10	78	96.410	96.188	-0.222
15	79	94.306	93.931	-0.375
15	80	92.868	92.504	-0.364
15	81	95.000	94.359	-0.641
15	82	94.775	94.386	-0.389
15	83	95.523	95.075	-0.448
15	84	96.353	95.924	-0.429
30	86	95.004	94.125	-0.879
30	89	95.541	94.671	-0.870
30	91	92.877	91.757	-1.120
30	92	96.085	95.080	-1.005
30	94	94.002	92.898	-1.104
30	95	96.410	95.685	-0.725
35	100	94.306	92.955	-1.351
35	101	92.868	91.747	-1.121
35	102	95.000	93.158	-1.842
35	103	94.775	93.392	-1.383
35	104	95.523	94.188	-1.335
35	105	96.353	94.758	-1.595
50	106	97.046	95.376	-1.670
50	107	97.369	95.465	-1.904
50	108	93.413	91.854	-1.559
50	109	95.160	93.795	-1.365
50	110	94.735	92.818	-1.917
50	111	95.968	94.399	-1.569
55	113	96.849	94.719	-2.130
55	114	93.032	90.917	-2.115
55	115	95.730	93.797	-1.933
55	116	96.662	94.702	-1.960
55	126	94.234	92.074	-2.160
55	128	96.384	94.332	-2.052
100	129	96.181	93.170	-3.011
100	131	93.552	90.724	-2.828
100	132	94.957	91.821	-3.136
100	133	92.815	90.162	-2.653
100	134	96.750	94.065	-2.685
100	135	95.942	93.339	-2.603
100	138	96.255	93.117	-3.138
100	139	91.461	88.811	-2.650
100	140	92.725	89.830	-2.895
100	141	95.824	92.843	-2.981
100	142	93.103	90.743	-2.360
100	143	92.497	89.362	-3.135
100	144	93.440	90.762	-2.678
100	220	94.698	92.177	-2.521
100	221	93.904	91.415	-2.489
100	224	92.612	90.001	-2.611
100	225	96.002	93.121	-2.881
100	226	95.599	93.051	-2.548
100	228	96.402	93.787	-2.615
100	232	94.564	92.068	-2.496
100	233	94.742	92.211	-2.531
100	236	92.293	89.811	-2.482
100	238	97.335	94.394	-2.941
100	243	93.757	90.861	-2.896
105	244	92.629	89.219	-3.410
105	246	93.256	90.041	-3.215
105	248	95.228	91.668	-3.560
105	374	92.163	88.716	-3.447
105	376	97.643	94.143	-3.500
Max		97.643	96.384	0.171
Average		94.781	93.219	-1.562
Min		91.461	88.716	-3.560
Std Dev		1.461	1.923	1.167



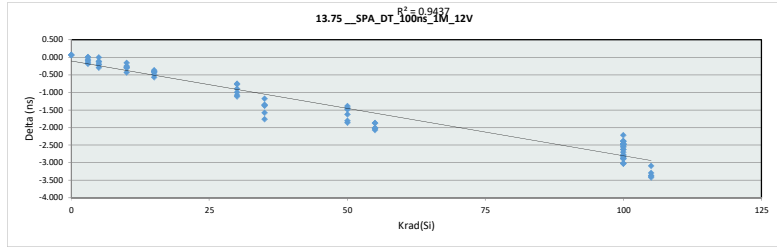
13.71 SPA_DT		0	3	5	10	15	30	35	50	55	100	105
LL		85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min		93.795	92.822	92.892	92.483	92.504	91.757	91.747	91.854	90.917	88.811	88.716
Average		93.860	94.912	94.664	94.697	94.363	94.036	93.366	93.951	93.424	91.735	90.757
Max		93.936	96.384	96.285	96.188	95.924	95.685	94.758	95.465	94.719	94.394	94.143
UL		110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



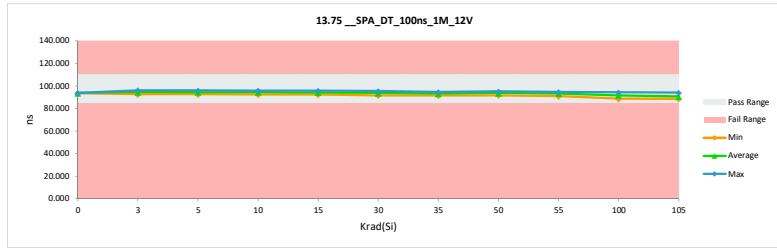
LDR TID Report  
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13.75 SPA_DT_100ns_1M_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	110 110
Min Limit	85 85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	93.612	93.689	0.077
0	118	93.515	93.589	0.074
0	122	93.496	93.571	0.075
3	61	94.767	94.785	0.018
3	62	95.166	95.180	0.014
3	63	92.679	92.507	-0.172
3	64	95.786	95.653	-0.133
3	65	93.676	93.591	-0.085
3	66	96.147	96.095	-0.052
5	67	94.074	93.890	-0.184
5	68	92.640	92.648	0.008
5	69	94.672	94.383	-0.289
5	70	94.475	94.272	-0.203
5	71	95.251	95.130	-0.121
5	72	96.089	95.987	-0.102
10	73	94.767	94.532	-0.235
10	74	95.166	95.014	-0.152
10	75	92.679	92.258	-0.421
10	76	95.786	95.489	-0.297
10	77	93.676	93.406	-0.270
10	78	96.147	95.865	-0.282
15	79	94.074	93.684	-0.390
15	80	92.640	92.284	-0.356
15	81	94.672	94.118	-0.554
15	82	94.475	94.063	-0.412
15	83	95.251	94.777	-0.474
15	84	96.089	95.687	-0.402
30	86	94.767	93.880	-0.887
30	89	95.166	94.405	-0.761
30	91	92.679	91.572	-1.107
30	92	95.786	94.804	-0.982
30	94	93.676	92.604	-1.072
30	95	96.147	95.405	-0.742
35	100	94.074	92.704	-1.370
35	101	92.640	91.471	-1.169
35	102	94.672	92.917	-1.755
35	103	94.475	93.121	-1.354
35	104	95.251	93.913	-1.338
35	105	96.089	94.521	-1.568
50	106	96.711	95.100	-1.611
50	107	97.019	95.222	-1.797
50	108	93.093	91.617	-1.476
50	109	94.922	93.546	-1.376
50	110	94.417	92.566	-1.851
50	111	95.622	94.186	-1.436
55	113	96.600	94.572	-2.028
55	114	92.775	90.777	-1.998
55	115	95.460	93.595	-1.865
55	116	96.391	94.528	-1.863
55	126	93.921	91.857	-2.064
55	128	96.170	94.165	-2.005
100	129	95.811	92.949	-2.862
100	131	93.322	90.631	-2.691
100	132	94.666	91.654	-3.012
100	133	92.528	89.986	-2.542
100	134	96.511	93.903	-2.608
100	135	95.652	93.129	-2.523
100	138	95.950	92.943	-3.007
100	139	91.262	88.650	-2.612
100	140	92.506	89.719	-2.787
100	141	95.561	92.692	-2.869
100	142	92.794	90.590	-2.204
100	143	92.216	89.195	-3.021
100	144	93.150	90.670	-2.480
100	220	94.492	92.013	-2.479
100	221	93.704	91.259	-2.445
100	224	92.269	89.806	-2.463
100	225	95.668	92.906	-2.762
100	226	95.236	92.856	-2.380
100	228	96.049	93.578	-2.471
100	232	94.277	91.909	-2.368
100	233	94.488	92.032	-2.456
100	236	92.036	89.657	-2.379
100	238	97.100	94.216	-2.884
100	243	93.501	90.673	-2.828
105	244	92.308	89.026	-3.282
105	246	93.029	89.946	-3.083
105	248	94.836	91.428	-3.408
105	374	91.947	88.592	-3.355
105	376	97.345	93.977	-3.368
	Max	97.345	96.095	0.077
	Average	94.503	92.997	-1.505
	Min	91.262	88.592	-3.408
	Std Dev	1.445	1.884	1.118



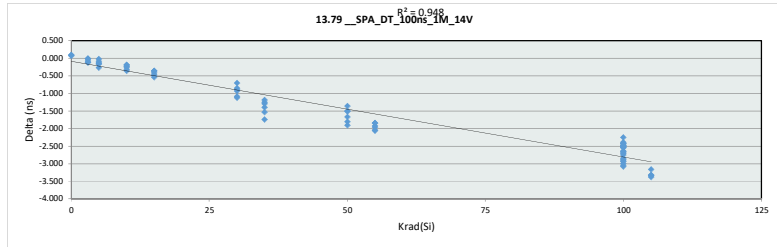
13.75 SPA_DT		0	3	5	10	15	30	35	50	55	100	105
LL		85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min		93.571	92.507	92.648	92.258	92.284	91.572	91.471	91.617	90.777	88.650	88.592
Average		93.616	94.635	94.385	94.427	94.102	93.778	93.108	93.706	93.249	91.567	90.594
Max		93.689	96.095	95.987	95.865	95.687	95.405	94.521	95.222	94.572	94.216	93.977
UL		110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



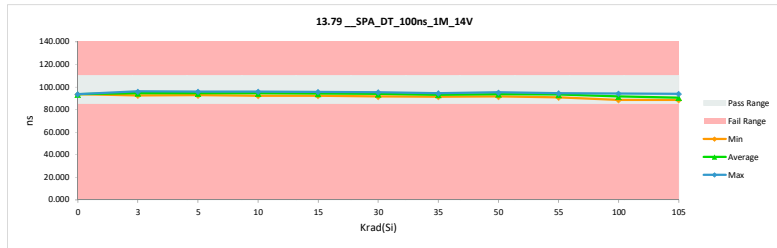
LDR TID Report  
TPS7H5001-SP

13.79 SPA_DT_100ns_1M_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	110 110
Min Limit	85 85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	93.565	93.659	0.094
0	118	93.535	93.616	0.081
0	122	93.462	93.566	0.104
3	61	94.745	94.742	-0.003
3	62	95.193	95.189	-0.004
3	63	92.639	92.536	-0.103
3	64	95.754	95.685	-0.069
3	65	93.700	93.584	-0.116
3	66	96.130	96.129	-0.001
5	67	94.002	93.881	-0.121
5	68	92.659	92.648	-0.011
5	69	94.659	94.405	-0.254
5	70	94.474	94.321	-0.153
5	71	95.192	95.049	-0.143
5	72	96.098	96.023	-0.075
10	73	94.745	94.572	-0.173
10	74	95.193	95.004	-0.189
10	75	92.639	92.291	-0.348
10	76	95.754	95.522	-0.232
10	77	93.700	93.398	-0.302
10	78	96.130	95.889	-0.241
15	79	94.002	93.653	-0.349
15	80	92.659	92.313	-0.346
15	81	94.659	94.130	-0.529
15	82	94.474	94.093	-0.381
15	83	95.192	94.755	-0.437
15	84	96.098	95.624	-0.474
30	86	94.745	93.907	-0.838
30	89	95.193	94.316	-0.877
30	91	92.639	91.560	-1.079
30	92	95.754	94.831	-0.923
30	94	93.700	92.584	-1.116
30	95	96.130	95.434	-0.696
35	100	94.002	92.712	-1.290
35	101	92.659	91.480	-1.179
35	102	94.659	92.927	-1.732
35	103	94.474	93.091	-1.383
35	104	95.192	93.949	-1.243
35	105	96.098	94.576	-1.522
50	106	96.748	95.090	-1.658
50	107	97.032	95.237	-1.795
50	108	93.058	91.573	-1.485
50	109	94.905	93.557	-1.348
50	110	94.406	92.513	-1.893
50	111	95.637	94.136	-1.501
55	113	96.532	94.476	-2.056
55	114	92.761	90.810	-1.951
55	115	95.446	93.616	-1.830
55	116	96.360	94.523	-1.837
55	126	93.924	91.894	-2.030
55	128	96.087	94.169	-1.918
100	129	95.886	92.969	-2.917
100	131	93.330	90.648	-2.682
100	132	94.685	91.609	-3.076
100	133	92.529	90.016	-2.513
100	134	96.405	93.868	-2.537
100	135	95.669	93.179	-2.490
100	138	95.944	92.961	-2.983
100	139	91.222	88.586	-2.636
100	140	92.521	89.688	-2.833
100	141	95.525	92.656	-2.869
100	142	92.839	90.595	-2.244
100	143	92.224	89.193	-3.031
100	144	93.258	90.622	-2.636
100	220	94.430	92.008	-2.422
100	221	93.661	91.256	-2.405
100	224	92.219	89.849	-2.470
100	225	95.636	92.932	-2.704
100	226	95.220	92.839	-2.381
100	228	96.092	93.562	-2.530
100	232	94.321	91.892	-2.429
100	233	94.512	92.021	-2.491
100	236	92.129	89.648	-2.481
100	238	97.106	94.187	-2.919
100	243	93.421	90.677	-2.744
105	244	92.320	89.010	-3.310
105	246	93.024	89.872	-3.152
105	248	94.854	91.522	-3.332
105	374	91.901	88.577	-3.324
105	375	97.334	93.962	-3.372
105	376	97.334	96.129	0.104
Max		94.494	92.996	-1.498
Average		91.222	88.577	-3.372
Min		1.439	1.888	1.129
Std Dev				



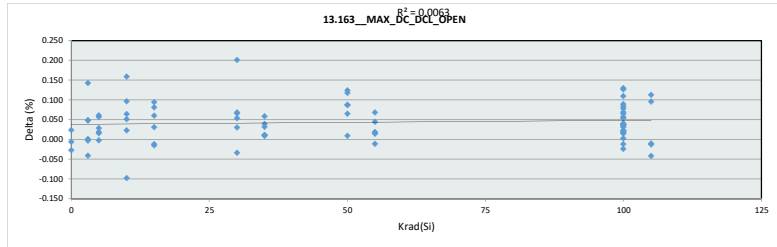
13.79 SPA_DT		0	3	5	10	15	30	35	50	55	100	105
LL		85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min		93.566	92.536	92.648	92.291	92.313	91.560	91.480	91.573	90.810	88.586	88.577
Average		93.614	94.644	94.388	94.446	94.095	93.772	93.123	93.684	93.248	91.561	90.589
Max		93.659	96.129	96.023	95.889	95.624	95.434	94.576	95.237	94.523	94.187	93.962
UL		110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



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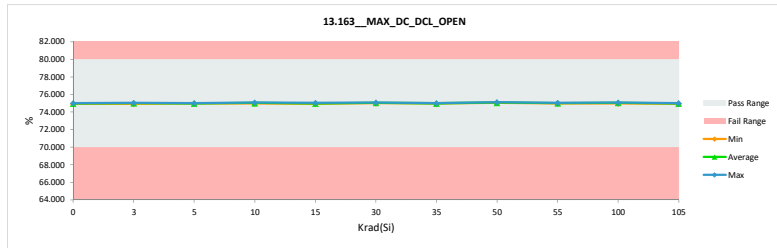
13.163_MAX_DC_DCL_OPEN	
Test Site	
Tester	
Test Number	
Unit	%
Max Limit	80
Min Limit	70

Krad(Si)	Serial #	PRE	POST	Delta
0	117	74.979	74.974	-0.005
0	118	75.036	75.010	-0.026
0	122	75.004	75.029	0.025
3	61	74.905	75.049	0.144
3	62	75.085	75.045	-0.040
3	63	74.995	75.044	0.049
3	64	75.051	75.049	-0.002
3	65	75.008	75.058	0.050
3	66	74.971	74.973	0.002
5	67	74.984	75.004	0.020
5	68	74.965	75.023	0.058
5	69	74.971	74.988	0.017
5	70	75.022	75.021	-0.001
5	71	74.941	75.003	0.062
5	72	74.970	75.000	0.030
10	73	74.905	75.065	0.160
10	74	75.085	74.988	-0.097
10	75	74.995	75.060	0.065
10	76	75.051	75.075	0.024
10	77	75.008	75.106	0.098
10	78	74.971	75.023	0.052
15	79	74.984	75.066	0.082
15	80	74.965	75.026	0.061
15	81	74.971	75.003	0.032
15	82	75.022	75.011	-0.011
15	83	74.941	75.036	0.095
15	84	74.970	74.956	-0.014
30	86	74.905	75.107	0.202
30	89	75.085	75.052	-0.033
30	91	74.995	75.050	0.055
30	92	75.051	75.082	0.031
30	94	75.008	75.077	0.069
30	95	74.971	75.038	0.067
35	100	74.984	75.017	0.033
35	101	74.965	75.025	0.060
35	102	74.971	74.983	0.012
35	103	75.022	75.035	0.013
35	104	74.941	74.981	0.040
35	105	74.970	74.980	0.010
50	106	74.975	75.064	0.089
50	107	75.014	75.133	0.119
50	108	74.988	75.076	0.088
50	109	75.075	75.085	0.010
50	110	74.971	75.096	0.125
50	111	74.995	75.061	0.066
55	113	75.010	75.000	-0.010
55	114	74.996	75.065	0.069
55	115	74.984	75.030	0.046
55	116	75.015	75.032	0.017
55	126	75.034	75.050	0.016
55	128	74.963	74.983	0.020
100	129	75.045	75.083	0.038
100	131	74.999	75.022	0.023
100	132	74.970	75.002	0.032
100	133	75.066	75.089	0.023
100	134	74.972	74.990	0.018
100	135	75.027	75.004	-0.023
100	138	74.988	75.046	0.058
100	139	75.100	75.116	0.016
100	140	75.022	75.064	0.042
100	141	74.932	75.011	0.079
100	142	75.004	75.043	0.039
100	143	75.008	75.030	0.022
100	144	74.999	75.069	0.070
100	220	75.002	75.092	0.090
100	221	74.980	75.111	0.131
100	224	75.036	75.071	0.035
100	225	75.026	75.015	-0.011
100	226	74.956	75.084	0.128
100	228	74.985	74.989	0.004
100	232	75.009	75.048	0.039
100	233	75.002	75.087	0.085
100	236	75.046	75.101	0.055
100	238	74.956	75.022	0.066
100	243	74.997	75.108	0.111
105	244	75.018	75.006	-0.012
105	246	75.006	74.965	-0.041
105	248	75.038	75.029	-0.009
105	374	74.910	75.007	0.097
105	376	74.913	75.027	0.114
105	Max	75.100	75.133	0.202
105	Average	74.996	75.039	0.043
105	Min	74.905	74.956	-0.097
105	Std Dev	0.042	0.040	0.050



13.163_MAX_DC	
Test Site	
Tester	
Test Number	
Max Limit	80 %
Min Limit	70 %

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000	70.000
Min	74.974	74.973	74.988	74.988	74.956	75.038	74.980	75.061	74.983	74.989	74.965
Average	75.004	75.036	75.007	75.053	75.016	75.068	75.004	75.086	75.027	75.054	75.007
Max	75.029	75.058	75.023	75.106	75.066	75.107	75.035	75.133	75.065	75.116	75.029
UL	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000	80.000

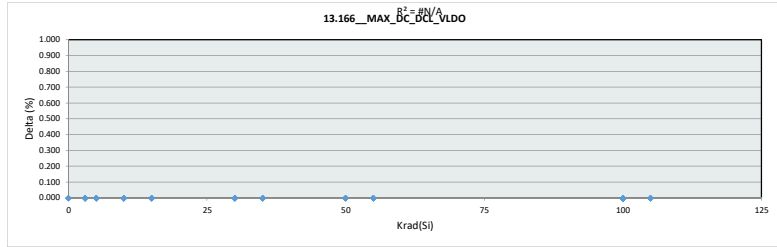




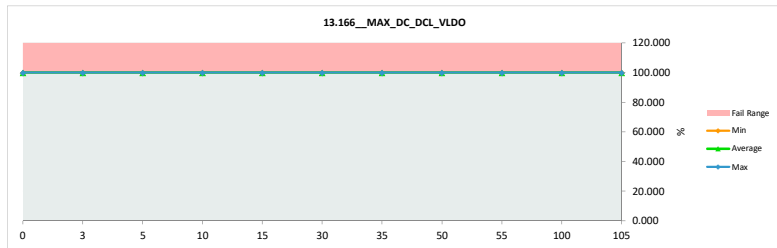
LDR TID Report  
TPS7H5001-SP

13.166_MAX_DC_DCL_VLDO	
Test Site	
Tester	
Test Number	
Unit	%
Max Limit	100
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	100.000	100.000	0.000
0	118	100.000	100.000	0.000
0	122	100.000	100.000	0.000
3	61	100.000	100.000	0.000
3	62	100.000	100.000	0.000
3	63	100.000	100.000	0.000
3	64	100.000	100.000	0.000
3	65	100.000	100.000	0.000
3	66	100.000	100.000	0.000
5	67	100.000	100.000	0.000
5	68	100.000	100.000	0.000
5	69	100.000	100.000	0.000
5	70	100.000	100.000	0.000
5	71	100.000	100.000	0.000
5	72	100.000	100.000	0.000
10	73	100.000	100.000	0.000
10	74	100.000	100.000	0.000
10	75	100.000	100.000	0.000
10	76	100.000	100.000	0.000
10	77	100.000	100.000	0.000
10	78	100.000	100.000	0.000
15	79	100.000	100.000	0.000
15	80	100.000	100.000	0.000
15	81	100.000	100.000	0.000
15	82	100.000	100.000	0.000
15	83	100.000	100.000	0.000
15	84	100.000	100.000	0.000
30	86	100.000	100.000	0.000
30	89	100.000	100.000	0.000
30	91	100.000	100.000	0.000
30	92	100.000	100.000	0.000
30	94	100.000	100.000	0.000
30	95	100.000	100.000	0.000
35	100	100.000	100.000	0.000
35	101	100.000	100.000	0.000
35	102	100.000	100.000	0.000
35	103	100.000	100.000	0.000
35	104	100.000	100.000	0.000
35	105	100.000	100.000	0.000
50	106	100.000	100.000	0.000
50	107	100.000	100.000	0.000
50	108	100.000	100.000	0.000
50	109	100.000	100.000	0.000
50	110	100.000	100.000	0.000
50	111	100.000	100.000	0.000
55	113	100.000	100.000	0.000
55	114	100.000	100.000	0.000
55	115	100.000	100.000	0.000
55	116	100.000	100.000	0.000
55	126	100.000	100.000	0.000
55	128	100.000	100.000	0.000
100	129	100.000	100.000	0.000
100	131	100.000	100.000	0.000
100	132	100.000	100.000	0.000
100	133	100.000	100.000	0.000
100	134	100.000	100.000	0.000
100	135	100.000	100.000	0.000
100	138	100.000	100.000	0.000
100	139	100.000	100.000	0.000
100	140	100.000	100.000	0.000
100	141	100.000	100.000	0.000
100	142	100.000	100.000	0.000
100	143	100.000	100.000	0.000
100	144	100.000	100.000	0.000
100	220	100.000	100.000	0.000
100	221	100.000	100.000	0.000
100	224	100.000	100.000	0.000
100	225	100.000	100.000	0.000
100	226	100.000	100.000	0.000
100	228	100.000	100.000	0.000
100	232	100.000	100.000	0.000
100	233	100.000	100.000	0.000
100	236	100.000	100.000	0.000
100	238	100.000	100.000	0.000
100	243	100.000	100.000	0.000
105	244	100.000	100.000	0.000
105	246	100.000	100.000	0.000
105	248	100.000	100.000	0.000
105	374	100.000	100.000	0.000
105	376	100.000	100.000	0.000
105	Max	100.000	100.000	0.000
105	Average	100.000	100.000	0.000
105	Min	100.000	100.000	0.000
105	Std Dev	0.000	0.000	0.000



13.166_MAX_DC											
Test Site											
Tester											
Test Number											
Max Limit	100 %										
Min Limit	%										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Average	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Max	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
UL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000



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