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ABSTRACT

C2000 IDE Assist, or C2000 IDEA, is a first-of-its-kind tool integrated within Visual Studio Code[™] and Code Composer Studio[™], designed to enhance development for Texas Instruments C2000[™] MCUs. C2000 IDE Assist provides an easier way to code, debug, and migrate C2000 application code with features already available in your development environment. This all-in-one tool speeds up the various steps of the software production process, from code initialization to runtime events to debug profiling, which includes ease of migration, targeted collateral findability, software template support, and more. Integrated into Code Composer Studio, C2000 IDE Assist offers easy access to various engineering tools, making the extension a comprehensive solution for C2000 application implementation.

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1 Introduction

The Texas Instruments C2000 software development kits (SDK) provide a cohesive suite of software and documentation designed to accelerate development of real-time control applications. These resources include device-specific drivers, libraries, and peripheral examples, helping developers streamline projects. The SDKs are built to accommodate users of all experience levels, with beginner-friendly features to guide first-time users. The C2000 SysConfig tool, included in the SDKs, enhances the setup process by simplifying device and peripheral complexities through an intuitive visual interface. To further improve the initialization, run-time, and debug user experience, the C2000 IDEA centralizes tools and collateral within a single environment for a more efficient workflow.

This features guide discusses how to get started with and utilize the C2000 IDE Assist to simplify the overall software implementation when used with the C2000 SDKs.

2 Getting Started

This section introduces the basics of the C2000 IDEA tool such as how to setup the development environment and basic commands to utilize the extension.

2.1 Software Installation

This section details the software installation procedure to enable the extension in your development environment.

2.1.1 Install Code Composer Studio IDE

Follow these steps to install TI's integrated development environment (IDE) for microcontrollers and processors:

- 1. Download the latest offline installer for Code Composer Studio (CCS):
 - a. Default Recommended Path C:/ti/ccs200X
- 2. Once Select Components is reached, select C2000 real-time MCUs.
- 3. Continue through the steps until installation is complete.
- 4. Launch CCS.

😚 Code Composer Studio Setup	– 🗆 X
Select Components	Û
Select the components you want to install; clear the components you do not want to MSPM0 Arm® Cortex®-M0+ microcontrollers MSP430 ^{rm} microcontrollers C2000 ^{rm} real-time microcontrollers Wireless Connectivity Arm®-based processors AM2x Arm®-based high performance microcontrollers mmWave Sensors Hercules ^{rm} Safety MCUs TM4C12x Arm® Cortex ®-M4F MCUs C6000 Power-Optimized DSP	o install. Click Next when you are ready to continue. Click on a component to get a detailed description
 OMAP-L1x DSP + Arm9[™] Processor SimpleLink[™] MSP432[™] low power + performance MCUs 	
nstallBuilder	< Back Next > Cancel

Figure 2-1. CCS Setup

This extension is only supported for Code Composer Studio v20.0.0+.

2.1.2 Install C2000 IDEA Extension

The C2000 IDE Assist Extension can be installed into either a CCS (v20.0.0+) or VS Code development environment. Installation instructions for the CCS IDE are provided in Section 2.1.2.1 and instructions for the VS Code IDE are provided in Section 2.1.2.2.

2.1.2.1 CCS Extension Installation

Follow these instructions to install the extension to CCS:

- 1. Navigate to the *Extensions* tab in the left side bar panel.
- 2. Search for C2000 IDEA and select install.

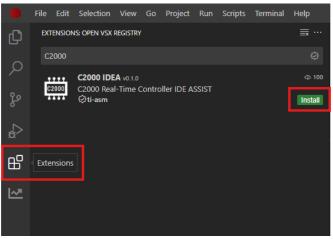


Figure 2-2. C2000 IDEA Setup in CCS

- a. If no results are displayed in the Extension Marketplace, your connection may be configured to use a proxy server. To set up the proxy settings:
 - i. Click the gear (Manage) icon in the left side bar panel and select Settings.
 - ii. Navigate to Application > Proxy and input proxy information.

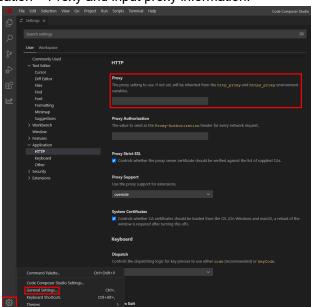


Figure 2-3. Proxy Setup

3. Refresh or restart CCS. The extension has been successfully installed when the *C2000 IDE Assist* icon is present in the left side bar panel on the left side of the screen.



2.1.2.2 VS Code Extension Installation

Follow these instructions to install the extension to VS Code:

- 1. Download the VSIX from OPEN-VSX.
 - a. NOTE: Make sure to always download the latest version. For more information regarding the tool, refer to the C2000-IDEA GitHub repository.
- 2. Navigate to the *Extensions* tab in the left side bar panel.
- 3. Click on the three dots and select the Install from VSIX. Select the downloaded VSIX in the file explorer.

× F	ile Edit Selection View Go Run Terminal Help
ф	EXTENSIONS U
	Search Extensions in Marketplace
\mathcal{P}	Check for Extension Updates
0.5	KECOMMENDED Disable Auto Update for All Extensions
દુરુ	Enable All Extensions
	Disable All Installed Extensions
Ŕ	Show Running Extensions
n0	Start Extension Bisect
μ©	
2	Install from VSIX

Figure 2-4. C2000 IDEA Setup in VS Code

Refresh or restart VS Code. The extension has been successfully installed when the *C2000 IDE Assist* icon is present in the left side bar panel on the left side of the screen.

2.2 Import Project

Once the code editor platform of choice is set up with the extension, the tool is ready to be used with CCS projects.

Follow these steps to import a project:

- 1. Open the CCS or VS Code IDE.
- 2. Open a new or existing workspace as normal.
- 3. Navigate to the CCS Extension by selecting the icon on the left side bar panel.

2.3 Detect Project

All projects using C2000 devices can be detected with a single click of the *Get Projects* element. This step allows the extension to recognize the device for each project and keep track of which project each file belongs to. Project Detection unlocks the complete capabilities of the extension in real-time such as Driverlib Migration across devices. Some extension features are only available with project detection (or default device setup, described in Section 2.5.1), whereas other features can be run on single files by requiring input of the current device. See the Section 4.4 for a full list of features and commands available.

Follow these steps to detect a project:

- 1. To detect all the projects in the workspace, use one of the two options below:
 - a. Enter CTRL+SHIFT+P and type and click C2000: Get Projects.
 - b. Click Project Detection > Get Projects in the C2000 IDEA Features pane of the Extension tree.
- Once run, all the projects in the workspace detected by the extension are present in the C2000 IDEA -Projects pane of the Extension tree. View your project and verify the device variant and current device details are as expected.

Note

C2000 IDEA is recommended to be used specifically within CCS to utilize all the features of the extension along with the complete debugging capabilities provided in CCS concurrently.



2.4 Basic Commands

CCS and VS Code provide a powerful palette of commands that allow users to perform most, if not all, tasks using solely the keyboard. These commands can be activated using CTRL+SHIFT+P. Refer to the table below for some easy-to-use commands enabled by the C2000 IDEA extension. Alternatively, select features have UI buttons available in the C2000 IDEA - Features pane that automatically run the associated commands to activate the feature.

Note

See Section 4.4 in this document for the full list of commands or search "C2000" in the command palette.

Table 2-1. C2000-IDEA Basic Commanus			
Purpose	Commands	Devices	Project Detection
Open Tool Walkthrough	C2000: Help Walkthrough	GEN2, GEN3, GEN4	NOT REQUIRED
Run Driverlib Register Vision	C2000: Run Driverlib Register Vision on Current File	GEN3, GEN4	NOT REQUIRED
Run Bitfield Register Vision	C2000: Run Bitfield RegisterVision on Current File	GEN3, GEN4	NOT REQUIRED
Enable Register Coder	C2000: Enable Register Code Write/Read	GEN3, GEN4	REQUIRED
Enable Interrupt Coder	C2000: Enable Interrupt Code Templates	GEN3, GEN4	REQUIRED
Enable Continuous Migration Check	C2000: Enable Continuous Migration Check on Current File	GEN3, GEN4	REQUIRED

Table 2-1. C2000-IDEA Basic Commands



2.5 Global Settings

Global settings can be utilized depending on the user's project needs. In the General Settings > Extensions > C2000 IDEA tab, there are several configuration options.

2.5.1 Project-less Detection

Users can manually select a default device to be used when a device is not automatically detected or when project detection has not been run. By setting a default device, most features of the tool can be utilized even if a file is not part of a detected project. In other words, when no device is automatically detected in a project-less file, the tool uses the user configured default device.

Follow these steps to set up the default C2000 device in CCS and VS Code:

- 1. Click the gear (Manage) icon in the left side bar panel and select the General Settings... option.
- 2. Navigate to User > Extensions > C2000 IDEA.
- 3. In Project: Default Device, select the desired device.

🕈 Settings ×			
Search			
User	Workspace		
Col	mmonly Used		
> Tex	kt Editor	C2000 IDEA	
> Wo	orkbench		
Wi	ndow	Collateral: Use Internal Browser	
> Fea	atures	🗹 Use internal browser (if available)	
> Ap	plication		
> Sec	curity	Project: Default Device	
∽ Ext	tensions	When working on a projectless file, what device should the tool default back to	
C	2000 IDEA	when working on a projectiess file, what device should the tool deladit back to	
O	ics	None ~	
cl	langd		
E	ditorConfig	CCS	
G	iit		

Figure 2-5. Default Device Selection

Note

TI recommends to always use the latest SDKs when developing with this extension as the C2000 IDEA correlates with the latest available SDKs.

2.5.2 Internal Browser

Internal Browser is the internal web browser used by the IDE, if available, to view collateral outputted by C2000 IDEA. The internal browser is the default and recommended setting. When this checkbox is not selected, CCS opens the page outside of the IDE in the default system web browser. This feature is similar to the 'Open in System Browser' feature in CCS Resource Explorer.

⋧ Settings ×		
	User Workspace	
	Commonly Used Text Editor	C2000 IDEA
	 > Workbench Window > Features 	Collateral: Use Internal Browser ☑ Use internal browser (if available)
	Application Security Extensions	Project: Default Device When working on a projectless file, what device should the tool default back to
	C2000 IDEA CCS	None Y
	clangd EditorConfig Git	ccs

Figure 2-6. Internal Browser



3 Overview

This section discusses an overview of the C2000 IDEA interface and how to easily navigate through the tool. The features of the tool are split into four panels on the left hand side of the screen. The following images showcase an overview of the tool when working with a sample C2000WARE example.

1. *C2000 COLLATERAL:* This panel showcases key collateral, including the Technical Reference Manual and Data Sheet, corresponding with the device associated with the project and/or file that is currently opened.

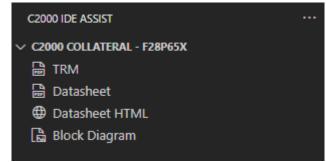


Figure 3-1. C2000 Collaterals

2. C2000 ADDITIONAL RESOURCES: This panel lists all the associated collateral with the project specific to the device and peripherals. The collateral is organized based on foundational, getting started, and expert material.

✓ C2000 ADDITIONAL RESOURCES
✓ ☐ ADC - Foundational Materials
ADC Input Circuit Evaluation for C2000 M
C2000 Academy - ADC
PSpice for TI design and simulation tool
Real-Time Control Reference Guide
TI Precision Labs - ADCs
TI Precision Labs: Driving the reference in
TI Precision Labs: Introduction to analog
TI Precision Labs: SAR ADC input driver d
TI e2e: Connecting VDDA to VREFHI
TI e2e: Topologies for ADC Input Protecti
TI e2e: Why does the ADC Input Voltage
Understanding Data Converters Applicati
✓ ☐ ADC - Getting Started Materials
ADC-PWM Synchronization Using ADC In
Analog-to-Digital Converter (ADC) Traini
Hardware Design Guide for F2800x C200
✓ ☐ ADC - Expert Materials
ADC Oversampling Application Report
Analog Engineer's Calculator
Analog Engineer's Pocket Reference
Charge-Sharing Driving Circuits for C200
Charge-Sharing Driving Circuits for C200
Debugging an integrated ADC in a micro
Hardware oversampling using C2000 ADC
Methods for Mitigating ADC Memory Cr

Figure 3-2. C2000 Additional Resources



3. *C2000 IDEA - FEATURES:* This panel provides quick access to all features and commands of the extension, including all the software development tools and walkthroughs.

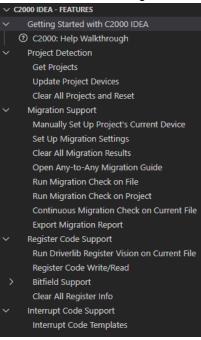


Figure 3-3. C2000 IDEA - Features

4. C2000 IDEA - PROJECTS: This panel displays all the projects currently opened in a workspace. Each project has an associated *Device Variant*, *Current Device*, and list of *Migration Devices* set.

V C2000 IDEA - PROJECTS
✓ ➡ adc_ex1_soc_software
Device Variant: TMS320C28XX.TMS320F28
☆ Current Device: F28P65x
✓ ➡ gpio_ex2_toggle
Device Variant: TMS320C28XX.TMS320F28
☆ Current Device: F2837xD
\rightleftharpoons Migration Devices:
Device Variant: TMS320C28XX.TMS320F28
☆ Current Device: F2838x
ightarrow Migration Devices:

Figure 3-4. C2000 IDEA - Projects



4 Features

This chapter details the full capabilities of features discussed in the previous section and how to optimize them while developing software.

4.1 Targeted Collateral Delivery

The C2000 IDEA tool offers a highly efficient method of accessing a wealth of collateral at your fingertips. With targeted collateral delivery, users can reach the most relevant information with a single click, eliminating the need to search through various documents. The tool automatically detects the topic at hand and provides immediate suggestions, including direct links to critical resources like device documentation and register definitions.

4.1.1 Key Collateral

When a detected project is edited, links to key collateral for the project's device display automatically in the **C2000 Collateral** pane of the Extension. This feature generates top level links to the device data sheet and the Technical Reference Manual (TRM), as well as links to key sections inside this documentation, like the device Functional Block Diagram.

4.1.2 Additional Resources

As the user cursor moves in an active file, the collateral recommendations change automatically to highlight additional helpful collateral beyond key documentation in the **C2000 Collateral** pane. This includes Application Reports, C2000 Academy pages, labs, E2E[™] forum answers, and videos related to the word or phrase (ex: C2000 peripheral names) the user is currently editing. Links to these resources can be accessed from the **C2000 Additional Resources** pane in the extension tree.

4.1.3 Register Vision

The Register Vision feature provides easy access to TRM register description for all the detected registers in a file. Once the command is run, all detected registers for the specific C2000 device are highlighted and direct links to the register description in the device HTML TRM are provided.

Follow these steps to enable the Driverlib Register Vision:

- 1. Open a C2000 application C-Code (.c) or Header (.h) file.
- 2. Run the Driverlib Register Vision by either:
 - a. Press CTRL+SHIFT+P, type and select C2000: Run Driverlib Register Vision on Current File.
 - b. Click on *Register Code Support > Run Driverlib Register Vision on Current File* in the **C2000 IDEA -***Features* pane of the Extension tree.
- 3. If no project has been detected (or if the file doesn't belong to a project), the extension prompts to input the C2000 device corresponding to the file.
- 4. Hover over an identified register that is highlighted in yellow.
- 5. Click on the *View Register Description in TRM* link to access the register description in the online HTML TRM.

C2000 IDE ASSIST ····	c adc_ex1_soc_software.c c adc.h ×
 ✓ C2000 COLLATERAL 	ti >> c2000 > C2000Ware 5 04 00 00 > driverlib > f28p65x > driverlib > C adc.h > 1554 //! SOCs. 1555 //! 1556 //! \note To trigger SOC0, SOC1 and SOC2, value (ADC_FORCE_SOC0
> C2000 ADDITIONAL RESOURCES	<pre>1557 //! ADC_FORCE_SOC1 ADC_FORCE_SOC2) should be passed as socMask. 1558 //!</pre>
V C2000 IDEA - FEATURES	1559 //! \return None.
 Getting Started with C2000 IDEA Project Detection Migration Support Register Code Support Run Driverlib Register Vision on Curren 60 	<pre>1560 // 1561 //***********************************</pre>
Register Code Write/Read Bitfield Support Clear All Register Info Interrupt Code Support	1566 // Check the argument 1567 // 1568 ASSERT(ADC_ising macro_ADC_0_SOCFRC1 1569 provided by "inc/hw_adc.h" 1570 // 1571 // Write to the argument of the second secon

This process of linking detected registers in user code to specific register descriptions in the TRM is not always instantaneous and can take some additional seconds depending on the contents of the file.

Figure 4-1. Driverlib Register Vision with F28P65x

C2000 IDEA also supports register accesses for bitfield-style software development.

Follow these steps to enable the Bitfield Register Vision:

- 1. Open a C2000 application C-Code file.
- 2. Run the Bitfield Register Vision by either:
 - a. Press CTRL+SHIFT+P, type and select C2000: Run Bitfield Register Vision on Current File.
 - b. Click on Register Code Support > Bitfield Support > Run Bitfield Register Vision on Current File in the C2000 IDEA Features pane of the Extension tree.
- 3. If no project has been detected (or if the file doesn't belong to a project), the extension prompts to input the C2000 device corresponding to the file.
- 4. Hover over an identified register that is highlighted in yellow.
- 5. Click on the *View Register Description in TRM* link to access the register description in the online HTML TRM.

C2000 IDE ASSIST ····	C rtdma_ex2_mem_transfer_mpu.c ×
V C2000 COLLATERAL	rtdma_ex2_mem_transfer_mpu > C rtdma_ex2_mem_transfer_mpu.c >
C2000 Real-Time Controllers	148 }
	149
	150 status = 1; // Test done. Code will never reach this line due to NMI error.
	151 return;
> C2000 ADDITIONAL RESOURCES	152 }
V C2000 IDEA - FEATURES	153 154 // MYI ISR
> Getting Started with C2000 IDEA	194 // mil 13A 155 void myMHI ISR(void)
> Project Detection	156 {
> Migration Support	157 //
 Register Code Support 	158 // Service the highest priority active RTOW could be appreciate space topose
Run Driverlib Register Vision on Current File	159 // field RTDMA1_DW_HIGHPRIO_ERROR_ADDRESS
-	160 if(ESM_getInterruptStatus(ESMCPU1_BASE,ESM_1 provided by "hwbf_error_aggregator.h"
Register Code Write/Read	161 { Type: uint32_t (aka unsigned int)
 Bitfield Support 	162 RTDMA1 DW Error aggregator High Priority Error address register
Run Bitfield Register Vision on Current 60	163 // In ERROR ASSREGATOR CONFIG.REDS 164 // Determine and store the specific add aphic:
Run Bitfield Register to Driverlib Migratio	164 // Determine and store the specific add public: uint32_t RTDMA1_Dw_HTGHPRID_ERROR_ADDRESS // possible to get the low priority addi
Clear All Register Info	166 // View Register Description in TRM ERROR_AGGREGATOR register access RTDMA1_DM_HIGHPRIO_ERROR_ADDRESS
> Interrupt Code Support	167 error location WR = ErrorAggregatorRegs RTOMA1 DW HIGHPRIO ERROR ADDRESS;
	168 error_location_RD = ErrorAggregatorRegs_RTDMA1_DR_HIGHPRIO_ERROR_ADDRESS;
	169



This process of linking detected registers in user code to specific register descriptions in the TRM is not always instantaneous and can take some additional seconds depending on the contents of the file.

Figure 4-2. Bitfield Register Vision with F29H85x

4.2 Developer Efficiency Tools

C2000 IDEA enhances developer efficiency by providing software template support, allowing users to easily insert code such as interrupt handler definitions and register accesses. This feature not only accelerates development, but also guides users on how to write efficient C2000-based software.

4.2.1 Register Coder

The Register Code feature automatically generates code to write or read from a specific register or register field. The auto-generated code provides a template for writing to or reading from a register.

Follow these steps to enable the Register Coder:

- 1. To run the Register Coder, either:
 - a. Press CTRL+SHIFT+P, type and select C2000: Enable Register Code Write/Read.
 - b. Click the check toggle in *Register Code Support* > *Register Code Write/Read* in the C2000 IDEA *Features* pane of the Extension tree.
- 2. Once enabled, open a C2000 application C-Code or Header file from a detected project.
- 3. Begin typing the peripheral, register, or field name. The Register Code feature displays an alphabetical list of all available registers and register fields to read or write for the current device.
- 4. Select a choice to autogenerate the template code. Press *TAB* on the keyboard to edit the base address field, variable to read into, and/or value to write, as needed.

C2000 IDE ASSIST ····	C adc_ex1_soc_software.c ● C adc.h
V C2000 COLLATERAL - F28P65X	adc_ex1_soc_software > C adc_ex1_soc_software.c > 🛇 main
TRM	143 while(ADC_getInterruptStatus(myADC1_BASE, ADC_INT_NUMBER1) == false)
🖶 Datasheet	
Datasheet HTML	
	146 ADC_clearInterruptStatus(myADC1_BASE, ADc_INT_NUMBER1);
> C2000 ADDITIONAL RESOURCES	
V C2000 IDEA - FEATURES	
> Getting Started with C2000 IDEA	
> Project Detection	150 // 151 mvADC0Result0 = ADC readResult(ADCARESULT BASE, ADC SOC NUMBER0);
> Migration Support	151 myADCORESUIT0 = ADC_readResuIt(ADCARESULT_DASE, ADC_SOC_NUMBER0); 152 myADCOResult1 = ADC_readResult(ADCARESULT_BASE, ADC_SOC_NUMBER1);
 Register Code Support 	152 myADC1Result0 = ADC_readResult(ADCCRESULT BASE, ADC_SOC_NUMBER0);
	154
Run Driverlib Register Vision on Current File	155 // Sample ADC read Result using C2000 TDEA
Register Code Write/Read 🛛 🛞 🤗	156 myADC1REsult1 = ADC read result
> Bitfield Support	157 The The International Inte
Clear All Register Info	158 // ADC Read RESULT1
> Interrupt Code Support	159 // Software breekpoint. At this T ADC Read RESULT10
	160 // myADC0Result(, myADC0Result1 🐴 ADC Read RESULT11
	161 // 4 ADC Read RESULT12
	162 // Hit run agair to get updated T ADC Read RESULT13
	163 // C Read RESULT14
	164 ESTOP0; C ADC Read RESULT15
	165 DADC Read RESULT2
	166 } DAC Read RESULT3
	167 C Read RESULT4
	4 th ADC Read RESULT5

Figure 4-3. Register Coder Read with F28P65x

4.2.2 Interrupt Coder

The Interrupt Code Template feature automatically generates a template Interrupt Service Routine (ISR) function for any interrupt available on the device.

Follow these steps to enable the Interrupt Coder:

- 1. To run the Interrupt Coder, either:
 - a. Press CTRL+SHIFT+P, type and select C2000: Enable Register Code Write/Read.



- b. Click on *Interrupt Code Support > Interrupt Code Templates* in the C2000 IDEA Features pane of the Extension tree.
- 2. Once enabled, open a C2000 application C-Code file to add an Interrupt Service Routine (ISR).
- 3. Begin typing the phrase *interrupt handler [interrupt name]* in the code editor. The Interrupt Coder feature displays an alphabetical list of all available interrupts for the current device.
- 4. Select a choice to autogenerate the template interrupt code. Fill in the rest of the code needed for the specific application.

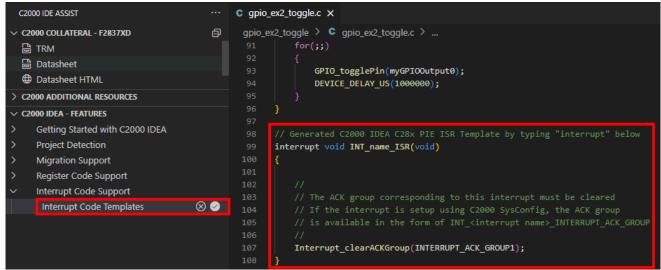


Figure 4-4. Interrupt Coder with C28x

For C28x devices, the correct ACK group for the given peripheral interrupt is automatically filled in. Make sure to add any application-specific code before this line.

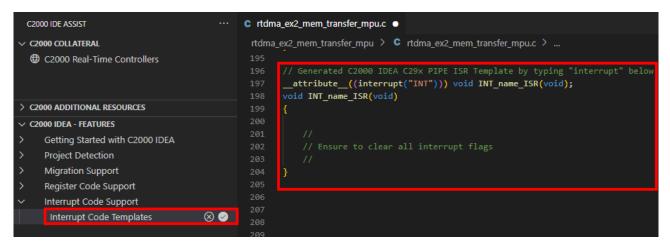


Figure 4-5. Interrupt Coder with C29x

Note

For C29x devices, the interrupt type attribute is automatically included to indicate the type of interrupt being defined. By default, the extension defines interrupts as *INT* but the attribute can be modified to *RTINT*.



C2000 IDEA simplifies code migration between C2000 devices by automatically detecting changes — such as added, removed, or modified registers and register fields — in driverlib code, making the migration process seamless. This support extends to migration for both detected projects and standalone files for driverlib migration. For select migration paths, bitfield code migration is also available. Additionally, the tool provides migration assistance between both F28x-to-F28x and F28x-to-F29x devices, highlighting differences between the two architectures and offering suggested solutions.

Note

For the complete list of specific devices supported by each command, refer to the Section 4.4, and for the differences between driverlib-style bitfield-style and code, refer to the Sofware Drivers documentation.

4.3.1 Driverlib Migration

The C2000 IDEA Extension can be used to run a F28x-to-F28x or F28x-to-F29x migration check on a project or file written with driverlib-style code. This code style is characterized by using calls to functions defined in the driverlib source files (ex: Device_init()) and/or register accesses containing the _0_ syntax.

4.3.1.1 File-Based Migration Check

Follow these steps to enable Driverlib Migration on a standalone file:

- 1. Open a C2000 application C-Code or Header file.
- 2. Run the migration check by either:
 - a. Pressing CTRL+SHIFT+P, typing and selecting C2000: Run Migration Check on File.
 - b. Clicking on *Migration Support > Run Migration Check on Project* in the C2000 IDEA Features pane of the Extension tree
- 3. Select the current C2000 device the code in the file applies to.
- 4. Select the C2000 device to migrate the file to.
- 5. The status bar at the bottom right of the screen displays *Migration check completed* when finished. All major migration concerns in the file are underlined with a red squiggly line. All other migration warnings in the file are underlined with a yellow squiggly line.
- 6. Review and resolve concerns throughout the file. The following options are provided when the underlined code is hovered over:
 - a. Select *View Problem* to quickly loop through the detected concerns in the file.

C2000 IDE ASSIST ····	Central consolitivates 3 ×		
> C2000 COLLATERAL - F28P65X	adc_ex1_soc_software > C adc_ex1_soc_software.c > 🗘 main		
> C2000 ADDITIONAL RESOURCES			
V C2000 IDEA - FEATURES			
> Getting Started with C2000 IDEA			
> Project Detection	129 ADC_forceHultipleSOC(=yADC0_BASE, (ADC_FORCE_SOC0 ADC_FORCE_SOC1));		
 Migration Support 			
Manually Set Up Project's Current Device	131 // 132 // Wait for ADCA to complete, then acknowledge flag		
Set Up Migration Settings			
Clear All Migration Results	<pre>134 while(ADC_getInterruptStatus(myADC0_BASE, ADC_INT_NUMBER1) == false)</pre>		
Open Any-to-Any Migration Guide			
Run Migration Check on File 🛛 🕄	136) 137 AC clearInterruptStatus(#VADC0 BASE, ADC INT NUMBER1):		
Run Migration Check on Project	137 ADC_clearInterruptStatus(myADC0_BASE, ADC_INT_NUPBER1); 138		
Continuous Migration Check on Current File	139 P ADC_forceHultipleSOC(myADC1_BASE, (ADC_FORCE_SOC0 ADC_FORCE_SOC1));		
Export Migration Report	A adc ex1_soc software. 3 of 3 problems ↓ ↑ ×		
> Register Code Support			
> Interrupt Code Support	function ADC_forceMultipleSOC: Function is compatible but socMask argument changed from uint16_t to uint32_t since F29 device supports 32 SOCs (2000 Migration Check((2000_MIGRATION_INCOMPAT		
	140 //		
C2000 IDEA - PROJECTS			
Device Variant: TMS320C28XX.TMS320F28	<pre>143 while(ADC_getInterruptStatus(myADC1_BASE, ADC_INT_NUMBER1) == false)</pre>		
→ Migration Devices: F29H85y			

Figure 4-6. Migration Check - View Problem

- b. Select Quick Fix to mitigate the migration concern. Select one of the below options:
 - i. *Review migration collateral for [current device] to [migration device]-* This option opens a link to the online migration collateral for the specific migration path using the latest version of C2000WARE.
 - ii. Wrap in device specific #IFDEF for [current device] and [migration device] This option autogenerates pre-processor wrappers around the line of code so that an updated version of the code can be compiled for the new device. Fill in the line with the *//Enter alternate code* comment with the modified code and add a #define for the current device somewhere in the file.



 Ignore code related errors - This option ignores this migration concern. 			
C2000 IDE ASSIST ····	Coadc_ex1_soc_software.c 3 ×		
> C2000 COLLATERAL - F28P65X	adc_ex1_soc_software > C adc_ex1_soc_software.c > 🛇 main		
> C2000 ADDITIONAL RESOURCES	119 ERTM;		
V C2000 IDEA - FEATURES	120		
> Getting Started with C2000 IDEA			
> Project Detection	122 // Loop indefinitely 123 //		
 Migration Support 	123 // 124 while(1)		
Manually Set Up Project's Current Device	125 {		
Set Up Migration Settings	126 //		
Clear All Migration Results	127 // Convert, wait for completion, and store results		
Open Any-to-Any Migration Guide	128 // 129 ADC_forceMultipleSOC(myADC0_BASE, (ADC_FORCE_SOC0 ADC_FORCE_SOC1));		
Run Migration Check on File 🛛 🕄			
Run Migration Check on Project	131 //		
Continuous Migration Check on Current File	132 // Wait for ADCA to complete, then acknowledge flag		
Export Migration Report	<pre>133 // 134 while(ADC getInterruptStatus(myADC0 BASE, ADC INT NUMBER1) == false)</pre>		
> Register Code Support	134 while(ADC_getInterruptStatus(myADC0_BASE, ADC_INI_NUMBERI) == Taise) 135 {		
> Interrupt Code Support	136 }		
	137 ADC_clearInterruptStatus(myADC0_BASE, ADC_INT_NUMBER1);		
C2000 IDEA - PROJECTS			
✓ ➡ adc_ex1_soc_software	139 P ADC_forceMultipleSOC(my Quick Fix		
Device Variant: TMS320C28XX.TMS320F28	141 // Wait for ADCC to com 🔓 Review migration collateral for F28P65x to F29H85x		
G Current Device: F28P65x	142 // 💡 Wrap in device specific #IFDEF for F28P65x and F29H85x		
	143 while(ADC_getInterruptS 💡 Ignore ADC forceMultipleSOC related errors		
✓ ➡ led_ex1_blinky			
Device Variant: TMS320C28XX.TMS320F28	145 } 146 ADC_clearInterruptStatus(myADC1_BASE, ADC_INT_NUMBER1);		
G Current Device: F2838x	147		
✓ ➡ gpio_ex2_toggle	149 // Store results		
Device Variant: TMS320C28XX.TMS320F28	<pre>150 // 151 myADCOResult0 = ADC readResult(ADCARESULT BASE, ADC SOC NUMBER0);</pre>		
G Current Device: F2837xD	151 myADCOResult0 = ADC_readResult(ADCARESULT_BASE, ADC_SOC_NUMBERD); 152 myADCOResult1 = ADC_readResult(ADCARESULT_BASE, ADC_SOC_NUMBER1);		
	<pre>153 myADC1Result0 = ADC_readResult(ADCCRESULT_BASE, ADC_SOC_NUMBER0);</pre>		
	<pre>154 myADC0Result1 = ADC_readResult(ADCARESULT_BASE, ADC_SOC_NUMBER1);</pre>		

Figure 4-7. Migration Check - Quick Fix

- 7. The migration report contains a list of the detected migration concerns. To export the migration report to the desired file path, either:
 - a. Enter CTRL+SHIFT+P, type and select C2000: Export Migration Report.
 - b. Click on *Migration Support > Export Migration Report* in the **C2000 IDEA Features** pane of the Extension tree.

✓ C2000 IDEA - PROJECTS ✓ ☐ adc_ex1_soc_software	D Unitid-1 •
Device Variant: TMS320C28XX.TMS320F28	2 From: F28F65x
Current Device: F28P65x	3 To: F29H85x
Z Migration Devices: F29H85x → 🖸 🖉	4 File: c:\Users\a0509477\workspace_ccs\adc_ex1_soc_software.adc_ex1_soc_software.c
✓ ➡ led_ex1_blinky	5 Migration time taken: 2.385seconds
Device Variant: TMS320C28XX.TMS320F28	6 Warning - CPU Macros ERTM: CPU Macro, ERTM is not available on F29 Devices[Ln 119, Col 5]
Current Device: F2838x	7 Harning - function ADC forcebultipleSOC: Function is compatible but scoMesk argument changed from unit15_t to unit32_t since f29 device supports 32 SOCs[In 129, Col 9] 8 Harning - function ADC forcebultipleSOC: Function is compatible but scoMesk argument changed from unit16_t to unit32_t since f29 devices supports 32 SOCs[In 139, Col 9]
→ Migration Devices:	
✓ ➡ gpio_ex2_toggle	
Device Variant: TMS320C28XX.TMS320F28	
G Current Device: F2837xD	
∠ Migration Devices:	
· · · · · · · · · · · · · · · · · · ·	

Figure 4-8. Migration Report

4.3.1.2 Project-Based Migration Check

Follow these steps to enable Driverlib Migration on a detected project:

- 1. Run project detection (see instructions in Section 2.3).
- 2. Set up the C2000 device to migrate from/to as well as customize which folders/files are to be ignored for the migration check by either:
 - a. Click the following icon next to *Migration Devices* under the detected project in the **C2000 IDEA Projects** pane of the Extension tree.
 - b. To set up the migration settings for the project, either:
 - i. Press CTRL+SHIFT+P, type and select C2000: Set Up Migration Settings.

ii. Click on *Migration Support* > Set Up Migration Settings in the C2000 IDEA - Features pane of the Extension tree.

C2000 IDE ASSIST ····	Coadc_ext_soc_software.c 3 Migration Setup - undefined X	🗖 🖓 🕬
> C2000 COLLATERAL - F28P65X	ade evil soc software	
Cook op Columbia Francess Cook op Columbia Resolutions Cook op Columbia Resolution Cook Support Interrupt Cook Support	sdc_ex1_soc_software Setup or current dence • 72H455 • 720055 • 720058 • 720058 • 720058 • 720058 • 720058 • 720058 • 720058 • 720058 • 720058 • 720058 • 720058 • 720058 • 720058 • 720058 • 720058 • 720058 • 720058 • 720058 • 720059 <td></td>	

Figure 4-9. Migration Settings Setup

- 3. To run the migration check, either:
 - a. Click the following icon next to *Migration Devices* under the detected project in the **C2000 IDEA Projects** pane of the Extension tree.
 - b. Press CTRL+SHIFT+P, type and select C2000: Run Migration Check on Project.
 - c. Click on *Migration Support > Run Migration Check on Project* in the **C2000 IDEA Features** pane of the Extension tree.

Note

An error is thrown by the extension if there is any overlap of folders or files in the migration folder and files to be ignored information in the migration setup page.

Note

Do not use these migration features while the tool is already running a migration check on the project. Wait for the *Migration check completed on [project name]* at the bottom right of the screen before enabling other migration features. The amount of time taken to run the check entirely depends on how many files, lines and code changes exist in a project. The migration report includes the time taken on each file.

- 4. Select the project in the workspace to run a migration check on.
- 5. The status bar at the bottom right of the screen displays *Migration check completed on [project name]* when finished. All major migration concerns in the file are underlined with a red squiggly line. All other migration warnings in the file are underlined with a yellow squiggly line.
- 6. Review and resolve concerns throughout the file. The following options are provided when the underlined code is hovered over:
 - a. Select View Problem to quickly loop through the detected concerns in the file .
 - b. Select Quick Fix to mitigate the migration concern. Select one of the below options:
 - i. *Review migration collateral for [current device] to [migration device]-* This option opens a link to the online migration collateral for the specific migration path using the latest version of C2000WARE.
 - ii. Wrap in device specific #IFDEF for [current device] and [migration device] This option autogenerates pre-processor wrappers around the line of code so that an updated version of the code can be compiled for the new device. Fill in the line with the *//Enter alternate code* comment with the modified code and add a #define for the current device somewhere in the file.
 - iii. Ignore code related errors This option ignores this migration concern.
- 7. The migration report contains a list of the detected migration concerns, the time taken to complete the migration, and any settings the user customized to ignore. To export the migration report to the desired file path, either:



- a. Click the following icon next to *Migration Devices* under the detected project in the **C2000 IDEA Projects** pane of the Extension tree.
- b. Enter CTRL+SHIFT+P, type and select *C2000: Export Migration Report*.
- c. Click on *Migration Support* > *Export Migration Report* in the **C2000 IDEA Features** pane of the Extension tree.

4.3.2 Bitfield Migration

The C2000 IDEA Extension can be used to run a F28x-to-F28x or F28x-to-F29x migration check on a file written with bitfield-style code. This code style is characterized by using calls to functions defined in the bitfield source files (ex: InitSysCtrl()) and/or register accesses containing the [base name].[register name].all or [base name].[register name].bit.[field name] syntax.

Follow these steps to enable Bitfield Migration:

- 1. Open a C2000 application C-Code file.
- 2. Run the migration check by pressing CTRL+SHIFT+P, typing and selecting C2000: Run Bitfield Migration Check on File
- 3. Select the current C2000 device the code in the file applies to.
- 4. Select the C2000 device to migrate the file to.
- 5. The status bar at the bottom right of the screen displays *Finished Bitfield Migration from [current device]* to *[migration device]* when finished. All migration concerns in the file are underlined with a red squiggly line.
- 6. Review and resolve concerns throughout the file. The following options are provided when the underlined code is hovered over:
 - a. Select View Problem to quickly loop through the detected concerns in the file.
 - b. Select Quick Fix to mitigate the migration concern. Select one of the below options:
 - i. *Review migration collateral for [current device] to [migration device]-* This option opens a link to the online migration collateral for the specific migration path using the latest version of C2000WARE.
 - ii. Wrap in device specific #IFDEF for [current device] and [migration device] This option autogenerates pre-processor wrappers around the line of code so that an updated version of the code can be compiled for the new device. Fill in the line with the *//Enter alternate code* comment with the modified code and add a #define for the current device somewhere in the file.
 - iii. Ignore code related errors This option ignores this migration concern.

4.4 Command List

Refer to the following table for the complete list of commands that can be utilized within the C2000 IDE Assist Tool.

		-101	
Command Name	Supported Devices	Supported Software	Project Detection OR Default Device Setup
C2000: Help Walkthrough	N/A	N/A	
C2000: Help Interrupt Coder Walkthrough	N/A	N/A	
C2000: Help Register Coder Walkthrough	N/A	N/A	
C2000: Help Register Vision Walkthrough	N/A	N/A	
C2000: Open C2000-IDEA Extension View	N/A	N/A	
C2000: Manually Set Up Project's Current Device	GEN3, GEN4	N/A	REQUIRED
C2000: Set Up Migration Settings	GEN3, GEN4	N/A	REQUIRED
C2000: Get Projects	GEN3, GEN4	Bitfield + Driverlib	
C2000: Clear All Projects and Reset	GEN3, GEN4	Bitfield + Driverlib	
C2000: Update Project Devices	GEN3, GEN4	Bitfield + Driverlib	

Table 4-1. Command List

Vision on Current FileGEN3, GEN4, F2803xBitfieldRECOMMENC2000: Run Bitfield Register Vision on Current FileGEN3, GEN4, F2803xBitfieldRECOMMENC2000: Run Bitfield Register to Driverlib Migration on Current File (BETA)GEN3, GEN4, F2803xBitfieldRECOMMENC2000: Clear All Register InfoGEN3, GEN4, F2803xDriverlib + BitfieldRECOMMENC2000: Enable Register Code Write/ReadGEN3, GEN4, F2803xDriverlib + BitfieldREQUIREC2000: Disable Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Disable Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Enable Interrupt Code TemplatesGEN3, GEN4DriverlibREQUIRE	Command Name	Supported Devices	Supported Software	Project Detection OR Default Device Setup
To: GEN3, GEN4C2000: Run Migration Check on FileFrom: GEN3, GEN4DriverlibC2000: Enable Continuous Migration Check on Current FileFrom: GEN3, GEN4DriverlibC2000: Clear All Migration ResultsFrom: GEN3, GEN4DriverlibC2000: Clear All Migration ResultsFrom: GEN3, GEN4Bitfield + DriverlibC2000: Clear All Migration Check on ProjectFrom: GEN3, GEN4Bitfield + DriverlibC2000: Clear All Migration Check on ProjectFrom: GEN3, GEN4Bitfield + DriverlibC2000: Run Bitfield Migration Check on FileFrom: GEN3, GEN4DriverlibC2000: Run Bitfield Migration Check on FileFrom: GEN3, GEN4BitfieldC2000: Run Bitfield Migration 			N/A	
FileTo: GEN3, GEN4C2000: Enable Continuous Migration Check on Current FileFrom: GEN3, GEN4DriverlibC2000: Disable Continuous Migration Check on Current FileFrom: GEN3, GEN4DriverlibC2000: Clear All Migration ResultsFrom: GEN3, GEN4Bitfield + DriverlibC2000: Clear All Migration Check on ProjectFrom: GEN3, GEN4Bitfield + DriverlibC2000: Run Migration Check on ProjectFrom: GEN3, GEN4DriverlibC2000: Run Migration Check on ProjectFrom: GEN3, GEN4DriverlibC2000: Run Bitfield Migration Check on FileFrom: GEN3, GEN4BitfieldC2000: Run Diffield Migration Check on FileFrom: GEN3, GEN4BitfieldC2000: Run Diffield Register Vision on Current FileGEN3, GEN4DriverlibC2000: Run Bitfield Register to Driverlib Register Code Write/ReadGEN3, GEN4, F2803xDriverlibC2000: Clear All Register InfoGEN3, GEN4, F2803xDriverlibRECOMMENC2000: Clear All Register to Driverlib Register Code Write/ReadGEN3, GEN4DriverlibRECOMMENC2000: Clear All Register InfoGEN3, GEN4, F2803xDriverlibRECOMMENC2000: Clear All Register Code Write/ReadGEN3, GEN4DriverlibRECOMMENC2000: Disable Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Disable Register Code Write/Read <t< td=""><td>C2000: Export Migration Report</td><td>-</td><td>Driverlib</td><td></td></t<>	C2000: Export Migration Report	-	Driverlib	
Migration Check on Current FileTo: GEN3, GEN4C2000: Disable Continuous Migration Check on Current FileFrom: GEN3, GEN4DriverlibC2000: Clear All Migration ResultsFrom: GEN3, GEN4Bitfield + DriverlibC2000: Clear All Migration ResultsFrom: GEN3, GEN4 & To: GEN3, GEN4Bitfield + DriverlibC2000: Run Migration Check on ProjectFrom: GEN3, GEN4 & To: F280013xDriverlibC2000: Run Bitfield Migration Check on FileFrom: GEN3, GEN4 To: GEN3, GEN4BitfieldC2000: Run Bitfield Migration Check on FileFrom: GEN3, GEN4 & & To: GEN3, GEN4BitfieldC2000: Run Driverlib Register Vision on Current FileGEN3, GEN4 & & C2000: Run Bitfield Register to ID C2000: Run Bitfield Register to DriverlibGEN3, GEN4, F2803x GEN3, GEN4Bitfield RECOMMENC2000: Run Bitfield Register to Driverlib Migration on Current FileGEN3, GEN4, F2803x GEN3, GEN4DriverlibRECOMMENC2000: Run Bitfield Register to Driverlib Migration on Current FileGEN3, GEN4, F2803x GEN3, GEN4Driverlib + BitfieldRECOMMENC2000: Clear All Register Info Write/ReadGEN3, GEN4, F2803x GEN3, GEN4DriverlibREQUIREC2000: Clear All Register Info Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Clear All Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Clear All Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Enable Register Code Write/ReadGEN3, GEN4DriverlibREQUIRE<			Driverlib	
Migration Check on Current File To: GEN3, GEN4 C2000: Clear All Migration Results From: GEN3, GEN4 To: CEN3, GEN4 & & From: F2803x To: F280013x Bitfield + Driverlib C2000: Run Migration Check on Project From: GEN3, GEN4 To: CEN3, GEN4 Driverlib C2000: Run Bitfield Migration Check on File From: GEN3, GEN4 To: GEN3, GEN4 Bitfield C2000: Run Driverlib Register Vision on Current File GEN3, GEN4 GEN3, GEN4 Bitfield C2000: Run Bitfield Register Vision on Current File GEN3, GEN4, F2803x Bitfield C2000: Run Bitfield Register to Driverlib Migration on Current File (BETA) GEN3, GEN4, F2803x Bitfield C2000: Run Bitfield Register Info GEN3, GEN4, F2803x Bitfield RECOMMEN C2000: Clear All Register Info GEN3, GEN4, F2803x Driverlib + Bitfield RECOMMEN C2000: Clear All Register Code Write/Read GEN3, GEN4 Driverlib + Bitfield REQUIRE C2000: Disable Register Code Write/Read GEN3, GEN4 Driverlib REQUIRE C2000: Disable Register Code Write/Read GEN3, GEN4 Driverlib REQUIRE C2000: Disable Register Code Write/Read GEN3, GEN4 Driverlib REQUIRE			Driverlib	
To: GEN3, GEN4 & From: F2803x To: F280013xTo: GEN3, GEN4C2000: Run Migration Check on ProjectFrom: GEN3, GEN4DriverlibC2000: Run Bitfield Migration Check on FileFrom: GEN3, GEN4BitfieldC2000: Run Driverlib Register Vision on Current FileGEN3, GEN4BitfieldC2000: Run Bitfield Register Vision on Current FileGEN3, GEN4DriverlibC2000: Run Bitfield Register Vision on Current FileGEN3, GEN4, F2803xBitfieldC2000: Run Bitfield Register InfoGEN3, GEN4, F2803xBitfieldC2000: Can Bitfield Register Code Write/ReadGEN3, GEN4, F2803xDriverlibC2000: Enable Register InfoGEN3, GEN4, F2803xDriverlibREQUIREC2000: Disable Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Enable Interrupt Code TemplatesGEN3, GEN4DriverlibREQUIREC2000: Disable Interrupt Code TemplatesGEN3, GEN4DriverlibREQUIREC2000: Disable Interrupt Code TemplatesGEN3, GEN4DriverlibREQUIREC2000: Disable Interrupt Code TemplatesGEN3, GEN4DriverlibREQUIRE		-	Driverlib	
ProjectTo: GEN3, GEN4C2000: Run Bitfield Migration Check on FileFrom: GEN3, GEN4 To: GEN3, GEN4 & 	C2000: Clear All Migration Results	To: GEN3, GEN4 & From: F2803x	Bitfield + Driverlib	
Check on FileTo: GEN3, GEN4 & From: F2803x To: F280013xRecommend Recommend Recommend Recommend 		-	Driverlib	
Vision on Current FileCenter SectionSection on Current FileRecommendationC2000: Run Bitfield Register Vision on Current FileGEN3, GEN4, F2803xBitfieldRecommendationC2000: Run Bitfield Register to Driverlib Migration on Current File (BETA)GEN3, GEN4, F2803xBitfieldRecommendationC2000: Clear All Register InfoGEN3, GEN4, F2803xDriverlib + BitfieldRecommendationC2000: Clear All Register Code Write/ReadGEN3, GEN4, F2803xDriverlib + BitfieldREQUIREC2000: Disable Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Disable Interrupt Code TemplatesGEN3, GEN4DriverlibREQUIRE	S S	To: GEN3, GEN4 & From: F2803x	Bitfield	
on Current FileGEN3, GEN4, F2803xBitfieldRECOMMENC2000: Run Bitfield Register to Driverlib Migration on Current File (BETA)GEN3, GEN4, F2803xBitfieldRECOMMENC2000: Clear All Register InfoGEN3, GEN4, F2803xDriverlib + BitfieldGEN3GEN3C2000: Enable Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Disable Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Disable Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Disable Interrupt Code TemplatesGEN3, GEN4DriverlibREQUIREC2000: Disable Interrupt Code 	S S	GEN3, GEN4	Driverlib	RECOMMENDED
Driverlib Migration on Current File (BETA)C2000: Clear All Register InfoGEN3, GEN4, F2803xDriverlib + BitfieldC2000: Enable Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Disable Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Disable Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Disable Interrupt Code TemplatesGEN3, GEN4DriverlibREQUIREC2000: Disable Interrupt Code TemplatesGEN3, GEN4DriverlibREQUIRE		GEN3, GEN4, F2803x	Bitfield	RECOMMENDED
C2000: Enable Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Disable Register Code Write/ReadGEN3, GEN4DriverlibREQUIREC2000: Enable Interrupt Code TemplatesGEN3, GEN4DriverlibREQUIREC2000: Disable Interrupt Code TemplatesGEN3, GEN4DriverlibREQUIRE	Driverlib Migration on Current File	GEN3, GEN4, F2803x	Bitfield	RECOMMENDED
Write/Read C2000: Disable Register Code GEN3, GEN4 Driverlib REQUIRE C2000: Enable Interrupt Code GEN3, GEN4 Driverlib REQUIRE C2000: Disable Interrupt Code GEN3, GEN4 Driverlib REQUIRE C2000: Disable Interrupt Code GEN3, GEN4 Driverlib REQUIRE C2000: Disable Interrupt Code GEN3, GEN4 Driverlib REQUIRE	C2000: Clear All Register Info	GEN3, GEN4, F2803x	Driverlib + Bitfield	
Write/Read C2000: Enable Interrupt Code Templates GEN3, GEN4 Driverlib REQUIRE C2000: Disable Interrupt Code Templates GEN3, GEN4 Driverlib REQUIRE		GEN3, GEN4	Driverlib	REQUIRED
Templates C2000: Disable Interrupt Code Templates GEN3, GEN4 Driverlib REQUIRE	e e	GEN3, GEN4	Driverlib	REQUIRED
Templates		GEN3, GEN4	Driverlib	REQUIRED
C2000: Project Tree View Refresh GEN3, GEN4 Bitfield + Driverlib		GEN3, GEN4	Driverlib	REQUIRED
	C2000: Project Tree View Refresh	GEN3, GEN4	Bitfield + Driverlib	

Table 4-1. Command List (continued)



5 Summary

The Texas Instruments C2000 SDKs integrate essential tools, including drivers and libraries, to help developers of all levels build real-time control applications. This includes the C2000 SysConfig which simplifies code initialization and peripheral configuration. The C2000 IDEA tool builds on these foundational capabilities to centralize the development environment for easier coding and debugging. The C2000 IDE Assist Extension facilitates migration across device portfolios and from legacy code to new, helping users navigate unique C2000 features and speeding up software implementation with live assistance at every stage of development.



6 References

Tools:

- Texas Instruments, C2000 IDEA Open VSX (VSIX Download)
- Texas Instruments, C2000 IDEA GitHub Repository (VSIX Download)
- Texas Instruments, Code Composer Studio (CCS) IDE
- Texas Instruments, C2000WARE (F28x SDK)
- Texas Instruments, F29X-SDK (F29x SDK)

Documentation:

- Texas Instruments, C28x Academy Migration Resources
- Texas Instruments, C29x Academy Migration Resources
- Texas Instruments, F28x to F29x Software Migration Guide
- Texas Instruments, Application Software Migration to the C29 CPU Application Note
- Texas Instruments, TMS320F2837x, TMS320F2838x, TMS320F28P65x Migration to TMS320F29H85x User's Guide
- Texas Instruments, C2000 Design & Development

7 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision * (January 2025) to Revision A (January 2025)		Page
•	Added CCS Extension Installation section	3
•	Added VS Code Extension Installation section	4
•	Added Internal Browser section	<mark>6</mark>
•	Updated Command List table title	16

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