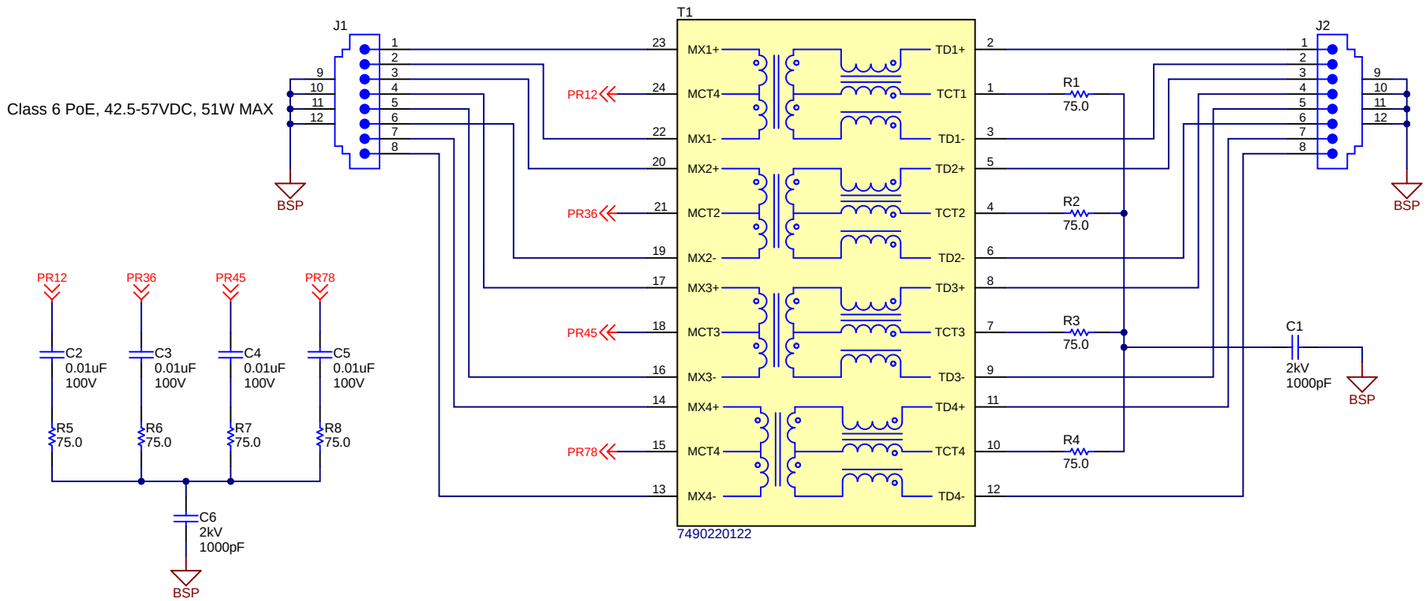

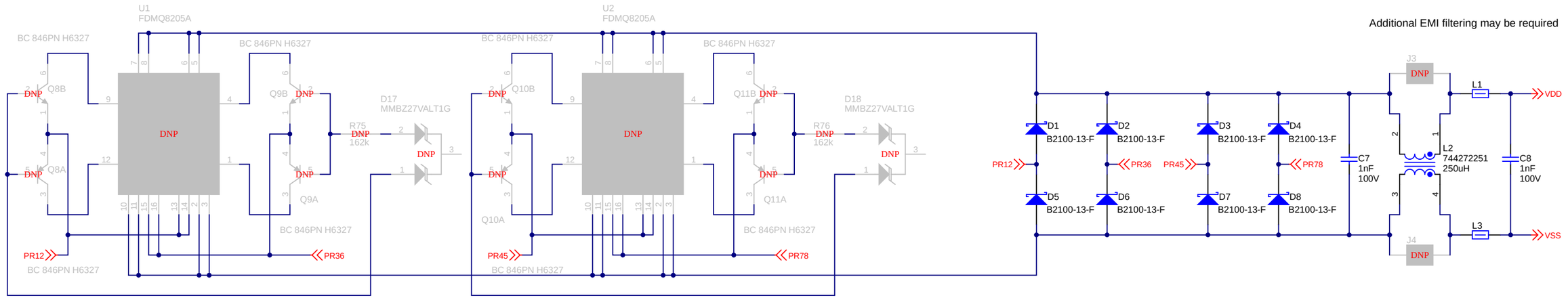

OPTIONAL INTEGRATED FET BRIDGE FOR HIGHER EFFICIENCY AND SMALLER SIZE

Logo1 PCB LOGO WEEE logo

Logo2 PCB LOGO FCC disclaimer

BUILD ON PMP22477 REV C PCB

REV D CHANGES:

ADD ALTERNATE PARTS FOR T2

CHANGE VALUE OF R18 TO 499 OHMS

Orderable: NO	Designed for: Public Release	Mod. Date: 3/5/2024
TID #: N/A	Project Title: Class 6 PoE PD with Power Limit	
Number: PMP22477 Rev: D	Sheet Title: PoE Input	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 3
Drawn By: D Strasser	File: PMP22477_RevD_SH1.SchDoc	Size: B
Engineer: D Strasser	Contact: N/A	

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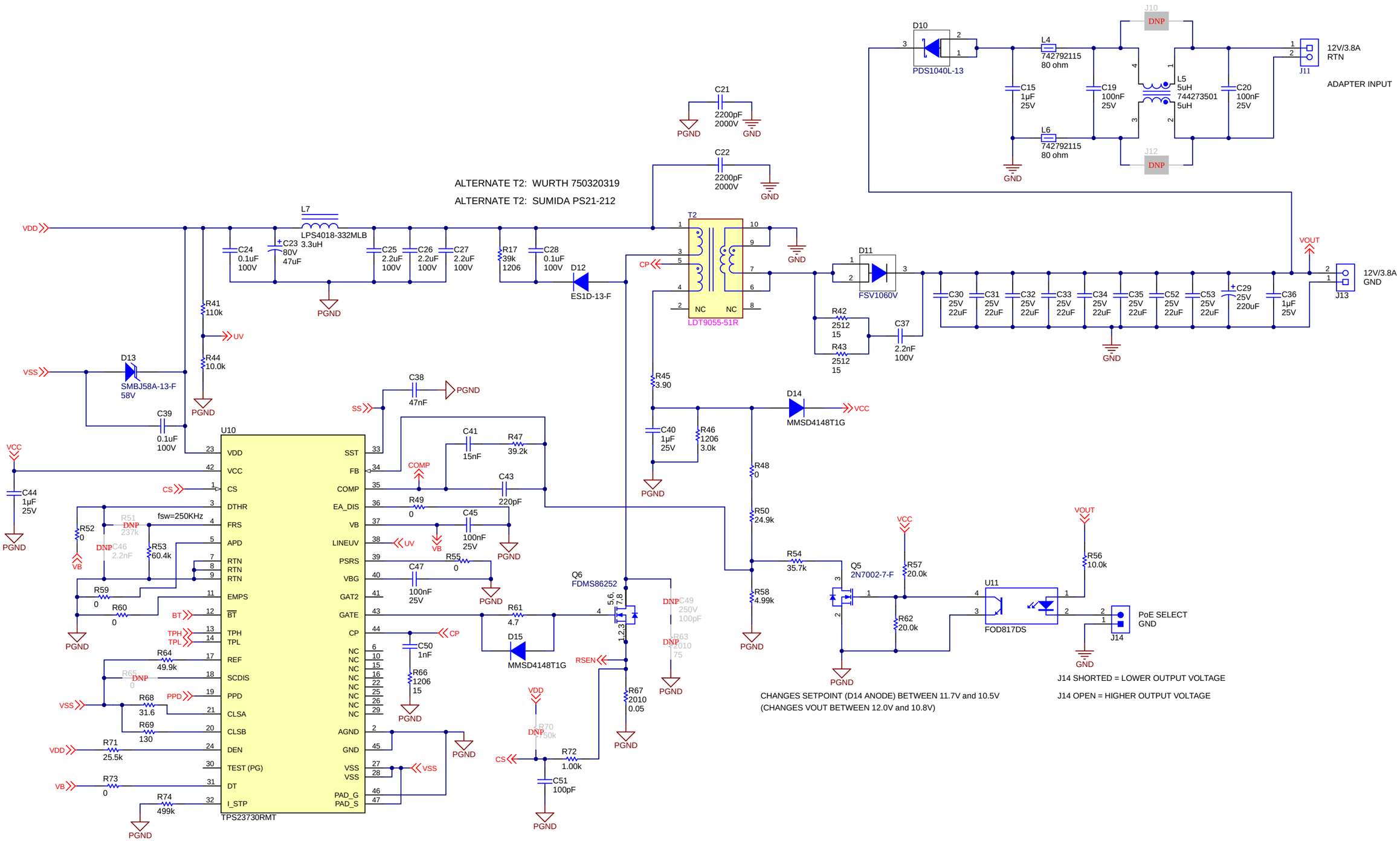

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Orderable: NO		Designed for: Public Release	Mod. Date: 3/5/2024
TID #: N/A		Project Title: Class 6 PoE PD with Power Limit	
Number: PMP22477	Rev: D	Sheet Title: Power Limit Circuit	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 2 of 3
Drawn By: D Strasser		File: PMP22477_RevD_SH2.SchDoc	Size: B
Engineer: D Strasser		Contact: N/A	

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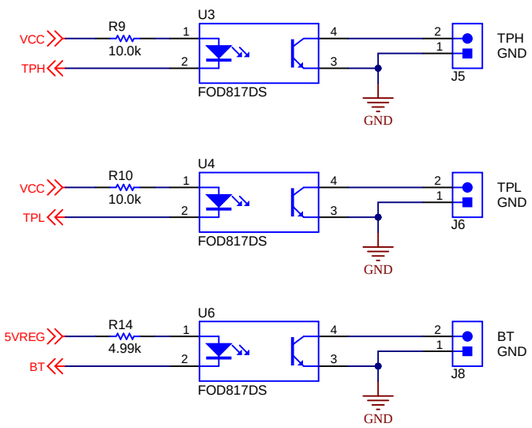
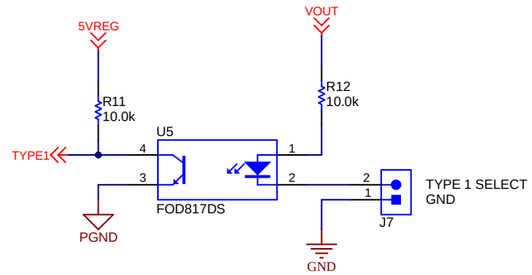
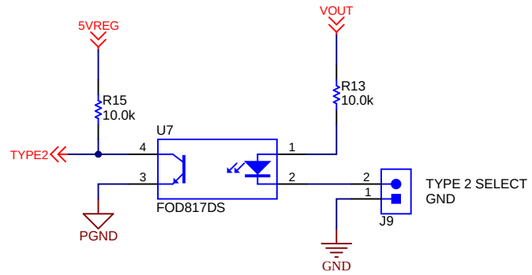


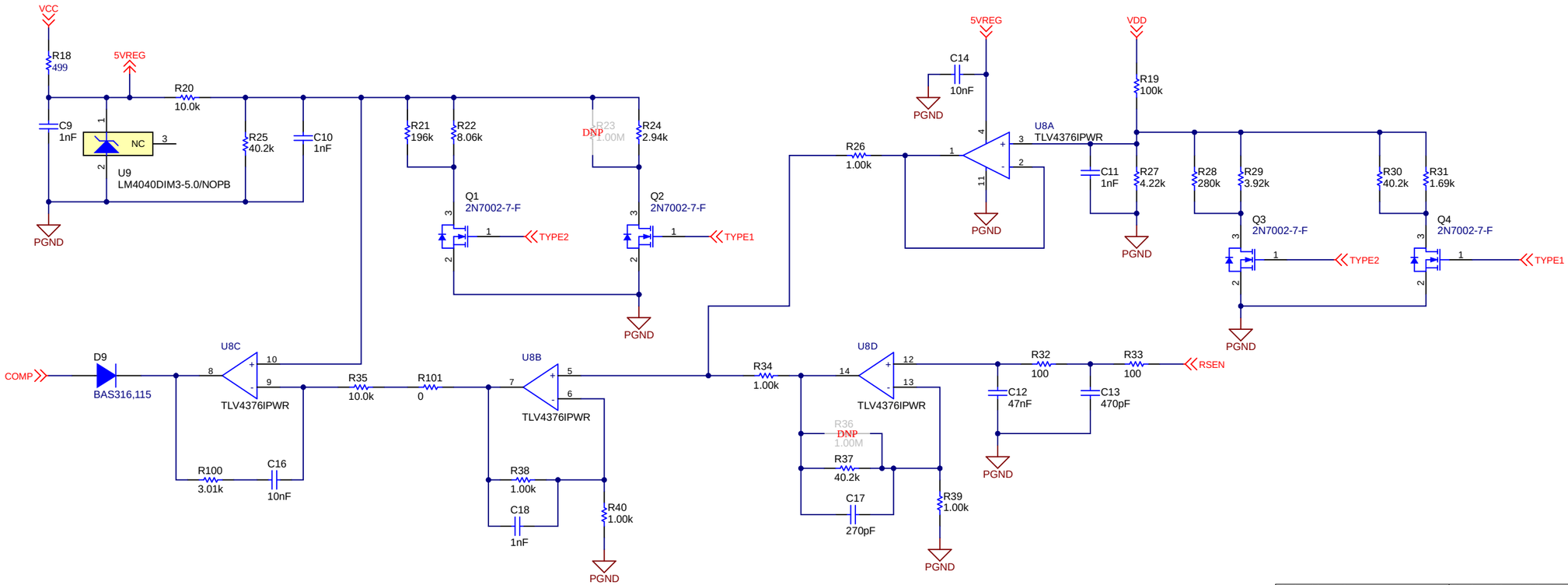


J7 AND J9 SHORTED: POWER LIMIT IS TYPE 3

J7 SHORTED AND J9 OPEN: POWER LIMIT IS TYPE 2

J7 OPEN AND 9 SHORTED: POWER LIMIT IS TYPE 1





Orderable: NO		Designed for: Public Release	Mod. Date: 3/5/2024
TID #: N/A		Project Title: Class 6 PoE PD with Power Limit	
Number: PMP22477	Rev: D	Sheet Title: Power Limit Circuit	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 3 of 3
Drawn By: D Strasser		File: PMP22477_RevD_SH3.SchDoc	Size: B
Engineer: D Strasser		Contact: N/A	

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