5 Schematic, Physical Layouts and Bill of Materials

5.1 Schematic

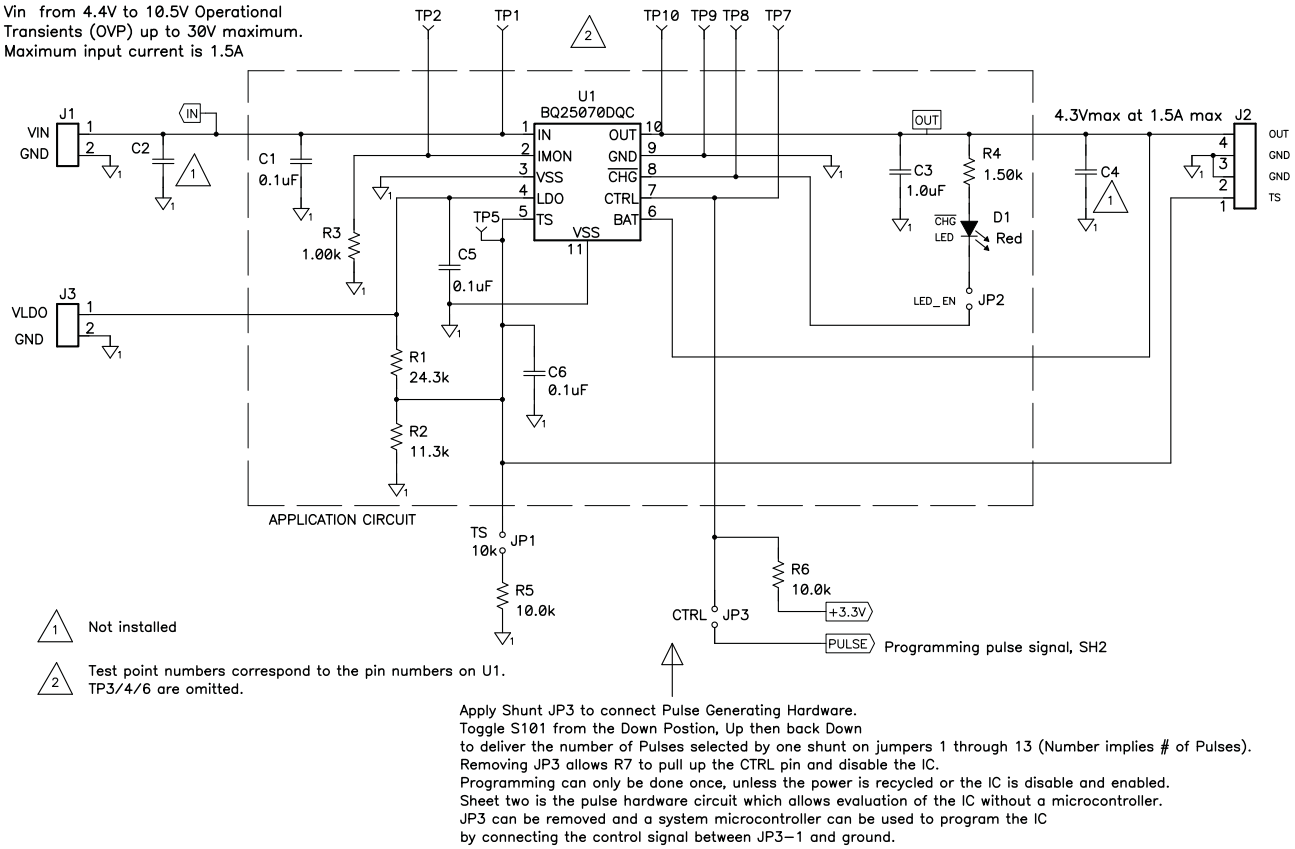
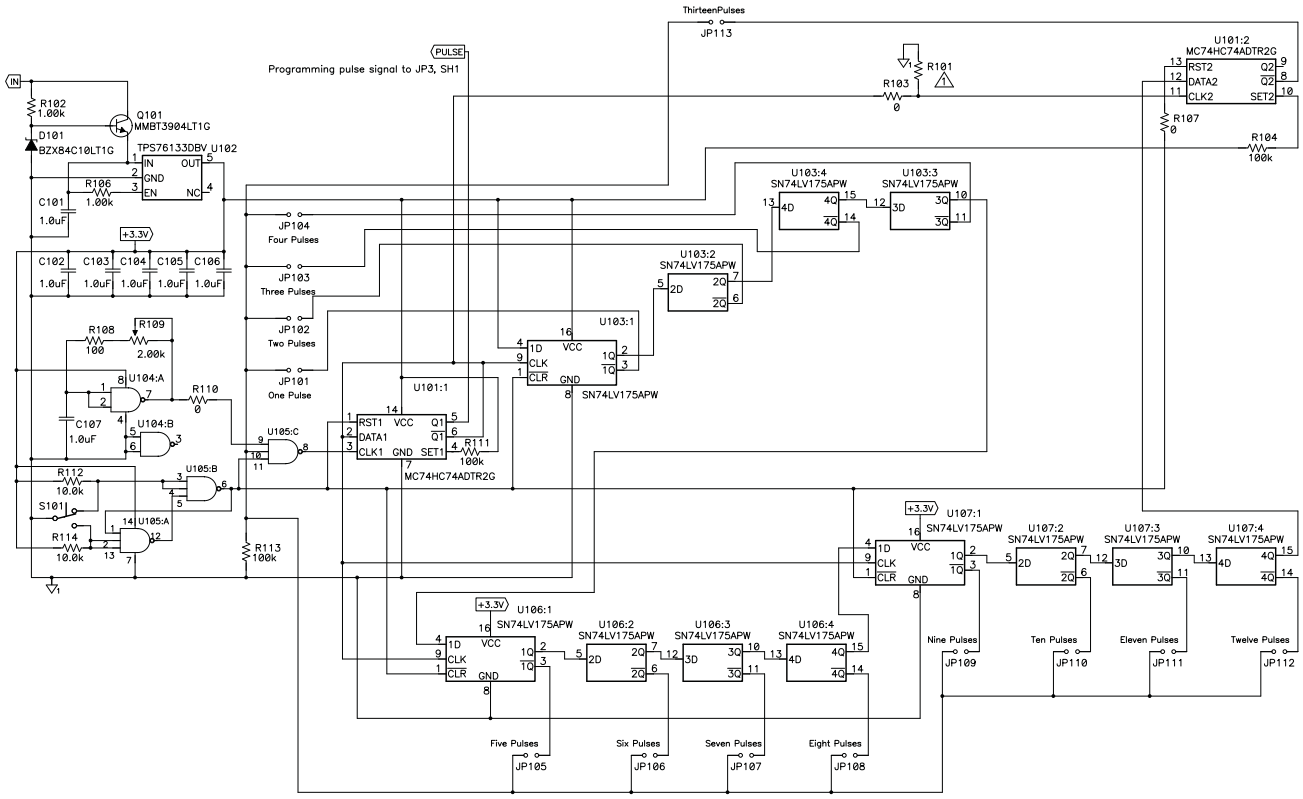


Figure 4. bq25070 EVM Board Schematic (Sheet 1 of 2)



The circuit on this sheet is typically not part of the charger design.

This circuit generates the pulses to program the charge, which is normally done by the host. Use only one Shunt on JP101 through JP113. Place shunt according to desired program pluses. The 100 numbered components are part of the hardware for creating the programming pulses and are not typically part of an application.

Figure 5. bq25070 EVM Board Schematic (Sheet 2 of 2)

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