

Layer	Name	Material	Thickness	Constant	Board Layer Stack	Board Layer Stack
1	Top Overlay					
2	Top Solder	Solder Resist	0.40mil	3.5		
3	Layer 1 - Top Layer	Copper	1.40mil			
4	Dielectric 1	370HR	9.00mil	4.2		
5	Layer 2 - GND	Copper	1.42mil			
6	Dielectric 2	370HR	9.00mil	4.2		
7	Layer 3 - PWR	Copper	1.42mil			
8	Dielectric 3	370HR	9.00mil	4.2		
9	Layer 4 - Bottom Layer	Copper	1.40mil			
10	Bottom Solder	Solder Resist	0.40mil	3.5		
11	Bottom Overlay					

Impedance Control:

- Top layer contains 50 ohm impedance (+/- 10%) single ended using 15 mil traces (from E1 to U7)

DESIGN INFORMATION

MIN. TRACK WIDTH: 8 MIL
 MIN. CLEARANCE: 0.2 mm
 MIN. VIA PAD SIZE: 20 MIL
 MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL
 PER IPC-D-275 CLASS 2 LEVEL C
 REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL

MATERIAL:
 FR-408 FR-4 High Tg OTHER _____
 THICKNESS: 62 MIL (1.6mm) +/-10% OTHER 33 MIL
 TOLERANCE: ANSI IPC-6012 TYPE 3 CLASS 2
 OTHER +/- _____
 BOW & TWIST: ANSI IPC-6012 TYPE 3 CLASS 2
 OTHER +/- _____

DRILLING:
 REFERENCE: AS SHOWN NC_DRILL FILES
 PTH MIN COPPER THICKNESS: 1MIL OTHER _____

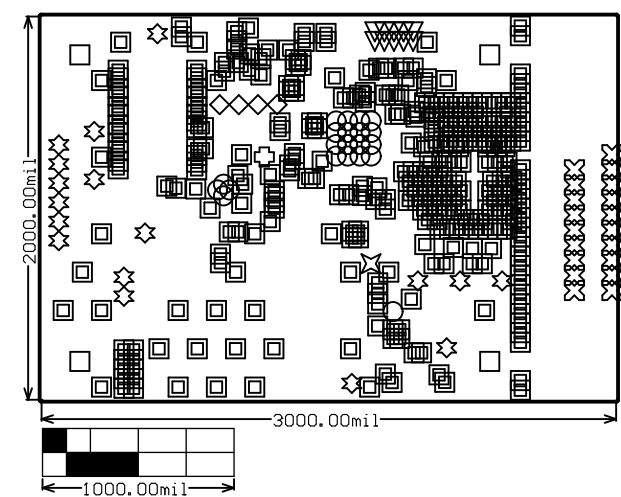
BOARD FINISH:
 SILKSCREEN: TOP BOTTOM
 SILKSCREEN COLOR: WHITE OTHER _____
 SOLDER RESIST COLOR:
 GREEN BLUE OTHER _____

SURFACE FINISH: IMMERSION GOLD (ENIG) ENEPIG
 IMM. TIN/SILVER OR EQUIV OTHER _____

ARRAY/PANEL: CUT AND TRIM PER MECH LAYER 1
 N.C. ROUTE V. SCORE

CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:
 ANSI IPC-A-600F CLASS -> 1 2 3
 UL 94V-0 RoHS OTHER PER ORDER

ADDITIONAL REQUIREMENTS:
 MICROSECTION: YES
 BARE BOARD ELEC. TEST: NONE REQUIRED PER ORDER
 MANUFACTURER'S UL: RAIL METAL SILK



Symbol	Quantity	Finished Hole Size	Plated	Hole Type
○	21	7.87mil (0.200mm)	PTH	Round
■	323	10.00mil (0.254mm)	PTH	Round
⊗	19	14.96mil (0.380mm)	PTH	Round
▽	10	27.56mil (0.700mm)	PTH	Round
◇	4	39.37mil (1.000mm)	PTH	Round
⊛	17	40.00mil (1.016mm)	PTH	Round
⊙	1	46.50mil (1.181mm)	PTH	Round
⊗	1	59.06mil (1.500mm)	PTH	Round
□	4	125.98mil (3.200mm)	PTH	Round
	400 Total			

ALL ARTWORK VIEWED FROM TOP SIDE
 BOARD #: ISE4045
 REV: E2
 SUN REV: Not In VersionControl
 LAYER NAME = 060856-ALS-01-01-01
 TID #: TIDA-00758
 PLOT NAME = Fabrication Drawing
 GENERATED : 5/12/2016 12:57:49 PM
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TEXAS INSTRUMENTS

PROJECT TITLE:
TIDA-00758 Interrupt-Based ALS and Environment Sensor

DESIGNED FOR:
Public Release

FILE NAME:
ISE4045_TIDA-00758_TID.PcbDoc

ENGINEER:
Adam Yager

LAYOUT BY:
Adam Yager

SCALE: 1.00

ALTIUM DESIGNER VERSION:
14.3.17.42447

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