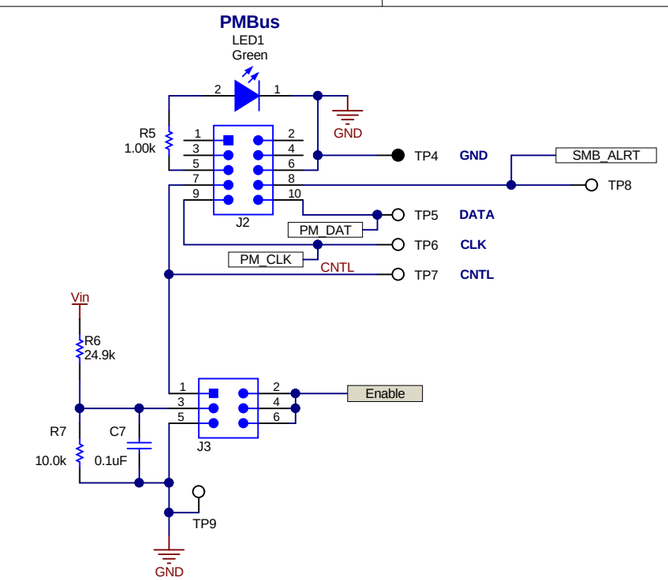
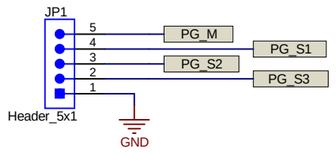
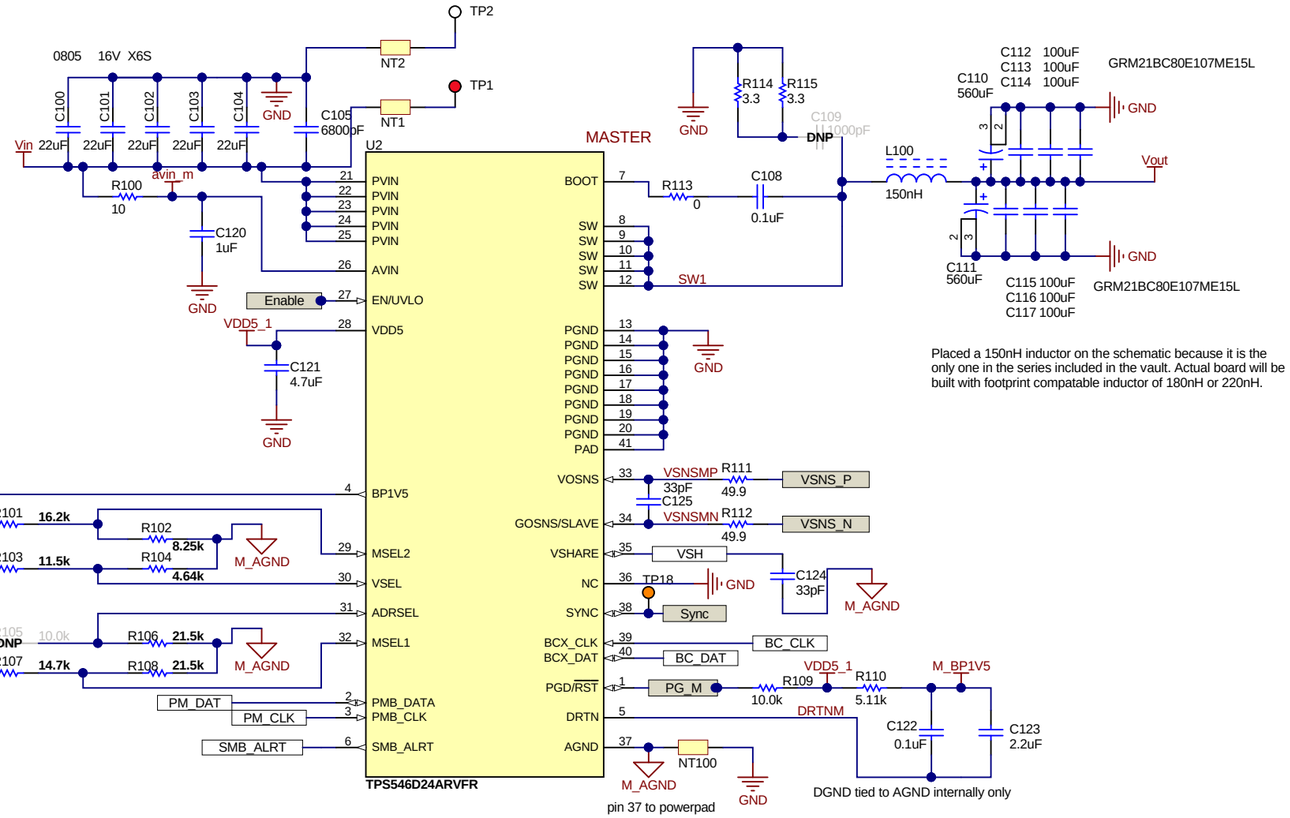


VIN_ON set to 3.84V. VIN_OFF set to 3.0V.





Placed a 150nH inductor on the schematic because it is the only one in the series included in the vault. Actual board will be built with footprint compatable inductor of 180nH or 220nH.

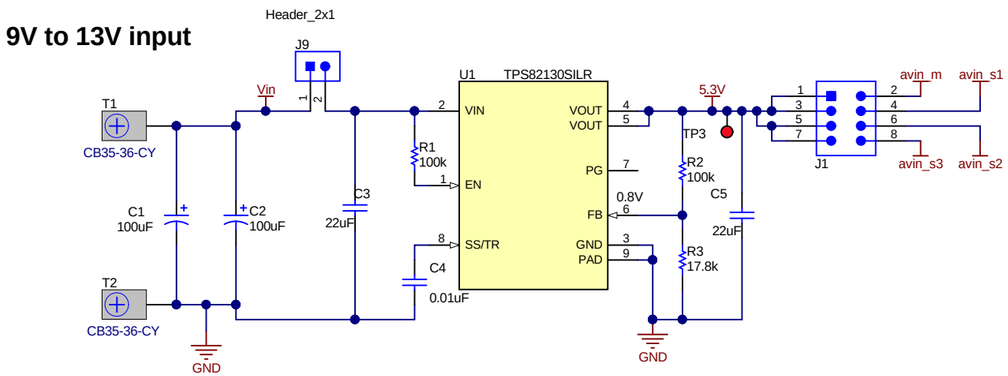
3 options for powering bias:

1) From main Power VIN: populate R100, R200, R300, R400 OMIT J9 & J1

2) Use U1 regulator off main Power VIN: Populate J9 & J1, OMIT R100, R200, R300, R400

3) Separate AVIN: Use TP3 and a ground for this aux power
Populate J1, OMIT J9, R100, R200, R300, R400

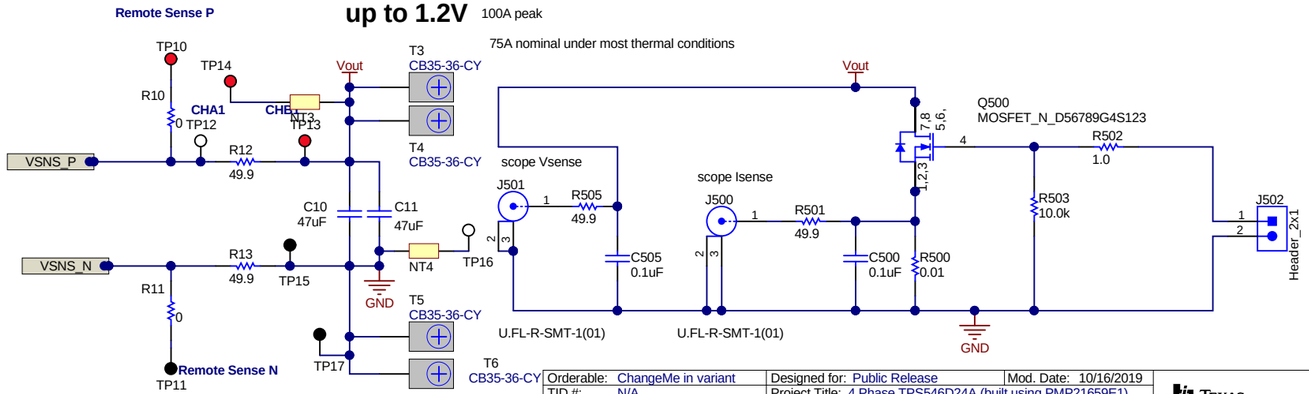
9V to 13V input



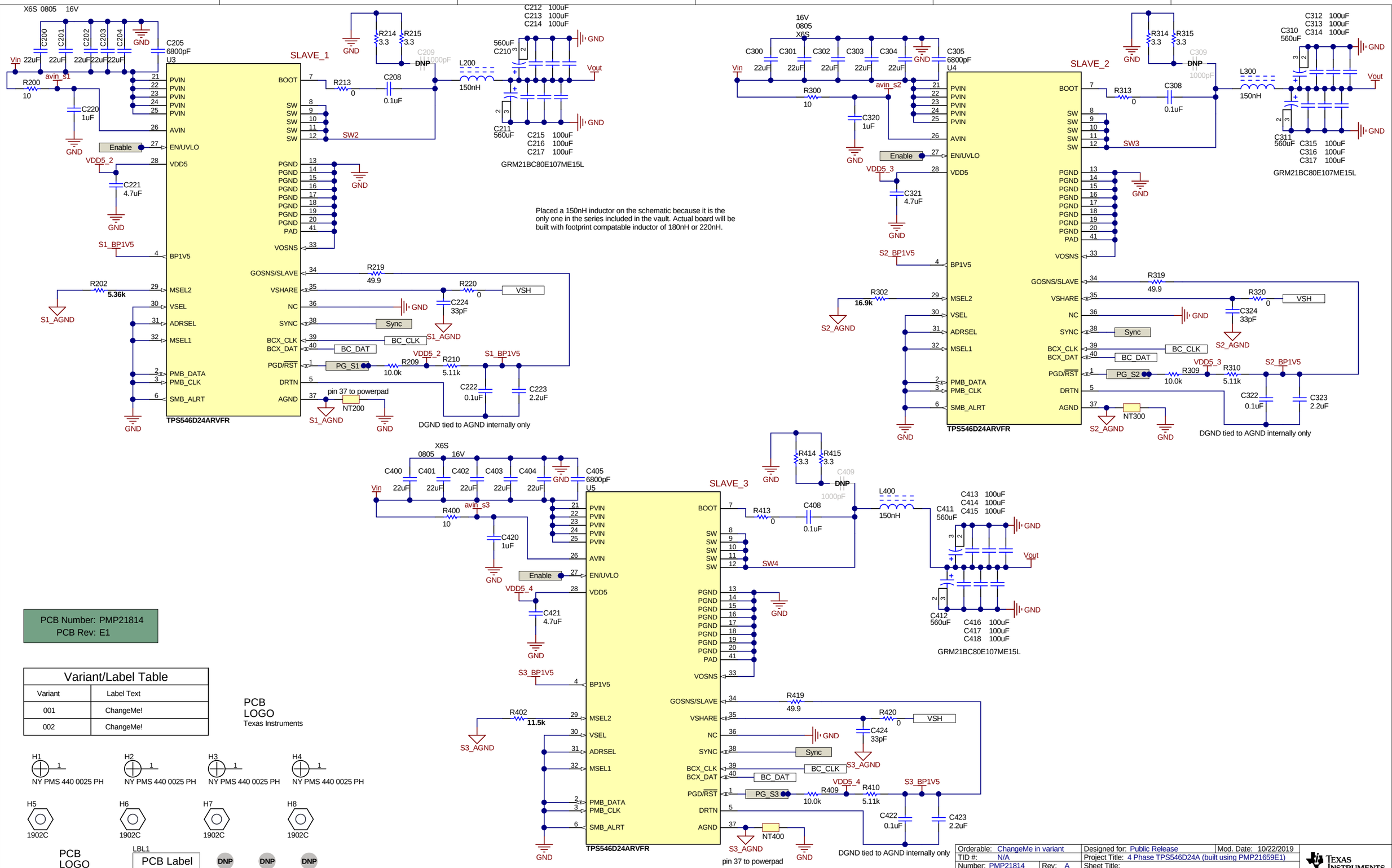
up to 1.2V

100A peak

75A nominal under most thermal conditions



Orderable: ChangeMe in variant		Designed for: Public Release	Mod. Date: 10/16/2019
TID #: N/A		Project Title: 4 Phase TPS546D24A (built using PMP21659E1)	
Number: PMP21814	Rev: A	Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 4Phase_Rev	Sheet: 2 of 3
Drawn By:		File: PMP21814_MasterAux_1.SchDoc	Size: B
Engineer: Pradeep Shenoy		Contact: http://www.ti.com/support	



PCB Number: PMP21814
PCB Rev: E1

Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

PCB LOGO
Texas Instruments

H1 NY PMS 440 0025 PH
H2 NY PMS 440 0025 PH
H3 NY PMS 440 0025 PH
H4 NY PMS 440 0025 PH

H5 1902C
H6 1902C
H7 1902C
H8 1902C

PCB LOGO
FCC disclaimer

LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

DNP FID1
DNP FID2
DNP FID3

Orderable: ChangeMe in variant		Designed for: Public Release	Mod. Date: 10/22/2019
TID #: N/A		Project Title: 4 Phase TPS546D24A (built using PMP21659E1)	
Number: PMP21814	Rev: A	Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 4Phase_Rev	Sheet: 3 of 3
Drawn By:		File: PMP21814_Slaves_Hardware_1.SchDoc	Size: B
Engineer: Pradeep Shenoy		Contact: http://www.ti.com/support	

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