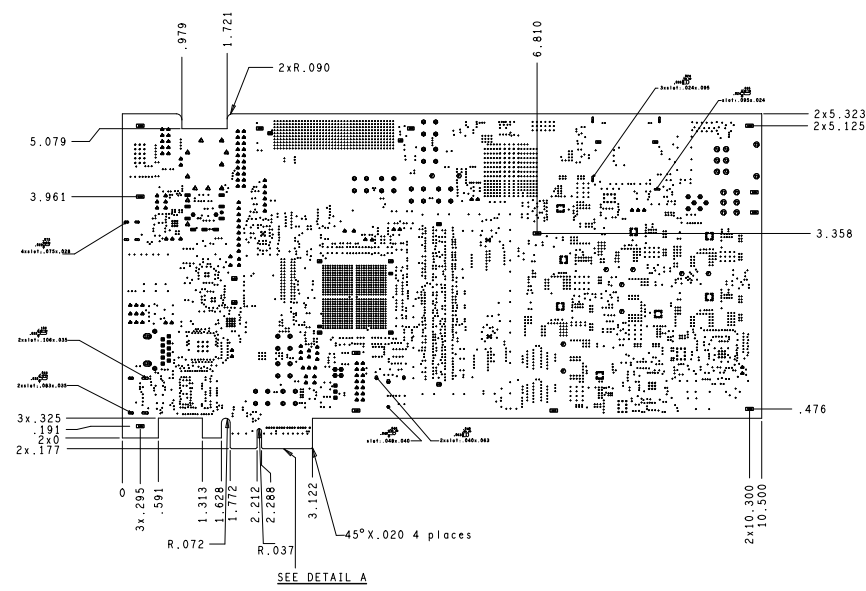
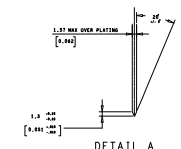


- NOTES: (UNLESS OTHERWISE SPECIFIED)
THIS FAB SHOULD BE "RoHS COMPLIANT".
- FABRICATE TO IPC-A-600, CURRENT REVISION
 - BOARD SHALL MEET THE INSPECTION CRITERIA OF
 - ACCEPTABILITY AS PER IPC-A-600 (LATEST REVISION) CLASS II
 - QUALIFICATION AND PERFORMANCE AS PER IPC-6012 (LATEST REVISION) CLASS II.
 - MATERIAL: ISOLA 370HR (RoHS COMPLIANT MATERIAL) OR EQUIVALENT. GLASS TRANSITION TEMPERATURE MUST MEET OR EXCEED THE TEMPERATURE EXHIBITED WITH HIGH TEMPERATURE PROCESSES ASSOCIATED WITH LEAD FREE ASSEMBLY.
 - APPLY SOLDER MASK OVER BARE COPPER (SMOBC) IAW IPC-SM-840, BOTH SIDES, USING LPI, COLOR GREEN.
 - LPI SOLDER MASK TAIYO PSR4000 (RoHS COMPLIANT MATERIAL) OR EQUIVALENT WILL BE USED ON BOTH SIDES.
 - SOLDER MASK REQUIREMENTS FOR VIAS:
 - VIA DRILLS TENTED (COVERED) FROM TOP SIDE AND ENCRACHED WITH SOLDER MASK RELIEF OF DRILL PLUS 4MIL FROM BOTTOM
 - ALL TEST POINT VIAS SHOWN IN DATA AS ONE TO ONE SOLDER MASK RELIEF ON TEST SIDE, SHALL BE FULLY EXPOSED AND ACCESSIBLE ON FINISHED BOARD.
 - PLUG TENTED VIA DRILLS FROM TOP SIDE ONLY AND FOLLOW GERBER FOR REST.
 - SOLDER MASK REGISTRATION TO BE WITHIN DIAMETRICAL TRUE POSITION OF +/- 0.002" WITH APPLICABLE HOLE / PAD.
 - FINISH: GOLD IMMERSION.
 - SILKSCREEN USING WHITE - HAVEN PC421 (NON-CONDUCTIVE OR EQUIVALENT RoHS COMPLIANT MATERIAL) BOTH SIDES DISTORTION OF SILKSCREEN IS ACCEPTABLE OVER TRACES. EPOXY INK ON SOLDER LANDS IS NOT ACCEPTABLE.
 - VENDOR LOGO AND DATE CODE TO BE MARKED SOLDER SIDE IN SILKSCREEN. MAXIMUM HEIGHT 0.12 INCHES.
 - 100% ELECTRICAL TEST REQUIRED FOR CONTINUITY. BOARD SHALL HAVE A UL-RATING OF 94V-0. UL SYMBOL AND RATING SHALL BE MARKED SOLDER SIDE IN SILKSCREEN.
 - REMOVE ALL UNUSED PADS FROM INTERNAL LAYERS.
 - 274X GERBER/ODB++ USED FOR FAB MUST BE VERIFIED AGAINST THE PROVIDED IPC356 NETLIST. COPPER SLIVERS THAT ARE LESS THAN 0.003" IN WIDTH BETWEEN ANTI-PAD TO PLANE EDGE. ANTI-PAD TO SPLIT PLANE AND ANTI-PAD TO ANTI-PAD MUST BE REMOVED FROM THE MANUFACTURING ARTWORK. A NETLIST COMPARISON MUST BE PASSED WITH NO VIOLATION AFTER THE REMOVAL OF SLIVERS. ANY REQUIREMENT FOR SILVER REMOVAL ABOVE OR EQUAL TO THE 0.003" COPPER WIDTH MUST BE ADDRESSED AND APPROVED IN WRITING BY SUPPLIER.
 - VIAS ARE SUPPOSED TO BE DIRECTLY CONNECTED TO RESPECTIVE PLANE.
 - FOR IMPEDANCE CONTROL DETAILS REFER TO THE FILE "5962_AC701_STACK-UP.PDF" PROVIDED WITH GERBER DATA.
 - INTENTIONAL SHORT:
PLEASE REPORT ANY SHORT OTHER THAN THESE:
MGTAVTT SHORTED TO MGTAVTT SENSE_P
VCCBRAM SHORTED TO VCCBRAM SENSE_P
MGTAVCC SHORTED TO MGTAVCC SENSE_P
FPGA_IV5 SHORTED TO FPGA_IV5 SENSE_P
VTTDR SHORTED TO DDR3_SODIMM_OV75_SENSE
DDR3_VTERM_R_OV75 SHORTED TO DDR3_VTERM_R_OV75_SENSE
VCCAUX SHORTED TO VCCAUX SENSE_P
VCCINT SHORTED TO VCCINT SENSE_P
FPGA_3V3 SHORTED TO FPGA_3V3 SENSE_P
FPGA_IV8 SHORTED TO FPGA_IV8 SENSE_P
VCCINT_R_SENSE_P SHORTED TO VCCINT_XADC_SENSE_P
VCCINT_R_SENSE_N SHORTED TO VCCINT_XADC_SENSE_N
VCCO_VADJ_R_SENSE_N SHORTED TO VCCO_VADJ_XADC_SENSE_N
VCCO_VADJ_R_SENSE_P SHORTED TO VCCO_VADJ_XADC_SENSE_P
VCCO_VADJ_SHORTED TO VCCO_VADJ SENSE_P
VCCBRAM_R_SENSE_N SHORTED TO VCCBRAM_XADC_SENSE_N
VCCAUX_R_SENSE_N SHORTED TO VCCAUX_XADC_SENSE_N
VCCAUX_R_SENSE_P SHORTED TO VCCAUX_XADC_SENSE_P
FPGA_IV8_R_SENSE_N SHORTED TO FPGA_IV8_XADC_SENSE_N
FPGA_IV8_R_SENSE_P SHORTED TO FPGA_IV8_XADC_SENSE_P
MGTAVTT_R_SENSE_N SHORTED TO MGTAVTT_XADC_SENSE_N
MGTAVTT_R_SENSE_P SHORTED TO MGTAVTT_XADC_SENSE_P
MGTAVCC_R_SENSE_N SHORTED TO MGTAVCC_XADC_SENSE_N
MGTAVCC_R_SENSE_P SHORTED TO MGTAVCC_XADC_SENSE_P
FPGA_IV5_R_SENSE_N SHORTED TO FPGA_IV5_XADC_SENSE_N
FPGA_IV5_R_SENSE_P SHORTED TO FPGA_IV5_XADC_SENSE_P
FPGA_3V3_R_SENSE_N SHORTED TO FPGA_3V3_XADC_SENSE_N
FPGA_3V3_R_SENSE_P SHORTED TO FPGA_3V3_XADC_SENSE_P
GND SHORTED TO MGTAVTT SENSE_N, MGTAVCC SENSE_N, AGND, FPGA_3V3 SENSE_N, FPGA_IV8_SENSE_N, FPGA_IV5_SENSE_N, VCCO_VADJ SENSE_N
 - HARD GOLD ON FINGERS, 30-50 MICRO INCHES OF GOLD OVER 150-200 MICRO INCHES OF NICKLE MINIMUM.

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
	1.0	INITIAL RELEASE	10/12/12	DN



DRILL CHART: TOP to BOTTOM					
ALL UNITS ARE IN MILS					
FIGURE	FHS	TOLERANCE	PLATED	QTY	ADDITIONAL
-	10.0	+0.0/-10.0	PLATED	4407	
-	12.0	+0.0/-12.0	PLATED	258	
-	20.0	+3.0/-3.0	PLATED	30	
-	33.0	+3.0/-3.0	PLATED	1	
-	35.0	+3.0/-3.0	PLATED	13	
-	40.0	+3.0/-3.0	PLATED	115	
-	41.0	+3.0/-3.0	PLATED	11	PRESS FIT
-	46.0	+3.0/-3.0	PLATED	4	
-	48.0	+3.0/-3.0	PLATED	6	
-	50.0	+3.0/-3.0	PLATED	10	
-	63.0	+3.0/-3.0	PLATED	42	
-	71.0	+3.0/-3.0	PLATED	6	
-	73.0	+3.0/-3.0	PLATED	8	
-	106.0	+3.0/-3.0	PLATED	2	
-	110.0	+3.0/-3.0	PLATED	3	
-	125.0	+3.0/-3.0	PLATED	8	
-	33.0	+2.0/-2.0	NON-PLATED	2	
-	37.0	+2.0/-2.0	NON-PLATED	9	
-	40.0	+2.0/-2.0	NON-PLATED	2	
-	43.0	+3.0/-3.0	NON-PLATED	1	
-	44.0	+1.0/-0.0	NON-PLATED	1	
-	50.0	+2.0/-2.0	NON-PLATED	2	
-	54.0	+2.0/-2.0	NON-PLATED	2	
-	61.0	+3.0/-3.0	NON-PLATED	2	
-	63.0	+2.0/-2.0	NON-PLATED	5	
-	128.0	+2.0/-2.0	NON-PLATED	2	
-	48.0x40.0	+3.0/-3.0	PLATED	1	
-	63.0x40.0	+3.0/-3.0	PLATED	2	
-	75.0x28.0	+2.0/-2.0	PLATED	4	
-	83.0x35.0	+2.0/-2.0	PLATED	2	
-	95.0x24.0	+3.0/-3.0	PLATED	4	
-	106.0x35.0	+2.0/-2.0	PLATED	2	
TOTAL HOLES: 4967					



TOP SIDE	SINGLE ENDED		EDGE-COUPLED DIFFERENTIAL	
	COPPER WEIGHT	COPPER THICKNESS	TRACE WIDTH	TARGET IMPEDANCE
LAYER 1 TOP	0.5 OZ-PLATING	1.7	5.5 MIL	50 OHMS
PRE-PREG			7.75 MIL	40 OHMS
LAYER 2 GND1	0.5 OZ	0.6	3.75 MIL	50 OHMS
CORE			5.5 MIL	40 OHMS
LAYER 3 SIG1	0.5 OZ	0.6	3.75 MIL	50 OHMS
PRE-PREG			5.5 MIL	40 OHMS
LAYER 4 GND2	0.5 OZ	0.6	3.75 MIL	50 OHMS
CORE			5.5 MIL	40 OHMS
LAYER 5 SIG2	0.5 OZ	0.6	3.75 MIL	50 OHMS
PRE-PREG			5.5 MIL	40 OHMS
LAYER 6 GND3	0.5 OZ	0.6	3.75 MIL	50 OHMS
CORE			5.5 MIL	40 OHMS
LAYER 7 SIG3	0.5 OZ	0.6	3.75 MIL	50 OHMS
PRE-PREG			5.5 MIL	40 OHMS
LAYER 8 GND4	1.0 OZ	1.2		
CORE				
LAYER 9 PWR1	1.0 OZ	1.2		
PRE-PREG				
LAYER 10 PWR2	0.5 OZ	0.6		
CORE				
LAYER 11 GND5	0.5 OZ	0.6		
PRE-PREG				
LAYER 12 SIG4	0.5 OZ	0.6	3.75 MIL	50 OHMS
CORE			5.5 MIL	40 OHMS
LAYER 13 GND6	0.5 OZ	0.6	3.75 MIL	50 OHMS
PRE-PREG			5.5 MIL	40 OHMS
LAYER 14 SIG5	0.5 OZ	0.6	3.75 MIL	50 OHMS
CORE			5.5 MIL	40 OHMS
LAYER 15 GND7	0.5 OZ	0.6	3.75 MIL	50 OHMS
PRE-PREG			5.5 MIL	40 OHMS
LAYER 16 BOT	0.5 OZ-PLATING	1.7	5.5 MIL	50 OHMS
BOTTOM SIDE			7.75 MIL	40 OHMS

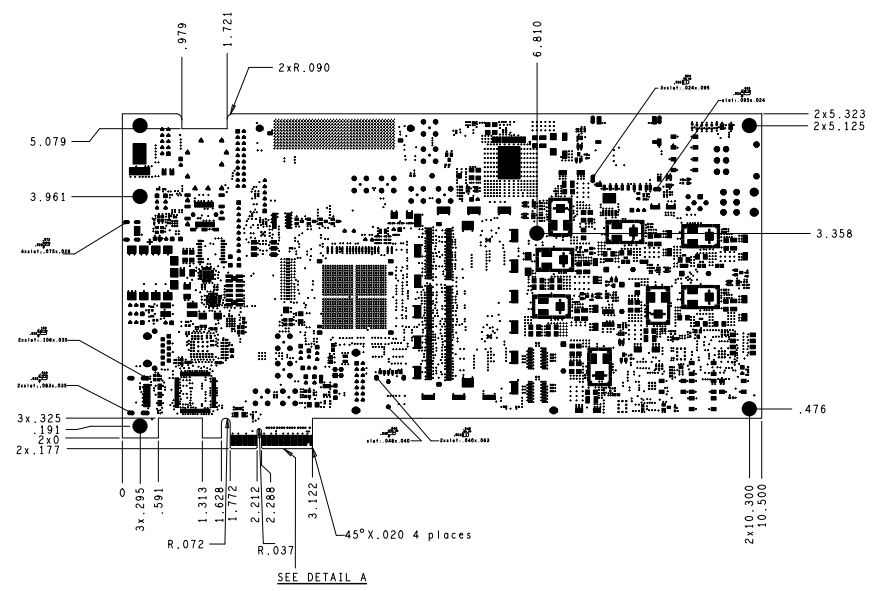
UNLESS OTHERWISE SPECIFIED: ALL DIMENSIONS ARE IN INCHES TOLERANCES ON: LINEAR DIMENSIONS: .XXX = ±.010, .XX = ±.030, .X = ±.100 ANGLES: XX.XX = ±0.30, XX.X = ±1.0, XX = ±5.0 RAWING SYMBOLS & TOLERANCE PER ANS1Y14.5M-1994	APPROVAL	DATE	CUSTOMER:
	DRAWN: Fawad Munawar CHECKED: Rashid Mehmood ENGR: David Naylor APPROVAL: David Naylor	10-12-12 10-12-12 10-12-12	XILINX, INC PROJECT NAME: HW-A7-AC701
WHIZZ SYSTEMS INC. 3240 SCOTT BLVD SANTA CLARA, CA 95054 TEL: 408-980-0400	WHIZZ JOB NO. 5962	SIZE: D SCALE: 1:1	FAB DWG. NO. 1280669 REV. 1.0 SHEET 1 OF 1

- NOTES: (UNLESS OTHERWISE SPECIFIED)
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 - ALL TEST POINT VIAS SHOWN IN DATA AS ONE TO ONE SOLDER MASK RELIEF ON TEST SIDE, SHALL BE FULLY EXPOSED AND ACCESSIBLE ON FINISHED BOARD.
 - PLUG TENTED VIA DRILLS FROM TOP SIDE ONLY AND FOLLOW GERBER FOR REST.
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 - FINISH: GOLD IMMERSION.
 - SILKSCREEN USING WHITE - HAVEN PC421 (NON-CONDUCTIVE OR EQUIVALENT RoHS COMPLIANT MATERIAL) BOTH SIDES DISTORTION OF SILKSCREEN IS ACCEPTABLE OVER TRACES. EPOXY INK ON SOLDER LANDS IS NOT ACCEPTABLE.
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 - INTENTIONAL SHORT:

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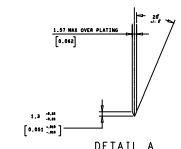
 - MGTAVTT SHORTED TO MGTAVTT SENSE P
 - VCCBRAM SHORTED TO VCCBRAM SENSE P
 - MGTAVCC SHORTED TO MGTAVCC SENSE P
 - FPGA_IV5 SHORTED TO FPGA_IV5 SENSE P
 - VTTDR SHORTED TO DDR3_SODIMM_OV75_SENSE
 - DDR3_VTERM_R_OV75 SHORTED TO DDR3_VTERM_R_OV75_SENSE
 - VCCAUX SHORTED TO VCCAUX SENSE P
 - VCCINT SHORTED TO VCCINT SENSE P
 - FPGA_3V3 SHORTED TO FPGA_3V3 SENSE P
 - FPGA_IV8 SHORTED TO FPGA_IV8 SENSE P
 - VCCINT_R_SENSE_P SHORTED TO VCCINT_XADC_SENSE_P
 - VCCINT_R_SENSE_N SHORTED TO VCCINT_XADC_SENSE_N
 - VCCO_VADJ_R_SENSE_N SHORTED TO VCCO_VADJ_XADC_SENSE_N
 - VCCO_VADJ_R_SENSE_P SHORTED TO VCCO_VADJ_XADC_SENSE_P
 - VCCO_VADJ_SHORTED TO VCCO_VADJ SENSE P
 - VCCBRAM_R_SENSE_N SHORTED TO VCCBRAM_XADC_SENSE_N
 - VCCBRAM_R_SENSE_P SHORTED TO VCCBRAM_XADC_SENSE_P
 - VCCAUX_R_SENSE_N SHORTED TO VCCAUX_XADC_SENSE_N
 - VCCAUX_R_SENSE_P SHORTED TO VCCAUX_XADC_SENSE_P
 - FPGA_IV8_R_SENSE_N SHORTED TO FPGA_IV8_XADC_SENSE_N
 - FPGA_IV8_R_SENSE_P SHORTED TO FPGA_IV8_XADC_SENSE_P
 - MGTAVTT_R_SENSE_N SHORTED TO MGTAVTT_XADC_SENSE_N
 - MGTAVTT_R_SENSE_P SHORTED TO MGTAVTT_XADC_SENSE_P
 - MGTAVCC_R_SENSE_N SHORTED TO MGTAVCC_XADC_SENSE_N
 - MGTAVCC_R_SENSE_P SHORTED TO MGTAVCC_XADC_SENSE_P
 - FPGA_IV5_R_SENSE_N SHORTED TO FPGA_IV5_XADC_SENSE_N
 - FPGA_IV5_R_SENSE_P SHORTED TO FPGA_IV5_XADC_SENSE_P
 - FPGA_3V3_R_SENSE_N SHORTED TO FPGA_3V3_XADC_SENSE_N
 - FPGA_3V3_R_SENSE_P SHORTED TO FPGA_3V3_XADC_SENSE_P
 - GND SHORTED TO MGTAVTT_SENSE_N, MGTAVCC_SENSE_N, AGND, FPGA_3V3_SENSE_N, FPGA_IV8_SENSE_N, FPGA_IV5_SENSE_N, VCCO_VADJ_SENSE_N
 - HARD GOLD ON FINGERS, 30-50 MICRO INCHES OF GOLD OVER 150-200 MICRO INCHES OF NICKLE MINIMUM.

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
	1.0	INITIAL RELEASE	10/12/12	DN



DRILL CHART: TOP to BOTTOM					
ALL UNITS ARE IN MILS					
FIGURE	FHS	TOLERANCE	PLATED	QTY	ADDITIONAL
-	10.0	+0.0/-10.0	PLATED	4407	
-	12.0	+0.0/-12.0	PLATED	258	
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-	41.0	+3.0/-3.0	PLATED	11	PRESS FIT
-	46.0	+3.0/-3.0	PLATED	4	
-	48.0	+3.0/-3.0	PLATED	6	
-	50.0	+3.0/-3.0	PLATED	10	
-	63.0	+3.0/-3.0	PLATED	42	
-	71.0	+3.0/-3.0	PLATED	6	
-	73.0	+3.0/-3.0	PLATED	8	
-	106.0	+3.0/-3.0	PLATED	2	
-	110.0	+3.0/-3.0	PLATED	3	
-	125.0	+3.0/-3.0	PLATED	8	
-	33.0	+2.0/-2.0	NON-PLATED	2	
-	37.0	+2.0/-2.0	NON-PLATED	9	
-	40.0	+2.0/-2.0	NON-PLATED	2	
-	43.0	+3.0/-3.0	NON-PLATED	1	
-	44.0	+1.0/-0.0	NON-PLATED	1	
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-	128.0	+2.0/-2.0	NON-PLATED	2	
-	48.0x40.0	+3.0/-3.0	PLATED	1	
-	63.0x40.0	+3.0/-3.0	PLATED	2	
-	75.0x28.0	+2.0/-2.0	PLATED	4	
-	83.0x35.0	+2.0/-2.0	PLATED	2	
-	95.0x24.0	+3.0/-3.0	PLATED	4	
-	106.0x35.0	+2.0/-2.0	PLATED	2	

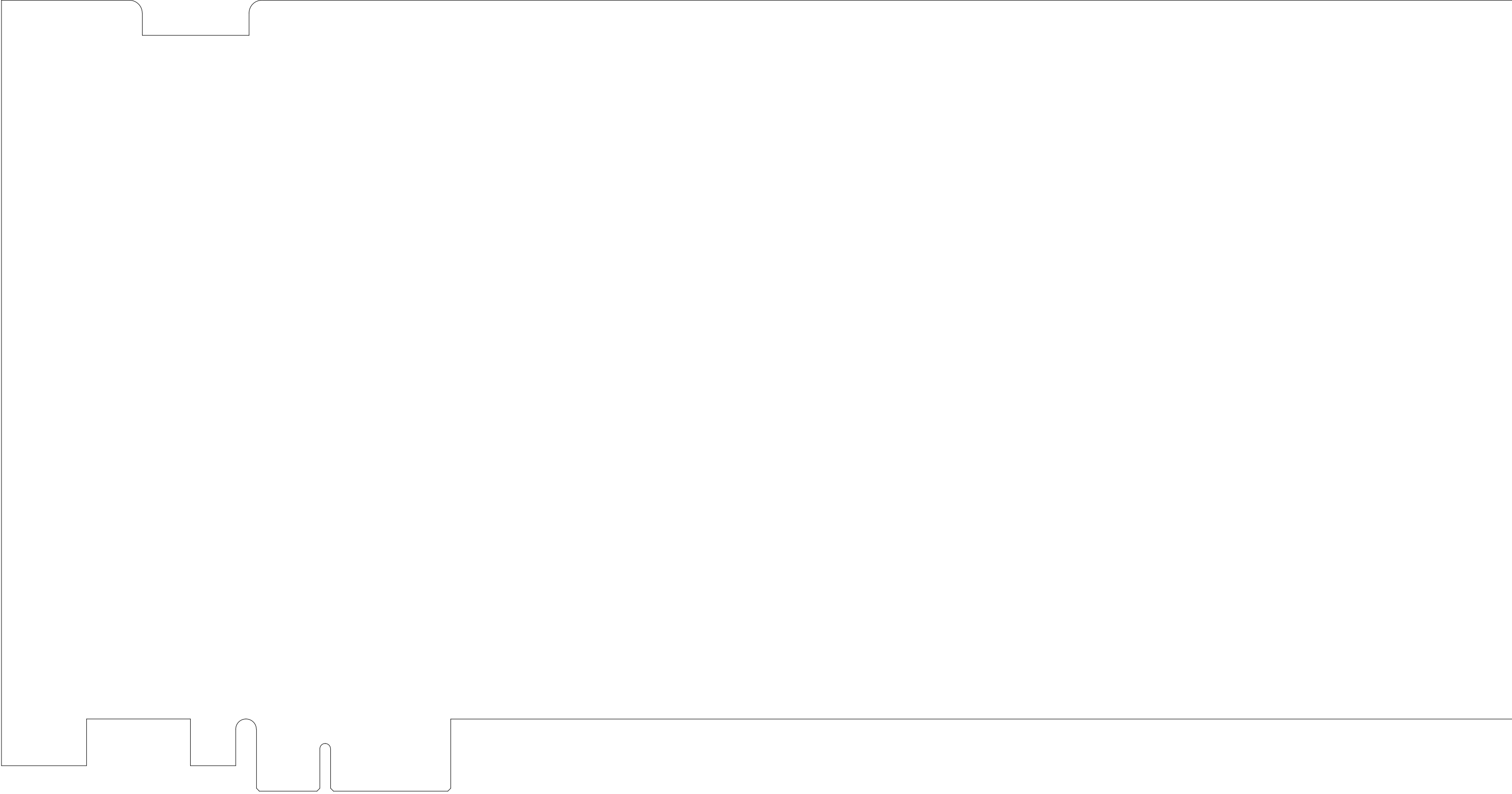
TOTAL HOLES: 4967

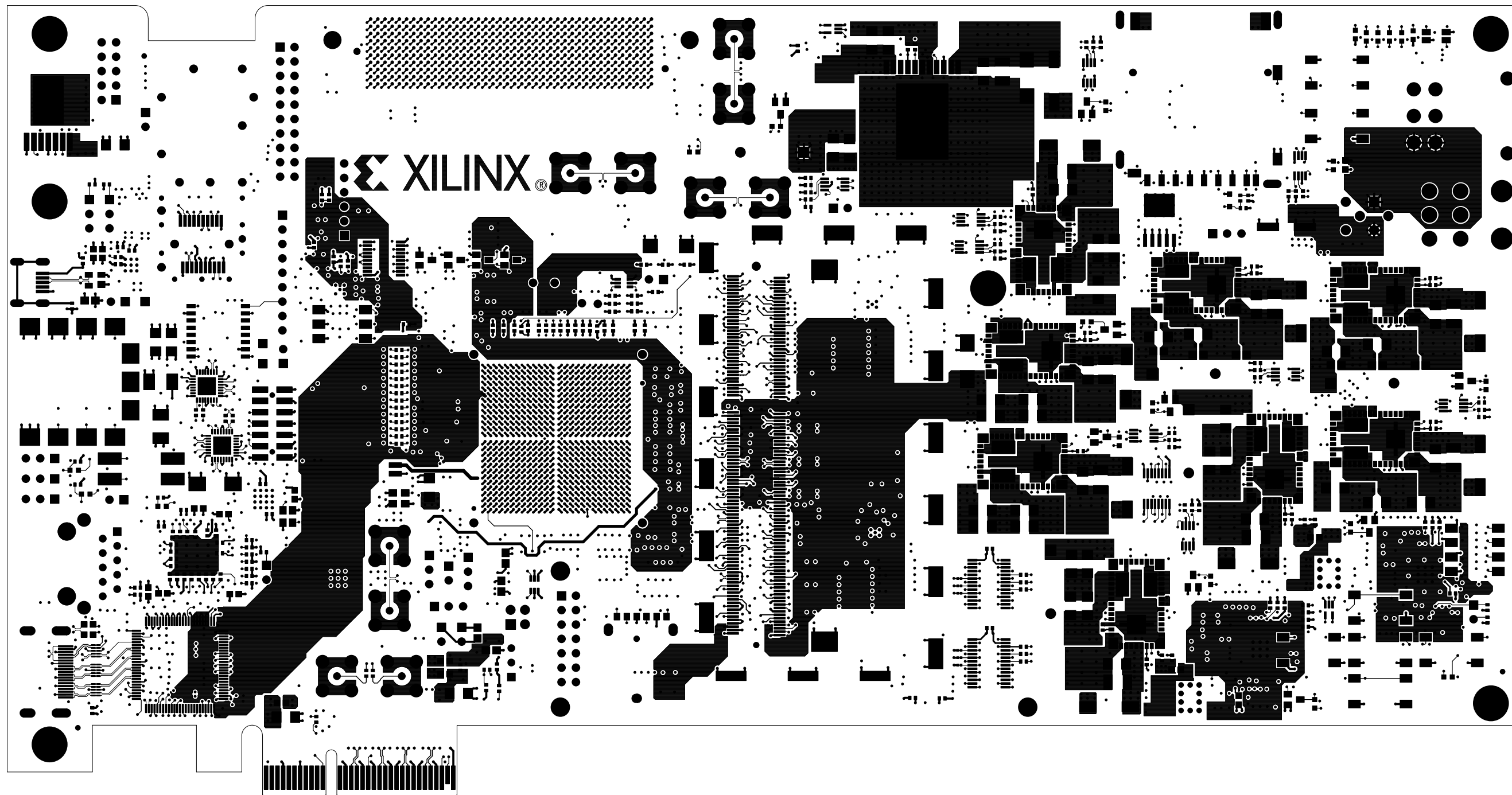



TOP SIDE	SINGLE ENDED		EDGE-COUPLED DIFFERENTIAL	
	COPPER WEIGHT	COPPER THICKNESS	TRACE WIDTH	TARGET IMPEDANCE
LAYER 1 TOP	0.5 OZ-PLATING	1.7	5.5 MIL	50 OHMS
PRE-PREG			7.75 MIL	40 OHMS
LAYER 2 GND1	0.5 OZ	0.6	3.75 MIL	50 OHMS
CORE			5.5 MIL	40 OHMS
LAYER 3 SIG1	0.5 OZ	0.6	3.75 MIL	50 OHMS
PRE-PREG			5.5 MIL	40 OHMS
LAYER 4 GND2	0.5 OZ	0.6	3.75 MIL	50 OHMS
CORE			5.5 MIL	40 OHMS
LAYER 5 SIG2	0.5 OZ	0.6	3.75 MIL	50 OHMS
PRE-PREG			5.5 MIL	40 OHMS
LAYER 6 GND3	0.5 OZ	0.6	3.75 MIL	50 OHMS
CORE			5.5 MIL	40 OHMS
LAYER 7 SIG3	0.5 OZ	0.6	3.75 MIL	50 OHMS
PRE-PREG			5.5 MIL	40 OHMS
LAYER 8 GND4	1.0 OZ	1.2		
CORE				
LAYER 9 PWR1	1.0 OZ	1.2		
PRE-PREG				
LAYER 10 PWR2	0.5 OZ	0.6		
CORE				
LAYER 11 GND5	0.5 OZ	0.6		
PRE-PREG				
LAYER 12 SIG4	0.5 OZ	0.6	3.75 MIL	50 OHMS
CORE			5.5 MIL	40 OHMS
LAYER 13 GND6	0.5 OZ	0.6	3.75 MIL	50 OHMS
PRE-PREG			5.5 MIL	40 OHMS
LAYER 14 SIG5	0.5 OZ	0.6	3.75 MIL	50 OHMS
CORE			5.5 MIL	40 OHMS
LAYER 15 GND7	0.5 OZ	0.6	3.75 MIL	50 OHMS
PRE-PREG			5.5 MIL	40 OHMS
LAYER 16 BOT	0.5 OZ-PLATING	1.7	5.5 MIL	50 OHMS
BOTTOM SIDE			7.75 MIL	40 OHMS

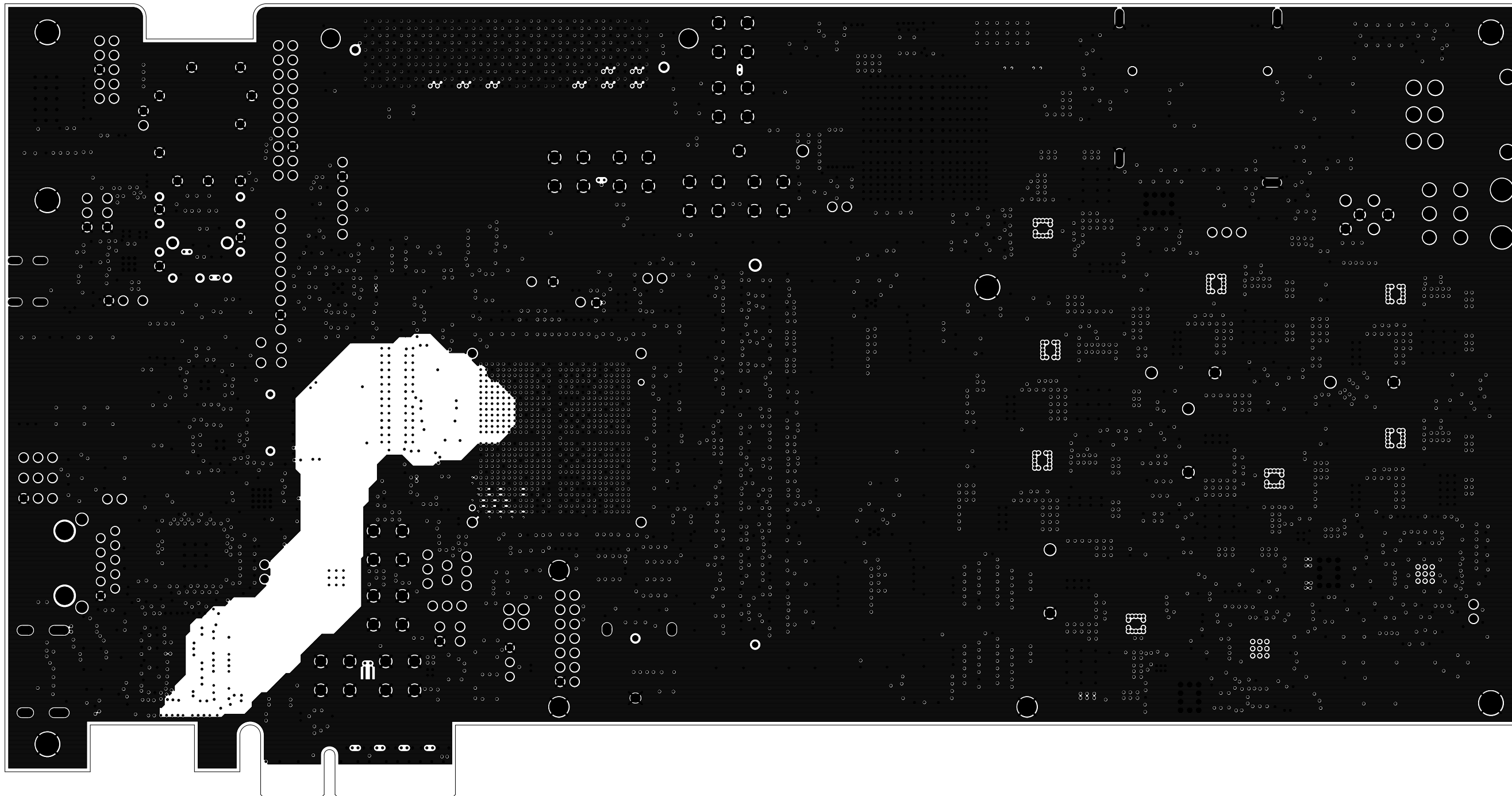
CROSS SECTION DETAIL


UNLESS OTHERWISE SPECIFIED: ALL DIMENSIONS ARE IN INCHES TOLERANCES ON: LINEAR DIMENSIONS ANGLES .XXX ±.010 XX.XX ±.030 .XX ±.030 XX.X ±.100 .X ±.100 XX ±.500	APPROVAL	DATE	CUSTOMER:
	DRAWN: Fawad Munawar CHECKED: Rashid Mehmood	10-12-12	10-12-12
ENGR: David Naylor APPROVAL: David Naylor	10-12-12	10-12-12	PROJECT NAME: HW-A7-AC701
WHIZZ SYSTEMS INC. 3240 SCOTT BLVD SANTA CLARA, CA 95054 TEL: 408-980-0400	DO NOT SCALE DRAWING WHIZZ JOB NO. 5962	SIZE: D SCALE: 1:1	FAB DWG. NO. 1280669 REV. 1.0 SHEET 1 OF 1

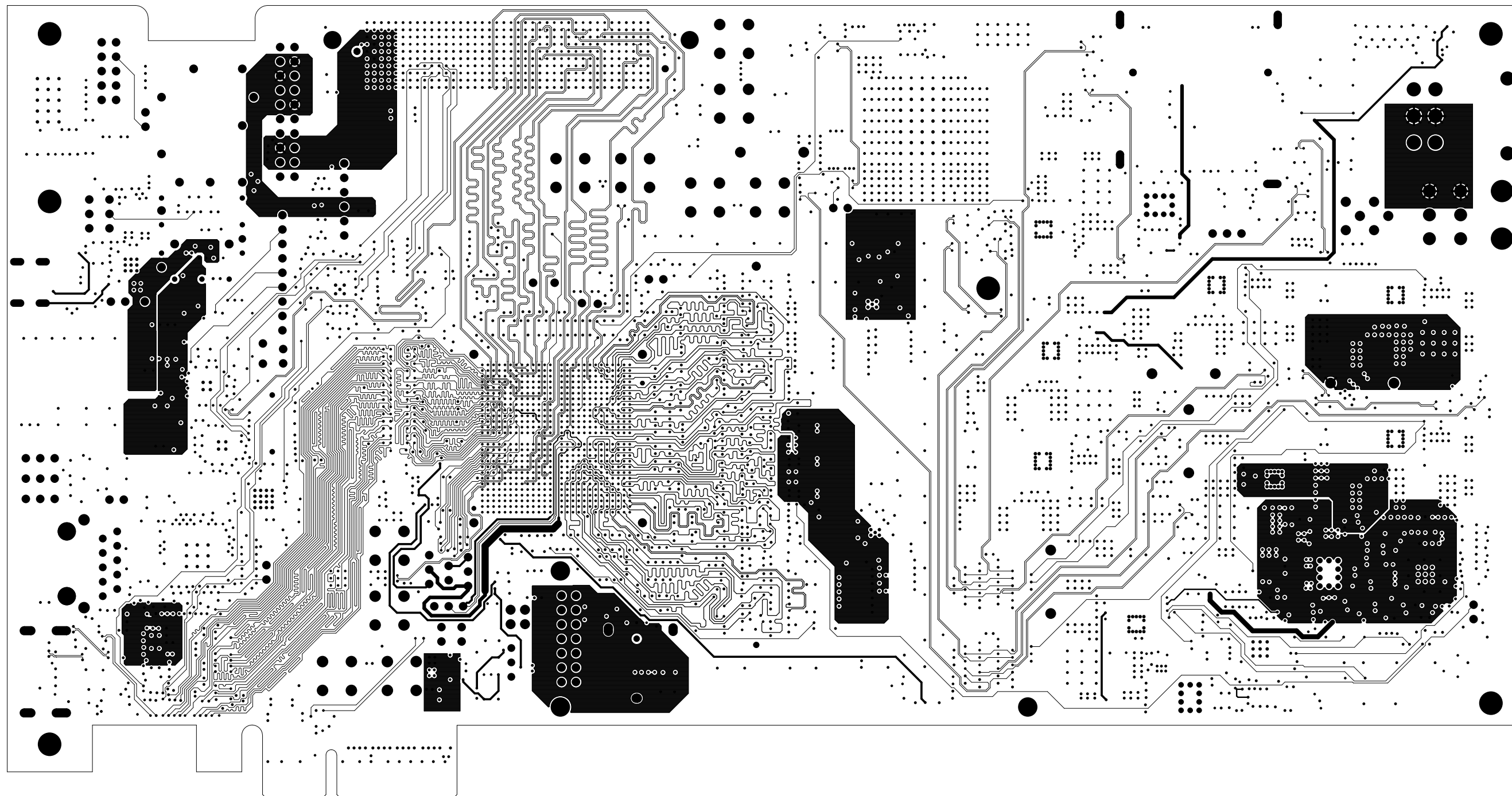





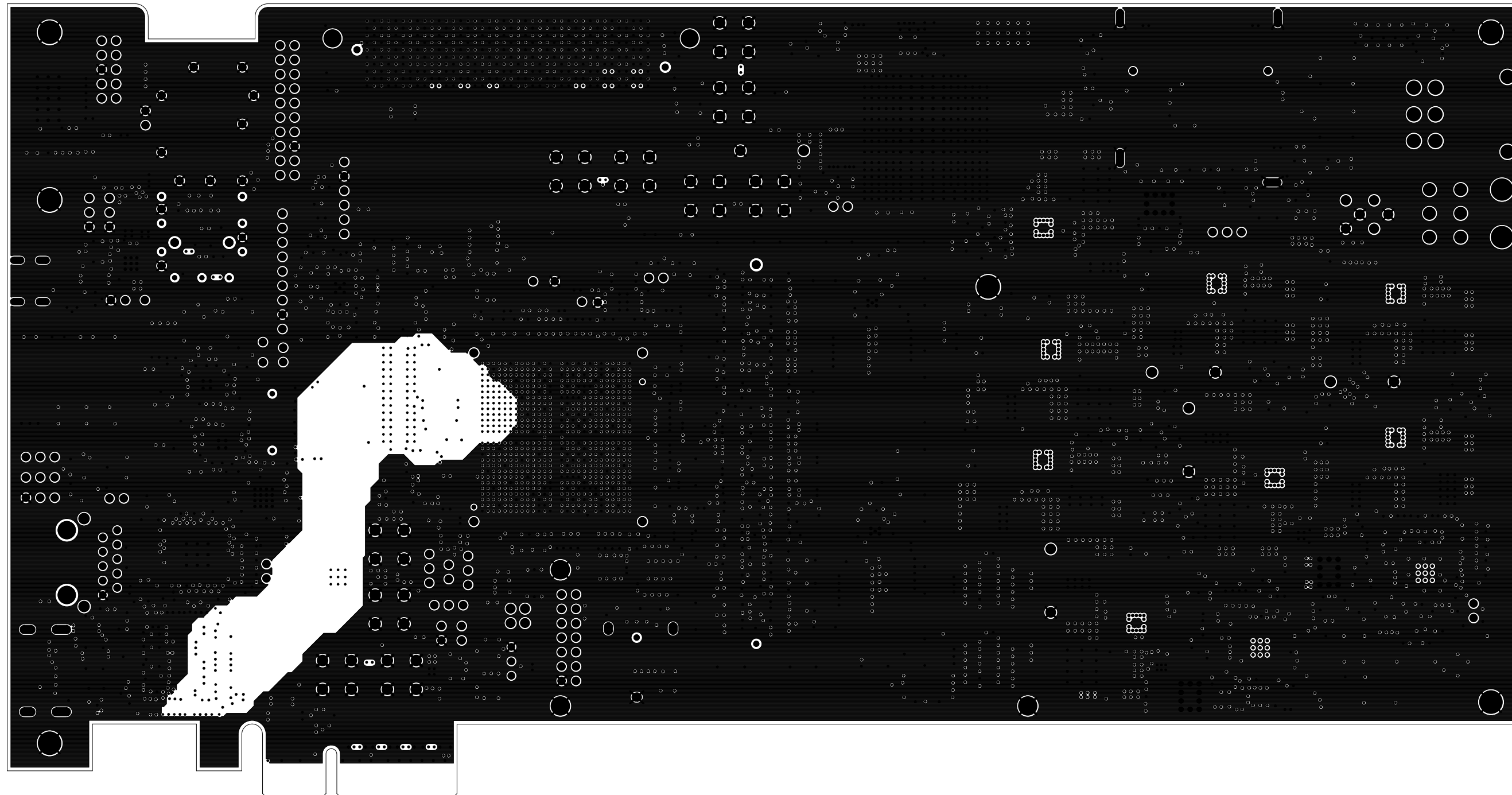
 <p>DESIGNED BY: WHIZZ SYSTEMS INC. 3240 SCOTT BLVD. SANTA CLARA, CA 95054 www.whizzsystems.com TEL:408-980-0400 FAX:408-980-1555</p>	This document contains information which is the proprietary property of Xilinx Inc. This document may not be disclosed to third parties without the prior written consent of Xilinx, Inc.			
	Layer: TOP	Sheet: 01	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




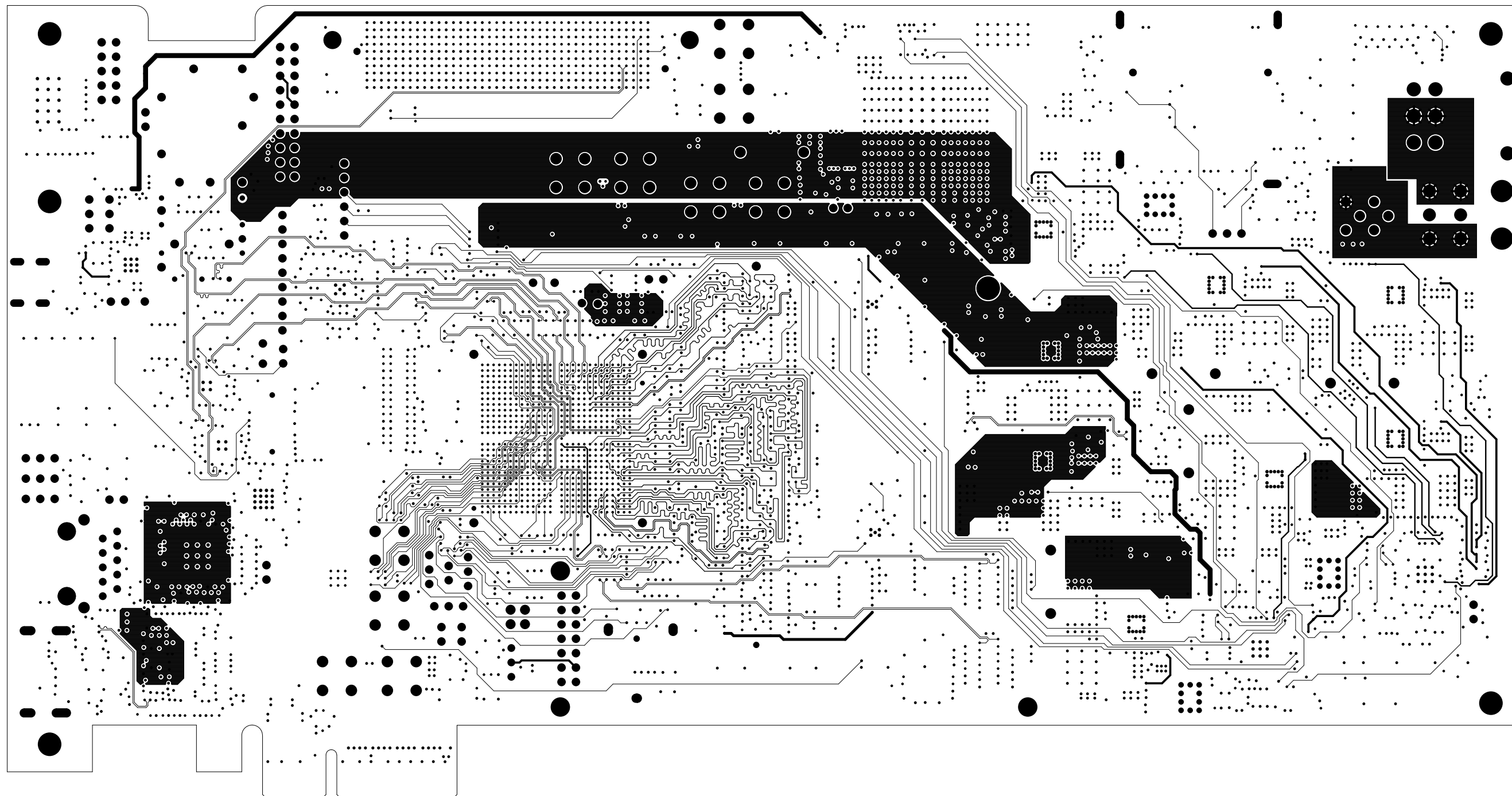
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	Layer: GND1	Sheet: 02	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




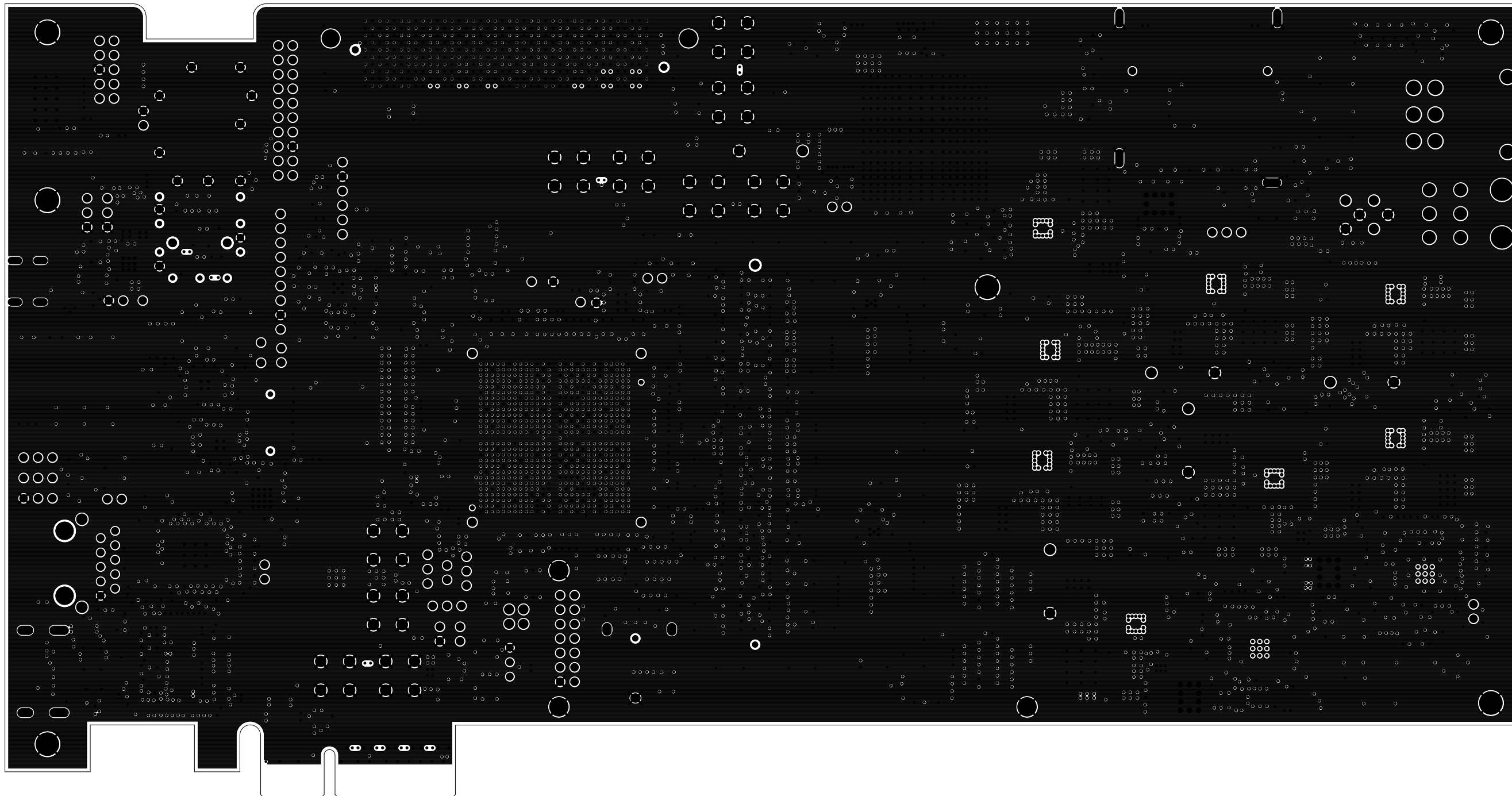
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	Layer: SIG1	Sheet: 03	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




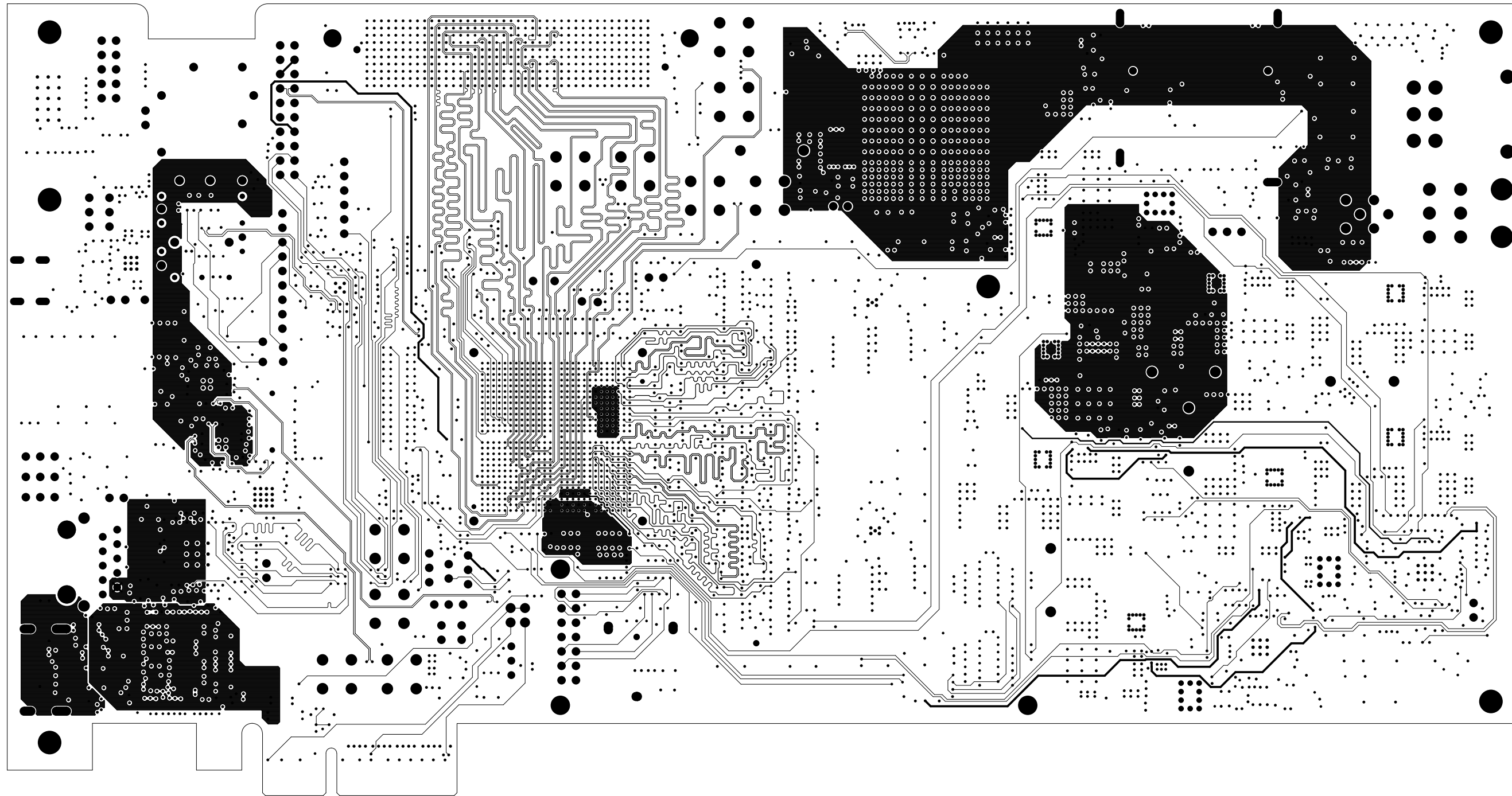
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	Layer: GND2	Sheet: 04	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




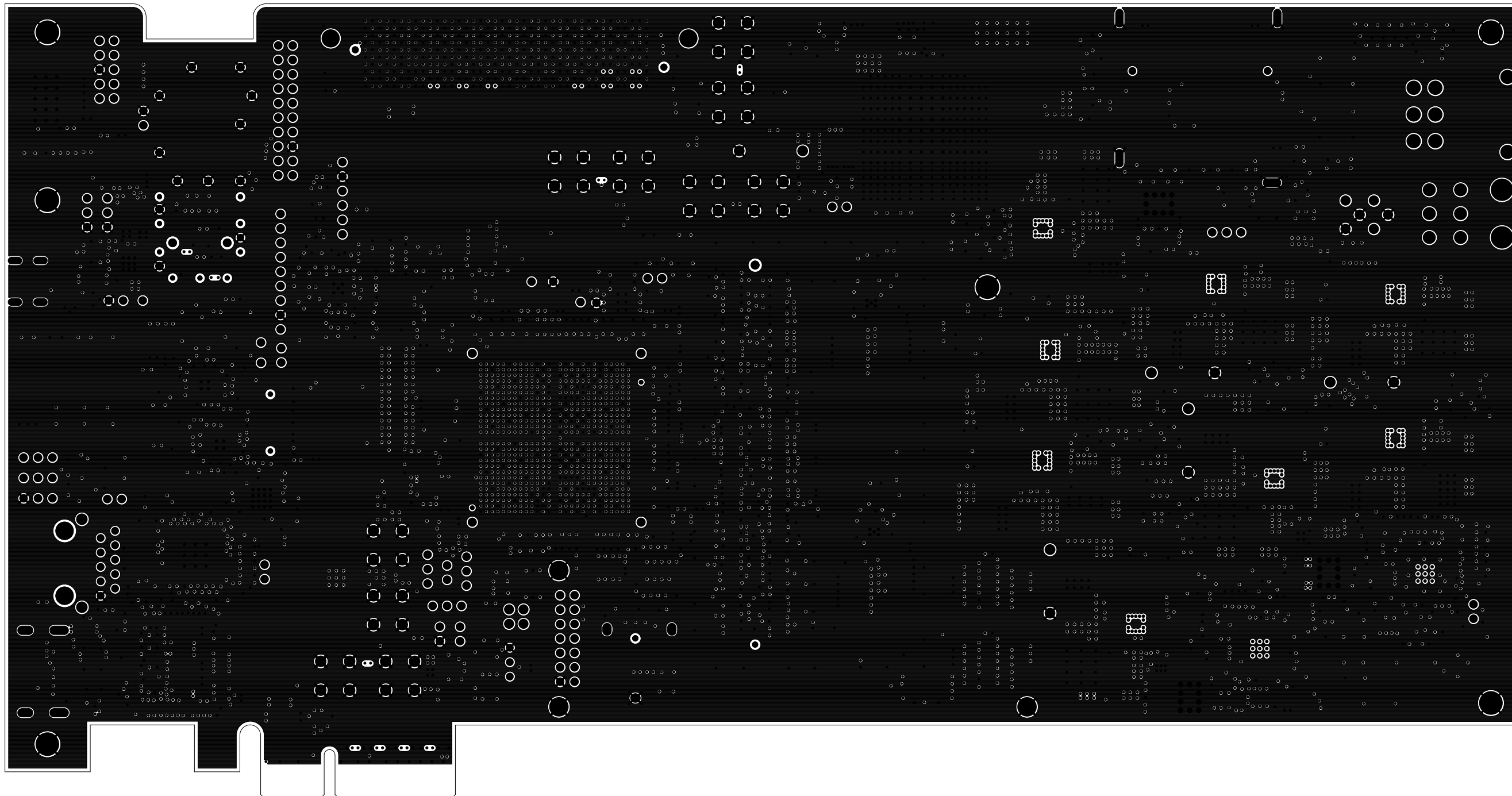
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	Layer: SIG2	Sheet: 05	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




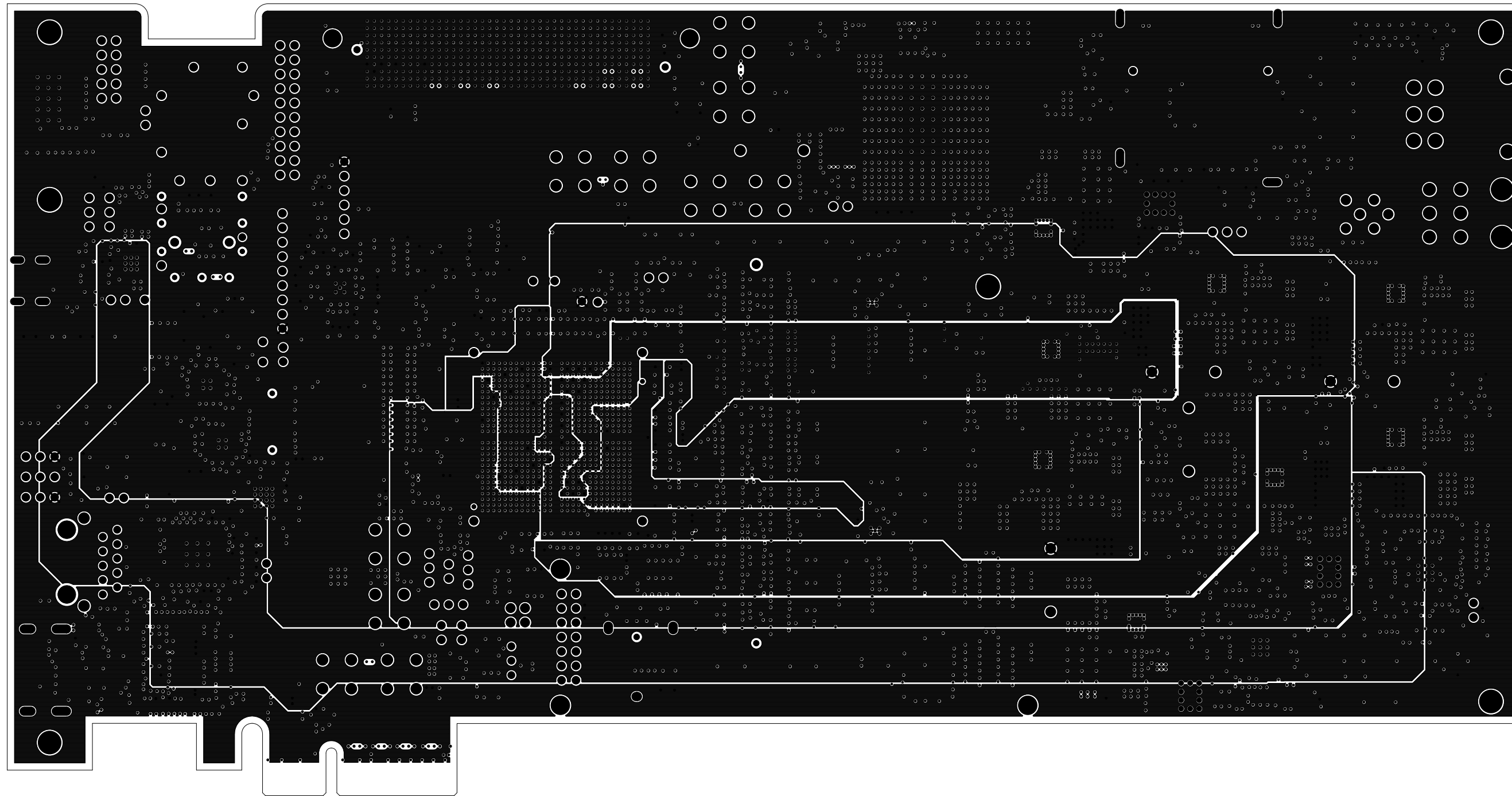
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	Layer: GND3	Sheet: 06	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		



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	Layer: SIG3	Sheet: 07	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		



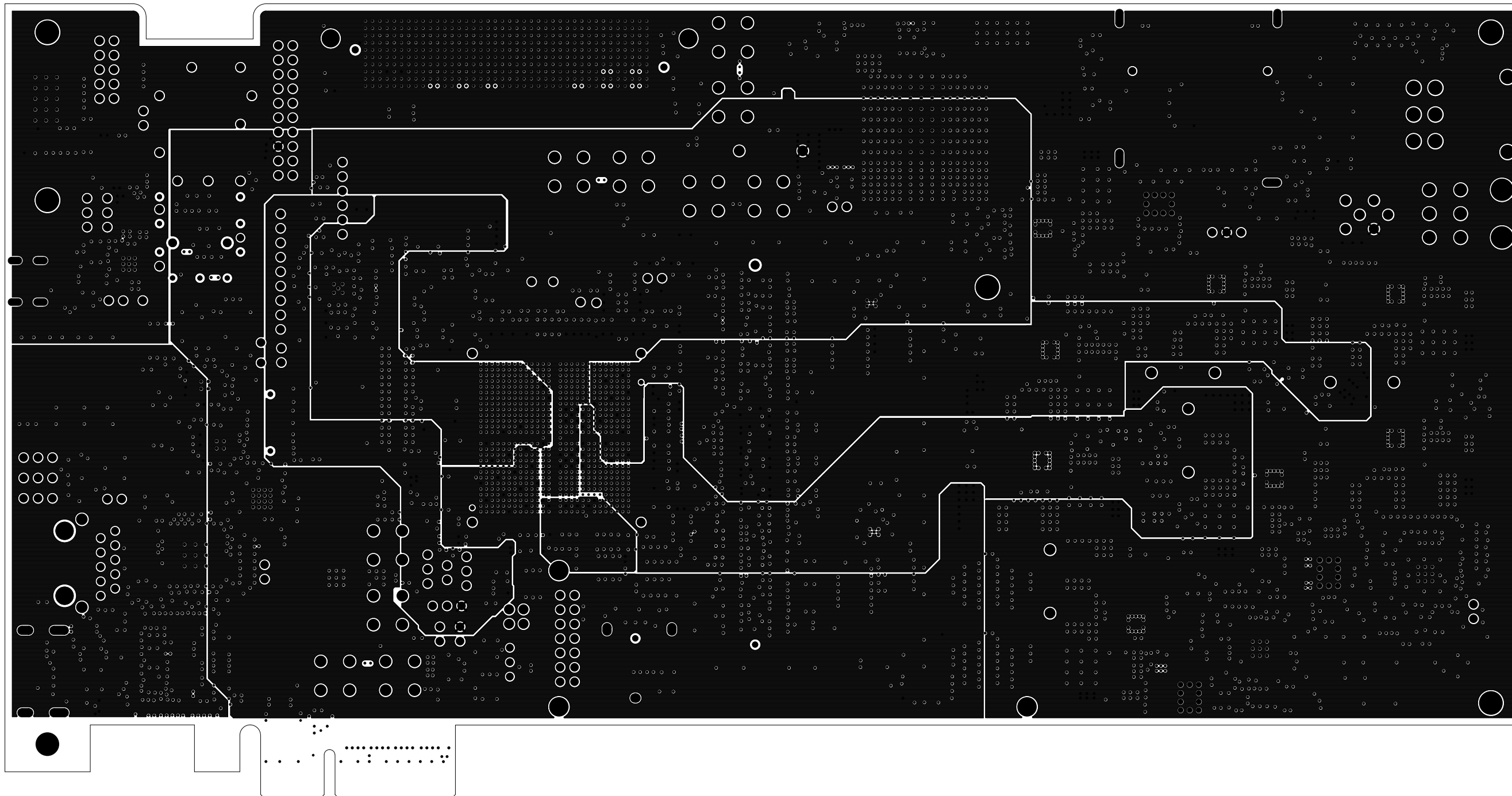
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	Layer: GND4	Sheet: 08	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




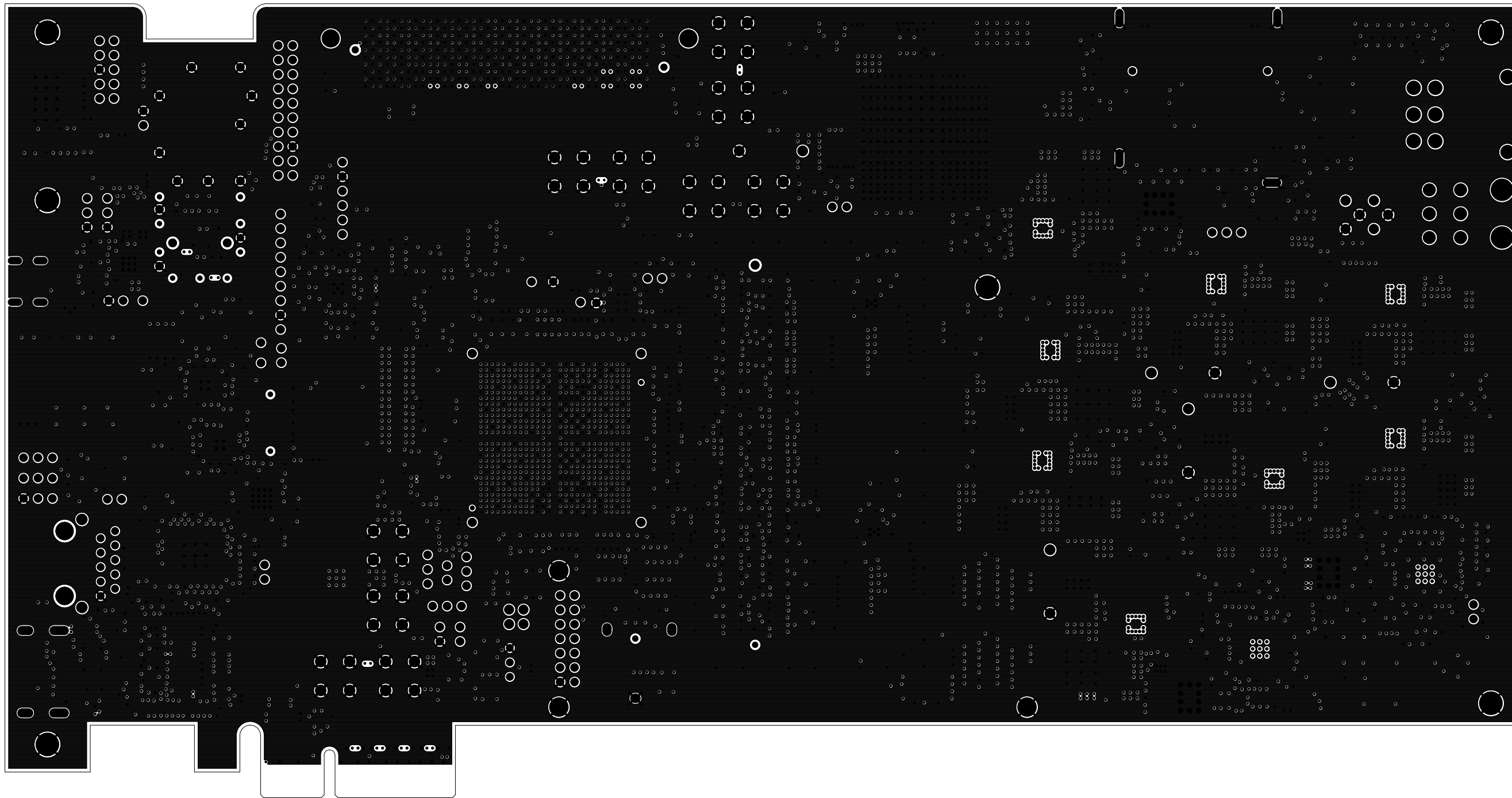
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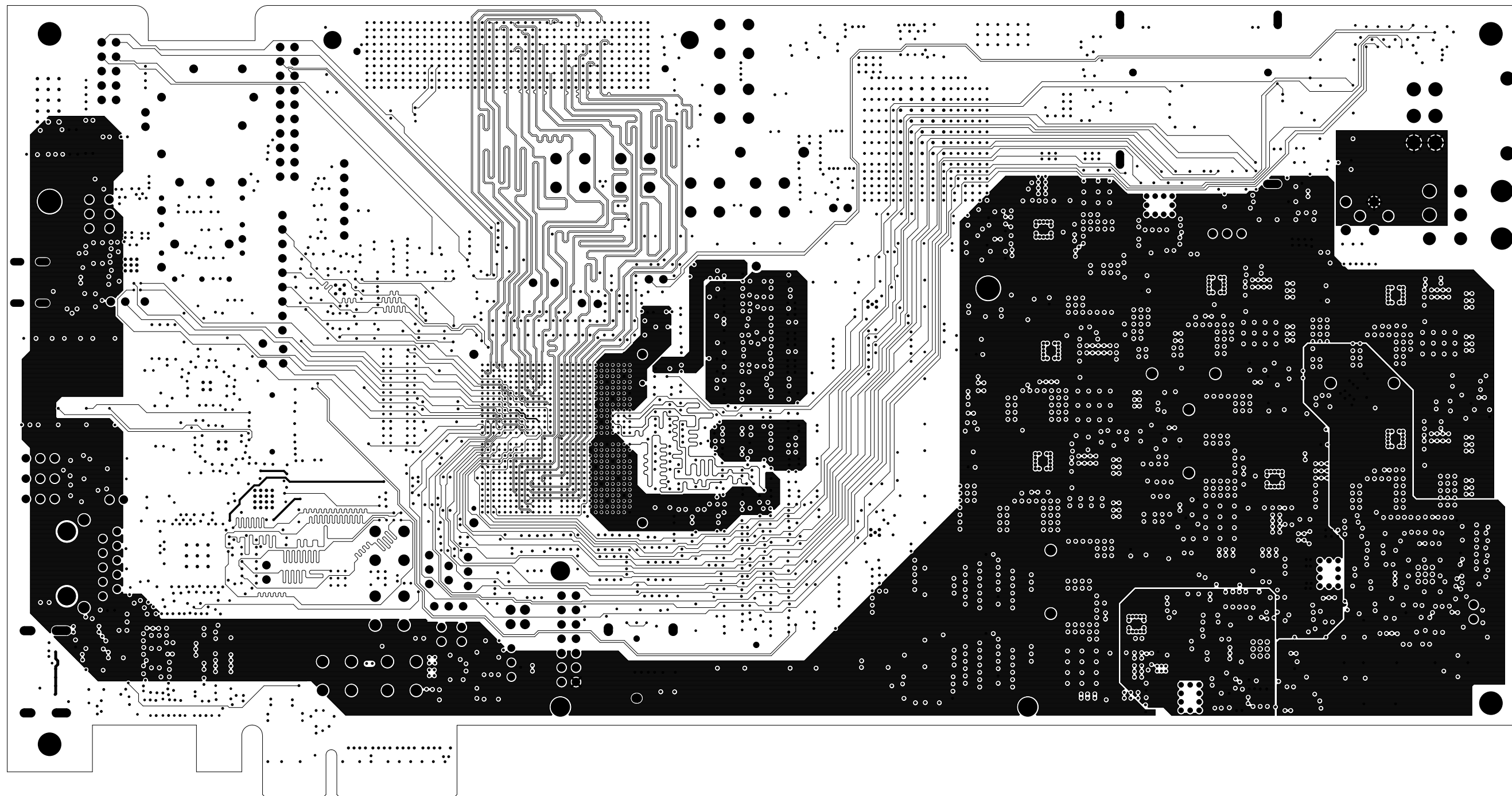
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Layer:	PWR1	Sheet:	09 of 24
Layout Engineer:	FAWAD MUNAWAR	Company Name:	XILINX, INC.
Design Engineer:	DAVID NAYLOR	Project Name:	HW-A7-AC701
Whizz Job Number:	5962	Date:	10.12.2012
		Project Number:	1280669
			REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771	




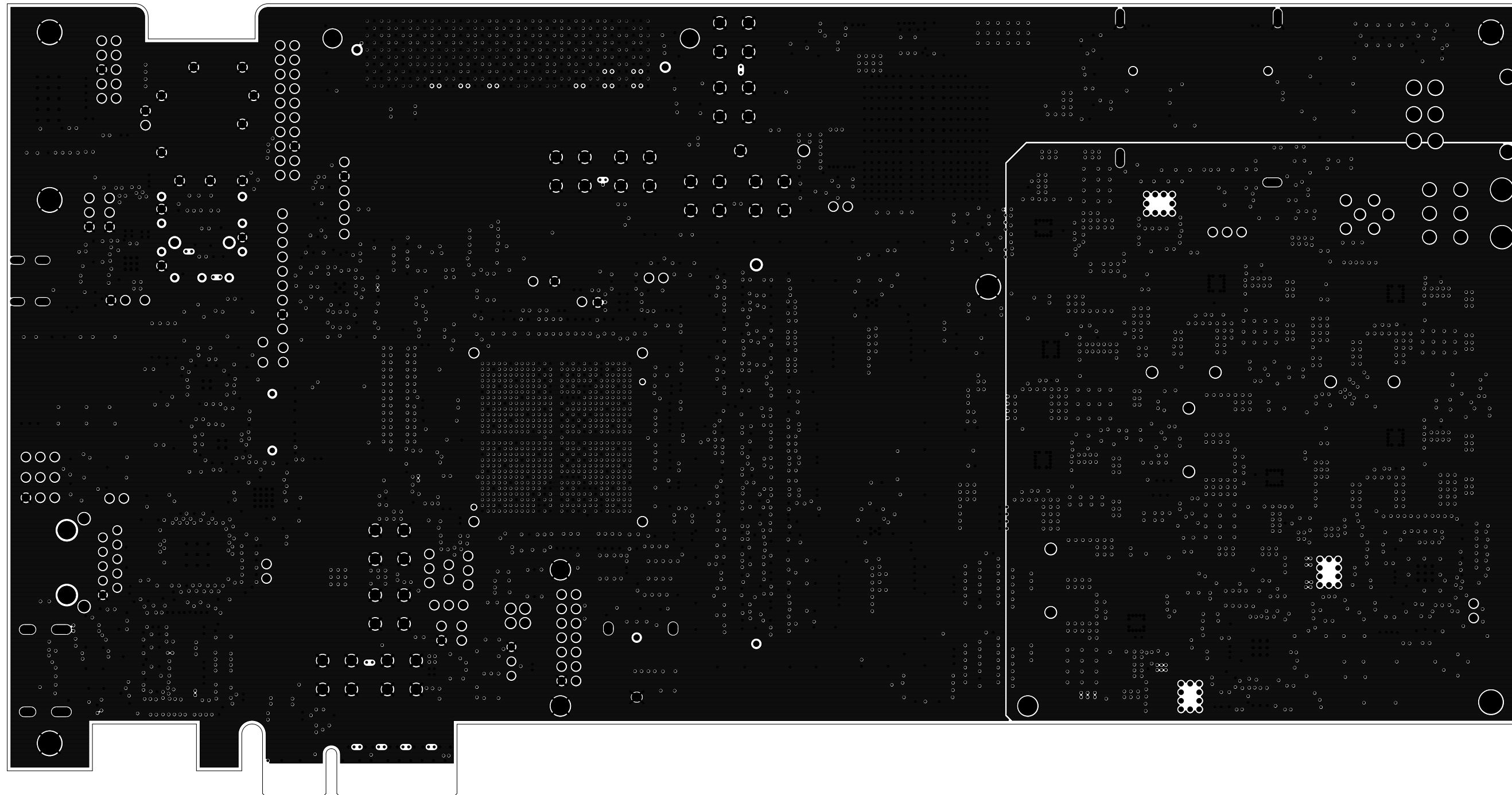
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	Layer: PWR2	Sheet: 10	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




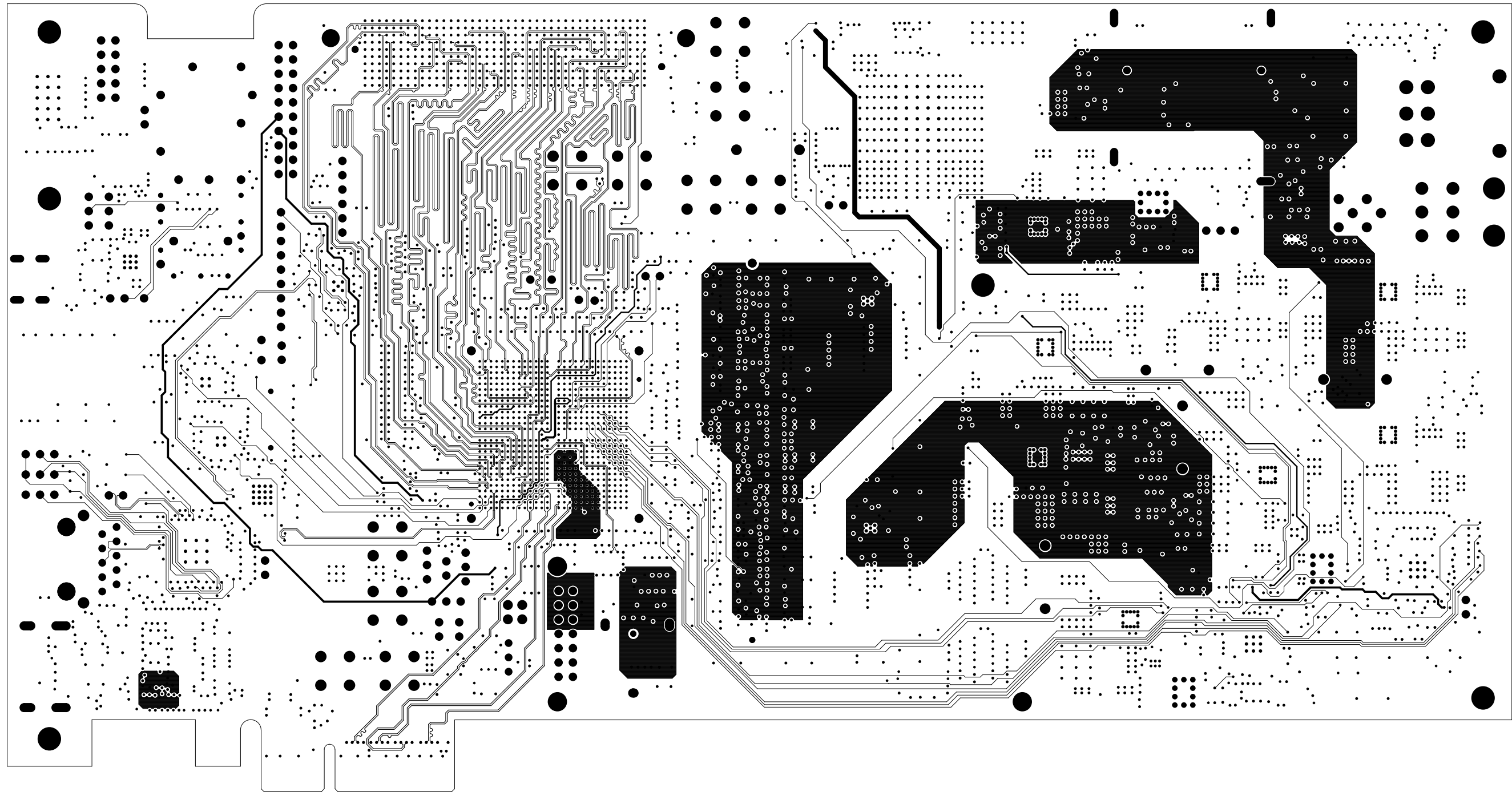
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	Layer: GND5	Sheet: 11	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




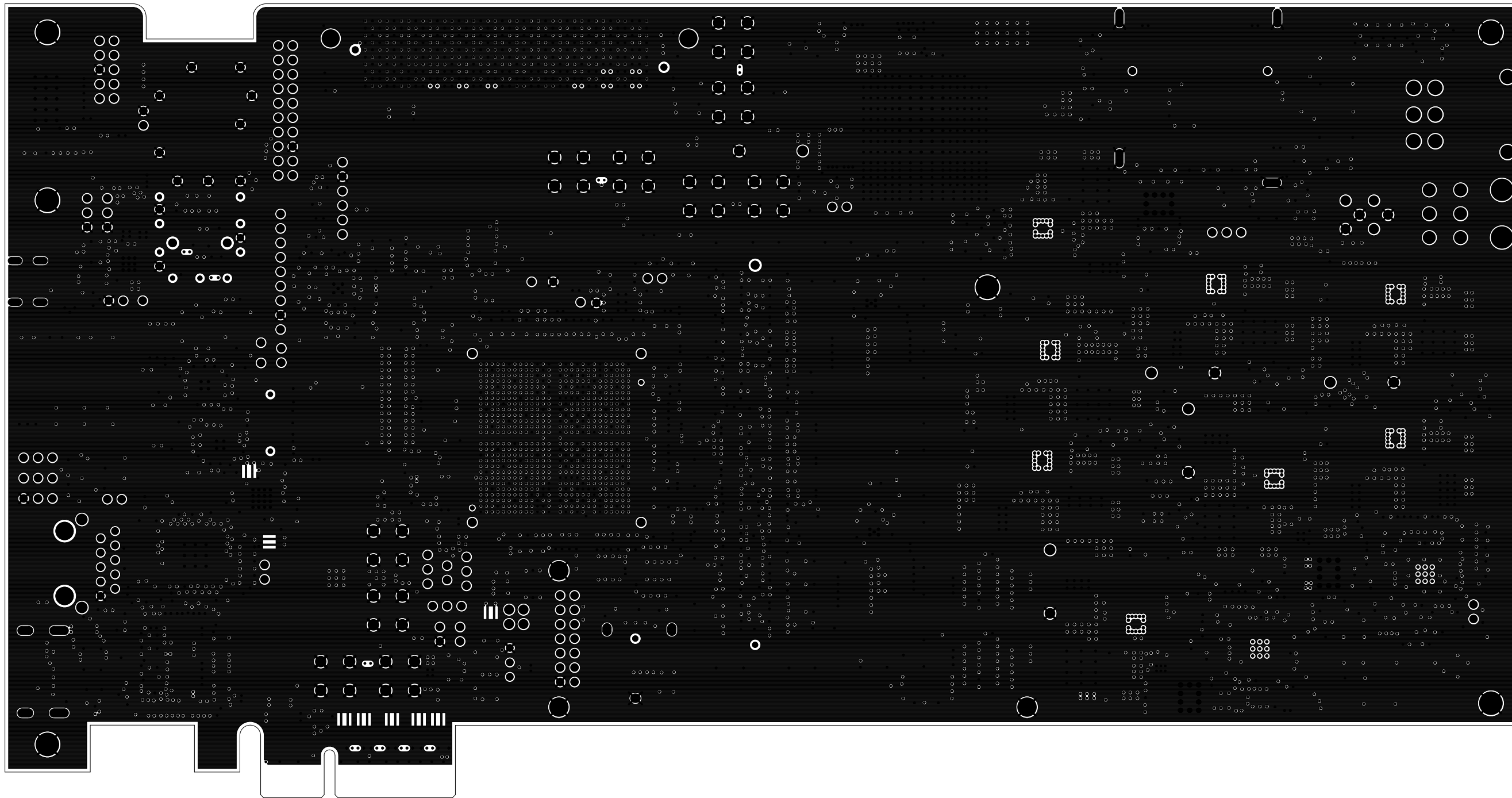
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	Layer: SIG4	Sheet: 12	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




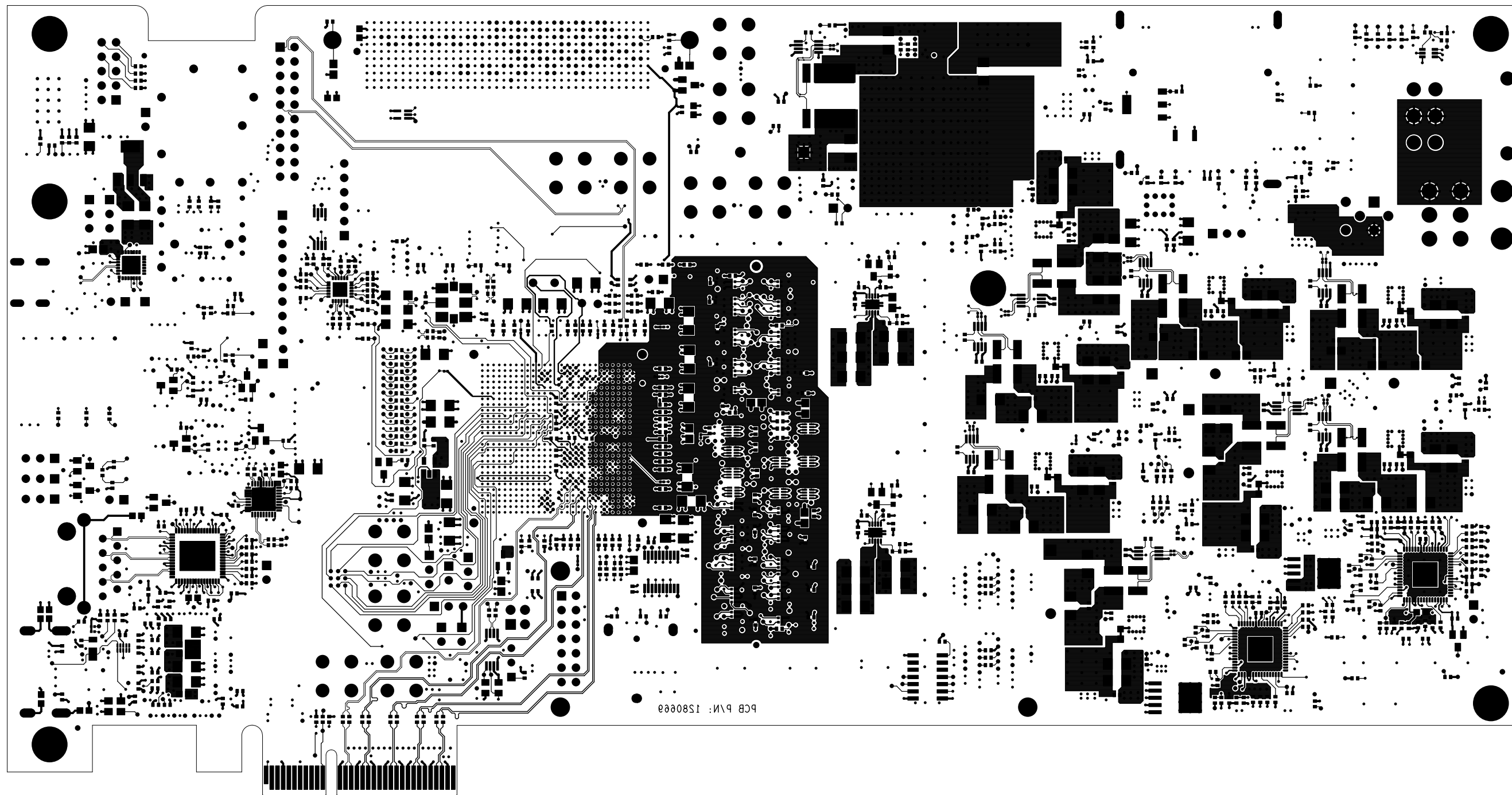
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	Layer: GND6	Sheet: 13	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




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	Layer: SIG5	Sheet: 14	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		

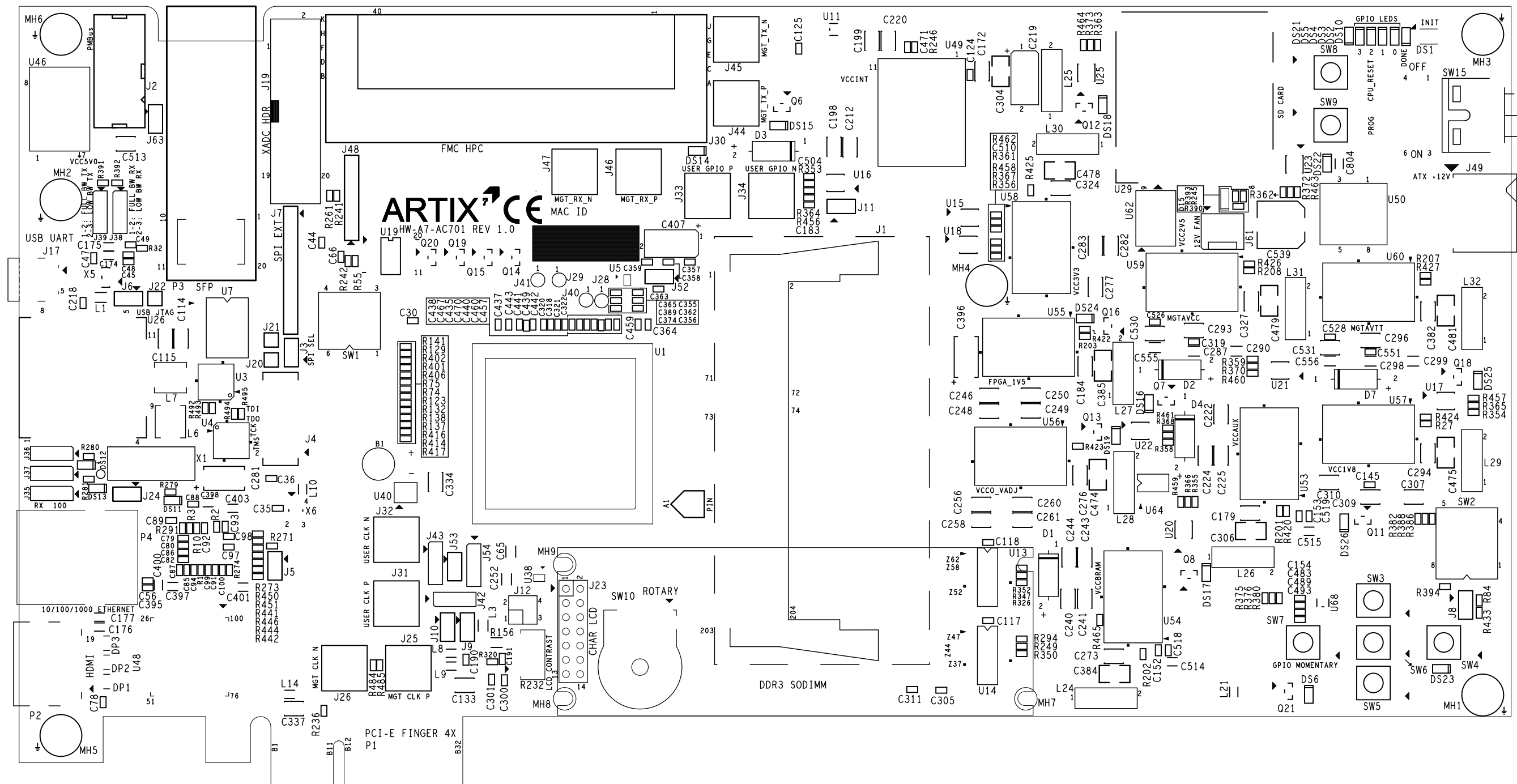



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	Layer: GND7	Sheet: 15	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		

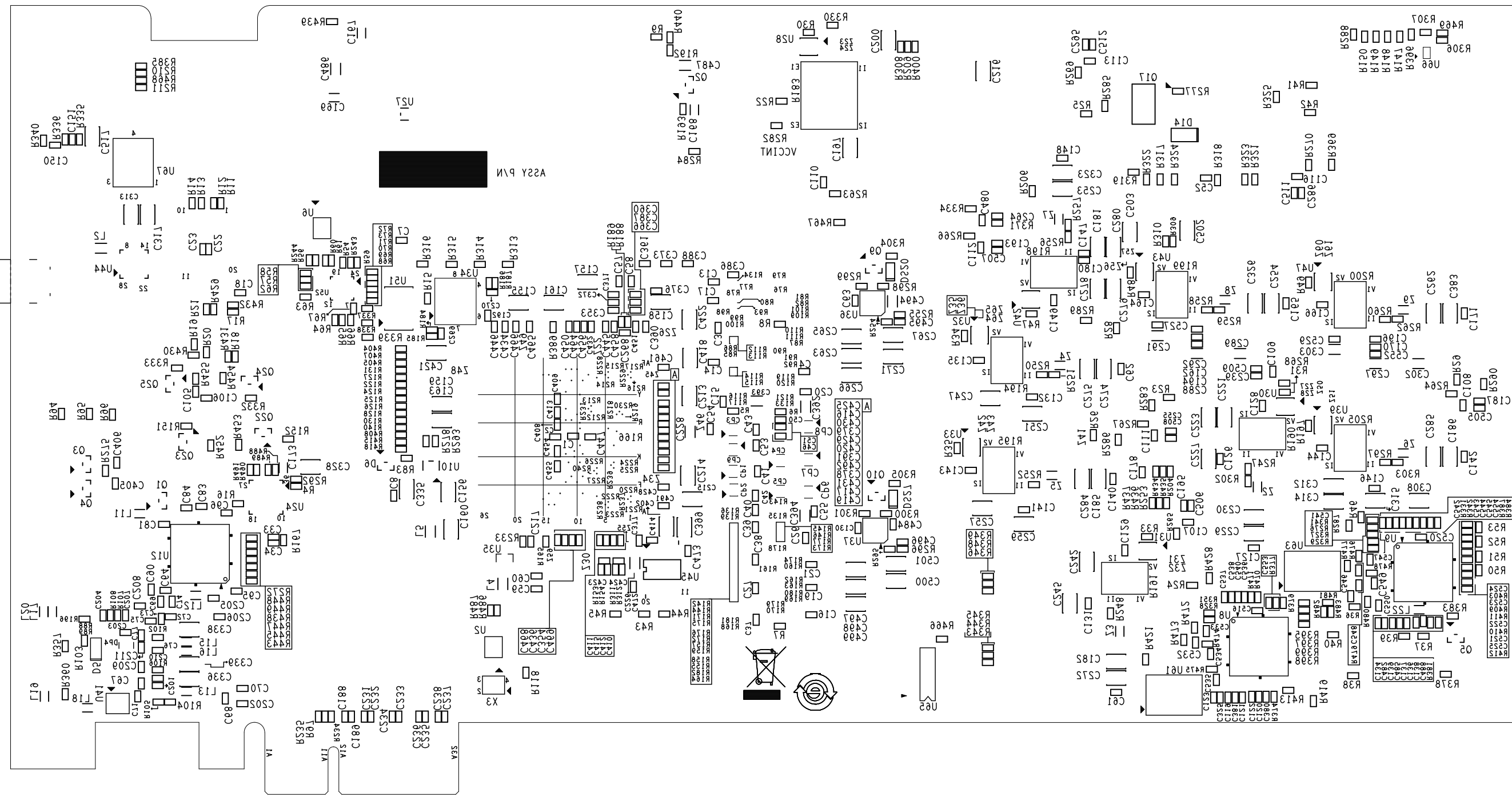



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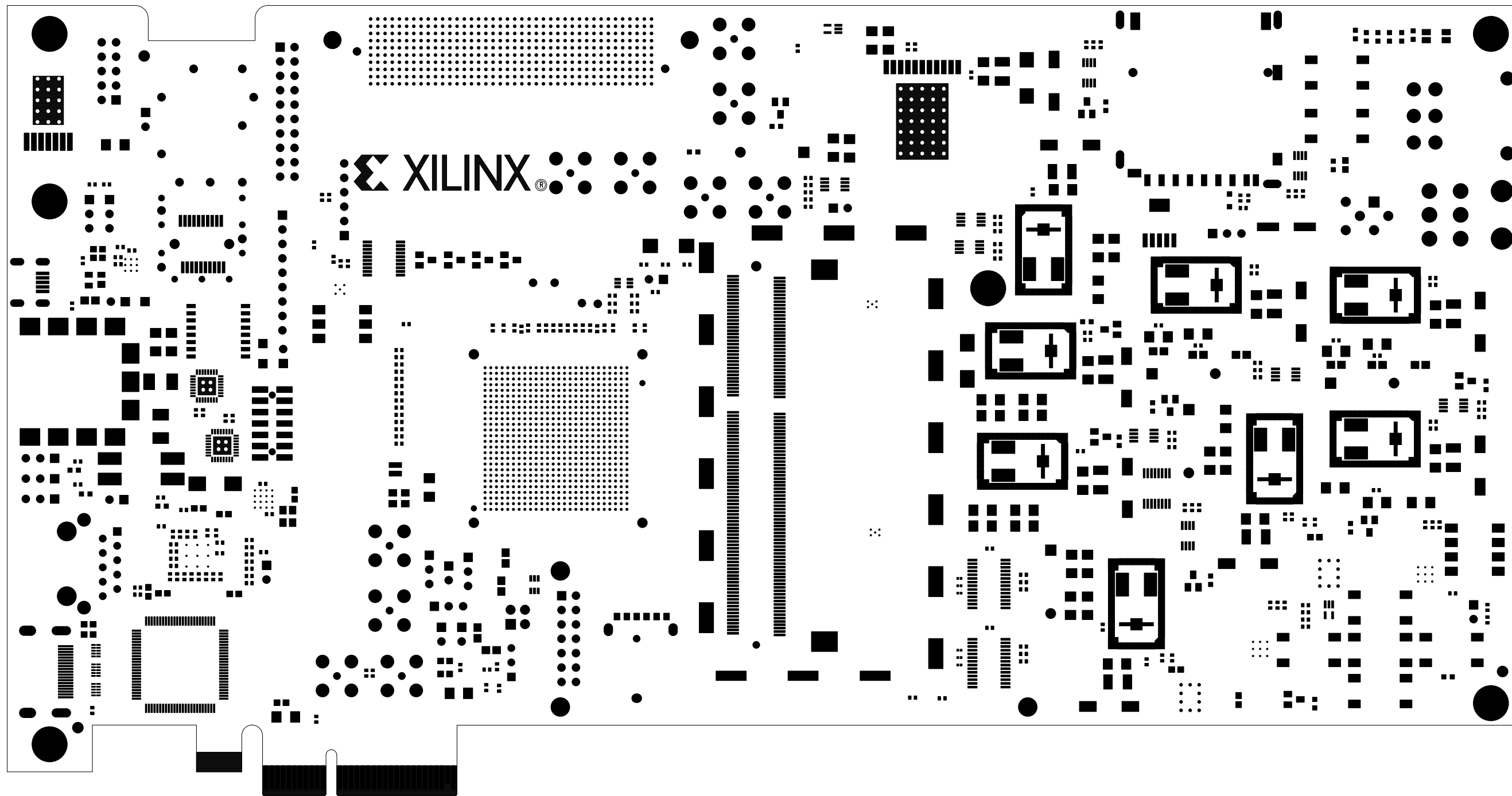
 <p>DESIGNED BY: WHIZZ SYSTEMS INC. 3240 SCOTT BLVD. SANTA CLARA, CA 95054 www.whizzsystems.com TEL:408-980-0400 FAX:408-980-1555</p>	This document contains information which is the proprietary property of Xilinx Inc. This document may not be disclosed to third parties without the prior written consent of Xilinx, Inc.			
	Layer: BOTTOM	Sheet: 16	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




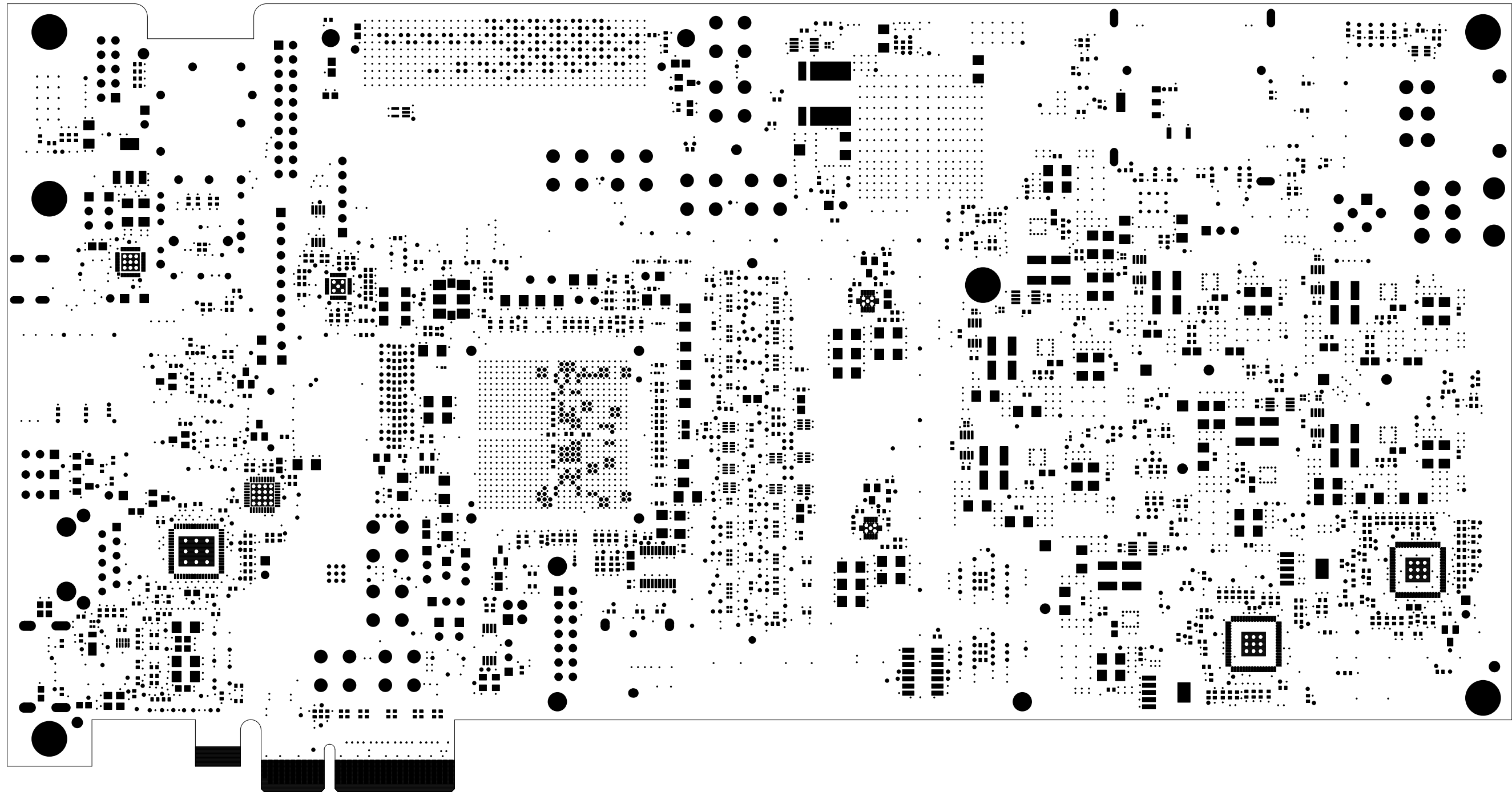
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	Layer: 17_SILK_TOP	Sheet: 17	of: 24	
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.		
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701		
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




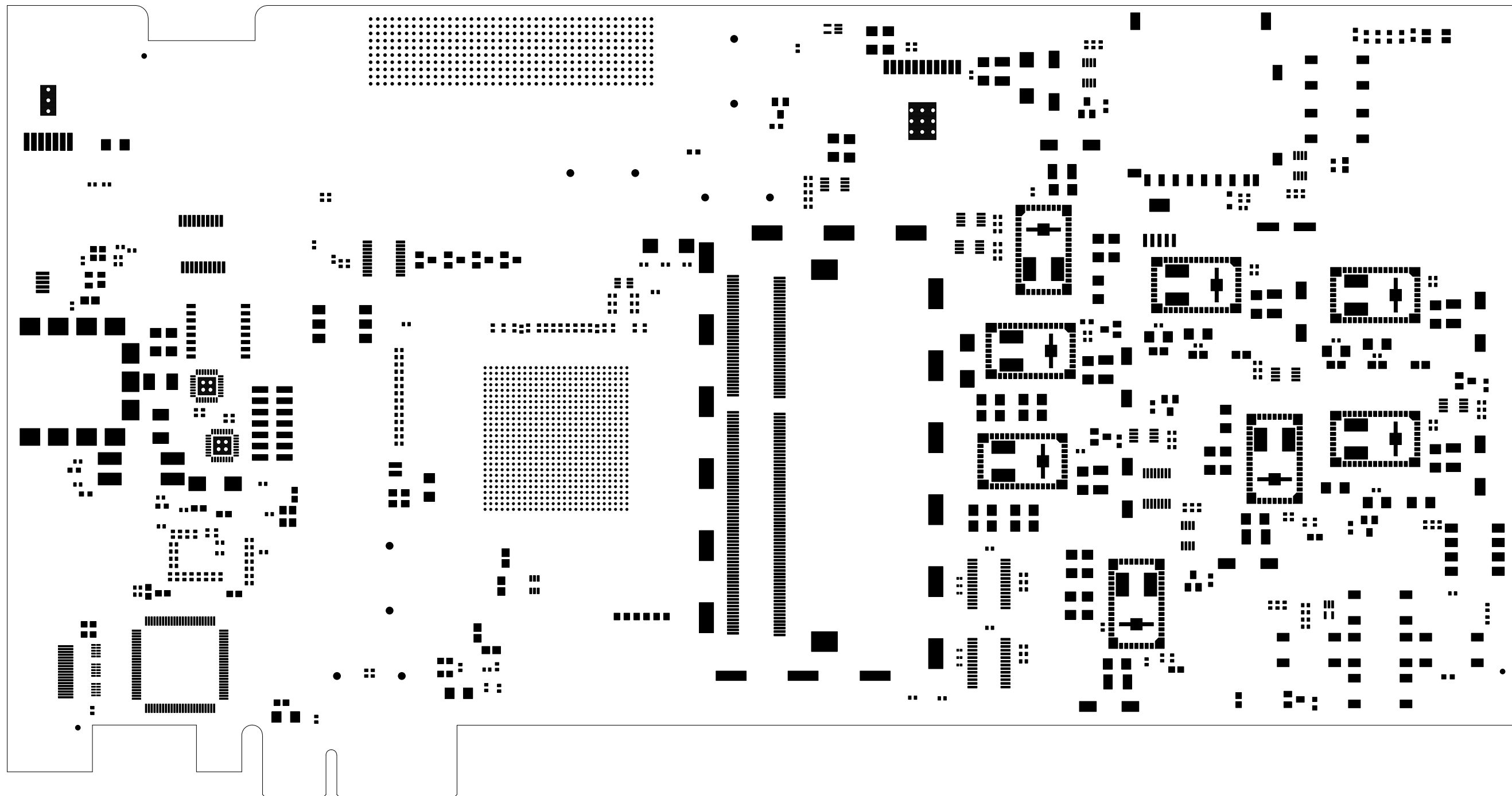
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DESIGNED BY: WHIZZ SYSTEMS INC. 3240 SCOTT BLVD. SANTA CLARA, CA 95054 www.whizzsystems.com		Layer: 18_SILK_BOT		Sheet: 18 of: 24			
		Layout Engineer: FAWAD MUNAWAR		Company Name: XILINX, INC.			
		Design Engineer: DAVID NAYLOR		Project Name: HW-A7-AC701			
Whizz Job Number: 5962		Date: 10.12.2012		Project Number: 1280669		REV. 1.0	
LATEST NETLIST REV OR DATE: 09.22.2012				LATEST MECHANICAL REV OR DATE: respin of 5771			




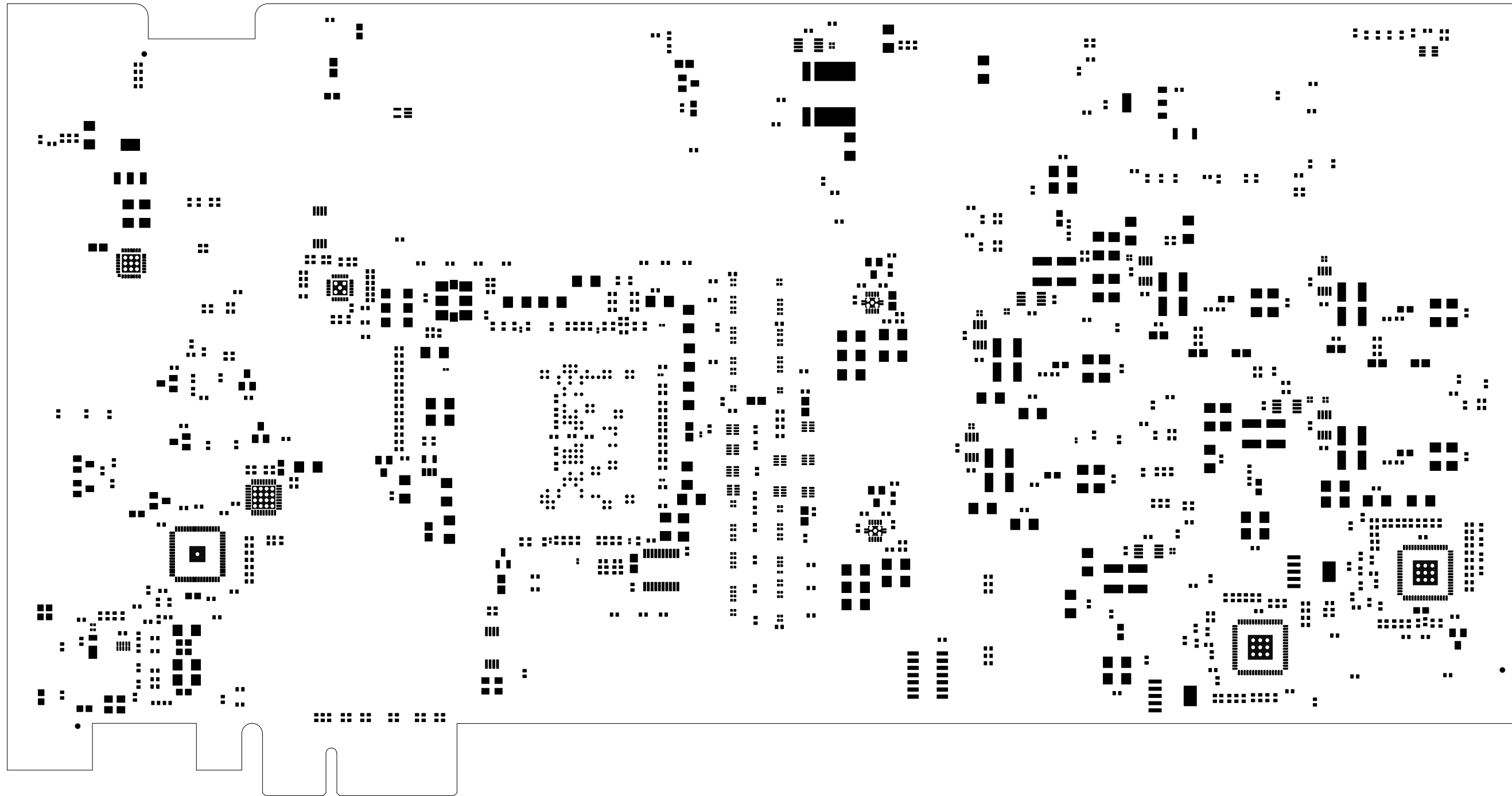
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	Layer: 19_SMASK_TOP		Sheet: 19 of: 24	
	Layout Engineer: FAWAD MUNAWAR		Company Name: XILINX, INC.	
	Design Engineer: DAVID NAYLOR		Project Name: HW-A7-AC701	
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




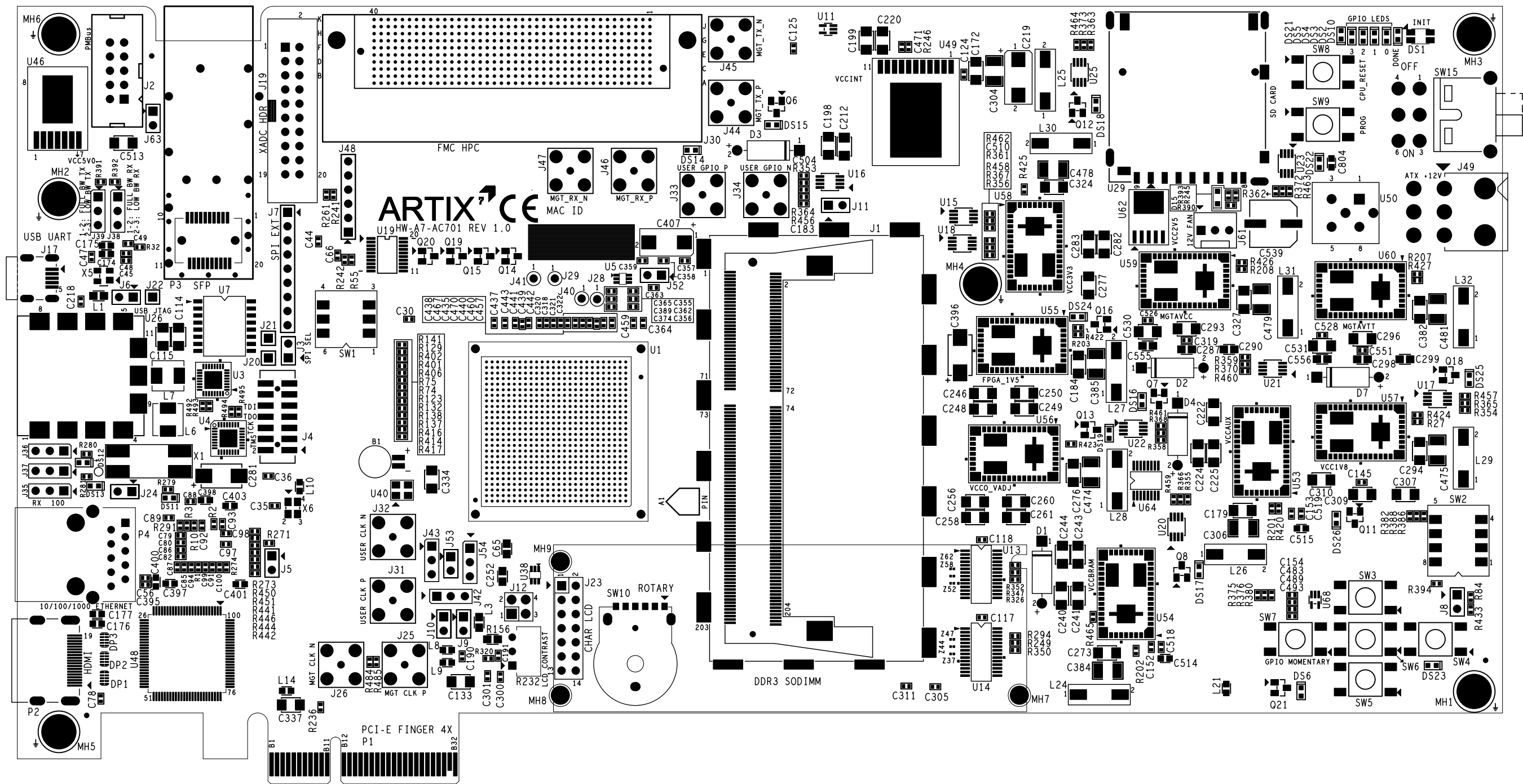
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	Layer: 20_SMASK_BOT		Sheet: 20 of: 24	
	Layout Engineer: FAWAD MUNAWAR		Company Name: XILINX, INC.	
	Design Engineer: DAVID NAYLOR		Project Name: HW-A7-AC701	
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




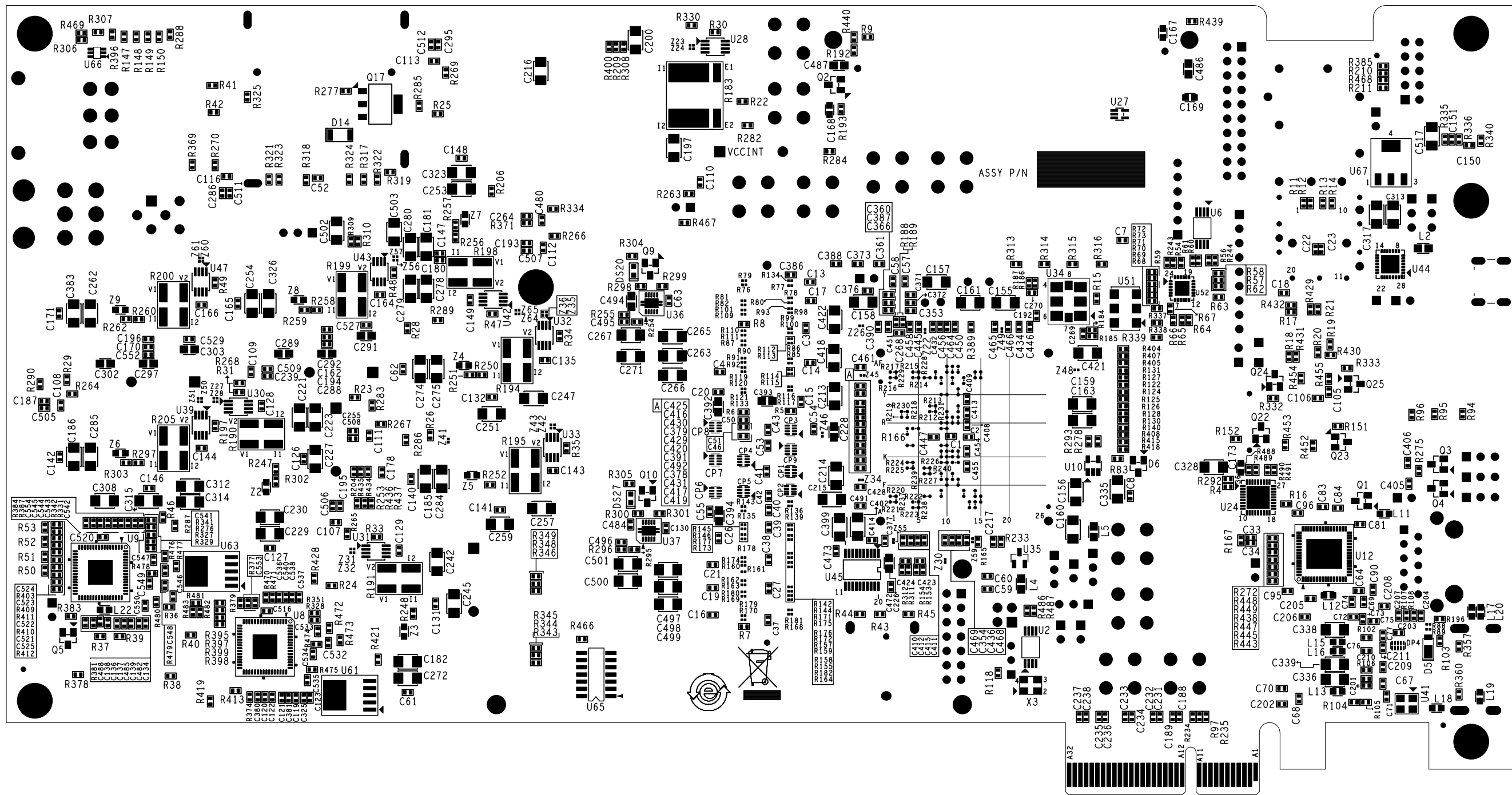
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	Layer: 21_PMASK_TOP		Sheet: 21 of: 24	
	Layout Engineer: FAWAD MUNAWAR		Company Name: XILINX, INC.	
	Design Engineer: DAVID NAYLOR		Project Name: HW-A7-AC701	
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		




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	Layer: 22_PMASK_BOT		Sheet: 22 of: 24	
	Layout Engineer: FAWAD MUNAWAR		Company Name: XILINX, INC.	
	Design Engineer: DAVID NAYLOR		Project Name: HW-A7-AC701	
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 1280669	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771		



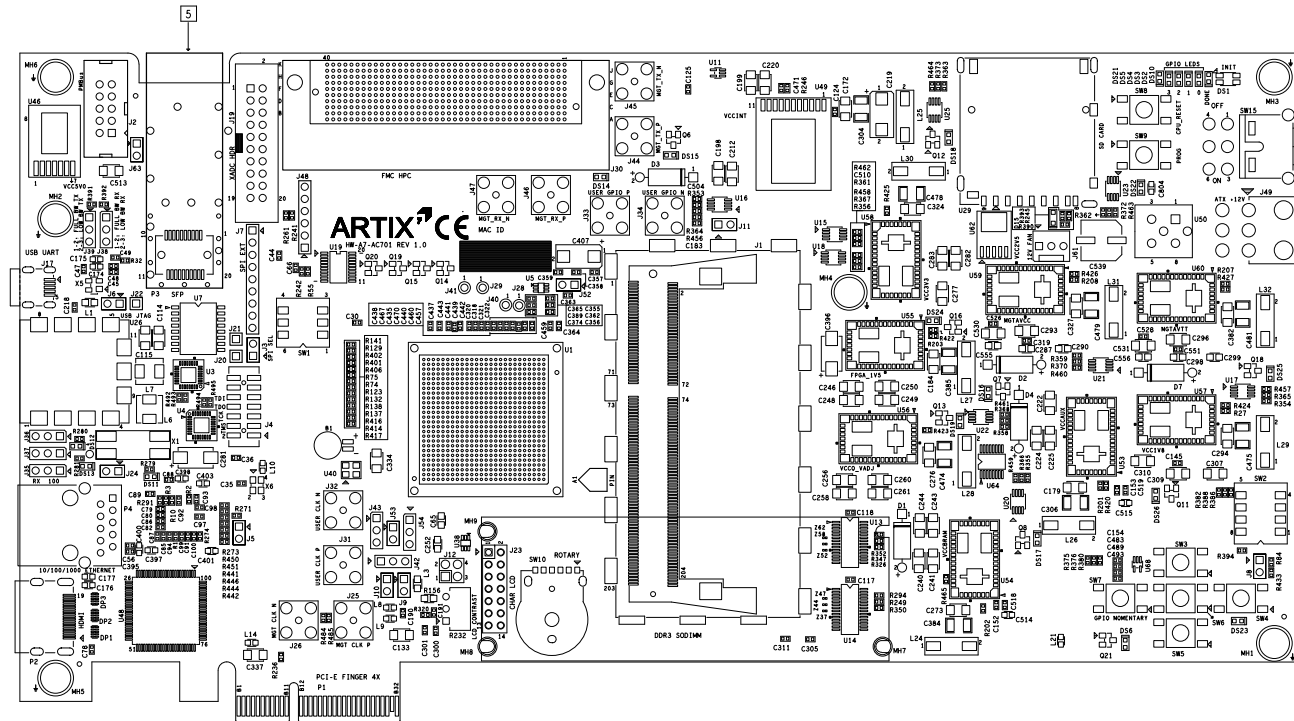
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	Layer: 23_ASSY_TOP	Sheet: 23	of: 24
	Layout Engineer: FAWAD MUNAWAR	Company Name: XILINX, INC.	
	Design Engineer: DAVID NAYLOR	Project Name: HW-A7-AC701	
	Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 0431747
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771	



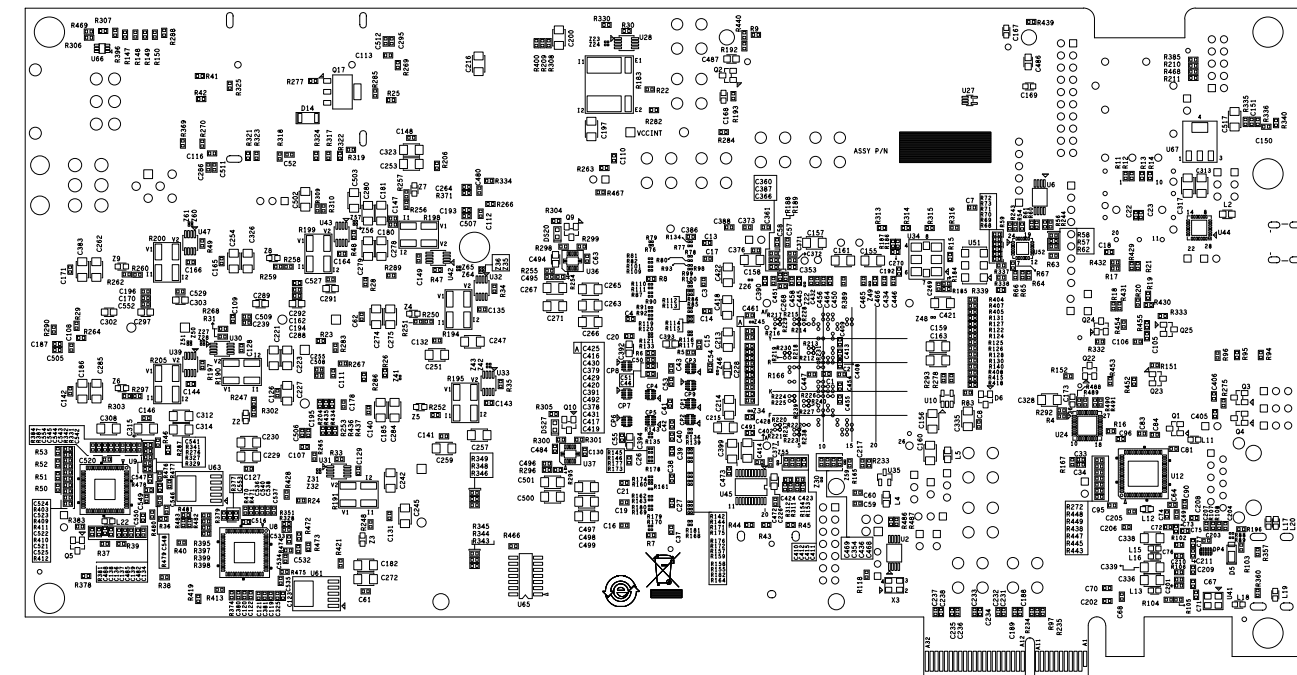
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		Layer: 24_ASSY_BOT	Sheet: 24 of: 24
Layout Engineer: FAWAD MUNAWAR		Company Name: XILINX, INC.	
Design Engineer: DAVID NAYLOR		Project Name: HW-A7-AC701	
Whizz Job Number: 5962	Date: 10.12.2012	Project Number: 0431747	REV. 1.0
LATEST NETLIST REV OR DATE: 09.22.2012		LATEST MECHANICAL REV OR DATE: respin of 5771	

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
	1.0	INITIAL RELEASE	10/12/12	DN


- NOTES: (UNLESS OTHERWISE SPECIFIED)
1. ASSEMBLE TO MEET INSPECTION CRITERIA OF IPC-A-610, CLASS 2, CURRENT REVISION.
 2. BAG AND TAG (OR MARK CONTAINER) WITH PART NUMBER.
 3. REFERENCE SCHEMATIC (OR AVL) FOR BILL OF MATERIALS.
 4. ASSEMBLY SHOULD BE "ROHS" AND "LEAD FREE" COMPLIANT.
 5. P3 IS PRESS FIT CONNECTOR.



VIEWED FROM TOP SIDE



VIEWED FROM BOTTOM SIDE

UNLESS OTHERWISE SPECIFIED: ALL DIMENSIONS ARE IN INCHES TOLERANCES ON: LINEAR DIMENSIONS ANGLES .XXX ± .010 XX.XX ± 0.30 .XX ± .030 XX.X ± 1.0 X ± 1.00 XX ± 5.0	APPROVAL	DATE	CUSTOMER:	
	DRAWN: Fawad Munwar		10.12.12	XILINX, INC
RAWING SYMBOLS & TOLERANCE PER ANS1Y14.5M-1994  WHIZZ SYSTEMS INC. 3240 SCOTT BLVD SANTA CLARA, CA 95054 TEL: 408-980-0400	CHECKED: Rashid Mehmood	10.12.12	PROJECT NAME:	
	ENGR: David Naylor		10.12.12	HW-A7-AC701
APPROVAL: David Naylor		10.12.12	SIZE: D	ASSY DWG. NO.
WHIZZ JOB NO. 5962		SCALE: 1:1	0431747	REV. 1.0 SHEET 1 OF 1

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