PMP40260 Test Results

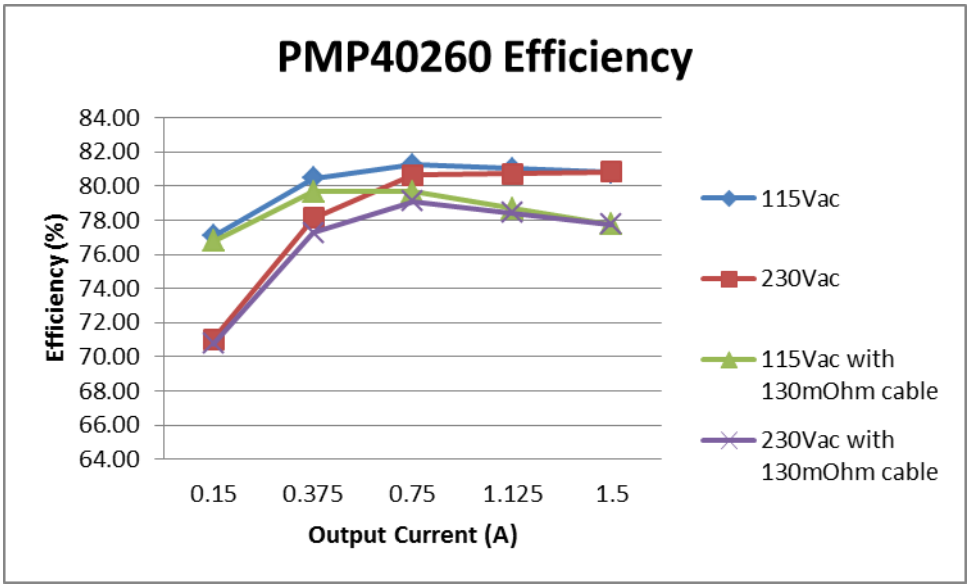
1. INPUT CHARACTERISTICS

1.1 STANDBY POWER

Vin (Vac)	Input Power(mW)
85	37.2
115	38.9
132	39.8
180	46.6
230	57.2
264	58.6

1.2 EFFICIENCY DATA

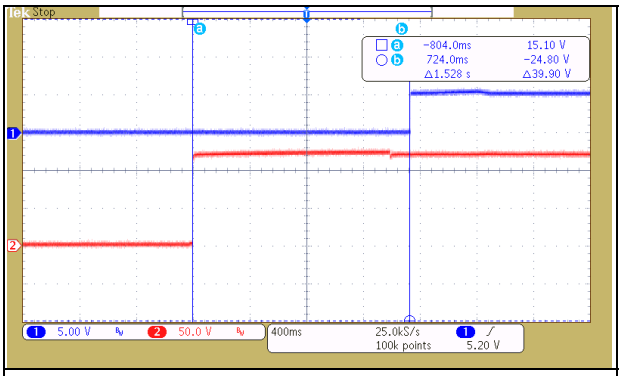
Vin (Vac)	Pin (W)	Io (A)	Vbus1 (V)	Vbus (V)	AC/DC Eff (%)	Board end Eff (%)	Board end Ave_Eff (%)	Ave_Eff with 130mOhm cable (%)	COC V5 Tier 2 2016 standard eff (%)
115V/60Hz		0	5.050	5.050					
	0.98	0.15	5.048	5.036	77.27	77.08	77.08	76.78	67.7
	2.34	0.372	5.090	5.060	80.92	80.44	80.87	78.95	76.9
	4.7	0.75	5.152	5.091	82.21	81.24			
	7.118	1.125	5.217	5.126	82.46	81.02			
	9.585	1.5	5.285	5.163	82.70	80.8			
230V/50Hz		0	5.051	5.051					
	1.064	0.15	5.051	5.039	71.21	71.04	71.04	70.76	67.7
	2.404	0.371	5.093	5.063	78.60	78.14	80.08	78.14	76.90%
	4.735	0.749	5.160	5.099	81.62	80.66			
	7.148	1.125	5.220	5.129	82.16	80.72			
	9.574	1.499	5.283	5.162	82.72	80.82			



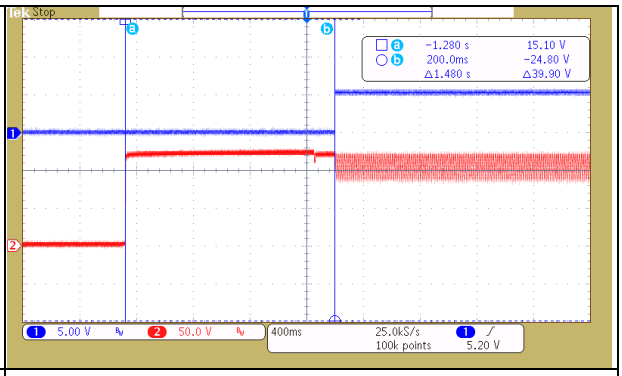
2. OUTPUT CHARACTERISTICS

2.1 Turn on delay time

Input voltage	Output current	Turn on delay time	Pass/Fail
85Vac 47Hz	0A	**1.528S**	**Pass**
85Vac 47Hz	1.5A	**1.480S**	**Pass**



Vin: 85Vac Io: 0A
Ch1: Output voltage, 5V/div
Ch2: The voltage after bridge diode, 50V/div



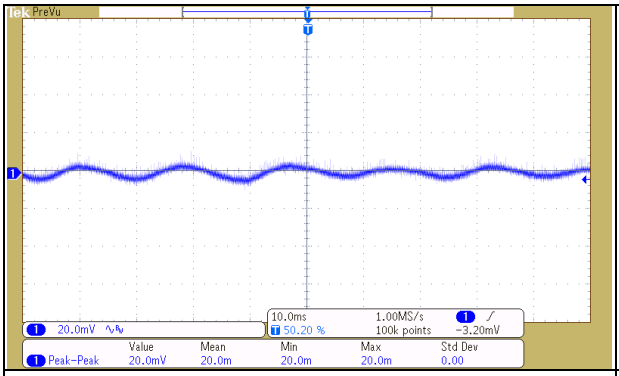
Vin: 85Vac Io: 1.5A
Ch1: Output voltage, 5V/div
Ch2: The voltage after bridge diode, 50V/div

2.2 RIPPLE VOLTAGE

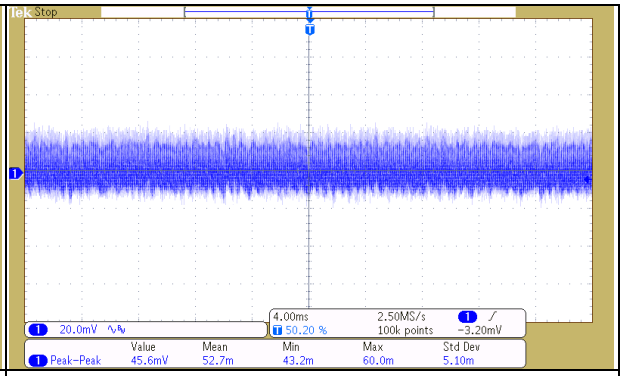
Test at board end

Input voltage	Output current	Ripple voltage
115Vac	0A	**20.0mV**
115Vac	0.5A	**45.6mV**

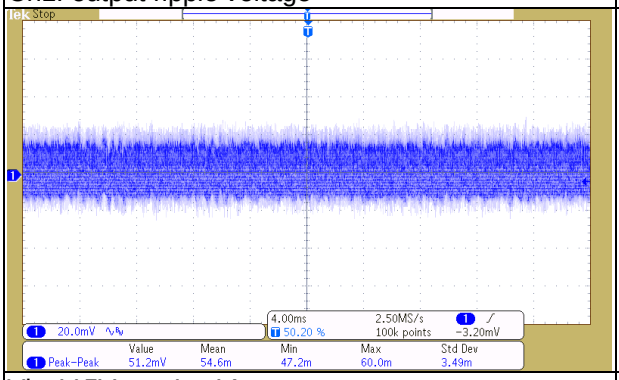
115Vac	1A	**51.2mV**
115Vac	1.5A	**56.8mV**
230Vac	0A	**15.2mV**
230Vac	0.5A	**48.0mV**
230Vac	1A	**55.2mV**
230Vac	1.5A	**57.6mV**



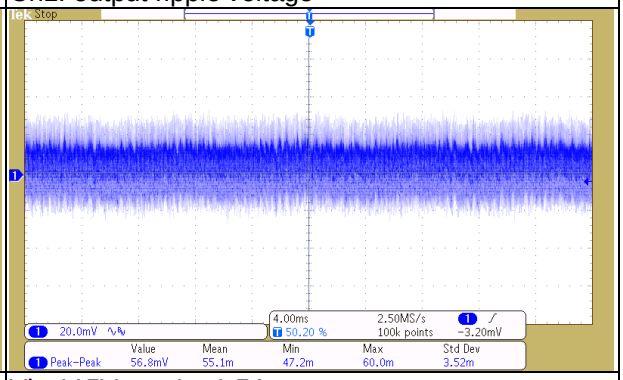
Vin:115Vac Io: 0A
Ch1: output ripple voltage



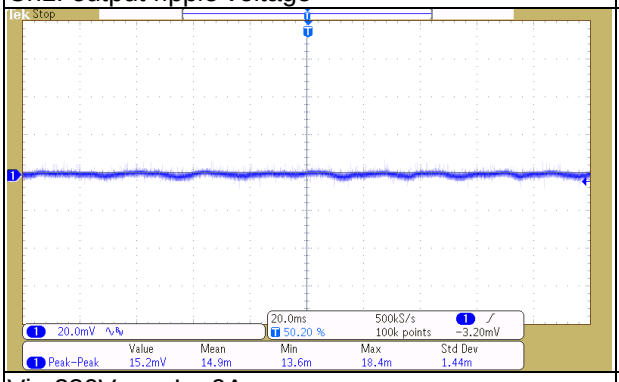
Vin:115Vac Io: 0.5A
Ch1: output ripple voltage



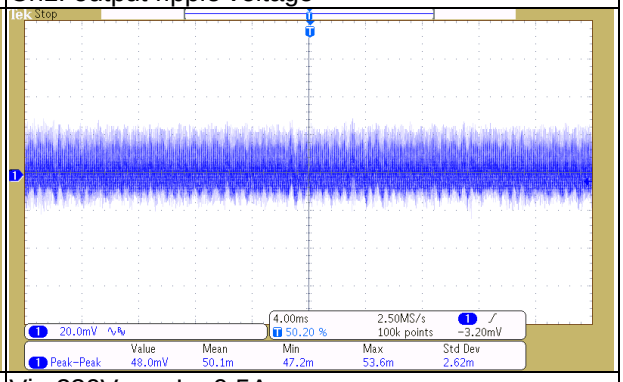
Vin:115Vac Io: 1A
Ch1: output ripple voltage



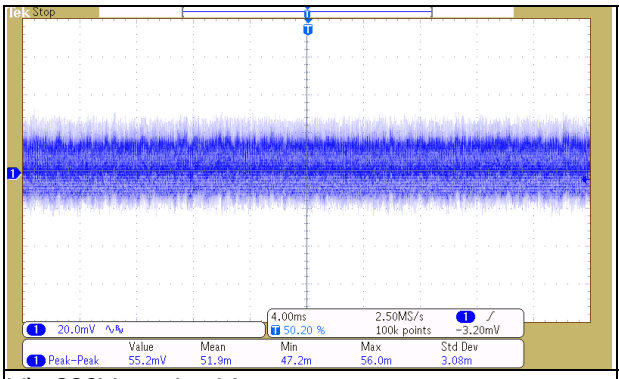
Vin:115Vac Io: 1.5A
Ch1: output ripple voltage



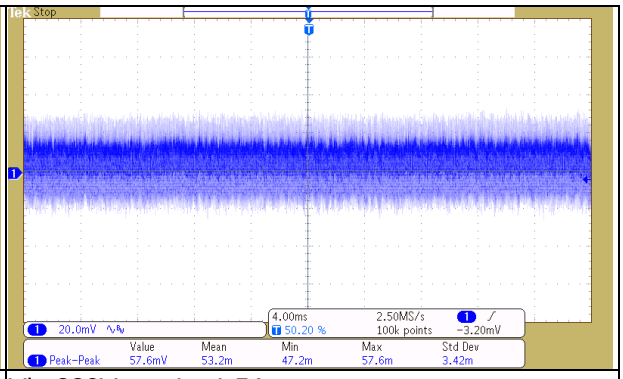
Vin:230Vac Io: 0A
Ch1: output ripple voltage



Vin:230Vac Io: 0.5A
Ch1: output ripple voltage



Vin:230Vac Io: 1A
Ch1: output ripple voltage

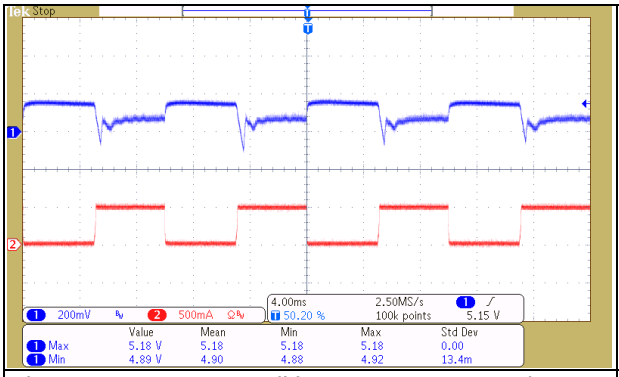


Vin:230Vac Io: 1.5A
Ch1: output ripple voltage

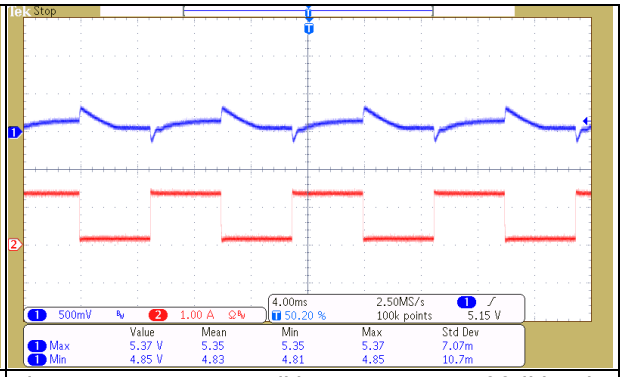
2.3 DYNAMIC RESPONSE

Test at board end

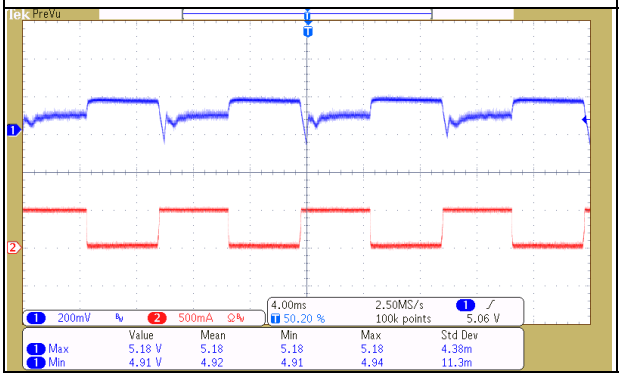
Input voltage	Output current	Min voltage	Max voltage
115Vac	0~500mA	**4.89V**	**5.18V**
115Vac	10%-90% of full load	**4.85V**	**5.37V**
230Vac	0~500mA	**4.91V**	**5.18V**
230Vac	10%-90% of full load	**4.83V**	**5.35V**

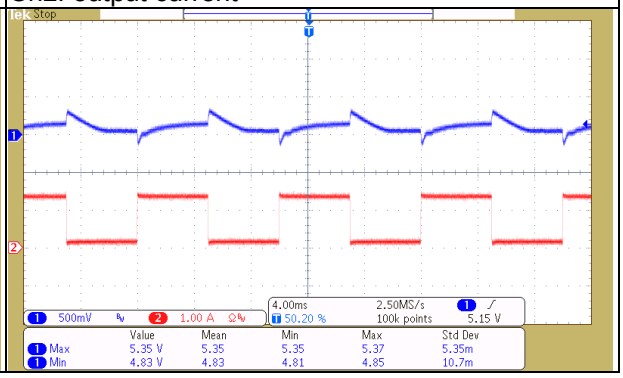


Vin:115Vac test condition: 0-500mA, 0.1A/us,
Ch1: output voltage
Ch2: output current



Vin:115Vac test condition: 10%-90% of full load, 0.1A/us
Ch1: output voltage
Ch2: output current

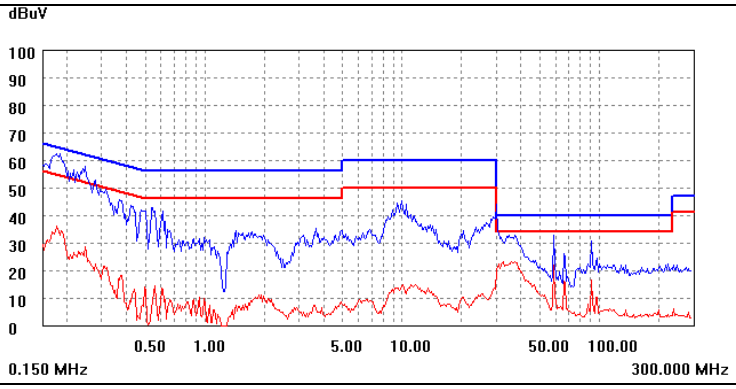




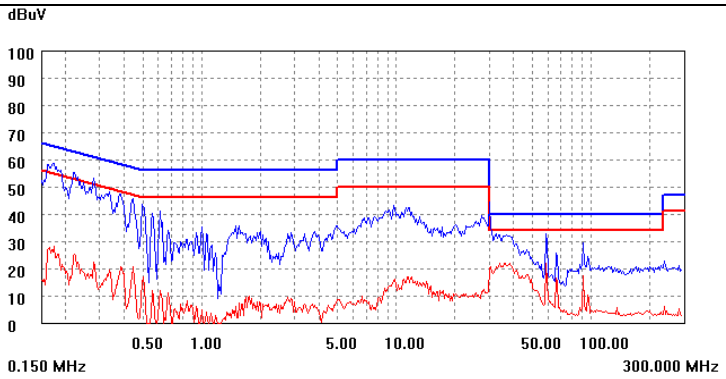
Vin: 230Vac test condition: 0-500mA, 0.1A/us, Ch1: output voltage Ch2: output current	Vin: 230Vac test condition: 10%-90% of full load, 0.1A/us Ch1: output voltage Ch2: output current

3. EMI Test

3.1 Conduction EMI

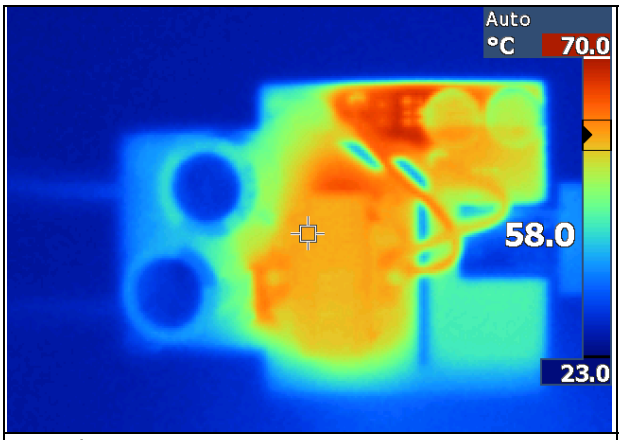
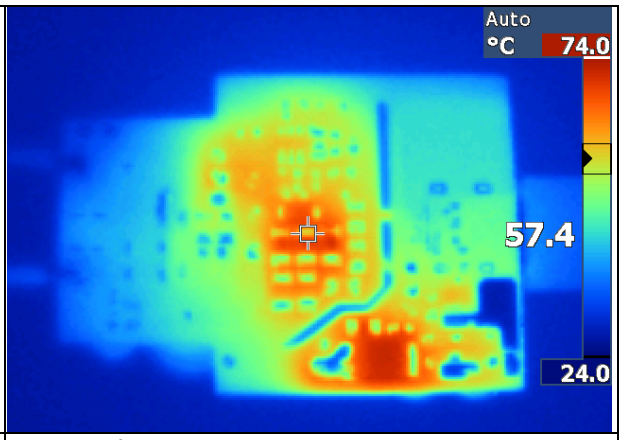


Vin: 115Vac Io: 1.5A



Vin: 230Vac Io: 1.5A

4. Thermal Test

	
Top View Vin: 230Vac Io: 1.5A	Bottom View Vin: 230Vac Io: 1.5A

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