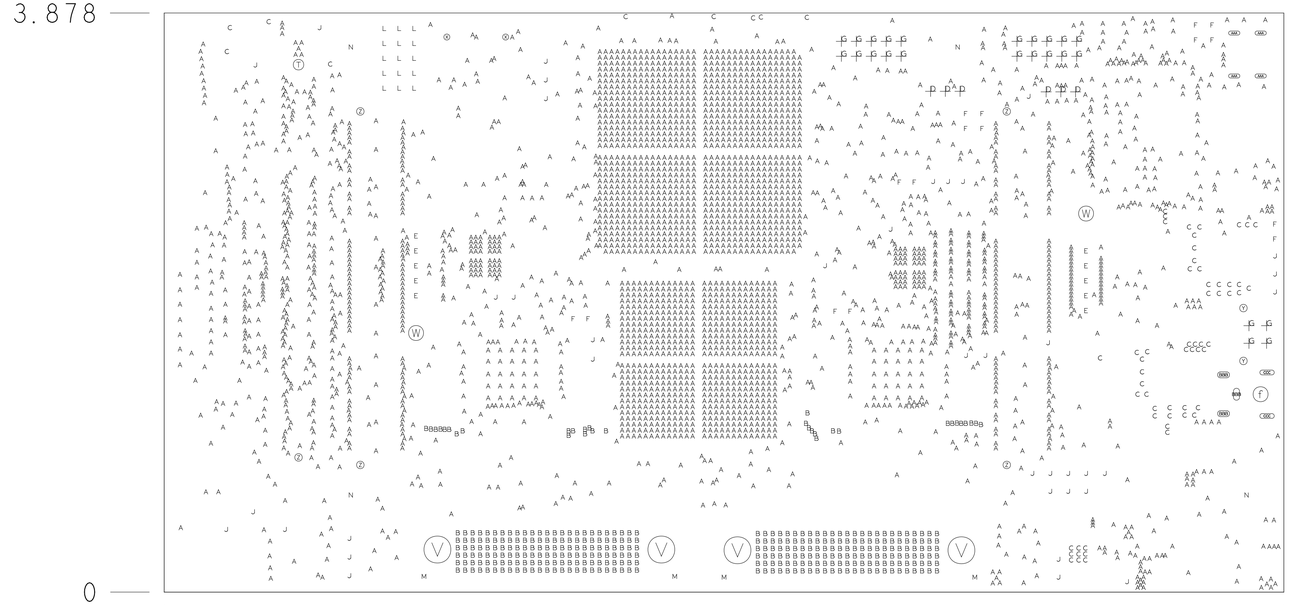
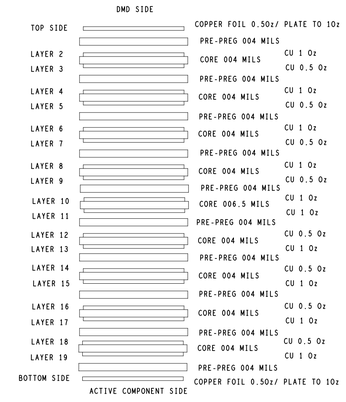
DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
A	8.0	+3.0/-3.0	PLATED	3637
B	10.0	+3.0/-3.0	PLATED	332
C	12.0	+3.0/-3.0	PLATED	66
D	27.95	+1.97/-1.97	PLATED	6
E	32.0	+3.0/-3.0	PLATED	10
F	36.0	+3.0/-3.0	PLATED	16
G	40.0	+2.0/-2.0	PLATED	24
J	40.0	+3.0/-3.0	PLATED	38
L	41.0	+3.0/-3.0	PLATED	15
M	66.0	+3.0/-3.0	PLATED	4
N	140.0	+3.0/-3.0	PLATED	4
⑥	39.0	+3.0/-3.0	NON-PLATED	2
⑤	40.0	+2.0/-2.0	NON-PLATED	2
⑦	44.0	+3.0/-3.0	NON-PLATED	5
Ⓣ	64.0	+3.0/-3.0	NON-PLATED	1
Ⓦ	95.0	+3.0/-3.0	NON-PLATED	2
ⓕ	96.0	+2.0/-2.0	NON-PLATED	1
Ⓥ	180.0	+10.0/-10.0	NON-PLATED	4
AAD	75.0x27.0	+2.0/-2.0	PLATED	4
AAE	80.0x40.0	+2.0/-2.0	PLATED	1
AAG	80.0x40.0	+2.0/-2.0	PLATED	2
AAH	96.0x32.0	+2.0/-2.0	PLATED	2



THIS PRODUCT MUST BE RoHS COMPLIANT. VENDORS MUST FOLLOW DIRECTIVE 2002/95/EC OF THE EUROPEAN PARLIAMENT, ALSO KNOWN AS THE "RoHS DIRECTIVE".

NOTES:

1. ALL FABRICATION ITEMS MUST MEET OR EXCEED BEST INDUSTRY PRACTICE. IPC-A-600C (COMMERICAL STD.).
2. FABRICATOR AND ASSEMBLER MUST FOLLOW IPC-1066 FOR ALL MARKING AND LABELING.
3. LAMINATE MATERIAL: POLYCLAD PC-FR-370HR OR ISOLA IS410 MATERIAL.
4. FINISHED THICKNESS: 0.100 +/- 0.007"
5. SOLDERMASK BOTH SIDES: RED
6. APPLY WHITE LPI SILKSCREEN TO BOTH SIDES PER ARTWORK.
7. MAXIMUM WARP AND TWIST TO BE 0.005 INCH PER INCH.
8. a. LAYERS 3,16 AND 18 HAVE 100ohm %5 DIFF PAIR AND USE THE FOLLOWING: 4.01MIL TRACE WITH 6MIL SPACE.
   b. LAYERS 3,5,7,12,14,16 AND 18 HAVE 100ohm %5 DIFF PAIR AND USE THE FOLLOWING: 3.99MIL TRACE WITH 6MIL SPACE.
9. a. LAYERS TOP,3,5,7,9,12,14,16,18 AND BOTTOM HAS 50ohm 5% SINGLE ENDED ROUTING AND USE THE FOLLOWING: 3.99MIL TRACE WIDTH.
   b. LAYERS 16 AND 18 HAS 50ohm 5% SINGLE ENDED ROUTING AND USE THE FOLLOWING: 4.01MIL TRACE WIDTH.
10. CHANGE ALL 8 AND 10MIL VIAS TO DIRECT PLANE CONNECTIONS.
11. BOARD TO BE FINISHED ENIG. 100-150 NICKEL and 3-5 GOLD
12. BY DESIGN ALL SIGNALS ARE 50ohms. EXCEPTIONS: PWR NETS, DIFF PAIRS, BGA FANOUTS AND DMD FANOUTS.
13. ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS. PCB MUST BARE THE UL94-V0 UL REGISTERED MATERIAL ID NUMBER.
14. 0.008" VIAS TO BE COVERED WITH LPI SOLDERMASK ON BOTTOM(TESTER) SIDE OF THE BOARD.



TOTAL BOARD THICKNESS 0.100"

APPROVED	
CHECKED	
DRAFTING	
DATE 06/09/2023	ENGR
DESIGN ENGR	TOLERANCES UNLESS OTHERWISE SPECIFIED
PROJECT ENGR	X.XX ± 0.01 X.XXX ± 0.005 ANGLES ± 1/2°
ENGR MGR	DO NOT SCALE DRAWING
NEXT ASSEMBLY	

TEXAS INSTRUMENTS

FABRICATION DRAWING

DLPLCRC410EVM

SCALE NONE

SHEET 1 OF 1

SIZE D