

AUTOMOTIVE PRU1 ICSSM RGMII/MII ADD ON BOARD  
PROC168E1

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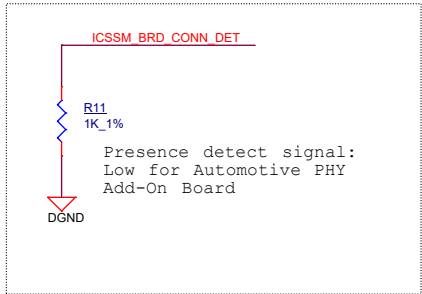
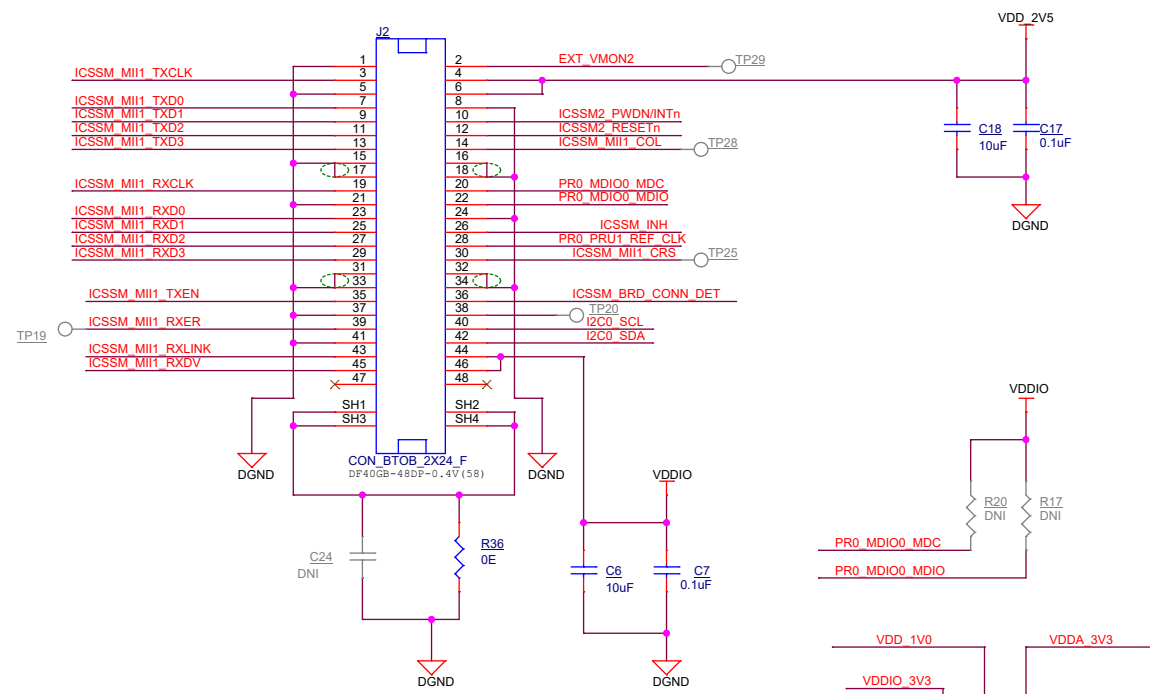
REV	E1
VER	

REVISION HISTORY

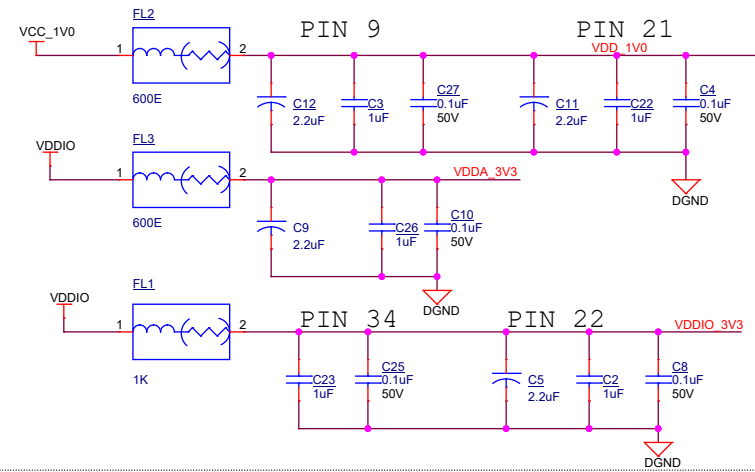
VER #	DATE	DESCRIPTION OF CHANGES	AUTHOR	REVIEWED BY	APPROVED BY
0.01	18 APRIL 2023	Initial Draft	Mistral Design Team	TI	
0.02	3 MAY 2023	Updated for TI's review comments	Mistral Design Team		
0.03	9 JUNE 2023	Added RC filter across ethernet board connector (J2)	Mistral Design Team		
0.04	16 OCT 2023	Added NOTES, HW & LABELS page to include Fiducial marks and CE mark etc	Mistral Design Team		

[illegible]

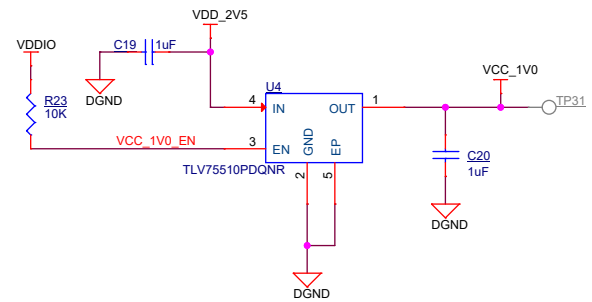
PRU1 ICSSM RGMII/MII Ethernet



Place Near power pins of 10/100/1000 Ethernet PHY

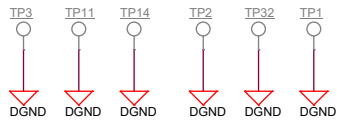


1.0V, 0.5AMPS SUPPLY

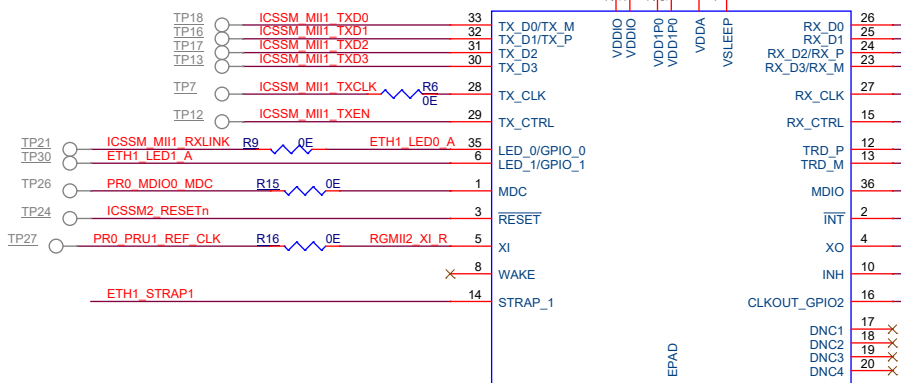


FOR AUTO PHY

GND Test Points



CAD NOTE: Place the TPs on ICSSM lines with minimum stub

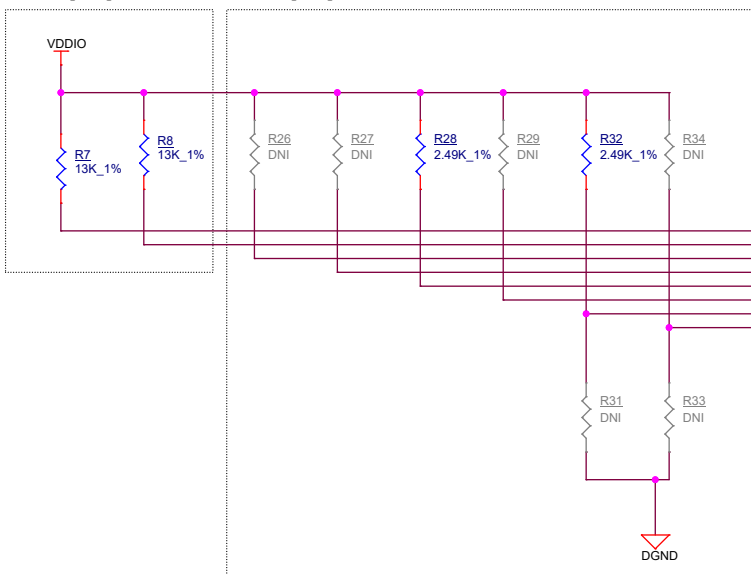


Phy ID: 1100

STRAPPING RESISTORS

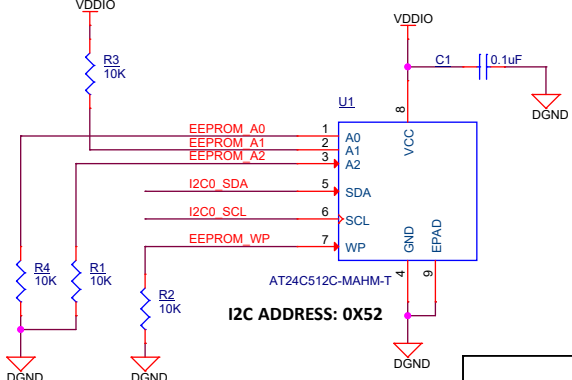
3-Strap option

2-Strap option



PHY ADDRESS = 01100  
MS = 0 Slave, AUTO = 0 Autonomous  
RGMII (Align Mode)

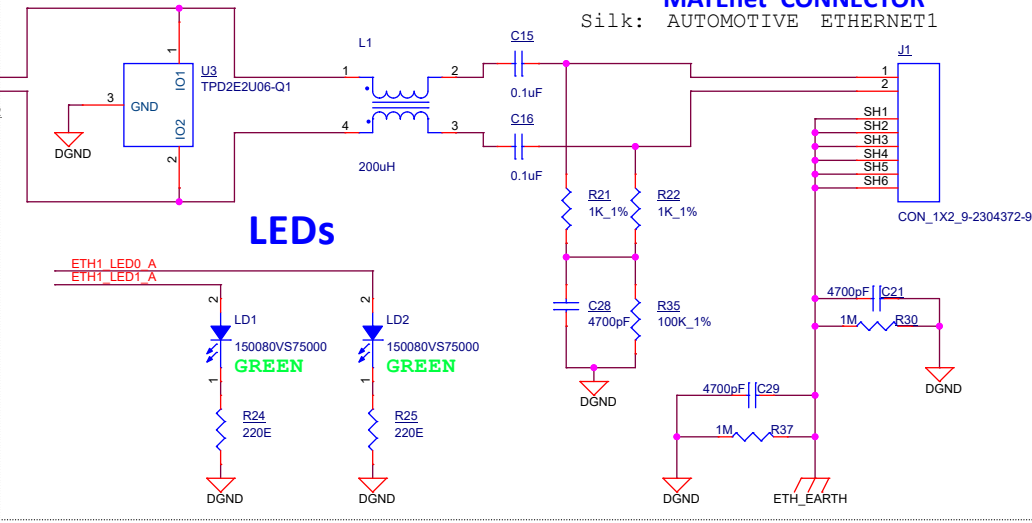
EEPROM



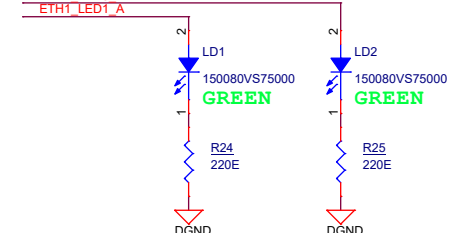
I2C ADDRESS: 0X52

MATenet CONNECTOR

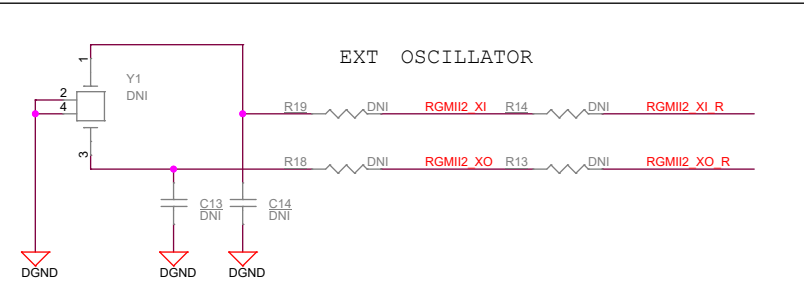
Silk: AUTOMOTIVE ETHERNET1



LEDs



EXT OSCILLATOR



Designed for TI by Mistral Solutions Pvt Ltd



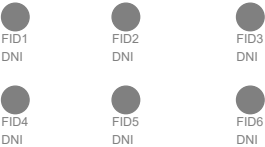
Title AUTOMOTIVE ICSSM RGMII/MII PHY		
Size	AUTOMOTIVE ICSSM ADD ON BOARD	Rev
C		E1
Date: Monday, October 16, 2023	Sheet 3 of 4	

NOTES, HW & LABELS

ASSEMBLY NOTES

- 1. All MSL components should be baked as per JEDEC standard.
- 2. PCB should be baked at 120 degree for 8 hours.
- 3. Board assembly must comply with workmanship standards. IPC-A-610 Class 2, unless otherwise specified.
- 4. These assemblies are ESD sensitive, ESD precautions shall be observed.
- 5. These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
- 6. Provide serial numbers to the assembled boards for identification.
- 7. The assembled board are wrapped in ESD Covers(individual) and packed securely before shipment.

FIDUCIALS



LOGOs



BARE PCB

