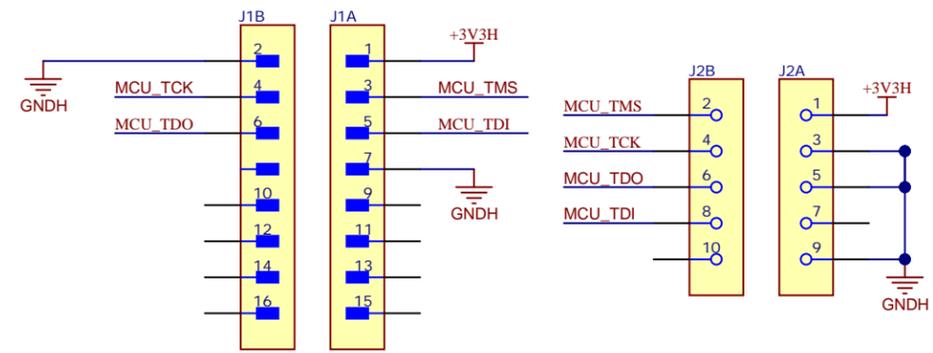


Revision History

Rev	ECN #	Approved Date	Approved by	Notes
E1				Initial Draft



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Orderable: <a href="#">TMDSCNCD280039C</a>	Designed for: XDS110ISO-EVM	Mod. Date: 4/15/2024	
TID #: N/A	Project Title:		
Number: XDS110ISO-EVM_ARM_10-pin_JTAG_adapter_board	Sheet: 1 of 8		
SVN Rev: Not in version control	Assembly Variant: 001		
Drawn By: Cody Watkins	File: XDS110ISO-EVM_ARM_10-pin_JTAG_adapter_board_SchDoc	http://www.ti.com	
Engineer: Sumit Patil	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>		© Texas Instruments 2022