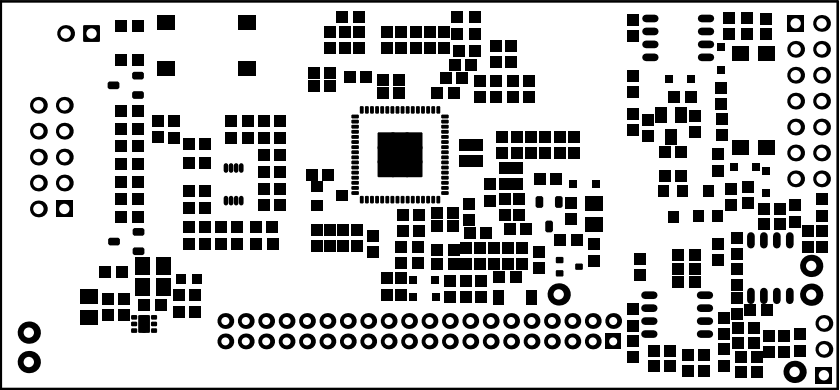
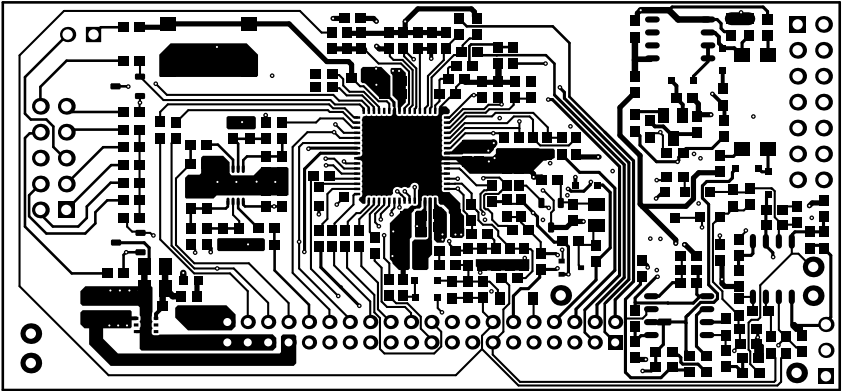


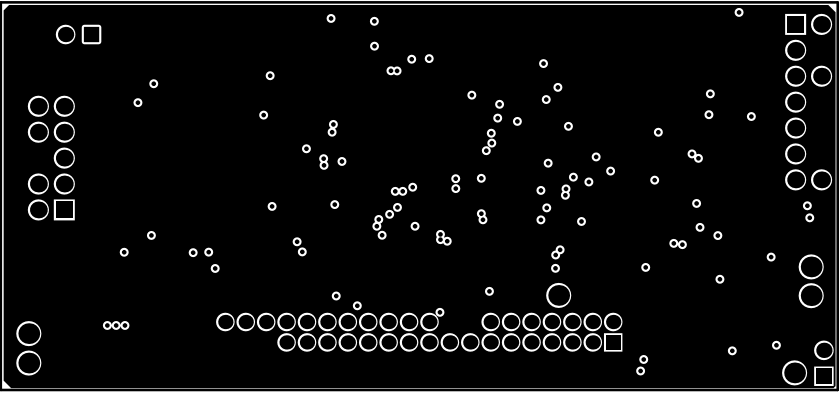
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	.PRJ_Number	REV:	.PCB_Rev	SVN REV:	Not in version control		
LAYER NAME =	Top Overlay					TID #:	.TID	
PLOT NAME =	Top Overlay					GENERATED :	9/19/2023 4:14:08 PM	TEXAS INSTRUMENTS



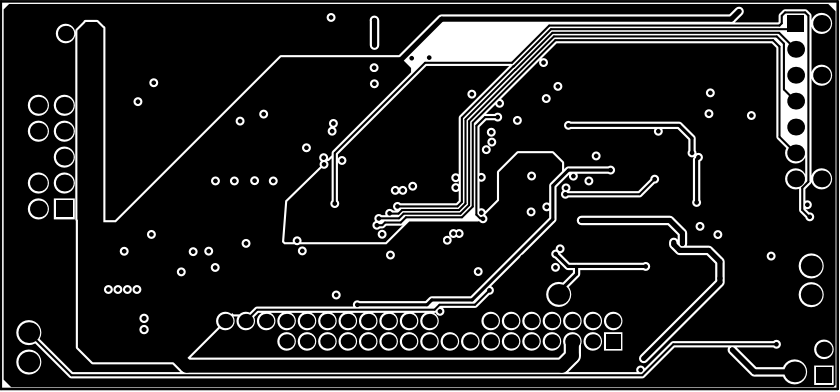
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: .PRJ_Number	REV: .PCB_Rev	SVN REV: Not in version control
LAYER NAME = Top Solder	TID #: .TID		
PLOT NAME = Top Solder Mask	GENERATED : 9/19/2023 4:14:09 PM		TEXAS INSTRUMENTS



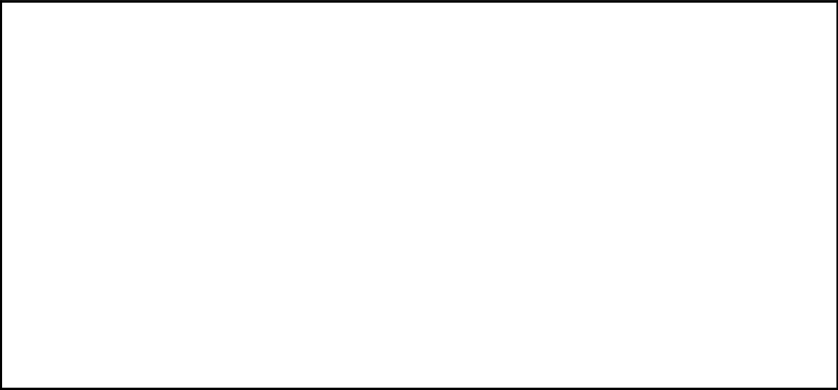
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: .PRJ_Number	REV: .PCB_Rev	SVN REV: Not in version control
LAYER NAME Top Layer	TID #: .TID		
PLOT NAME = Top Layer	GENERATED : 9/19/2023 4:14:09 PM		TEXAS INSTRUMENTS



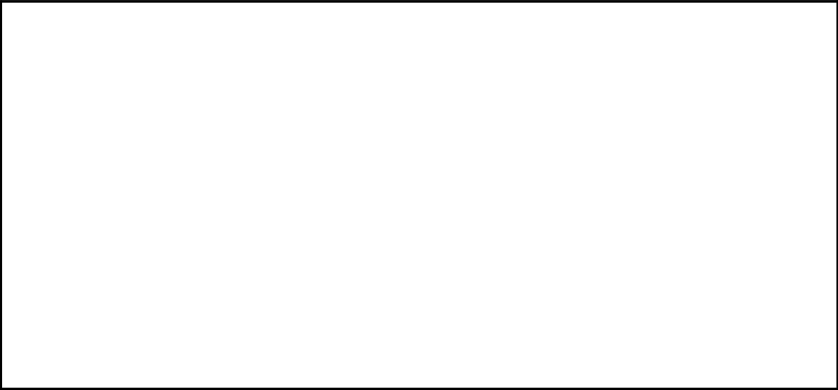
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: .PRJ_Number	REV: .PCB_Rev	SVN REV: Not in version control
LAYER NAME =	TID #: .TID		
PLOT NAME = Signal 1	GENERATED : 9/19/2023 4:14:10 PM		TEXAS INSTRUMENTS



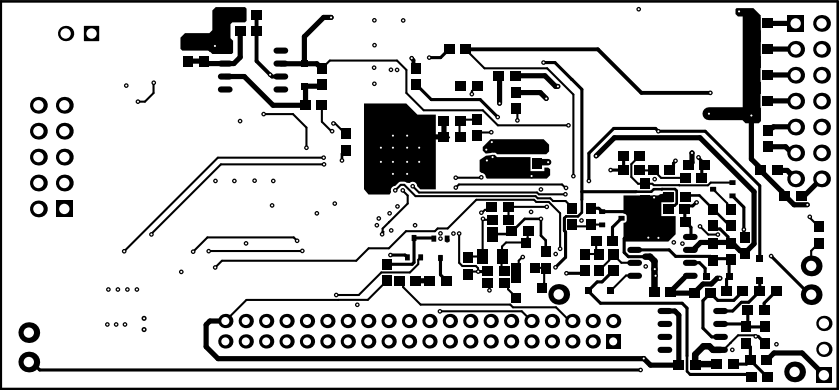
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: .PRJ_Number	REV: .PCB_Rev	SVN REV: Not in version control
LAYER NAME =	TID #: .TID		
PLOT NAME = Signal 2	GENERATED : 9/19/2023 4:14:10 PM		TEXAS INSTRUMENTS



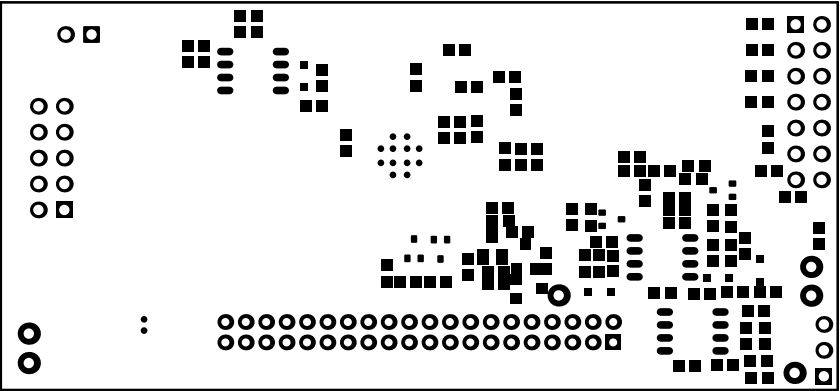
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: .PRJ_Number	REV: .PCB_Rev	SVN REV: Not in version control
LAYER NAME =	TID #: .TID		
PLOT NAME = Signal 3	GENERATED : 9/19/2023 4:14:11 PM		TEXAS INSTRUMENTS



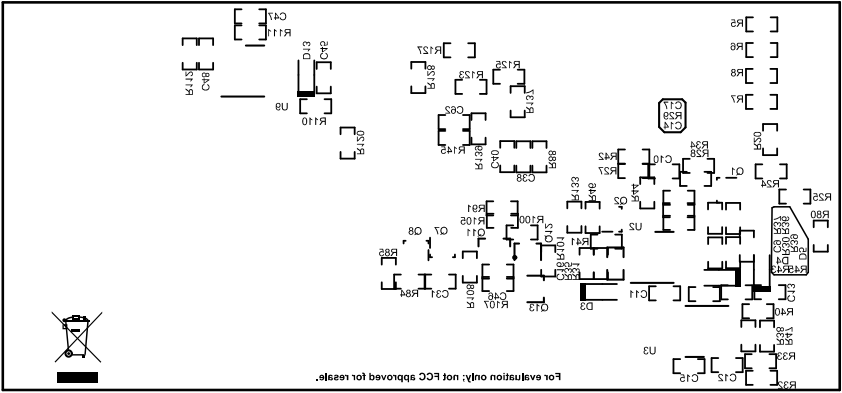
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: .PRJ_Number	REV: .PCB_Rev	SVN REV: Not in version control
LAYER NAME =	TID #: .TID		
PLOT NAME = Signal4	GENERATED : 9/19/2023 4:14:11 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: .PRJ_Number	REV: .PCB_Rev	SVN REV: Not in version control
LAYER NAME = Bottom Layer	TID #: .TID		
PLOT NAME = Bottom Layer	GENERATED : 9/19/2023 4:14:11 PM		TEXAS INSTRUMENTS

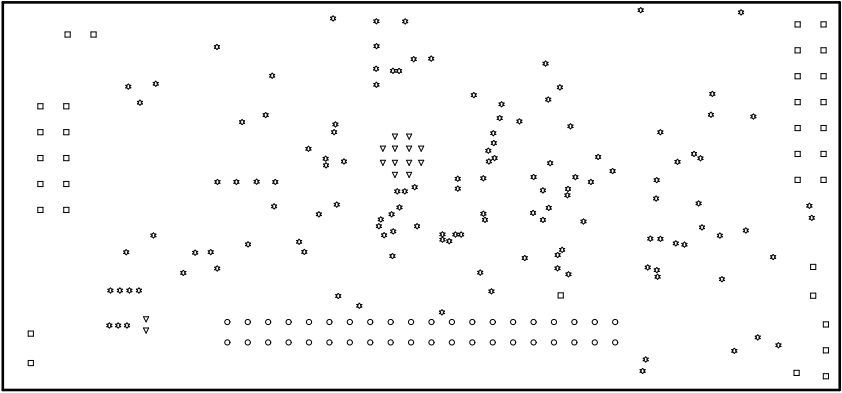


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: .PRJ_Number	REV: .PCB_Rev	SVN REV: Not in version control
LAYER NAME = Bottom Solder	TID #: .TID		
PLOT NAME = Bottom Solder Mask	GENERATED : 9/19/2023 4:14:12 PM		TEXAS INSTRUMENTS

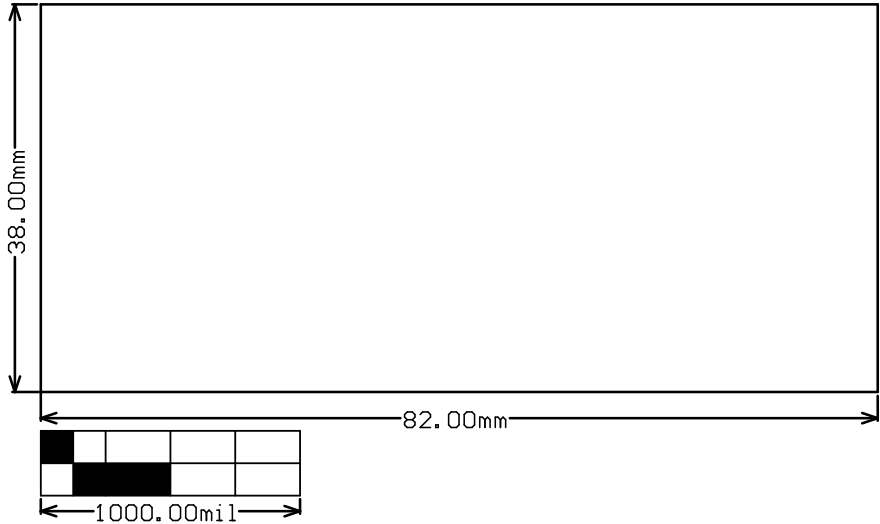


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: .PRJ_Number	REV: .PCB_Rev	SVN REV: Not in version control
LAYER NAME = Bottom Overlay	TID #: .TID		
PLOT NAME = Bottom Overlay	GENERATED : 9/19/2023 4:14:12 PM		TEXAS INSTRUMENTS

Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
▽	14	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
⚙	134	8.00mil (0.203mm)	PTH	Round	Top Layer - Bottom Layer	
○	40	31.00mil (0.787mm)	PTH	Round	Top Layer - Bottom Layer	
□	35	40.00mil (1.016mm)	PTH	Round	Top Layer - Bottom Layer	
	223 Total					



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: .PRJ_Number	REV: .PCB_Rev	SVN REV: Not in version control
LAYER NAME = Drill Drawing	TID #: .TID		
PLOT NAME = Drill Drawing	GENERATED : 9/19/2023 4:14:13 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: .PRJ_Number	REV: .PCB_Rev	SVN REV: Not in version control
LAYER NAME = M2 Board Dimensions	TID #: .TID		
PLOT NAME = Board Dimensions	GENERATED : 9/19/2023 4:14:13 PM		TEXAS INSTRUMENTS

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