

1		2		3		4		5		6		
								Revision History				
								Rev	ECN #	Approved Date	Approved by	Notes
								N/A	N/A	N/A	N/A	N/A
A	Page 2		Block Diagram									
	Page 3		CT1 and CT2									
	Page 4		CT3 and CT4									
	Page 5		VOLATGE1, VOLTAGE2 WITH ISOLATION AMPLIFIER									
	Page 6		VOLATGE3, VOLTAGE4 WITH ISOLATION AMPLIFIER									
	Page 7		COMPARATOR FOR ZCD									
	Page 8		SPLIT RAIL CONVERTER									
	Page 9		LDO									
	Page 10		ISOLATED POWER									
	Page 11		FILTERS and ADC EVM I/P CONNECTOR									
B	Page 12		Mounting holes, Fiducials Marking									
C												
D												

Orderable: N/A		Designed for: Public Release		Mod. Date: 9/14/2017	
TID #: TIDA-00834 ISO		Project Title: TIDA-00834 ISO-E2			
Number: TIDA-834 ISO		Rev: E2		Sheet Title: Cover Sheet	
SVN Rev: Not in version control		Assembly Variant: 001		Sheet: 1 of 12	
Drawn By: Sreenivasa		File: TIDA-00834 ISO-E2 Pg1 CoverSheet.SchDoc Size: B			
Engineer: Sreenivasa		Contact: http://www.ti.com/support			

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

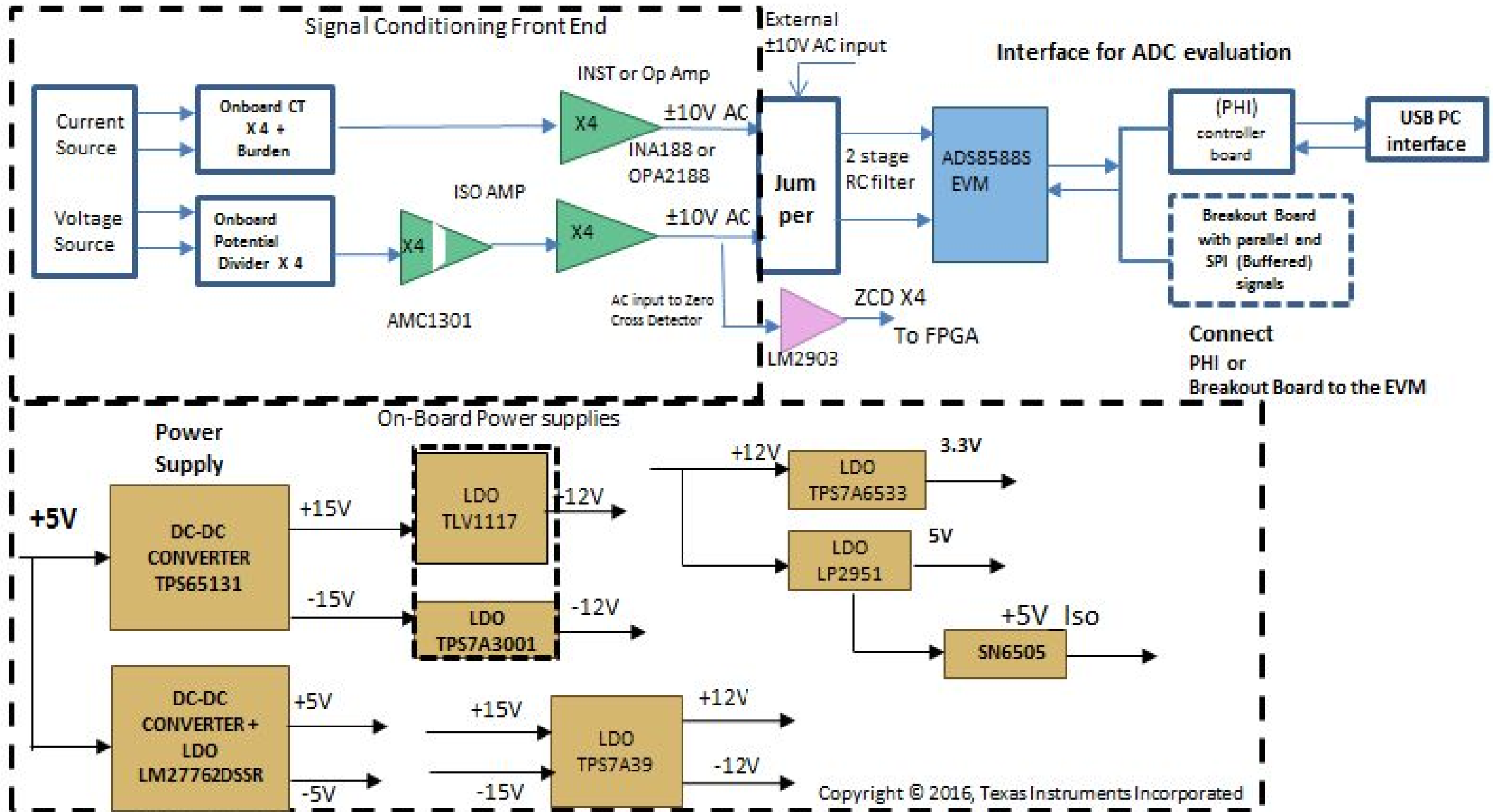


TEXAS  
INSTRUMENTS

http://www.ti.com

© Texas Instruments 2016

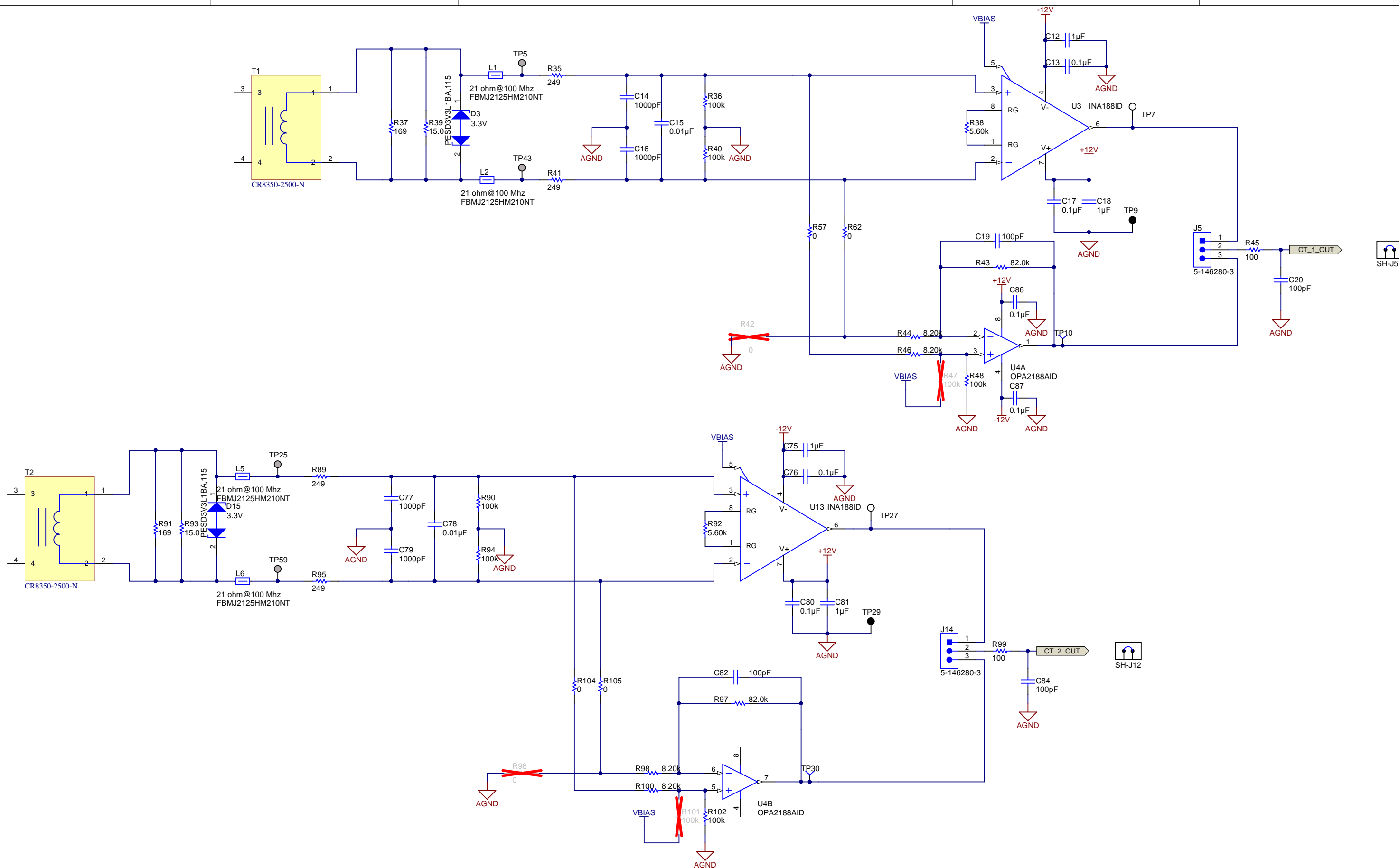
# Simultaneous Sampled, Data Acquisition front End



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

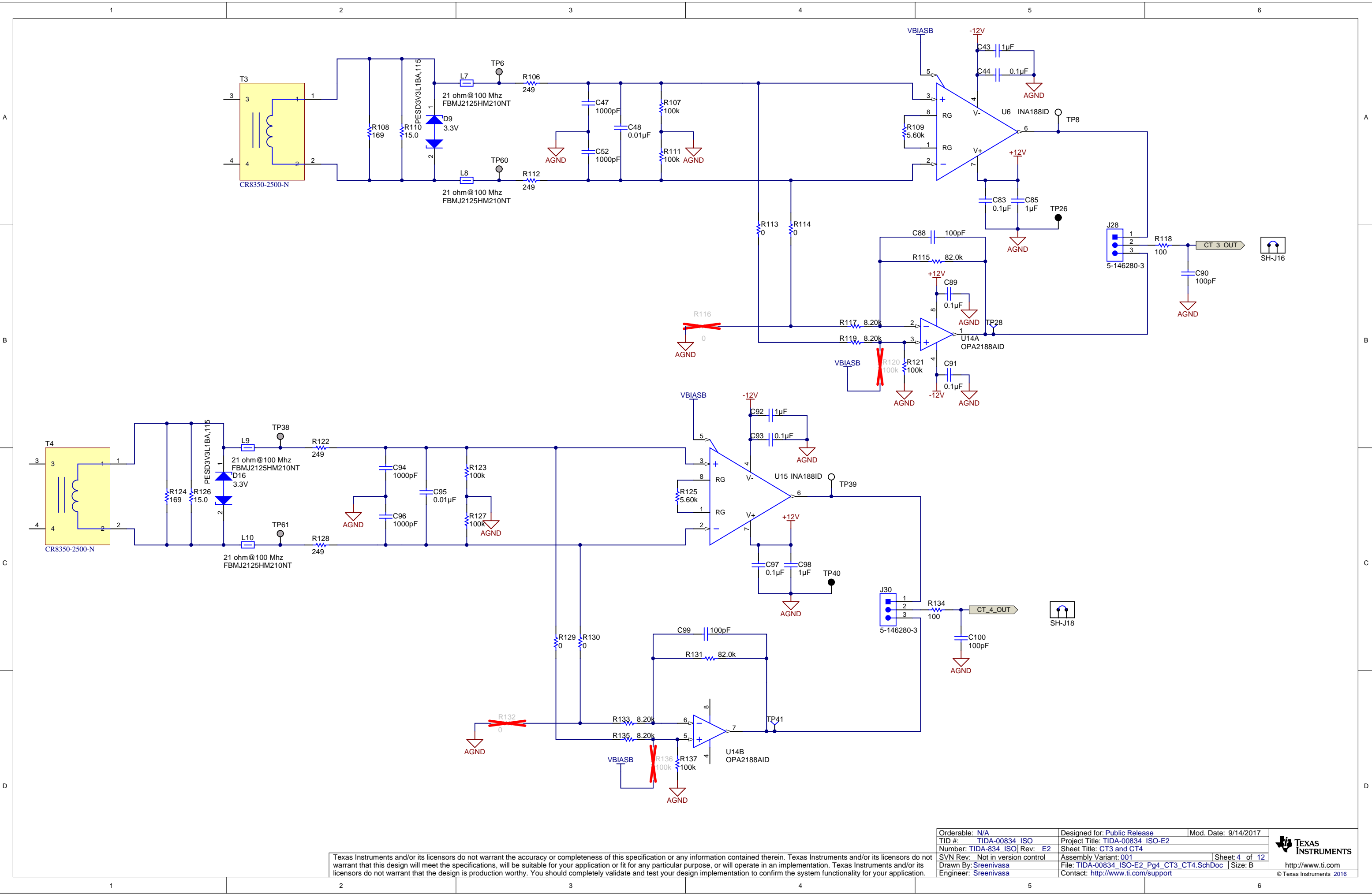
Orderable: N/A	Designed for: Public Release	Mod. Date: 9/15/2017
TID #: TIDA-00834_ISO	Project Title: TIDA-00834_ISO-E2	
Number: TIDA-834_ISO Rev: E2	Sheet Title: Block Diagram	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 12
Drawn By: Sreenivasa	File: TIDA-00834_ISO-E2_Pg2_Block Diagram.Sch	Size: B
Engineer: Sreenivasa	Contact: http://www.ti.com/support	

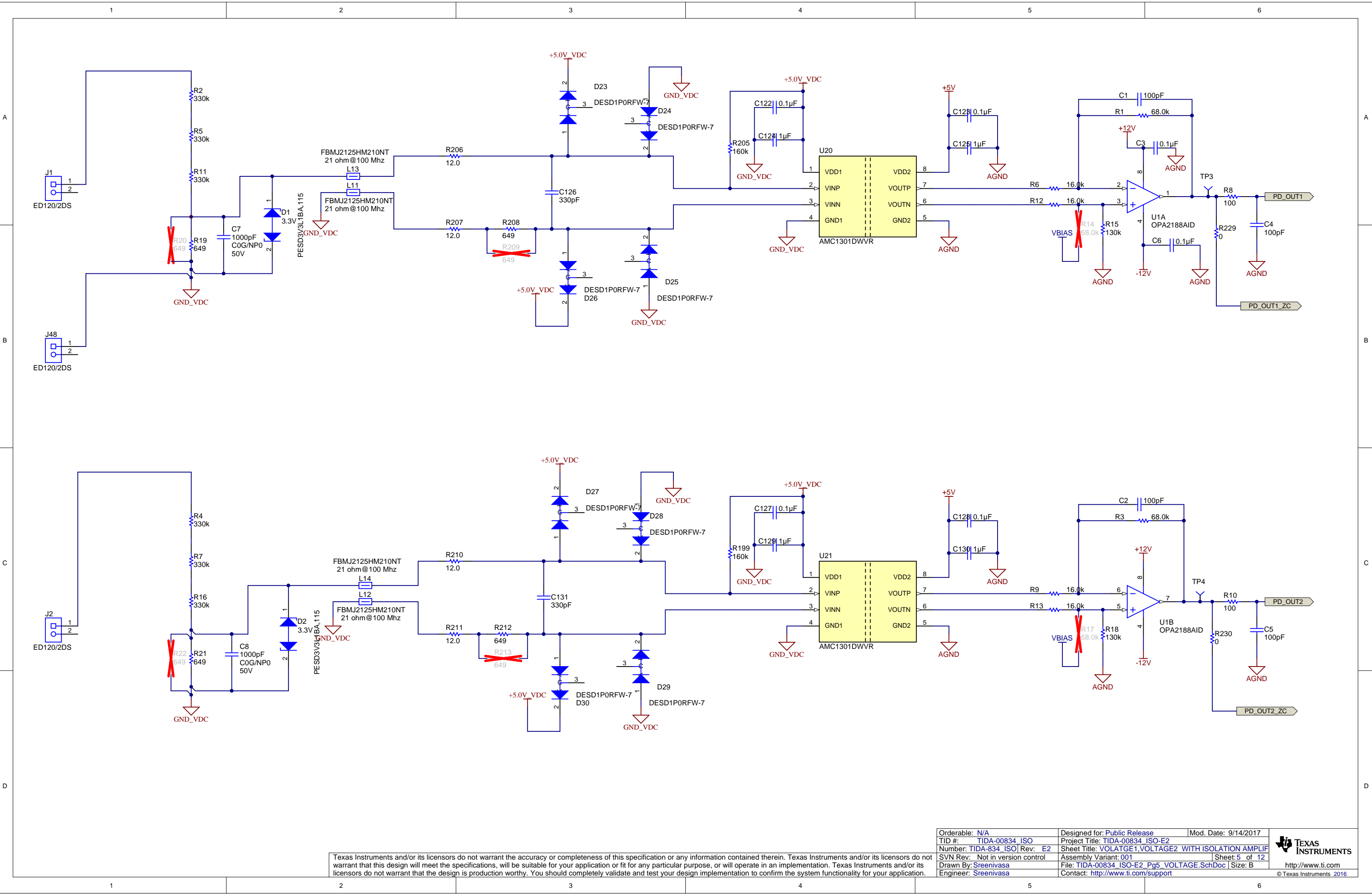
**TEXAS INSTRUMENTS**  
http://www.ti.com  
© Texas Instruments 2016

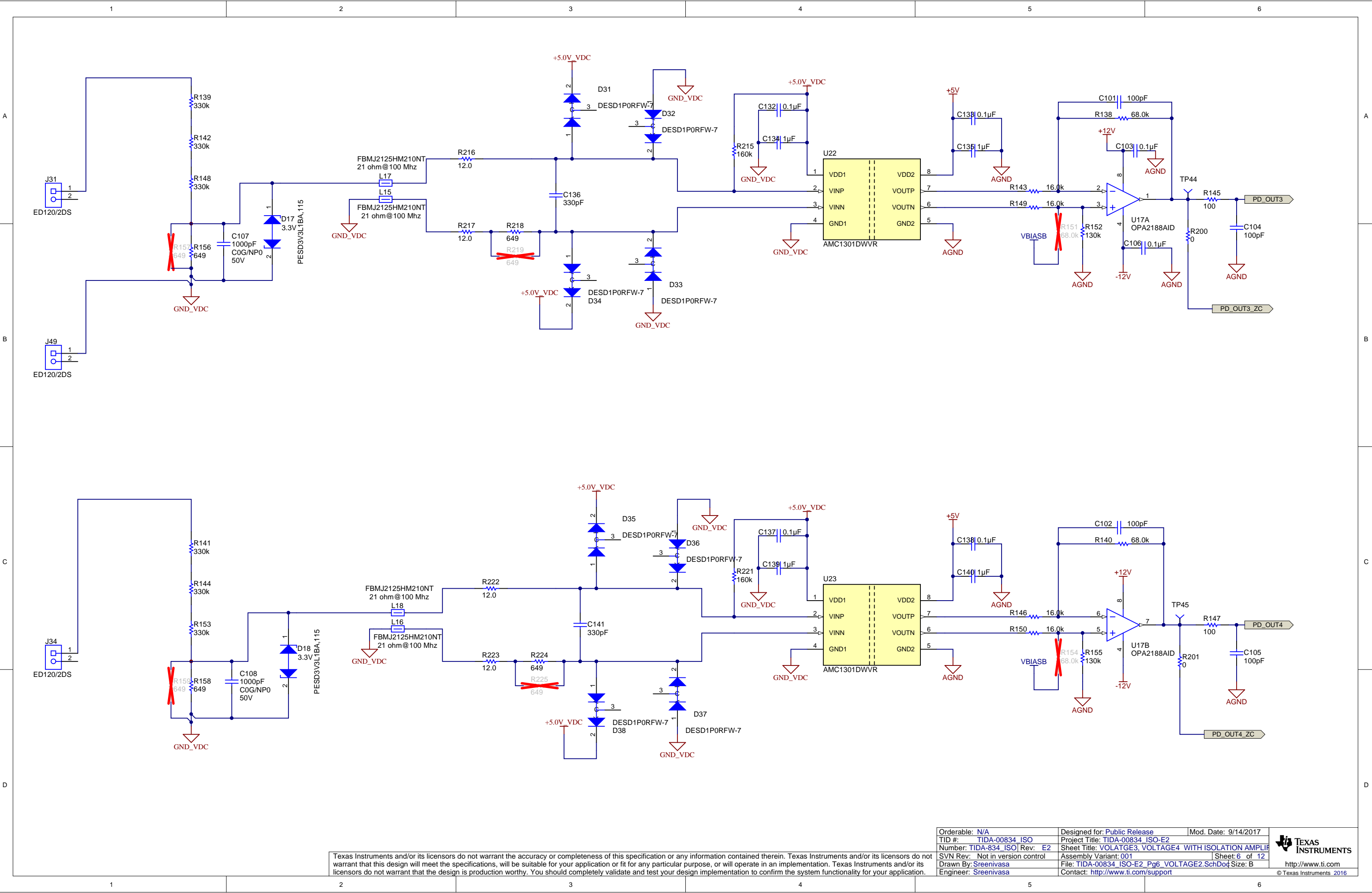


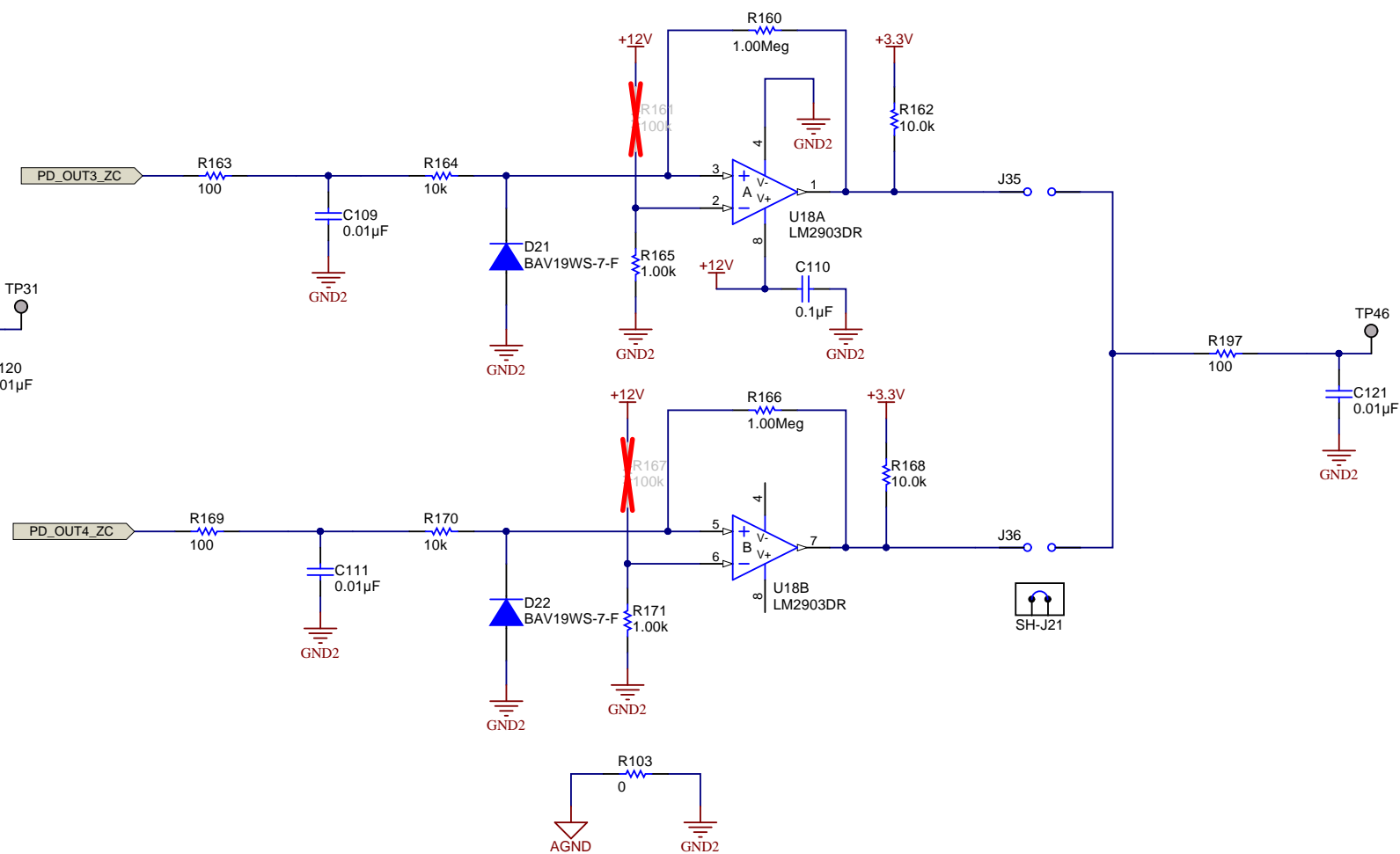
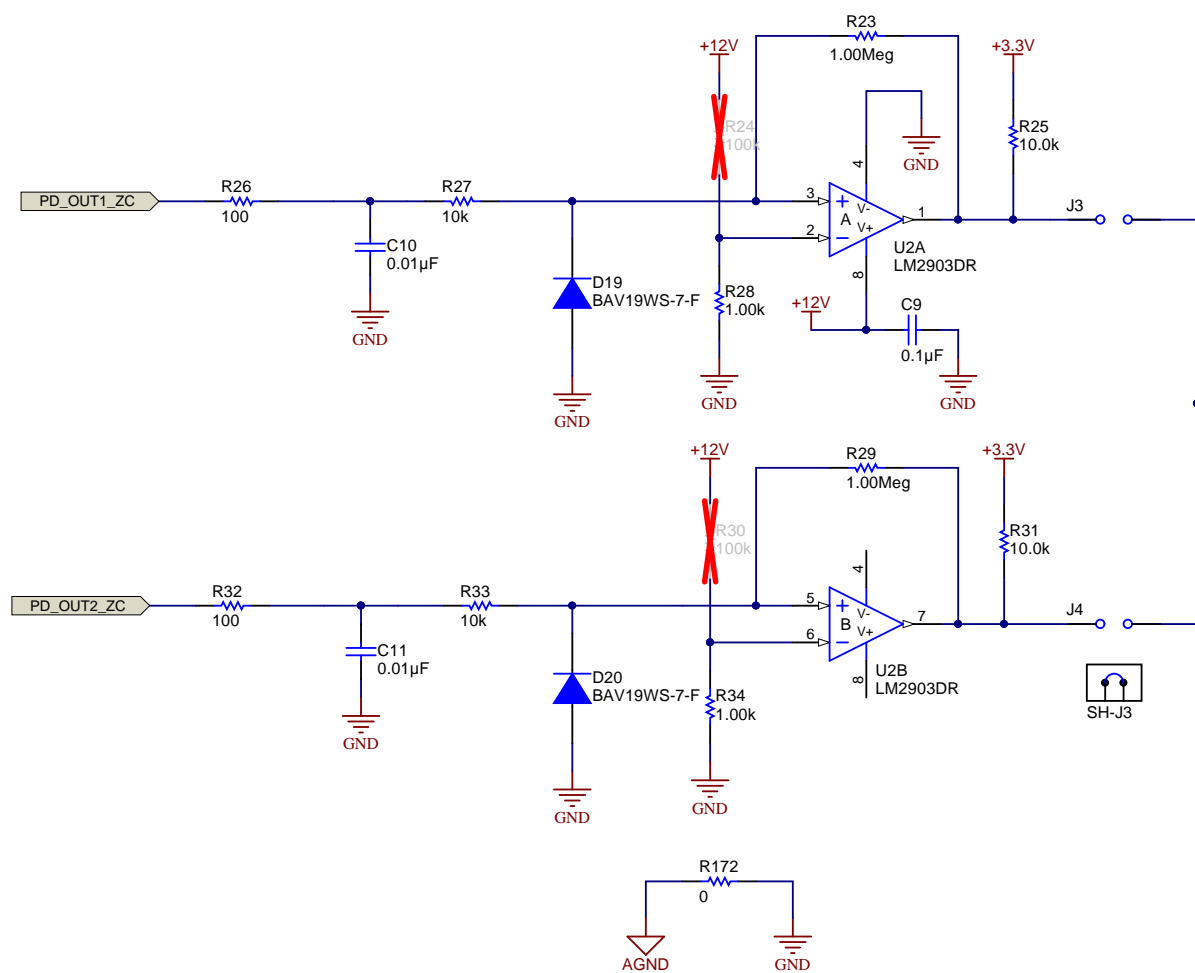
Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: N/A	Designed for: Public Release	Mod. Date: 9/14/2017
TID #: TIDA-00834_ISO	Project Title: TIDA-00834_ISO-E2	
Number: TIDA-834_ISO Rev: E2	Sheet Title: CT1 and CAT2	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 12
Drawn By: Sreenivasa	File: TIDA-00834_ISO-E2_Pg3_CT1_CT2.SchDoc	Size: B
Engineer: Sreenivasa	Contact: http://www.ti.com/support	





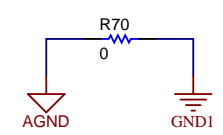
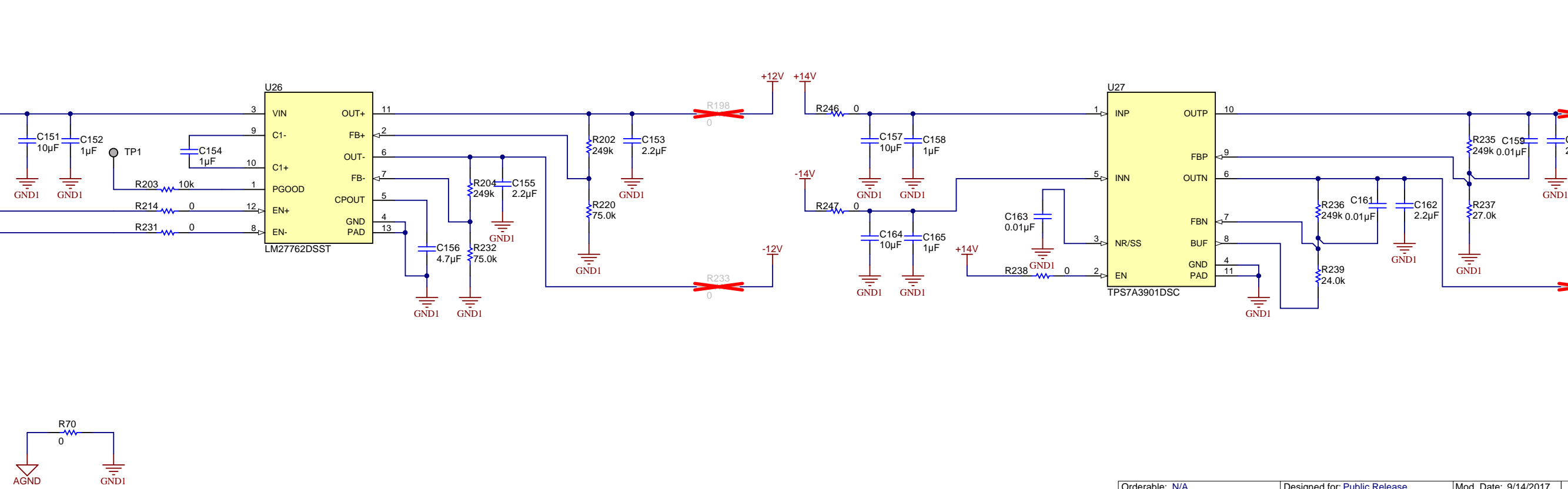
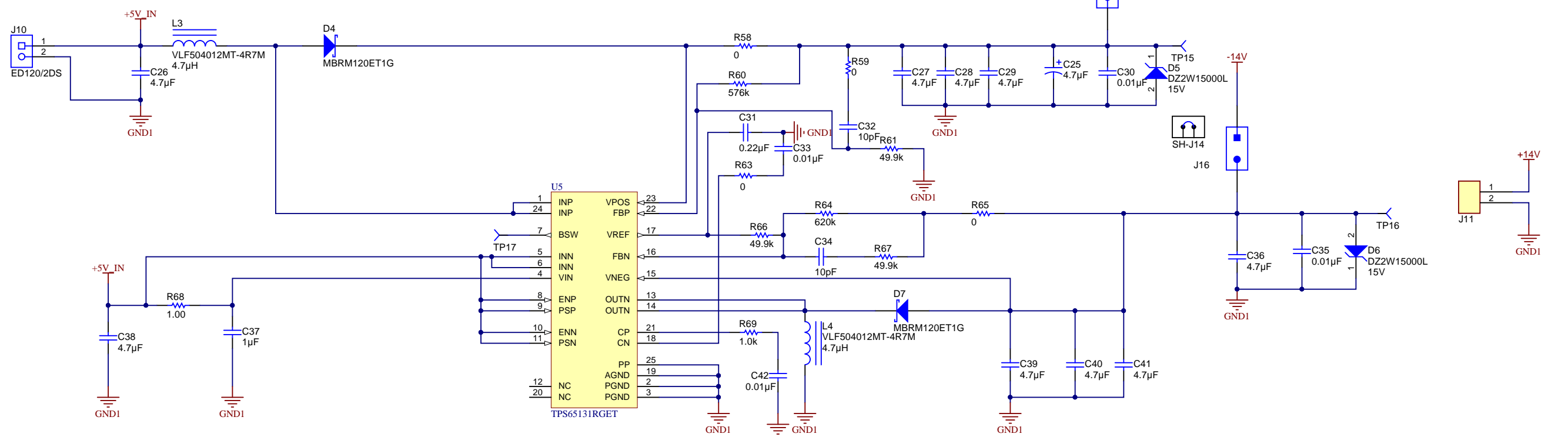




Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

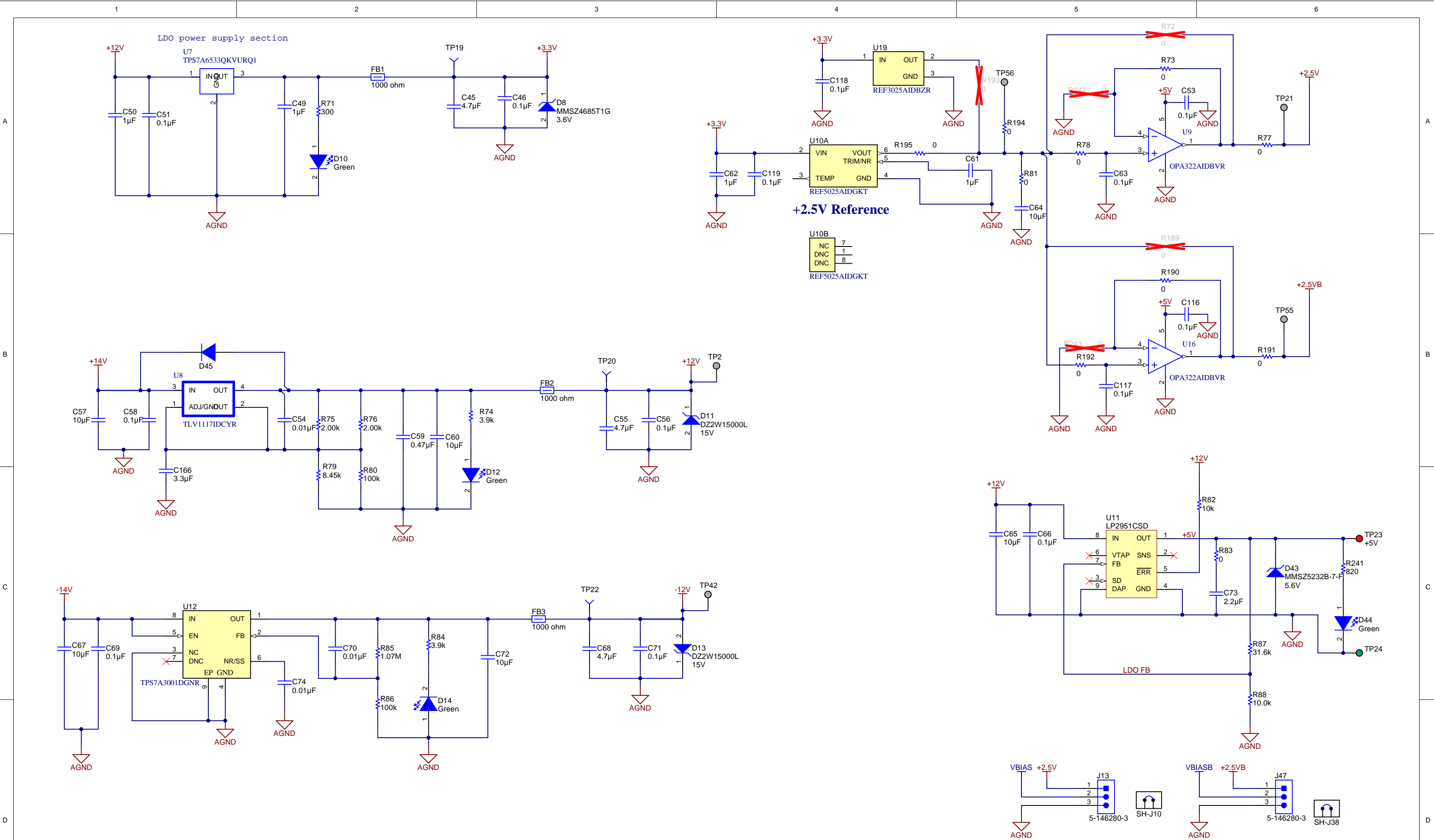
Orderable: N/A	Designed for: Public Release	Mod. Date: 9/14/2017
TID #: TIDA-00834_ISO	Project Title: TIDA-00834_ISO-E2	
Number: TIDA-834_ISO Rev: E2	Sheet Title: COMPARATOR FOR ZCD	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 7 of 12
Drawn By: Sreenivasa	File: TIDA-00834_ISO-E2_Pg7_COMPARATOR.Sch	
Engineer: Sreenivasa	Contact: http://www.ti.com/support	

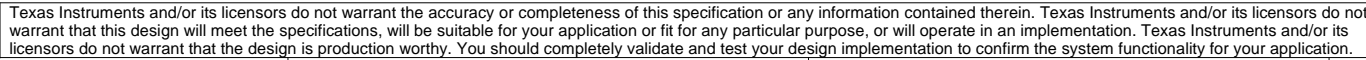
1 2 3 4 5 6



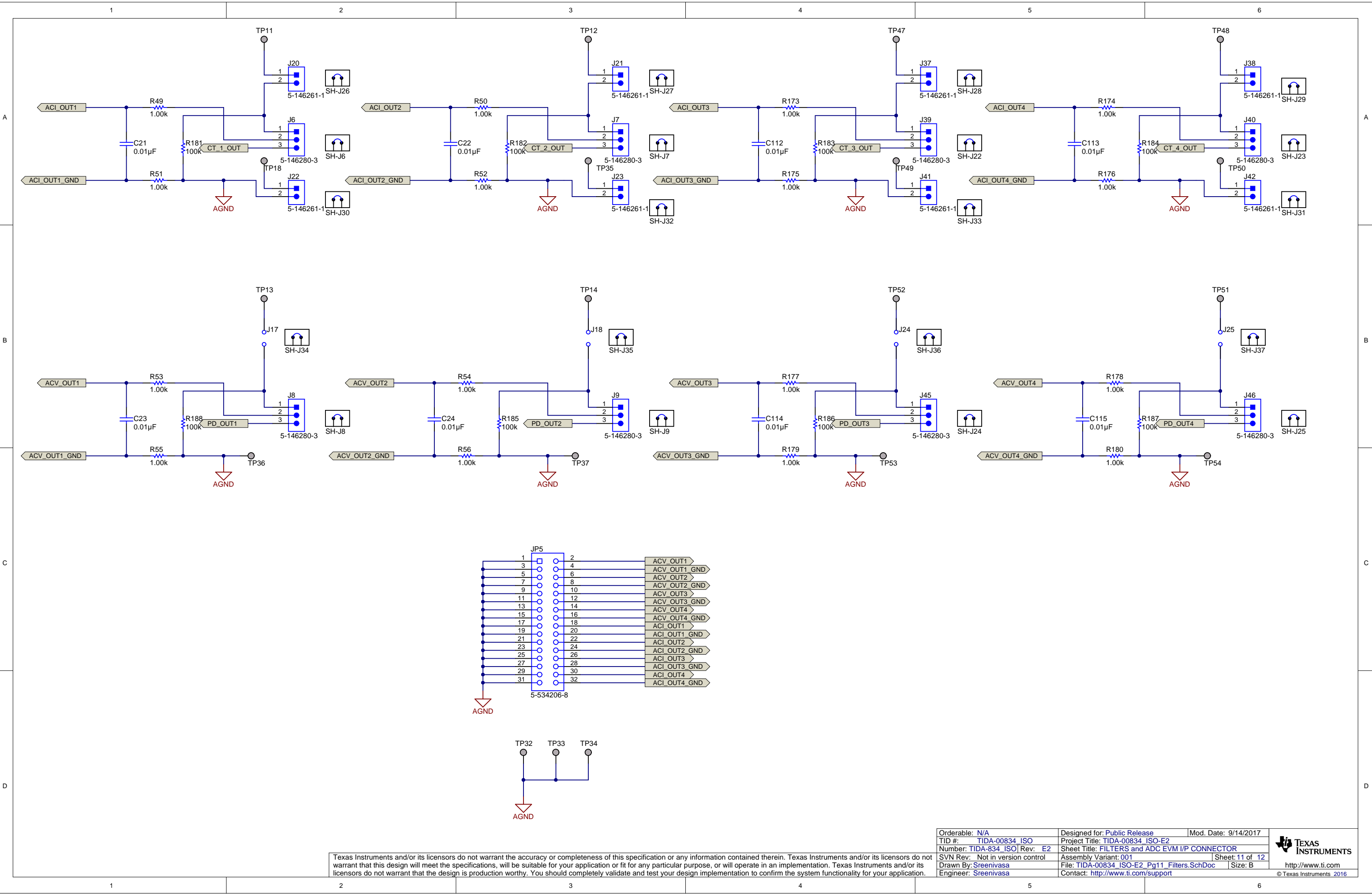
1 2 3 4 5 6



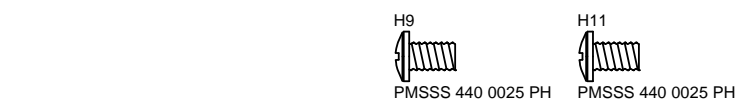
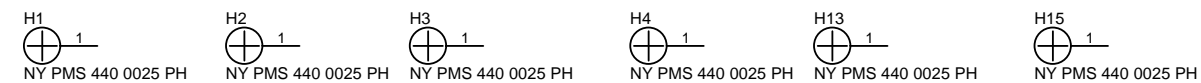




 **TEXAS  
INSTRUMENTS**  
<http://www.ti.com>  
© Texas Instruments 2016



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.



PCB Number: TIDA-834\_ISO  
PCB Rev: E2

PCB  
LOGO  
Texas Instruments

PCB  
LOGO  
Pb-Free Symbol

PCB  
LOGO  
FCC disclaimer



You should delete the nylon screws/standoffs and/or the bumpoms as needed for your design (or substitute other parts from Hardware.IntLib). Bumpoms are cheaper, but provide less clearance.

Deleting anything else from this page may result in your EVM submission being rejected (until you add them back).

Update the Label Text in the Label Table as needed for each Assembly Variant.

You should delete this note too.

Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

LBL1

PCB Label
-----------

Size: 0.65" x 0.20"

ZZ1  
Label Assembly Note  
This Assembly Note is for PCB labels only

**ZZ2**  
**Assembly Note**  
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

**Assembly Note**

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note
---------------

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: <a href="#">N/A</a>	Designed for: <a href="#">Public Release</a>	Mod. Date: 9/14/2017
TID #: <a href="#">TIDA-00834</a> <a href="#">ISO</a>	Project Title: <a href="#">TIDA-00834</a> <a href="#">ISO-E2</a>	
Number: <a href="#">TIDA-834</a> <a href="#">ISO</a> Rev: <a href="#">E2</a>	Sheet Title: <a href="#">EVM</a> <a href="#">Hardware</a>	
SVN Rev: Not in version control	Assembly Variant: <a href="#">001</a>	Sheet 12 of 12
Drawn By: <a href="#">Sreenivasa</a>	File: <a href="#">TIDA-00834</a> <a href="#">ISO-E2</a> <a href="#">Pg12</a> <a href="#">EVM</a> <a href="#">Hardware</a> <a href="#">Sheet: B</a>	
Engineer: <a href="#">Sreenivasa</a>	Contact: <a href="#">http://www.ti.com/support</a>	