


Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

Page 2 BLOCK DIAGRAM_DIO

Page 3 12V and 24V DC input overload and transient protection

Page 4 5V DC input overload and transient protection

Orderable: NA	Designed for: Public Release	Mod. Date: 5/17/2019	
TID #: 010008	Project Title: TIDA-010008_DCIinput_Protection		
Number: TIDA-010008_DCIinput_Protection	Sheet Title: COVERSHEET		
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 5	
Drawn By: Sreenivasa	File: TIDA-010008_DCIinput_Protection_CoverSheet_SchDoc		
Engineer: Sreenivasa	Contact: http://www.ti.com/support		http://www.ti.com © Texas Instruments 2019

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A

B

C

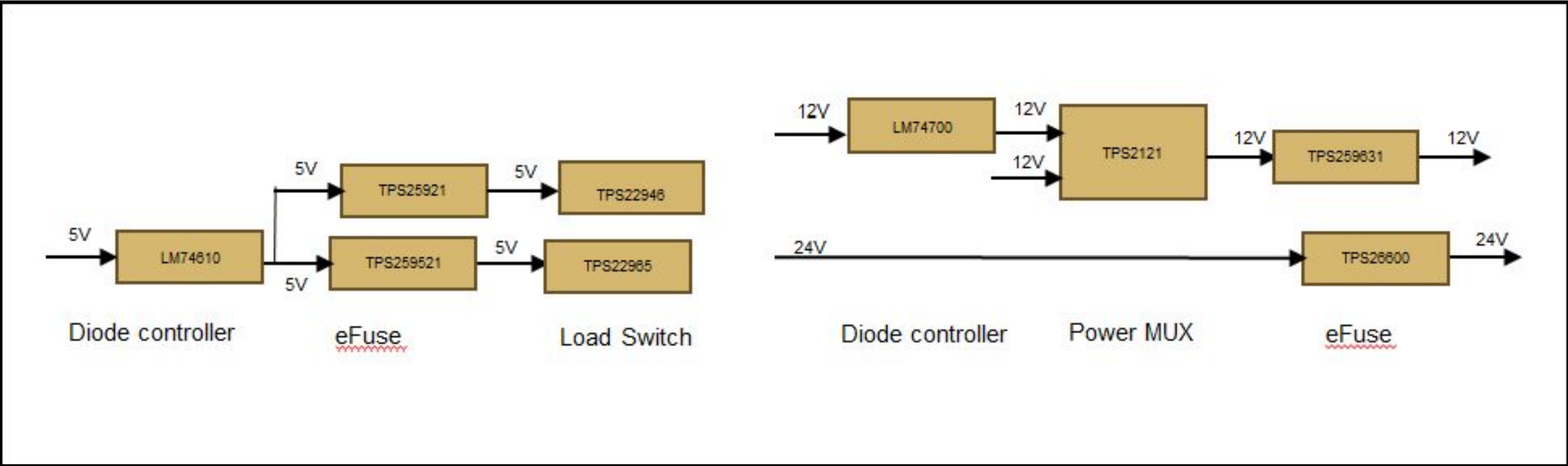
D

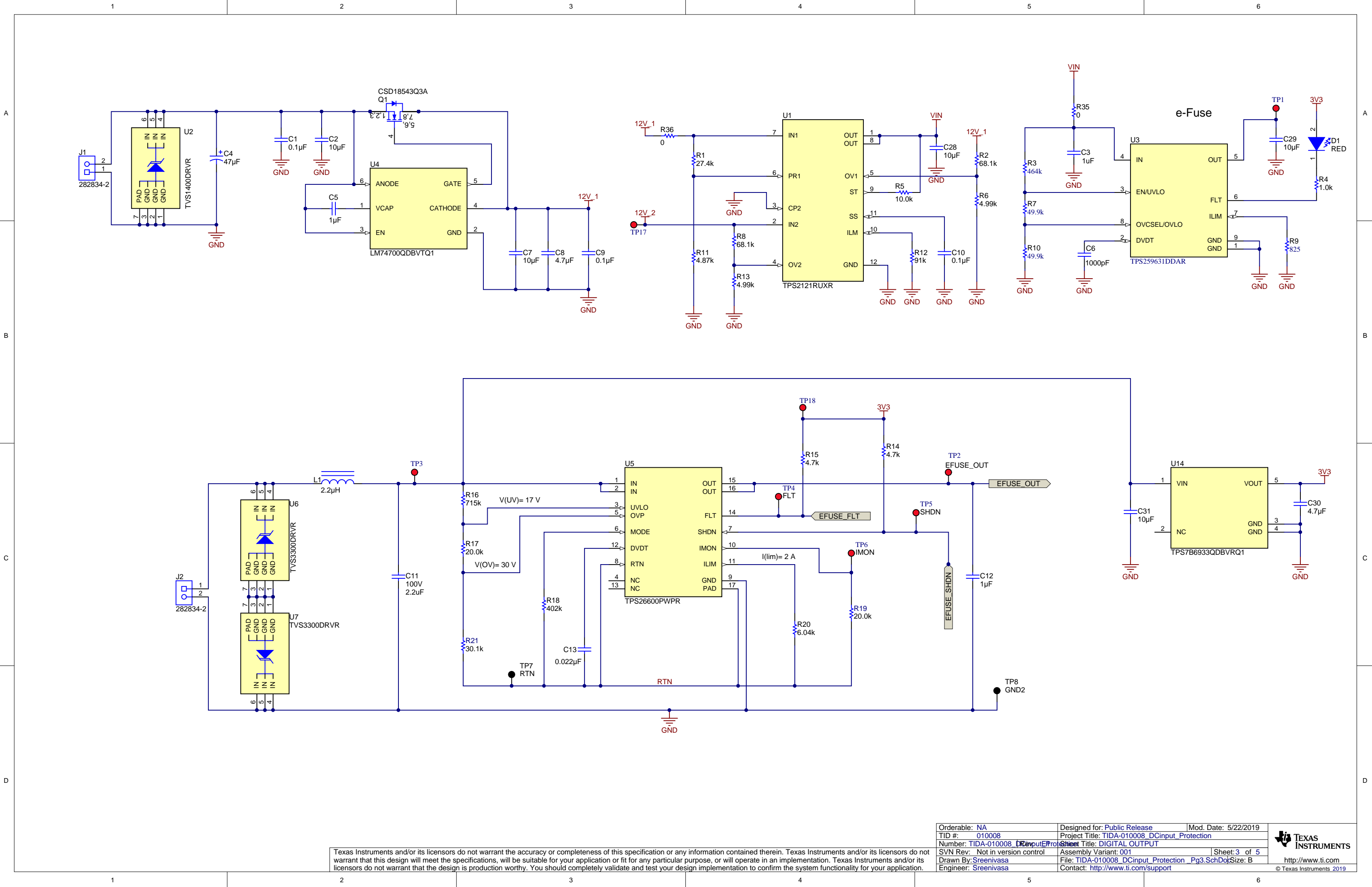
A

B

C

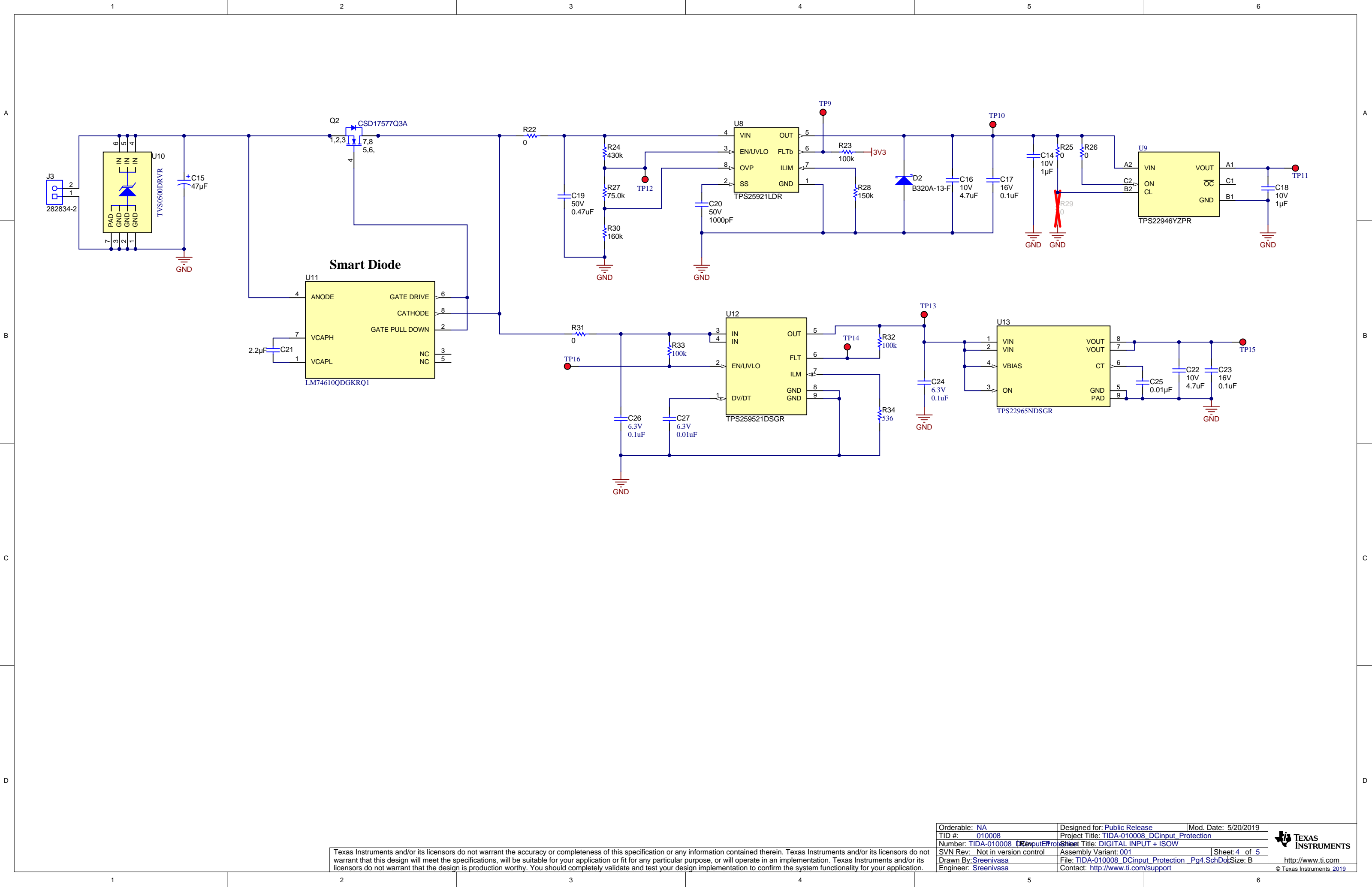
D





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Orderable: NA	Designed for: Public Release	Mod. Date: 5/22/2019
TID #: 010008	Project Title: TIDA-010008_DCInput_Protection	
Number: TIDA-010008_DCInput_Protection	Sheet Title: DIGITAL OUTPUT	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 5
Drawn By: Sreenivasa	File: TIDA-010008_DCInput_Protection_Pg3.SchDoc	Size: B
Engineer: Sreenivasa	Contact: http://www.ti.com/support	



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Orderable: NA	Designed for: Public Release	Mod. Date: 5/20/2019
TID #: 010008	Project Title: TIDA-010008_DcInput_Protection	
Number: TIDA-010008_DcInput_Protection	Sheet Title: DIGITAL INPUT + ISOW	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 4 of 5
Drawn By: Sreenivasa	File: TIDA-010008_DcInput_Protection_Pg4.SchDoc	Size: B
Engineer: Sreenivasa	Contact: http://www.ti.com/support	

A

A



H.



H2



H3



H4



PCB Number: TIDA-010008_DCinput_Protection
PCB Rev: E1

PCB
LOGO
FCC disclaimer

PCB
LOGO
Logo4

PCB
LOGO
WEEE logo

[illegible]ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note


These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Preliminary Version. Not tested. Subject to change without notice

Orderable: NA	Designed for: Public Release	Mod. Date: 5/24/2019	
TID #: 010008	Project Title: TIDA-010008_DCinpu Protection		
Number: TIDA-010008	DocuProtect Title: Hardware		
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 5 of 5	
Drawn By: Sreenivasa	File: TIDA-010008_DCinpu Protection_EVM_Hardw Size B		
Engineer: Sreenivasa	Contact: http://www.ti.com/support		http://www.ti.com © Texas Instruments 2019