## SN54150, SN54151A, SN54LS151, SN54S151, SN74150, SN74151A, SN74LS151, SN74S151 DATA SELECTORS/MULTIPLEXERS

DECEMBER 1972-REVISED MARCH 1988

- '150 Selects One-of-Sixteen Data Sources
- Others Select One-of-Eight Data Sources
- All Perform Parallel-to-Serial Conversion
- All Permit Multiplexing from N Lines to One Line
- Also For Use as Boolean Function Generator
- Input-Clamping Diodes Simplify System Design
- Fully Compatible with Most TTL Circuits

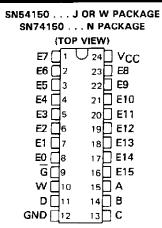
	TYPICAL AVERAGE	TYPICAL
TYPE	PROPAGATION DELAY TIME	POWER
	DATA INPUT TO W OUTPUT	DISSIPATION
150	13 ns	200 mW
151A	8 ns	145 mW
'LS151	13 ns	30 mW
'S151	4.5 ns	225 mW

#### description

These monolithic data selectors/multiplexers contain full on-chip binary decoding to select the desired data source. The '150 selects one-of-sixteen data sources; the '151A, 'LS151, and 'S151 select one-of-eight data sources. The '150, '151A, 'LS151, and 'S151 have a strobe input which must be at a low logic level to enable these devices. A high level at the strobe forces the W output high, and the Y output (as applicable) low.

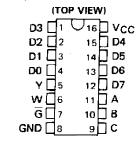
The '150 has only an inverted W output; the '151A, 'LS151, and 'S151 feature complementary W and Y outputs.

The '151A and '152A incorporate address buffers that have symmetrical propagation delay times through the complementary paths. This reduces the possibility of transients occurring at the output(s) due to changes made at the select inputs, even when the '151A outputs are enabled (i.e., strobe low).

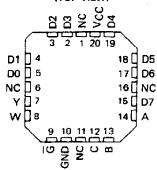


SN54151A, SN54LS151, SN54S151 . . . J OR W PACKAGE SN74151A . . . N PACKAGE

SN74LS151, SN74S151 . . . D OR N PACKAGE



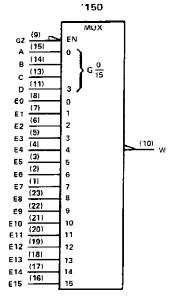
SN54LS151, SN54S151 . . . FK PACKAGE (TOP VIEW)

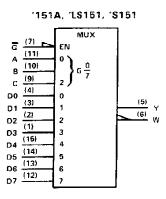


NC - No internal connection

## SN54150, SN54151A, SN54LS151, SN54S151, SN74150, SN74151A, SN74LS151, SN74S151 DATA SELECTORS/MULTIPLEXERS

logic symbols†





'150 FUNCTION TABLE

		INI	PUT	S	OUTPUT
	SEL	ECT	•	STROBE	w
D	С	В	_A	Ğ	VV
Х	X	Х	Х	Н	Ŧ
L	L	L	L	L	ΕÖ
L	L	L	H	L	E1
L	L	H	L	L	E2
L	L	Н	н	L	Ē3
L	Н	L	L	L,	Ē4
L	н	L	Η :	L	E5
L	н	Н	L.	Ļ	<u>E6</u>
L	н	Н	н	L	E7
н	L	L	Ł	L	€8
Н	L	L	Н	L	Ē9
Н	L	н	L	L	E10
н	L	Н	н	L	E11
н	н	L	L	L	E12
н	Н	L	н	L	E13
н	Н	Н	L	L	E14
н	н	н	н	L	E15

'151A, 'LS151, 'S151 FUNCTION TABLE

	11	VPUT	S	OUT	PUTS
S	ELEC	T	STROBE	v	w
C	В	Α	Ğ	*	**
Х	Х	Х	Н	L	I
L	L	L	L	DO	<u>500</u>
L	L	Н	L	D1	D1
L	Н	L	L	D2	D2
L	н	Н	L	D3	D3
Н	L	L	L	D4	D4
Н	L	H	L	D5	D5
Н	н н Ļ		L	D6	D6
Н	Н	н	L	D7	D7

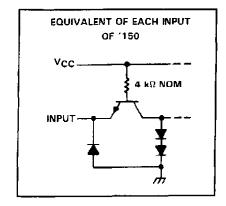
H = high level, L = low level, X = irrelevant

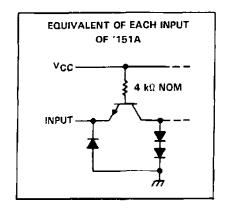
 $\overline{\text{E0}}$ ,  $\overline{\text{E1}}$  . . .  $\overline{\text{E15}}$  = the complement of the level of the respective E input

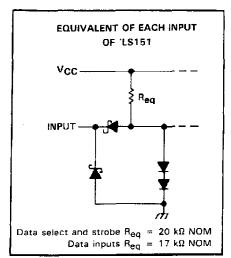
DO, D1 . . . D7 = the level of the D respective input

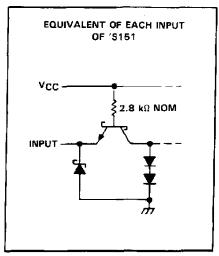
<sup>&</sup>lt;sup>†</sup>These symbols are in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12. Pin numbers shown are D, J, N, and W packages.

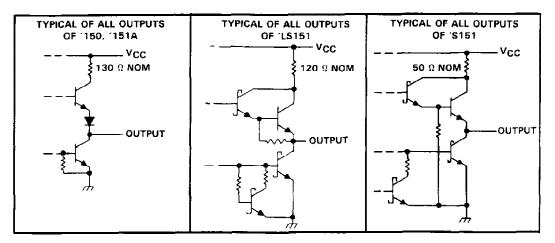
### schematics of inputs and outputs











# SN54150, SN54151A, SN74150, SN74151A DATA SELECTORS/MULTIPLEXERS

## recommended operating conditions

		SN54'			SN74'		
	MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Supply voltage, VCC	4.5	5	5.5	4.75	5	5.25	V
High-level output current, IOH		-	-800			-800	μА
Law-level output current, IOL			16			16	mΑ
Operating free-air temperature, TA	-55		125	0		70	C

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	CA CAMPTED	TEST CONDI	TIONST		1150			151A		UNIT
	PARAMETER 	TEST CONDI	TIONS.	MIN	TYP <sup>‡</sup>	MAX	MIN	TYP‡	MAX	UNII
VιΗ	High-level input voltage			2			2			٧
VIL	Low-level input voltage			•		0.8			0.8	٧
VIK	Input clamp voltage	VCC = MIN. I	= -8 mA			- 1.5			-1.5	V
∨он	High-level output voltage	$V_{CC} = MIN, V_{H}$ $V_{IL} = 0.8 \text{ V}, I_{OI}$	· 1	2.4	3.4		2.4	3.4		V
VOL	Low-level output voltage	$V_{CC} = MIN, V_{II}$ $V_{IL} = 0.8 \text{ V}, I_{OI}$			0.2	0.4		0.2	0.4	٧
l <sub>j</sub>	Input current at maximum input voltage	VCC = MAX, V	= 5.5 V			1 ,			1	mΑ
lite	High-level input current	VCC = MAX, VI	= 2.4 V			40			40	μА
l <sub>IL</sub>	Low-level input current	$V_{CC} = MAX, V_I$	= 0.4 V		·	-1.6			-1.6	mA
		14 MANY	SN54'	- 20		- 55	- 20		- 55	
los	Short-circuit output current <sup>9</sup>	V <sub>CC</sub> = MAX	SN74'	- 18		- 55	-18		- 55	mA
lcc	Supply current	V <sub>CC</sub> = MAX, See	Note 3		40	68		29	48	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable device type.

## switching characteristics, VCC = 5 V, TA = 25°C

	FROM	то	TEST	,	150			151/	4	
PARAMETER ¶	(INPUT)	(OUTPUT)	CONDITIONS	MIN 7	TYP	MAX	MIN	TYP	MAX	UNIT
tPLH	A, B, or C	Y						25	38	
<sup>†</sup> PHL	(4 levels)	] '						25	38	пş
tPLH	A, B, C, or D	w			23	35		17	26	ns
<sup>t</sup> PHL	(3 levels)	,			22	33		19	30	1115
tPLH	Strobe G	Y	CL = 15 pF,					21	33	ns
tPHL .	Strone G		$R_L = 400 \Omega$					22	33	1115
tPLH	Strobe $\vec{G}$	w	See Note 4	1	5.5	24		14	21	ns
tPH L	Strope G	**	200 17013 17		21	30		15	23	,''\$
†PLH	DO thru D7	Y						13	20	
₹PHL	Bo till a D	<u> </u>						18	27	ns
tPLH	E0 thru E15, or	W			8.5	14		8	14	
tPHL .	D0 thru D7				13	20		8	14	ns

 $f_{\text{tpLH}} = \text{propagation delay time, low-to-high-level output}$   $t_{\text{pHL}} = \text{propagation delay time, high-to-low-level output}$ 

<sup>†</sup> All typical values at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25 ^{\circ}\text{C}$ .

Not more than one output of the '151A should be shorted at a time.

NOTE 3: ICC is measured with the strobe and data select inputs at 4.5 V, all other inputs and outputs open.

NOTE 4: Load circuits and voltage waveforms are shown in Section 1.

#### recommended operating conditions

	S	N54LS	151	Si	N74LS1	51	LINICT
	MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Supply voltage, VCC	4.5	5	b,b	4.75	5	5.25	٧
High-level output current, IOH			-400			-400	μА
Low-level output current, IOL			4			8	mA
Operating free-air temperature, TA	-65		125	0		70	C

#### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	DAGAMETEO	TEST CONDIT	unant.	s	N54LS1	 51	s	N74LS1	51	UNIT
	PARAMETER	TEST CONDIT	IONS .	MIN	ΤΥ₽ <sup>‡</sup>	MAX	MIN	TYP‡	MAX	UNIT
VIH	High-level input voltage			2			2			٧
VIL	Low-level input voltage					0.7			0.B	٧
Vik	Input clamp voltage	V <sub>CC</sub> - MIN, I <sub>f</sub> = -1	8 mA			- 1.5			-1.5	٧
Vон	High-level output voltage	V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V <sub>IL</sub> = V <sub>IL</sub> max, I <sub>OH</sub> =		2.5	3.4		2.7	3.4		٧
VoL	Low-level output voltage	V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V <sub>II</sub> = V <sub>II</sub> max	2 V, I <sub>OL</sub> = 4 mA I <sub>OL</sub> = 8 mA		0.25	0.4		0.25 0.35	0.4 0.5	٧
Ιţ	Input current at maximum input voltage	$V_{CC} = MAX$ , $V_1 = 7$				0.1			0.1	mA
ЧН	High-level input current	$V_{CC} = MAX$ , $V_1 = 2$ .	7 V			20			20	μΑ
IIL	Low-level input current	$V_{CC} = MAX$ , $V_{I} = 0$ .	4 V			-0.4			-0.4	mA
los	Short-circuit output current§	V <sub>CC</sub> = MAX		- 20		- 100	- 20		- 100	mΑ
lcc	Supply current	V <sub>CC</sub> = MAX, Outputs All inputs at 4.5 V	open,		6.0	10		6.0	10	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable device type.

## switching characteristics, VCC = 5 V, TA 25 °C

PARAMETER¶	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	ТҮР	MAX	UNIT
<sup>†</sup> PLH	A, B, or C	Y			27	43	
tPHL	(4 levels)	ı .			18	30	ns
tPLH	A, B, or C	W	1		14	23	
tPHL	(3 levels)				20	32	ns
<sup>t</sup> PLH	Strobe G	Y	0. 15.5		26	42	
t <sub>PHL</sub>	2 strope G	irobe d	C <sub>L</sub> = 15 pF,		20	32	ns
<sup>†</sup> PLH	Strobe G	W	R <sub>L</sub> = 2 kΩ,		15	24	
tpHL	Strope G	W See Note 4	18	30	ns		
t <sub>PLH</sub>			]		20	32	
tpHL	Any D	Y	İ		16	26	ns
tPLH	A D	w			13	21	
<sup>†</sup> PHL	Any D	vv			12	20	ns



 $<sup>^{\</sup>ddagger}$  All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25 °C.  $^{\$}$  Not more than one output should be shorted at a time and duration of short-circuit should not exceed one second.

<sup>¶</sup>tpLH = propagation delay time, low-to-high-level output tpHL = propagation delay time, high-to-low-level output NOTE 4: Load circuits and voltage waveforms are shown in Section 1.

## SN54S151, SN74S151 DATA SELECTORS/MULTIPLEXERS

#### recommended operating conditions

	S	N54S1	51		N74S1	51	
	MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Supply voltage, V <sub>CC</sub>	4.5	5	5.5	4.75	5	5.25	V
High-level output current, IOH		_	-1			-1	mA
Low-level output current, IOL		-	20			20	πА
Operating free-air temperature, TA	55		125	0		70	°C

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	PARAMETER	TEST CONDITIONST		MIN	TYP‡	MAX	UNIT
VIH	High-level input voltage			2			V
VIL	Low-level input voltage					0.8	V
Vik	Input clamp voltage	V <sub>CC</sub> = MIN, I <sub>I</sub> = -18 mA				-1.2	V
· · · · ·	High-level output voltage	V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V,	SN54S151	2.5	3.4		
νон	riign-rever ou tout vortage	VIL = 0.8 V, IOH = -1 mA	SN74S151	2.7	3.4		V
VOL	Low-level output voltage	VCC = MIN, VIH = 2 V,	-			a c	v
VOL	Low-level output voltage	V <sub>IL</sub> = 0.8 V, I <sub>OL</sub> = 20 mA	i			0.5	v
l <sub>k</sub>	Input current at maximum input voltage	VCC = MAX, V1 = 5.5 V				1	mA
ΉН	High-level input current	V <sub>CC</sub> = MAX, V <sub>I</sub> = 2.7 V				50	μА
1 <sub>IL</sub>	Low-level input current	V <sub>CC</sub> - MAX, V <sub>I</sub> = 0.5 V				-2	mA
los	Short-circuit output current 8	V <sub>CC</sub> = MAX		-40		-100	mA
¹cc	Supply current	VCC = MAX, All inputs at 4.5 V,			45	70	mA
		All outputs open	Ī		. •		,

<sup>†</sup>For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable device

# switching characteristics. V<sub>CC</sub> = 5 V. T<sub>A</sub> 25 °C

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<sup>t</sup> PLH	A, B, or C	Y	•		12	18	
<sup>t</sup> PHL	(4 levels)	ţ	ļ		12	18	ns
tPLH	A, B, or C	w	1		10	15	
<sup>†</sup> PHL	(3 levels)				9	13.5	ns
tPLH	Any D	Y	G 45-5		8	12	
₹PHL	Any	,	C <sub>L</sub> = 15 pF,		8	12	ns
tpLH	Any D	W	R <sub>L</sub> = 280 kΩ, See Note 4		4.5 4.5	7	ns
tPHL	T ANY D	VV	See Note 4			7	
tpLH	Strobe G	Y			11	16.5	
tpHL	3trope G	т			12	18	ns
<sup>t</sup> PLH	Strobe G	w			9	13	
tPHL	Strope G				8.5	12	กร

TtpLH = propagation delay time, low-to-high-level output



 $<sup>\</sup>ddagger$ All typical values are at  $\lor$ CC = 5  $\lor$ ,  $\lnot$ A = 25°C.  $\ddagger$ Not more than one output should be shorted at a time, and duration of the short-circuit should not exceed one second.

tpHL - propagation delay time, high-to-low-level output
NOTE 4: Load circuits and voltage waveforms are shown in Section 1.

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## **PACKAGING INFORMATION**

Orderable part number	Status (1)	Material type	Package   Pins	Package qty   Carrier	<b>RoHS</b> (3)	Lead finish/ Ball material	MSL rating/ Peak reflow	Op temp (°C)	Part marking (6)
76010012A	Active	Production	LCCC (FK)   20	55   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	76010012A SNJ54LS 151FK
7601001EA	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	7601001EA SNJ54LS151J
7601001EA	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	7601001EA SNJ54LS151J
JM38510/07901BEA	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 07901BEA
JM38510/07901BEA.A	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 07901BEA
JM38510/07901BFA	Active	Production	CFP (W)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 07901BFA
JM38510/07901BFA.A	Active	Production	CFP (W)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 07901BFA
JM38510/30901B2A	Active	Production	LCCC (FK)   20	55   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901B2A
JM38510/30901B2A	Active	Production	LCCC (FK)   20	55   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901B2A
JM38510/30901B2A.A	Active	Production	LCCC (FK)   20	55   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901B2A
JM38510/30901B2A.A	Active	Production	LCCC (FK)   20	55   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901B2A
JM38510/30901BEA	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901BEA
JM38510/30901BEA	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901BEA
JM38510/30901BEA.A	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901BEA
JM38510/30901BEA.A	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901BEA
JM38510/30901BFA	Active	Production	CFP (W)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901BFA





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Orderable part number	Status	Material type	Package   Pins	Package qty   Carrier	(3)	Lead finish/ Ball material	MSL rating/ Peak reflow	Op temp (°C)	Part marking (6)
JM38510/30901BFA	Active	Production	CFP (W)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901BFA
JM38510/30901BFA.A	Active	Production	CFP (W)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901BFA
JM38510/30901BFA.A	Active	Production	CFP (W)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901BFA
M38510/07901BEA	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 07901BEA
M38510/07901BFA	Active	Production	CFP (W)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 07901BFA
M38510/30901B2A	Active	Production	LCCC (FK)   20	55   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901B2A
M38510/30901B2A	Active	Production	LCCC (FK)   20	55   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901B2A
M38510/30901BEA	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901BEA
M38510/30901BEA	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901BEA
M38510/30901BFA	Active	Production	CFP (W)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901BFA
M38510/30901BFA	Active	Production	CFP (W)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	JM38510/ 30901BFA
SN54LS151J	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	SN54LS151J
SN54LS151J	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	SN54LS151J
SN54LS151J.A	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	SN54LS151J
SN54LS151J.A	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	SN54LS151J
SN54S151J	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	SN54S151J
SN54S151J.A	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	SN54S151J
SN74LS151D	Obsolete	Production	SOIC (D)   16	-	-	Call TI	Call TI	0 to 70	LS151
SN74LS151D	Obsolete	Production	SOIC (D)   16	-	-	Call TI	Call TI	0 to 70	LS151
SN74LS151DR	Active	Production	SOIC (D)   16	2500   LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS151
SN74LS151DR	Active	Production	SOIC (D)   16	2500   LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS151
SN74LS151DR.A	Active	Production	SOIC (D)   16	2500   LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS151
SN74LS151DR.A	Active	Production	SOIC (D)   16	2500   LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS151





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Orderable part number	erable part number Status Material type Package   Pin (2)		Package   Pins	Package qty   Carrier	<b>RoHS</b> (3)	Lead finish/ Ball material	MSL rating/ Peak reflow	Op temp (°C)	Part marking (6)	
SN74LS151N	Active	Production	PDIP (N)   16	25   TUBE	Yes	NIPDAU	N/A for Pkg Type	0 to 70	SN74LS151N	
SN74LS151N	Active	Production	PDIP (N)   16	25   TUBE	Yes	NIPDAU	N/A for Pkg Type	0 to 70	SN74LS151N	
SN74LS151N.A	Active	Production	PDIP (N)   16	25   TUBE	Yes	NIPDAU	N/A for Pkg Type	0 to 70	SN74LS151N	
SN74LS151N.A	Active	Production	PDIP (N)   16	25   TUBE	Yes	NIPDAU	N/A for Pkg Type	0 to 70	SN74LS151N	
SN74LS151NE4	Active	Production	PDIP (N)   16	25   TUBE	Yes	NIPDAU	N/A for Pkg Type	0 to 70	SN74LS151N	
SN74LS151NE4	Active	Production	PDIP (N)   16	25   TUBE	Yes	NIPDAU	N/A for Pkg Type	0 to 70	SN74LS151N	
SN74LS151NSR	Active	Production	SOP (NS)   16	2000   LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS151	
SN74LS151NSR	Active	Production	SOP (NS)   16	2000   LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS151	
SN74LS151NSR.A	Active	Production	SOP (NS)   16	2000   LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS151	
SN74LS151NSR.A	Active	Production	SOP (NS)   16	2000   LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS151	
SNJ54LS151FK	Active	Production	LCCC (FK)   20	55   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	76010012A SNJ54LS 151FK	
SNJ54LS151FK	Active	Production	LCCC (FK)   20	55   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	76010012A SNJ54LS 151FK	
SNJ54LS151FK.A	Active	Production	LCCC (FK)   20	55   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	76010012A SNJ54LS 151FK	
SNJ54LS151FK.A	Active	Production	LCCC (FK)   20	55   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	76010012A SNJ54LS 151FK	
SNJ54LS151J	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	7601001EA SNJ54LS151J	
SNJ54LS151J	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	7601001EA SNJ54LS151J	
SNJ54LS151J.A	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	7601001EA SNJ54LS151J	
SNJ54LS151J.A	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	SNPB N/A for Pkg Type		7601001EA SNJ54LS151J	
SNJ54S151J	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	SNJ54S151J	
SNJ54S151J.A	Active	Production	CDIP (J)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	SNJ54S151J	
SNJ54S151W	Active	Production	CFP (W)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	SNJ54S151W	
SNJ54S151W.A	Active	Production	CFP (W)   16	25   TUBE	No	SNPB	N/A for Pkg Type	-55 to 125	SNJ54S151W	

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- (1) Status: For more details on status, see our product life cycle.
- (2) Material type: When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.
- (3) RoHS values: Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.
- (4) Lead finish/Ball material: Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.
- (5) MSL rating/Peak reflow: The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.
- (6) Part marking: There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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#### OTHER QUALIFIED VERSIONS OF SN54LS151, SN74LS151:

Catalog: SN74LS151

Military: SN54LS151

NOTE: Qualified Version Definitions:

- Catalog TI's standard catalog product
- Military QML certified for Military and Defense Applications

# **PACKAGE MATERIALS INFORMATION**

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## TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



#### \*All dimensions are nominal

Device		Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74LS151DR	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.1	8.0	16.0	Q1
SN74LS151NSR	SOP	NS	16	2000	330.0	16.4	8.1	10.4	2.5	12.0	16.0	Q1

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## \*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74LS151DR	SOIC	D	16	2500	353.0	353.0	32.0
SN74LS151NSR	SOP	NS	16	2000	353.0	353.0	32.0



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## **TUBE**



\*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (µm)	B (mm)
76010012A	FK	LCCC	20	55	506.98	12.06	2030	NA
JM38510/07901BFA	W	CFP	16	25	506.98	26.16	6220	NA
JM38510/07901BFA.A	W	CFP	16	25	506.98	26.16	6220	NA
JM38510/30901B2A	FK	LCCC	20	55	506.98	12.06	2030	NA
JM38510/30901B2A.A	FK	LCCC	20	55	506.98	12.06	2030	NA
JM38510/30901BFA	W	CFP	16	25	506.98	26.16	6220	NA
JM38510/30901BFA.A	W	CFP	16	25	506.98	26.16	6220	NA
M38510/07901BFA	W	CFP	16	25	506.98	26.16	6220	NA
M38510/30901B2A	FK	LCCC	20	55	506.98	12.06	2030	NA
M38510/30901BFA	W	CFP	16	25	506.98	26.16	6220	NA
SN74LS151N	N	PDIP	16	25	506	13.97	11230	4.32
SN74LS151N	N	PDIP	16	25	506	13.97	11230	4.32
SN74LS151N.A	N	PDIP	16	25	506	13.97	11230	4.32
SN74LS151N.A	N	PDIP	16	25	506	13.97	11230	4.32
SN74LS151NE4	N	PDIP	16	25	506	13.97	11230	4.32
SN74LS151NE4	N	PDIP	16	25	506	13.97	11230	4.32
SNJ54LS151FK	FK	LCCC	20	55	506.98	12.06	2030	NA
SNJ54LS151FK.A	FK	LCCC	20	55	506.98	12.06	2030	NA



SOP



- 1. All linear dimensions are in millimeters. Dimensions in parenthesis are for reference only. Dimensioning and tolerancing
- per ASME Y14.5M.

  2. This drawing is subject to change without notice.

  3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 mm, per side.
- 4. This dimension does not include interlead flash. Interlead flash shall not exceed 0.25 mm, per side.



SOF



## NOTES: (continued)

- 5. Publication IPC-7351 may have alternate designs.
- 6. Solder mask tolerances between and around signal pads can vary based on board fabrication site.



SOF



#### NOTES: (continued)

- 7. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
- 8. Board assembly site may have different recommendations for stencil design.



# W (R-GDFP-F16)

# CERAMIC DUAL FLATPACK



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only.
- E. Falls within MIL STD 1835 GDFP2-F16



8.89 x 8.89, 1.27 mm pitch

LEADLESS CERAMIC CHIP CARRIER

This image is a representation of the package family, actual package may vary. Refer to the product data sheet for package details.



## 14 LEADS SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package is hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
- E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

# N (R-PDIP-T\*\*)

# PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- The 20 pin end lead shoulder width is a vendor option, either half or full width.



# D (R-PDS0-G16)

## PLASTIC SMALL OUTLINE



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AC.



## **MECHANICAL DATA**

# NS (R-PDSO-G\*\*)

# 14-PINS SHOWN

## PLASTIC SMALL-OUTLINE PACKAGE



- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



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