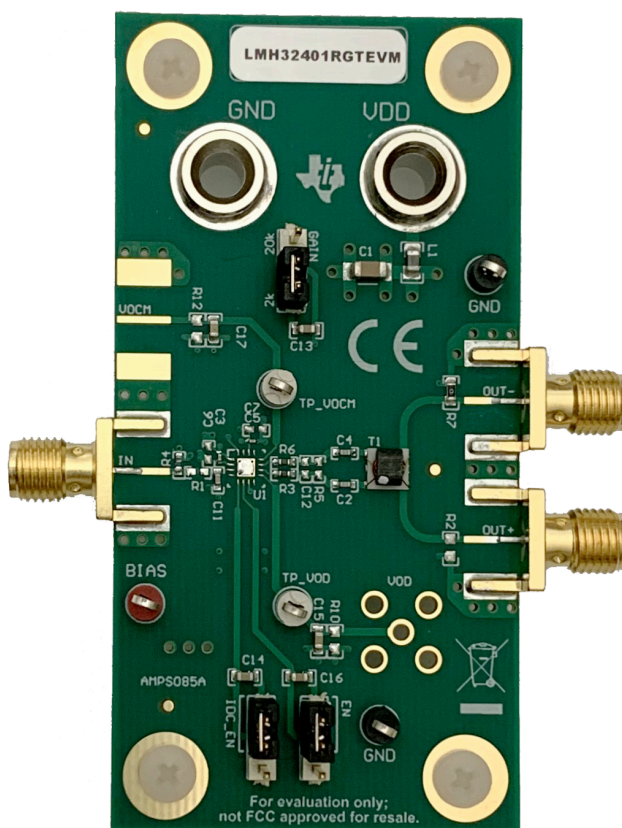


LMH32401RGTEVM Evaluation Module



This user's guide describes the characteristics, operation, and use of the LMH32401RGTEVM. This evaluation module (EVM) is an evaluation and development kit for evaluating the LMH32401IRGT device, a programmable-gain, single-ended input to differential output transimpedance amplifier for light detection and ranging (LIDAR) applications and laser distance measurement systems. A complete circuit description as well as schematic diagram and bill of materials are included in this document.

Throughout this document, the abbreviation *EVM* and the term *evaluation module* are synonymous with the LMH32401RGTEVM.

The following related documentation is available through the Texas Instruments web site at www.ti.com.

Related Documentation

| Device | Literature Number |
|-------------------------------------|-------------------------|
| LMH32401 data sheet | SBOS965 |

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1 Introduction

The LMH32401RGTEVM is an evaluation module (EVM) for the LMH32401IRGT differential output transimpedance amplifier. The EVM features a transformer to convert the output to a 50- Ω single-ended connection for easy measurement with standard test equipment, and the option to convert to fully-differential DC-coupled signals. The input is 50- Ω matched and configured by default to receive a voltage input that is converted to a current through a 2-k Ω resistor, but can be reconfigured to receive a true current input with optional photodiode bias connections on the back of the board. The EVM also includes easy-to-use jumpers to control the device gain, input bias current cancellation, and shutdown functions.

1.1 Features

- Configured for single-ended voltage input and output with 50- Ω matched SMA connections
- Includes options to connect a current input source with bias connections for edge mounting photodiodes
- Optional DC-coupled differential output configuration
- Jumpers included for easy control of gain, bias current cancellation, and power-down
- Designed for single 3.3-V supply operation

1.2 EVM Specifications

Table 1 lists the typical performance specifications for the LMH32401RGTEVM.

Table 1. LMH32401RGTEVM Specifications

| Specification | Typical Value Range |
|--|---------------------|
| Single-supply voltage range (VEE = ground) | 3 V to 3.45 V |
| Quiescent current | 29 mA |
| Output voltage swing (VCC = 3.3 V, 100- Ω load) | 5 V _{pp} |
| Linear output current (VCC = 3.3 V, 25- Ω load) | 26.6 mA |

2 Power Connections

The LMH32401RGTEVM is equipped with a wire socket to easily connect power. The positive supply input is labeled VDD and ground is labeled GND.

3 Input and Output Connections

The LMH32401RGTEVM is equipped with SMA connectors for easy connection of signal generators and analysis equipment. As shipped, the EVM is configured for a single-ended input and output, both with 50- Ω termination. The differential output of the amplifier is converted to a single-ended output through transformer T1 on the board. OUT+ is the output connector for single-ended output signals, and is terminated to 50- Ω single-ended. To use the EVM with a DC-coupled differential output, remove resistor R7 and transformer T1, and short the connections across the removed transformer input and outputs. When converting to differential, the output resistors may need to be modified to achieve a desired impedance match. For more details and instructions on how to reconfigure the EVM, see the applications section, schematics, and layouts in the [LMH32401 500-MHz, Programmable Gain, Differential Output Transimpedance Amplifier](#) data sheet.

3.1 Gain Control

The amplifier gain can be adjusted using the GAIN jumper on the board to switch between the 2-k Ω and 20-k Ω gain settings. By default, the EVM has R1 populated with a 2-k Ω resistor, which creates an effective voltage gain of 1 V/V and 10 V/V, respectively, for the two different gain settings.

3.2 Input DC Current Cancellation

The LMH32401 device features an input DC current cancellation circuit that is designed to remove any DC current that is present from a typical current input device such as a photodiode. This feature can be enabled or disabled using the IDC_EN jumper on the board. By default, the DC current cancellation circuit is enabled.

3.3 Enable Function

The LMH32401 device includes an optional disable function to put the device in a low-power mode when it is not being used. The EVM ships with jumper EN that can be used to easily enable and disable the device. By default, the board ships with the device enabled.

3.4 Optional VOCM Pin Connection

The common-mode voltage of the differential outputs of the amplifier can be controlled using the VOCM test point or optional SMA connector. If left unconnected, the amplifier output common mode will default to 1.1 V. By default, the transformer on the EVM will AC couple the output of the device and remove the effect of the output common-mode voltage. To match the board to a device that requires DC outputs with a specified common mode (such as an analog to digital converter) the transformer must be removed.

3.5 Option VOD Connection

The LMH32401 device also features a differential output offset pin that controls the DC differential offset of the two outputs. The EVM features a test point VOD as well as an optional through-hole SMA connector footprint to drive this pin. If left floating, the DC output differential voltage defaults to 510 mV. This feature is designed to compensate for unipolar input signals to achieve the maximum dynamic range of the differential outputs. For more information see the [LMH32401 500-MHz, Programmable Gain, Differential Output Transimpedance Amplifier](#) data sheet.

4 Optional EVM Configuration for Photodiode Input

The LMH32401RGTEVM features an optional configuration to accept a photodiode input in a typical TO style package. By removing the IN SMA connector, R4, and C11, and shorting C10 and R1, the trace from IN is shorted to the input of the device. Along with traces A, B, and C on the bottom of the board, the input trace can be used to connect a common three-terminal photodiode in a TO package. The photodiode can be mounted over the edge of the board where the cathode can be connected to the input of the amplifier and the case and anode connections can be soldered to any of the three traces on the bottom of the board. The A, B, and C traces can then be configured to connect to either ground or the BIAS test point where an external bias voltage can be applied. [Table 2](#) shows a list of the resistor connections that can be shorted to connect the traces to bias or ground. By default, the EVM has trace A connected to the bias test point and trace B connected to ground.

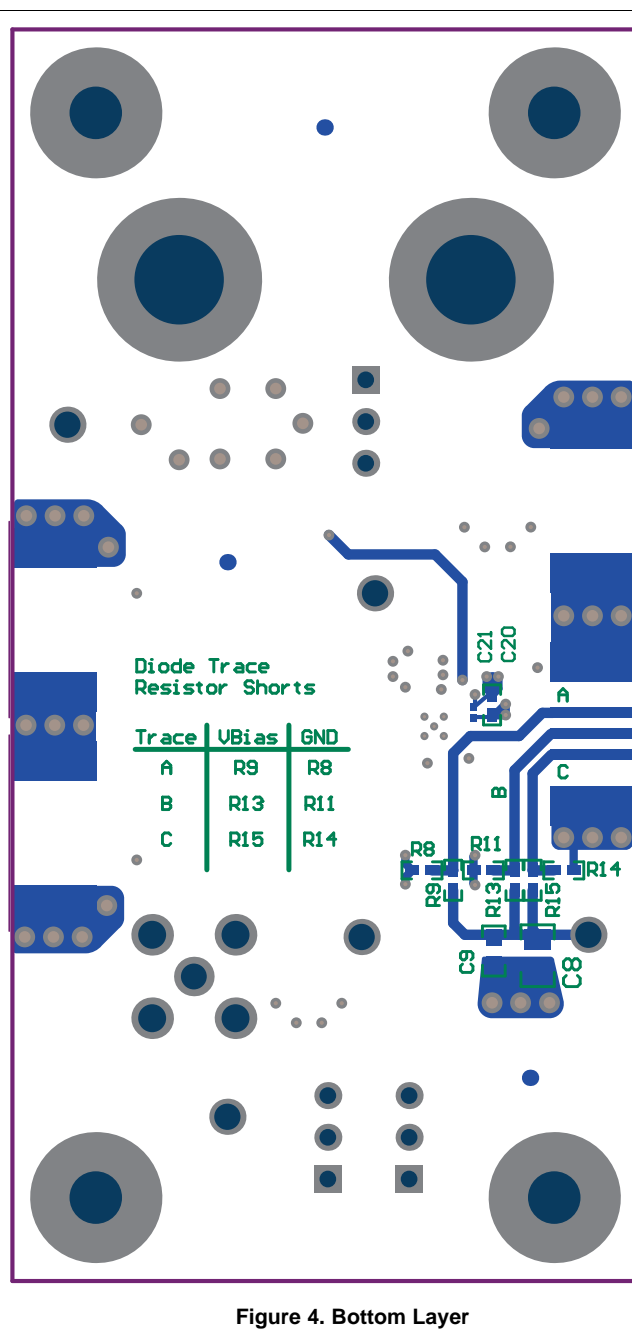
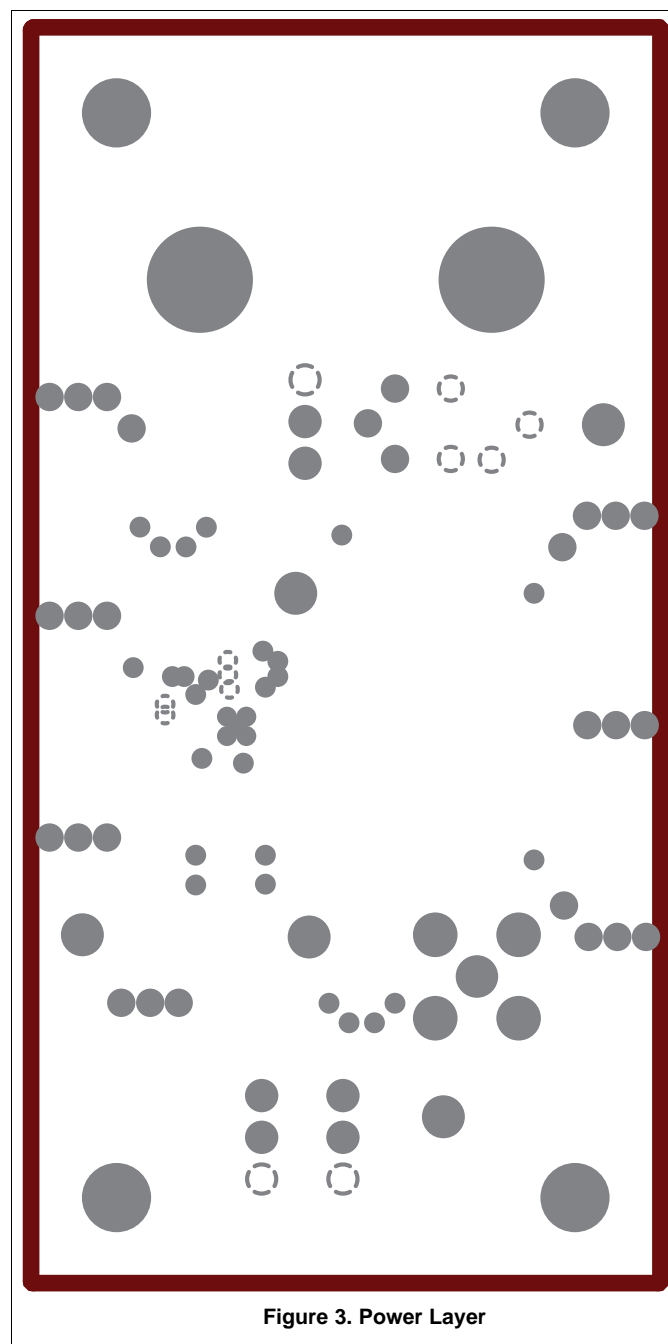
Table 2. EVM Bottom-Side Diode Trace Connections⁽¹⁾

| Trace | Short to Connect to Bias | Short to Connect to Ground |
|-------|--------------------------|----------------------------|
| A | R9 | R8 |
| B | R13 | R11 |
| C | R15 | R14 |

⁽¹⁾ Each trace should only be shorted to either bias or ground. Do not short the traces to both bias and ground simultaneously or a power-supply short could occur.

Figure 1 to Figure 4 illustrate the board layers in top-to-bottom order.





6 Schematic and Bill of Materials

This section provides the schematic and bill of materials (BOM) for the LMH32401RGTEVM.

6.1 Schematic

Figure 5 shows the EVM schematic.

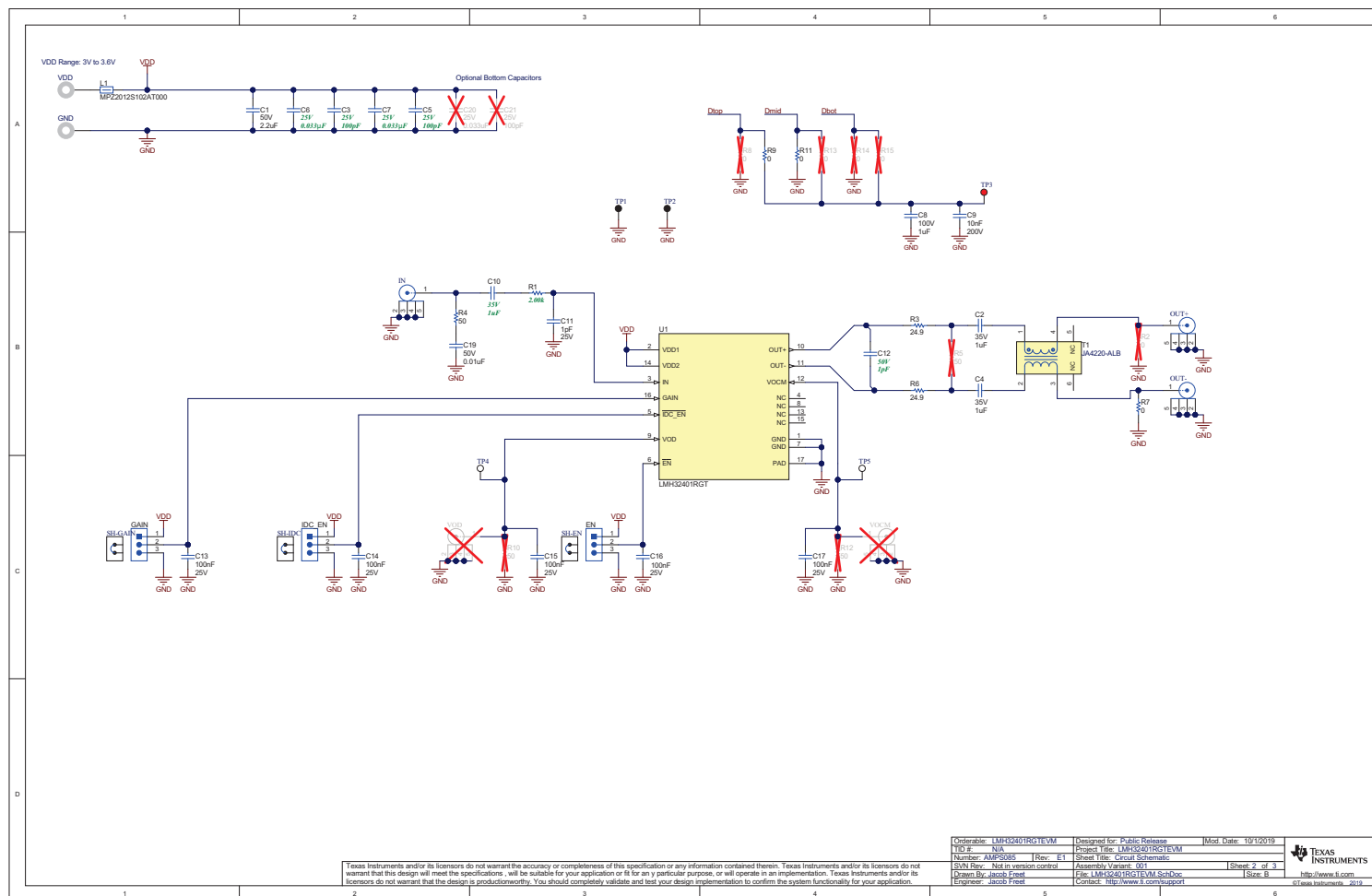


Figure 5. LMH32401RGTEVM Schematic

6.2 Bill of Materials

Table 3 lists the EVM BOM.

Table 3. Bill of Materials

| Item # | Designator | Qty | Value | Part Number | Manufacturer | Description | Package Reference |
|--------|-------------------------|-----|-----------------|---------------------|-----------------------------|---|-------------------------|
| 2 | C1 | 1 | 2.2 μ F | GCM31CR71H225KA55L | MuRata | CAP, CERM, 2.2 μ F, 50 V, \pm 10%, X7R, AEC-Q200 Grade 1, 1206 | 1206 |
| 3 | C2, C4, C10 | 3 | 1 μ F | GRM155R6YA105KE11D | MuRata | CAP, CERM, 1 μ F, 35 V, \pm 10%, X5R, 0402 | 402 |
| 4 | C3, C5 | 2 | 100 pF | CC0201JRNPO8BN101 | Yageo | CAP, CERM, 100 pF, 25 V, \pm 5%, C0G/NP0, 0201 | 201 |
| 5 | C6, C7 | 2 | 0.033 μ F | CC0402KRX7R8BB333 | Yageo | CAP, CERM, 0.033 μ F, 25 V, \pm 10%, X7R, 0402 | 402 |
| 6 | C8 | 1 | 1 μ F | C2012X7S2A105K125AB | TDK | CAP, CERM, 1 μ F, 100 V, \pm 10%, X7S, 0805 | 805 |
| 7 | C9 | 1 | 0.01 μ F | C0603C103K2RACTU | Kemet | CAP, CERM, 0.01 μ F, 200 V, \pm 10%, X7R, 0603 | 603 |
| 8 | C11 | 1 | 1 pF | GJM0335C1E1R0WB01D | MuRata | CAP, CERM, 1 pF, 25 V, \pm 5%, C0G/NP0, 0201 | 201 |
| 9 | C12 | 1 | 1 pF | GJM1555C1H1R0BB01D | MuRata | CAP, CERM, 1 pF, 50 V, \pm 10%, C0G/NP0, 0402 | 402 |
| 10 | C13, C14, C15, C16, C17 | 5 | 0.1 μ F | 06033C104KAT2A | AVX | CAP, CERM, 0.1 μ F, 25 V, \pm 10%, X7R, 0603 | 603 |
| 11 | C19 | 1 | 0.01 μ F | GCM155R71H103KA55D | MuRata | CAP, CERM, 0.01 μ F, 50 V, \pm 10%, C0G/NP0, 0402 | 402 |
| 12 | EN, GAIN, IDC_EN | 3 | | PBC03SAAN | Sullins Connector Solutions | Header, 100 mil, 3 x 1, Gold, TH | PBC03SAAN |
| 13 | GND, VDD | 2 | | 575-4 | Keystone | Standard Banana Jack, Uninsulated, 5.5 mm | Keystone_575-4 |
| 14 | H1, H2, H3, H4 | 4 | | NY PMS 440 0025 PH | B&F Fastener Supply | Machine Screw, Round, #4-40 x 1/4, Nylon, Phillips panhead | Screw |
| 15 | H5, H6, H7, H8 | 4 | | 1902C | Keystone | Standoff, Hex, 0.5"L #4-40 Nylon | Standoff |
| 16 | IN, OUT-, OUT+ | 3 | | 142-0701-851 | Cinch Connectivity | Connector, End launch SMA, 50 Ω , SMT | SMA End Launch |
| 17 | L1 | 1 | 1000 Ω | MPZ2012S102AT000 | TDK | Ferrite Bead, 1000 Ω @ 100 MHz, 1.5 A, 0805 | 805 |
| 18 | R1 | 1 | 2.00 k Ω | CRCW02012K00FKED | Vishay-Dale | RES, 2.00 k, 1%, 0.05 W, 0201 | 201 |
| 19 | R3, R6 | 2 | 24.9 | CRCW040224R9FKED | Vishay-Dale | RES, 24.9, 1%, 0.063 W, 0402 | 402 |
| 20 | R4 | 1 | 50 | FC0402E50R0BTBST1 | Vishay Thin Film | RES, 50, 0.1%, 0.5 W, 0402 | 402 |
| 21 | R7 | 1 | 0 | CRCW06030000Z0EA | Vishay-Dale | RES, 0, 5%, 0.1 W, 0603 | 603 |
| 22 | R9, R11 | 2 | 0 | CRCW04020000Z0ED | Vishay-Dale | RES, 0, 5%, 0.063 W, 0402 | 402 |
| 23 | SH-EN, SH-GAIN, SH-IDC | 3 | 1 x 2 | SNT-100-BK-G | Samtec | Shunt, 100mil, Gold plated, Black | Shunt |
| 24 | T1 | 1 | 15 μ H | JA4220-ALB | Coilcraft CPS | Transformer, 15 μ H, SMT | 3.81x3.81mm |
| 25 | TP1, TP2 | 2 | | 5006 | Keystone | Test Point, Compact, Black, TH | Black Compact Testpoint |
| 26 | TP3 | 1 | | 5005 | Keystone | Test Point, Compact, Red, TH | Red Compact Testpoint |
| 27 | TP4, TP5 | 2 | | 5007 | Keystone | Test Point, Compact, White, TH | White Compact Testpoint |
| 28 | U1 | 1 | | LMH32401RGT | Texas Instruments | Programmable Gain, Differential Output Transimpedance Amplifier, RGT0016C (VQFN-16) | RGT0016C |

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This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

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