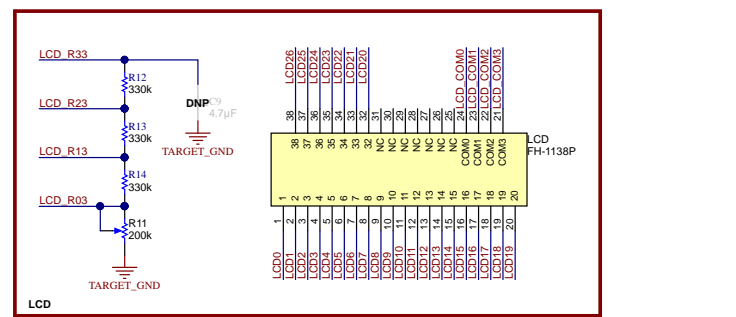
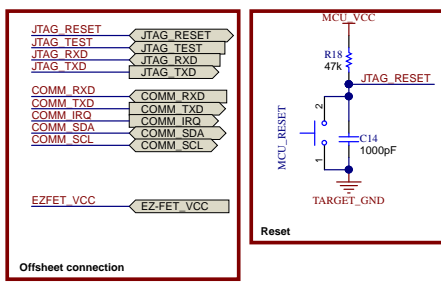
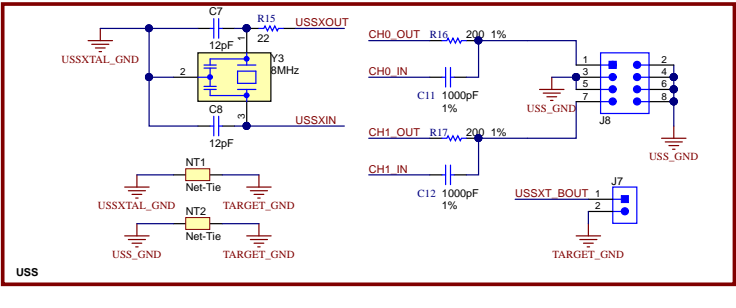
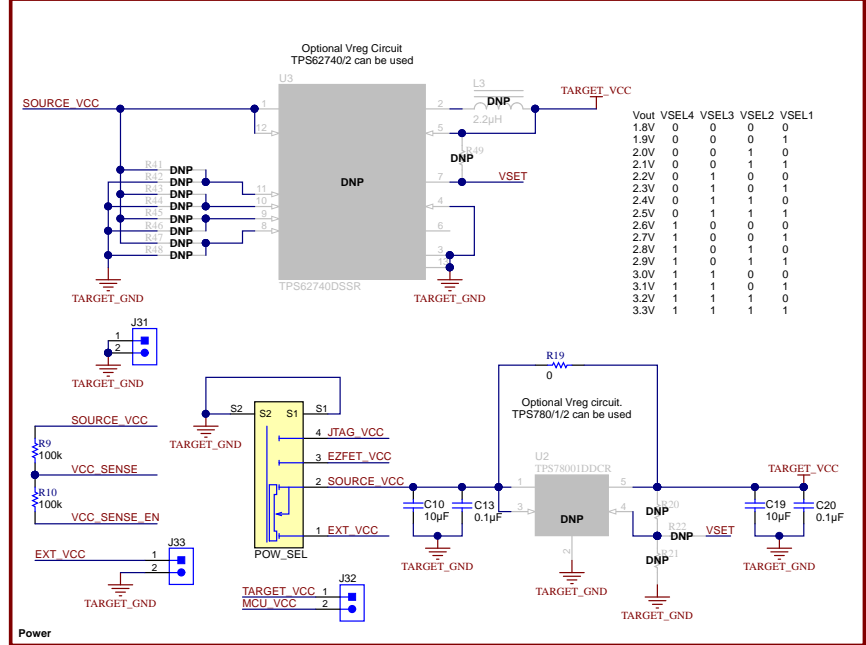
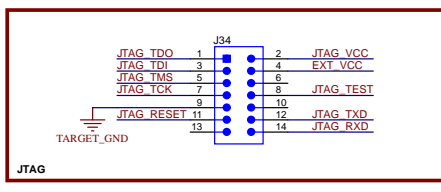
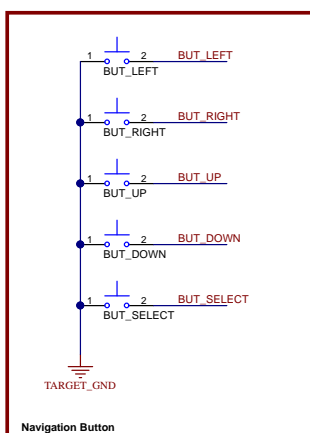
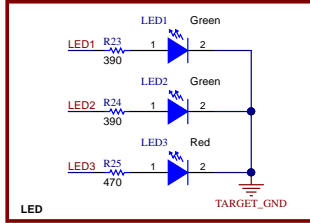
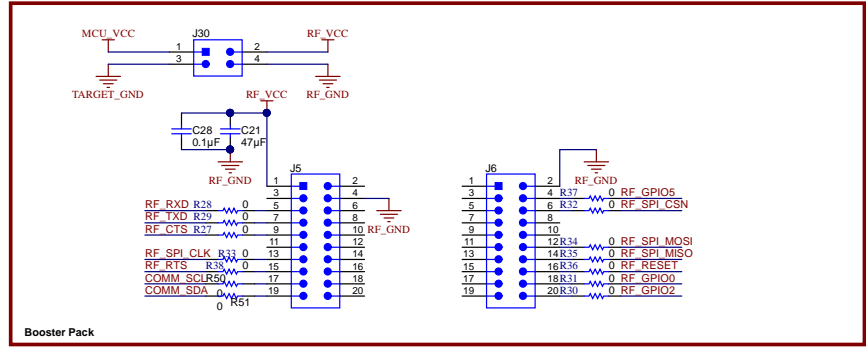
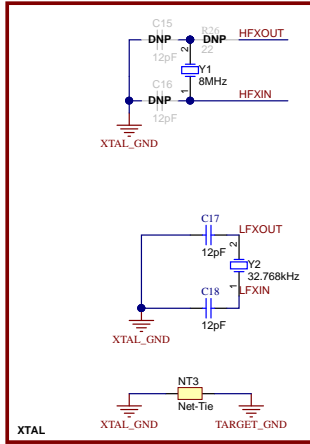
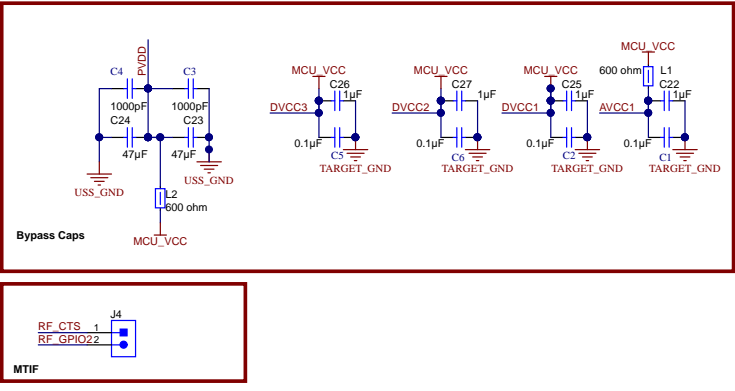


Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: EVM430-FR6047	Designed for: Public Release	Mod. Date: 4/17/2018
TID #:	Project Title: EVM430-FR6047	
Number: MCU018	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 6
Drawn By:	File: MCU018A_Block.SchDoc	Size: B
Engineer: Eddie LaCost	Contact: http://www.ti.com/support	



U1		MSP430FR6047PZR	
AVCC1	100	AVCC1	100
DVCC1	27	DVCC1	27
DVCC2	52	DVCC2	52
DVCC3	76	DVCC3	76
PVDD	88	PVDD	88
LED1	3	P1.0/UC1CLK/TA1.0/A0/C0/REF+/VEREF+	
LED2	4	P1.1/UC1STE/TA4.0/A1/C1/REF+/VEREF+	
JTAG_TXD	18	P1.2/UC1S1M0/UC1TXD/A8/C8	
JTAG_RXD	19	P1.3/UC1S0M1/UC1RXD/A9/C9	
LED3	15	P1.4/TB0.4/UCB0STE/A2/C2	
COMM_IRQ	13	P1.5/TB0.5/UCB0CLK/A3/C3	
COMM_SDA	14	P1.6/UCB0S1M0/UCB0SDA/A4/C4	
COMM_SCL	15	P1.7/UCB0S0M1/UCB0SCL/A5/C5	
COMM_RXD	19	P2.0/UCA0S1M0/UCA0TXD/A6/C6/BSLTX	
COMM_TXD	17	P2.1/UCA0S0M1/UCA0RXD/A7/C7/BSLRX	
VCC_SENSE_EN	2	P2.2/COU7A.0/CA0CLK/A14/C14	
VCC_SENSE	2	P2.3/TA0.0/UCAG0STE/A15/C15	
BUT_LEFT	28	P2.4/TA0CLK/TB0CLK/TA1CLK/LCDS32	
BUT_UP	29	P2.5/TA4.0/LCDS31	
LCD21	38	P2.6/TA4.1/LCDS30	
LCD21	38	P2.7/TA0.2/LCDS21	
BUT_DOWN	31	P3.0/TB0.0/LCDS29	
BUT_SELECT	32	P3.1/TB0.1/LCDS28	
BUT_RIGHT	33	P3.2/TB0.2/LCDS27	
LCD26	35	P3.3/TB0.3/LCDS26	
LCD25	35	P3.4/TB0.4/LCDS25	
LCD24	35	P3.5/TB0.4/LCDS24	
LCD23	35	P3.6/TB0.5/LCDS23	
LCD22	35	P3.7/TB0.6/LCDS22	
LCD16	44	P4.0/RTCLK/LCDS16	
LCD15	45	P4.1/UC0CLK/LCDS15	
LCD14	45	P4.2/UC0STE/LCDS14	
LCD13	45	P4.3/UC0S1M0/UC0TXD/LCDS13	
LCD12	45	P4.4/UC0S0M1/UC0RXD/LCDS12	
LCD11	45	P4.5/TA0CLK/TA1CLK/LCDS11	
LCD10	45	P4.6/TB0CLK/TA1CLK/LCDS10	
LCD9	53	P4.7/DMAEQ/LCDS9	
LCD8	54	P5.0/UC2S1M0/UC2TXD/LCDS8	
LCD7	55	P5.1/UC2S0M1/UC2RXD/LCDS7	
LCD6	58	P5.2/UC2CLK/LCDS6	
LCD5	59	P5.3/UC2STE/LCDS5	
LCD4	59	P5.4/UCB1CLK/LCDS4	
LCD3	59	P5.5/TA0CLK/UCB1S0M1/UCB1SDA/LCDS3	
LCD2	60	P5.6/UCB1S0M1/UCB1SCL/LCDS2	
LCD1	61	P5.7/UCB1STE/LCDS1	
LCD0	62	P6.0/COU7A.LCDS0	
LCD_R03	71	P6.1/R03	
LCD_R13	72	P6.2/R13/LCDREF	
LCD_R23	73	P6.3/R23	
LCD_COM0	63	P6.4/COM0	
LCD_COM1	64	P6.5/COM1	
LCD_COM2	65	P6.6/COM2/LCDS38	
LCD_COM3	66	P6.7/COM3/LCDS37	
DVSS1	26	DVSS1	26
DVSS2	51	DVSS2	51
DVSS3	75	DVSS3	75
AVSS1	99	AVSS1	99
AVSS2	5	AVSS2	5
AVSS3	8	AVSS3	8
AVSS4	11	AVSS4	11
AVSS5	14	AVSS5	14
AVSS6	17	AVSS6	17
AVSS7	20	AVSS7	20
PVSS	87	PVSS	87
PVSS	88	PVSS	88

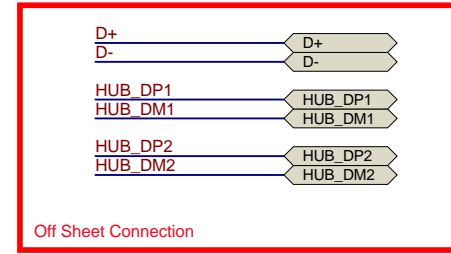
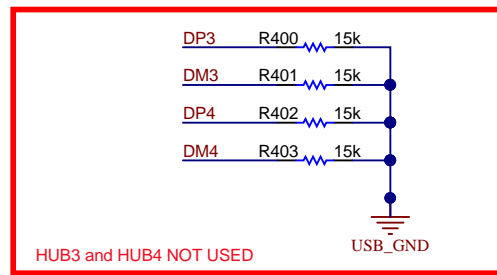
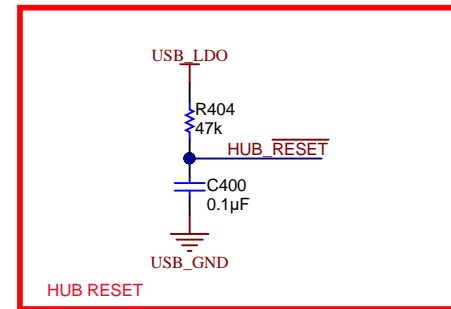
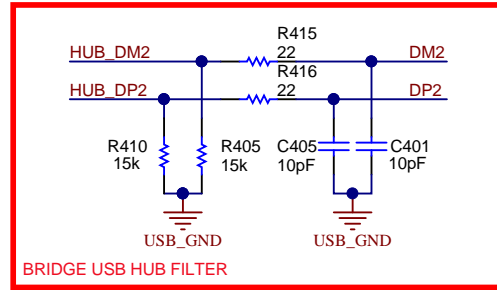
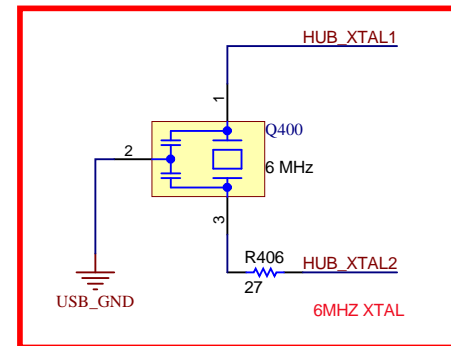
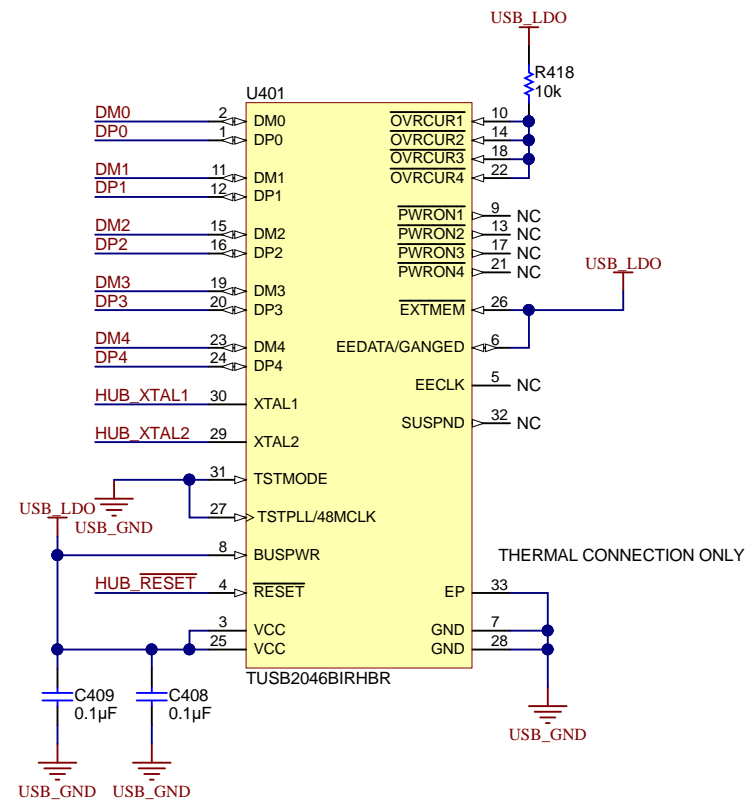
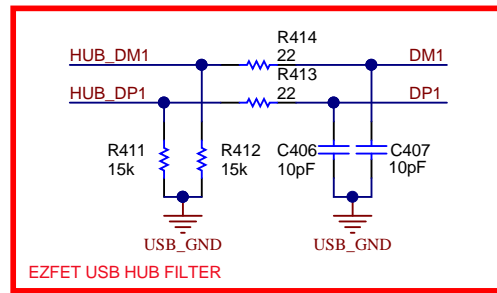
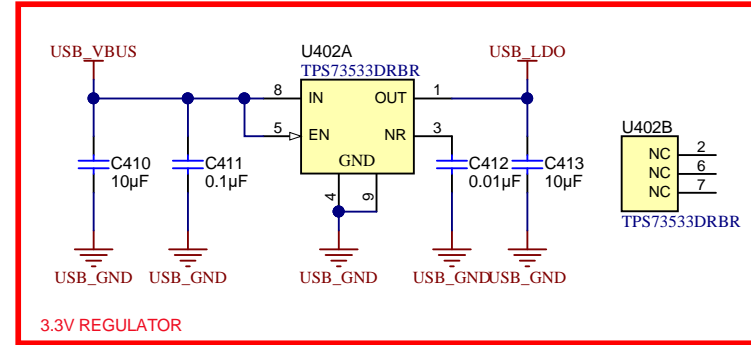
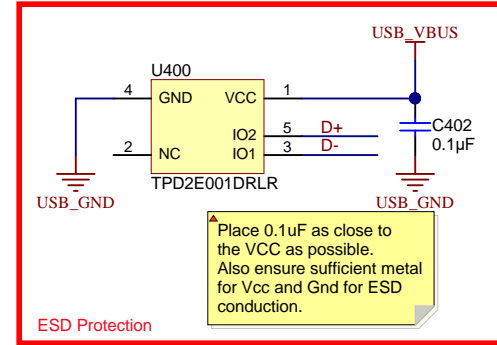
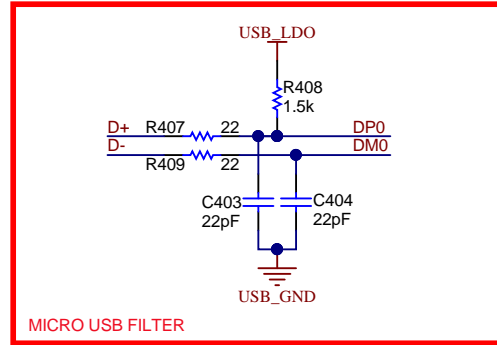


A

B

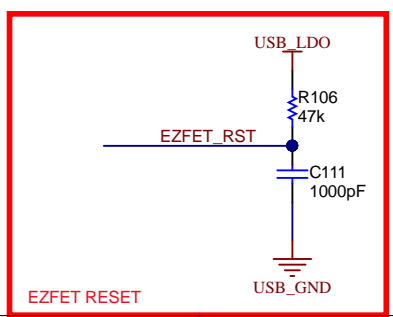
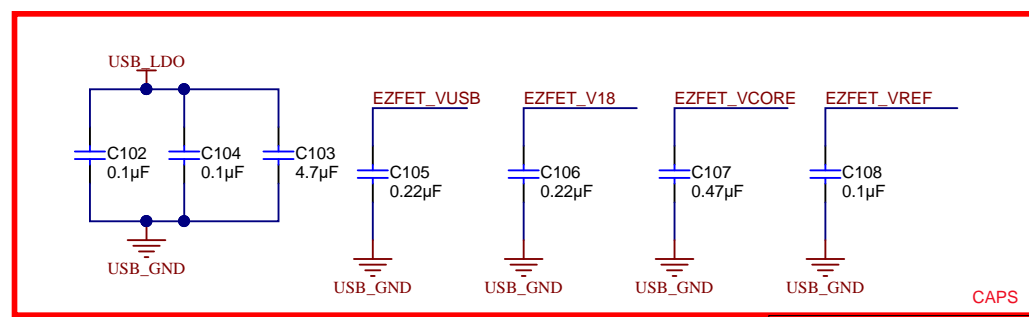
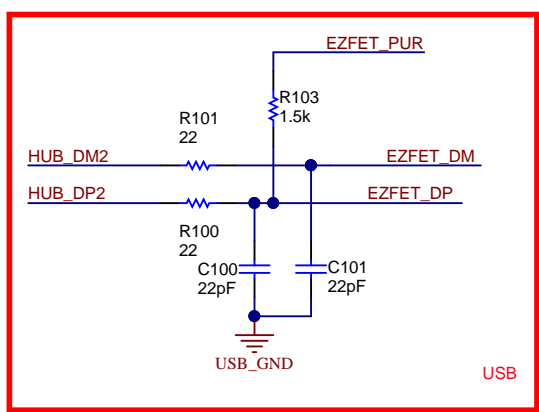
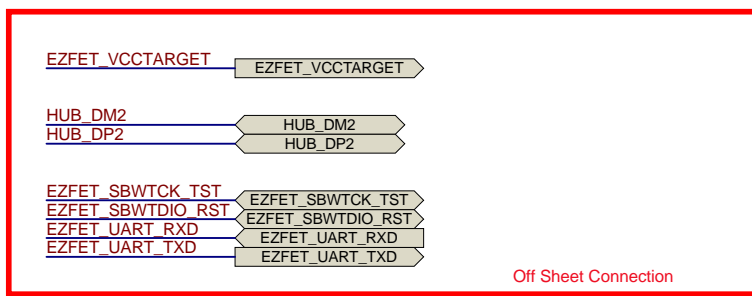
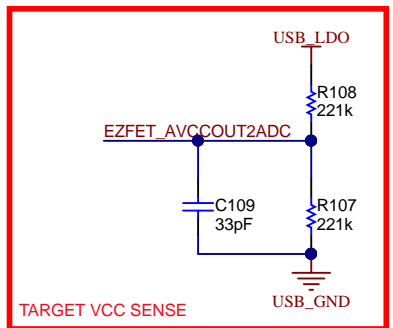
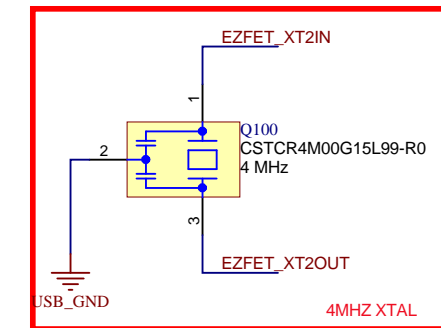
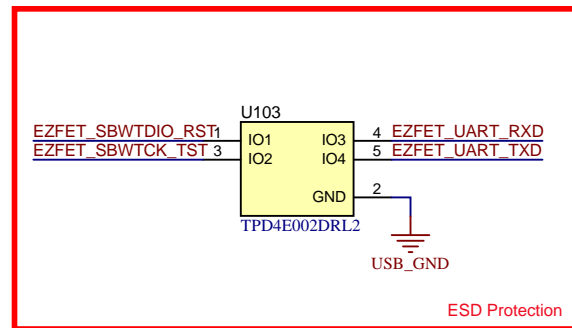
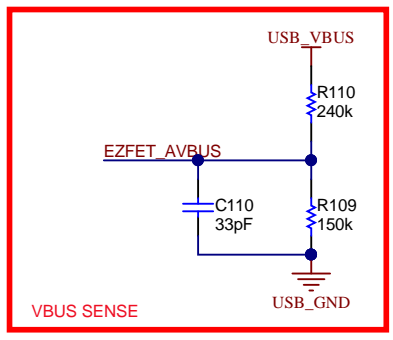
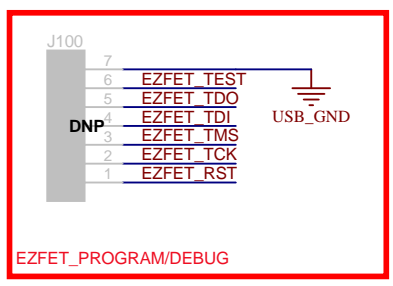
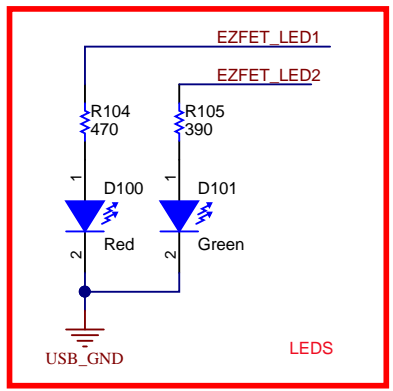
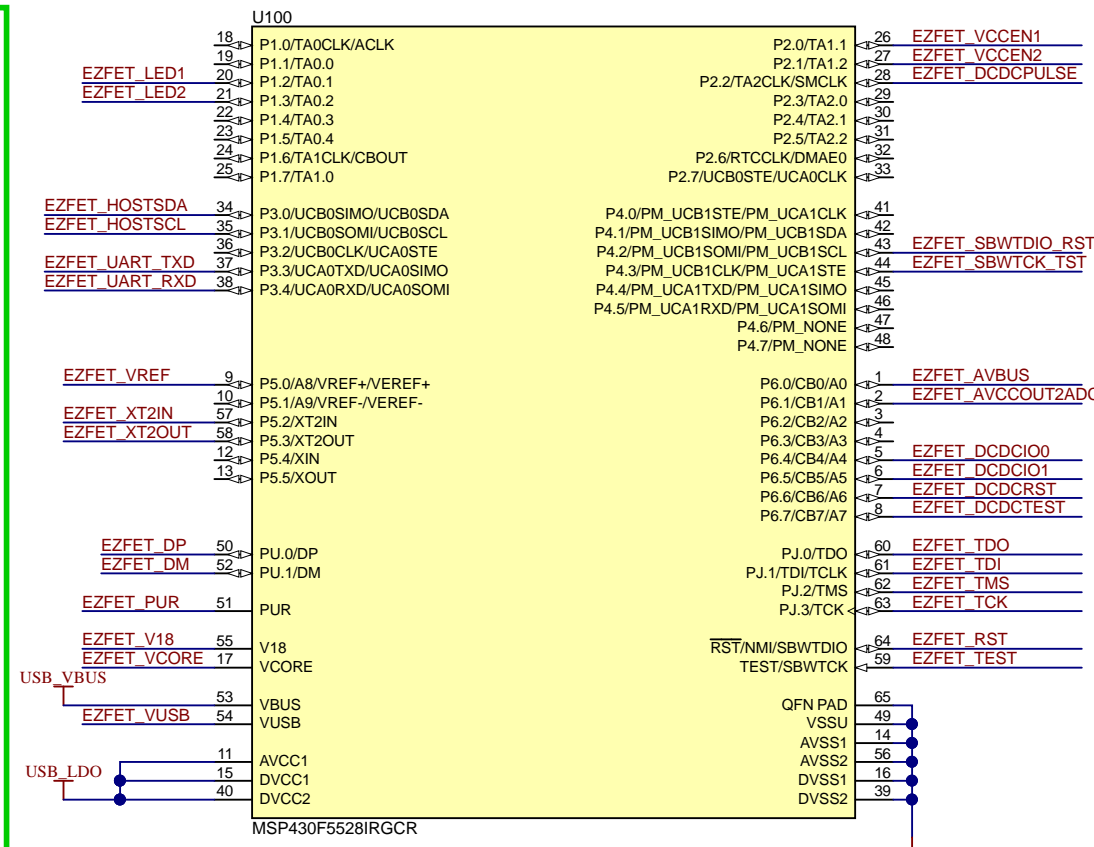
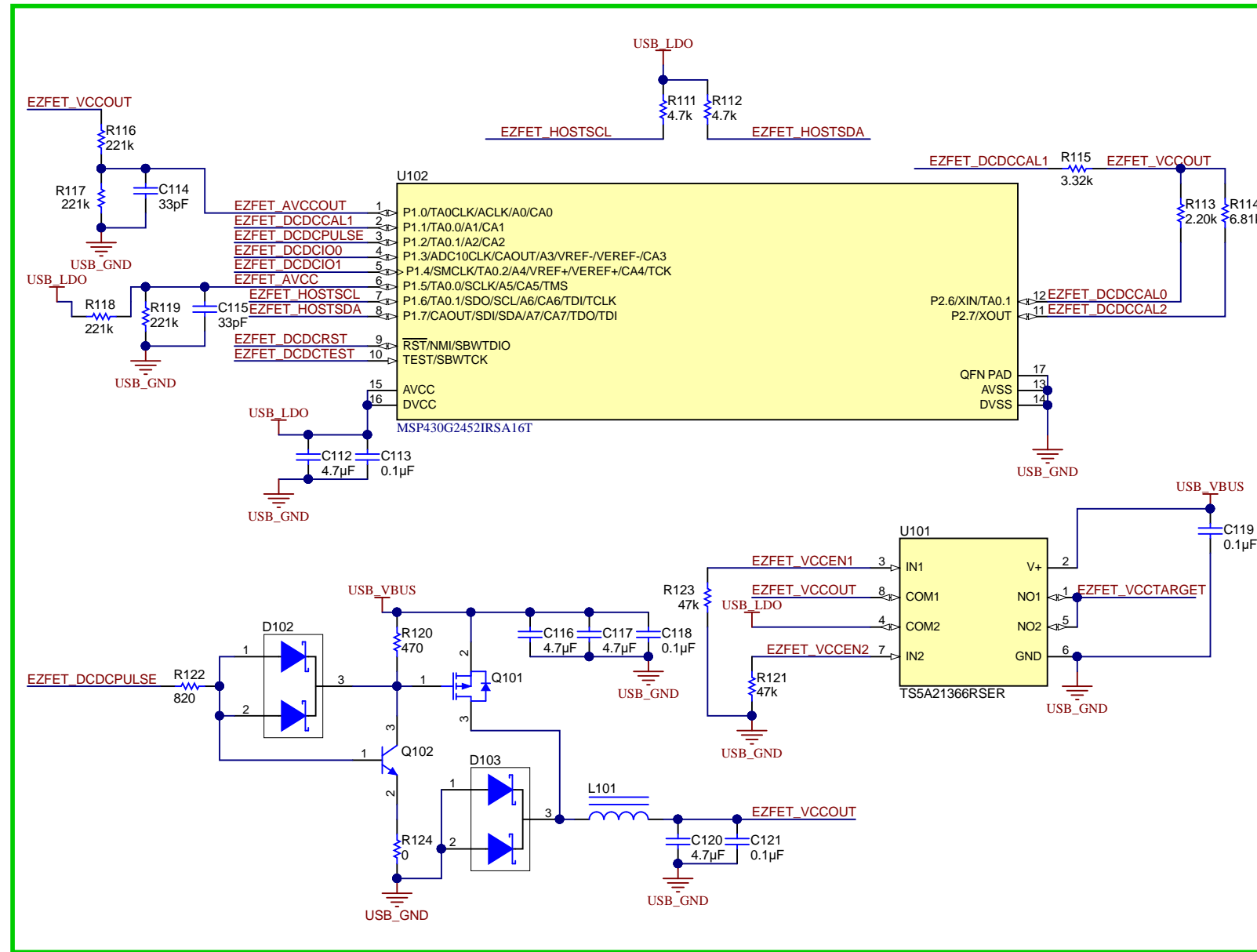
C

D

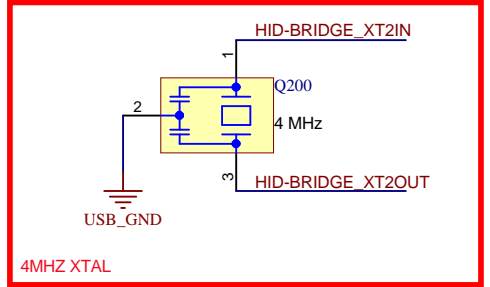
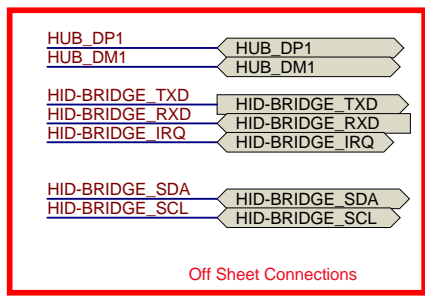
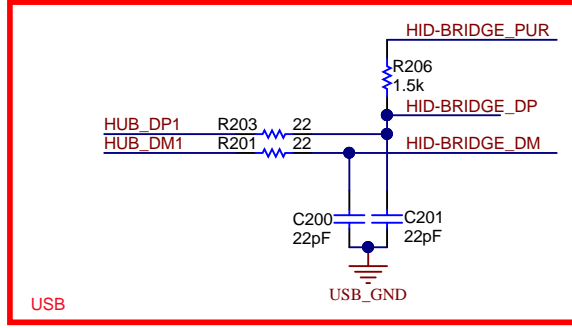
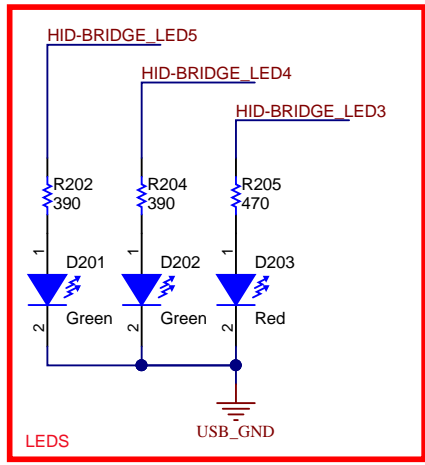
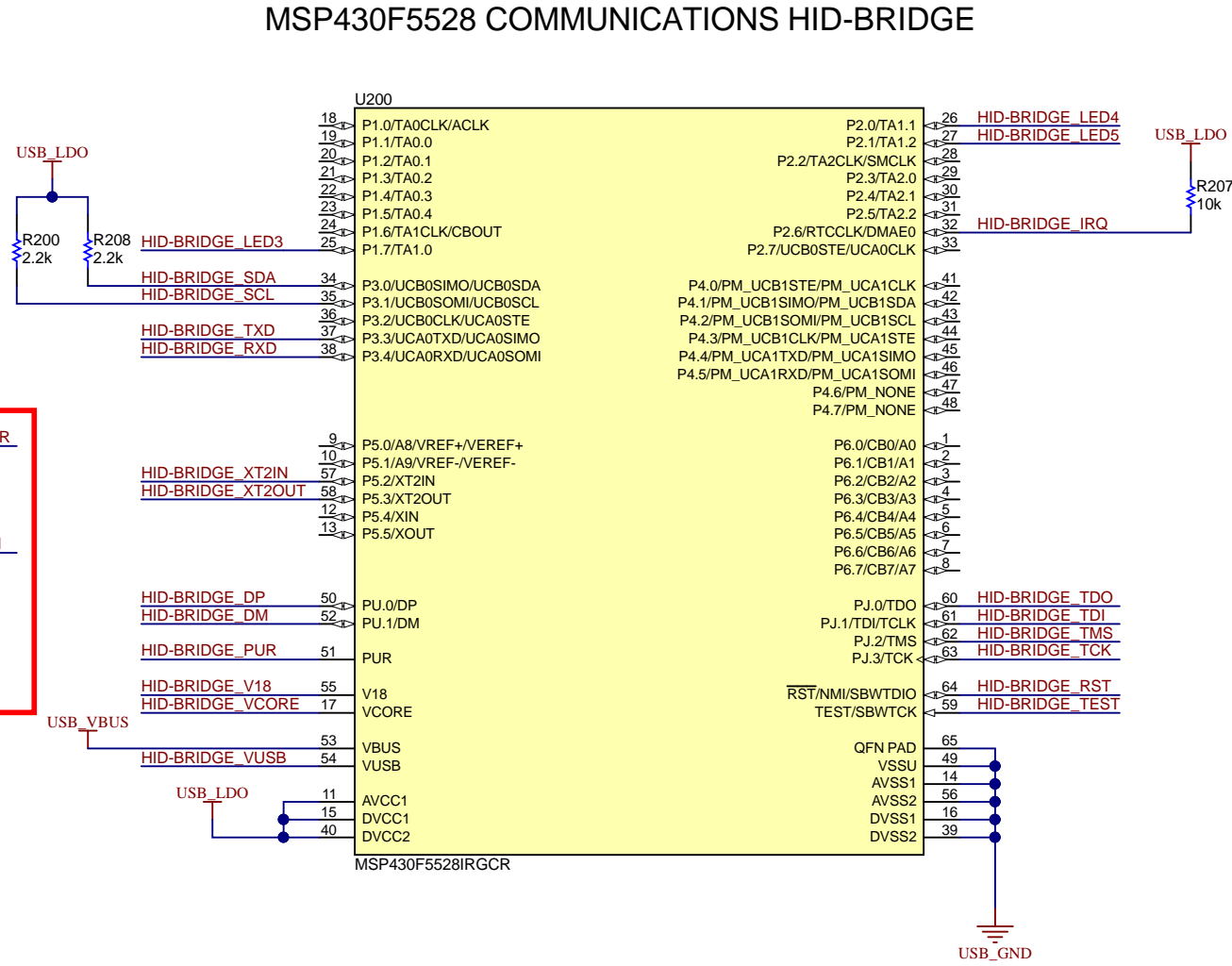
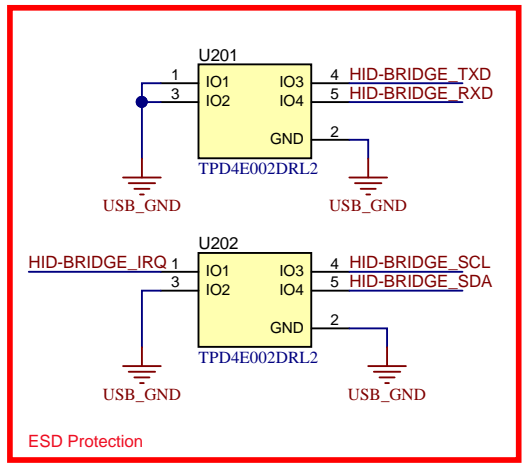
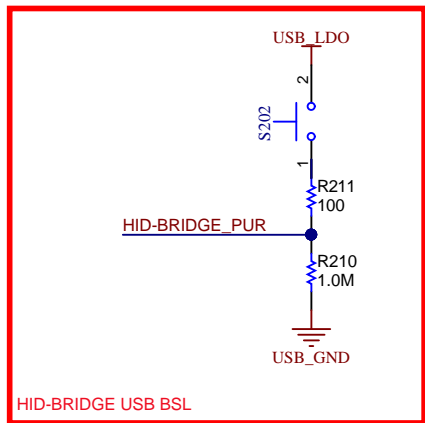
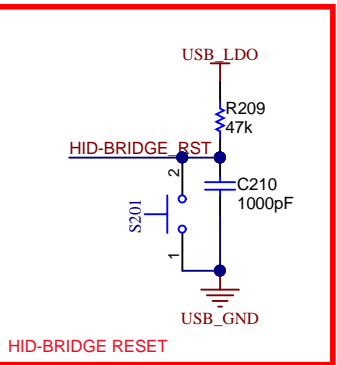
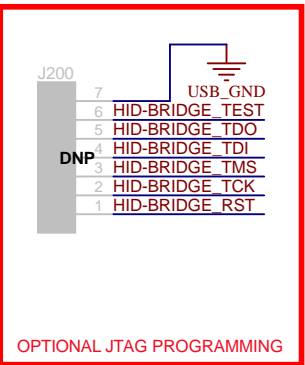
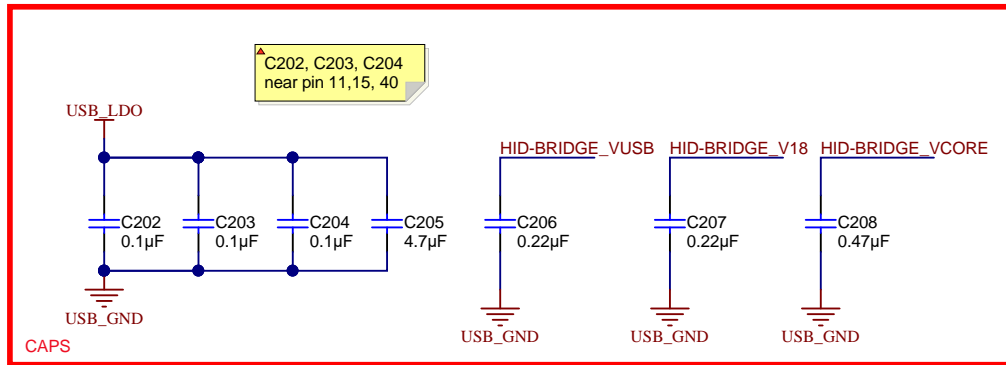


Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

MSP430F5528 EZFET-LITE



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

PCB Number: MCU018
PCB Rev: A

IPCB

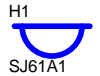
Logo1
PCB
LOGO
Texas Instruments

Logo3
PCB
LOGO
FCC disclaimer

Logo2
PCB
LOGO
WEEE logo



LOGO4



H1
SJ61A1



H2
SJ61A1



H3
SJ61A1



H4
SJ61A1



FID1
DNP
Fiducial



FID2
DNP
Fiducial



FID3
DNP
Fiducial

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.



SH-J1



SH-J2



SH-J3



SH-J4



SH-J5



SH-J6



SH-J7



SH-J8



SH-J9



SH-J10



SH-J11

Orderable: EVM430-FR6047	Designed for: Public Release	Mod. Date: 4/17/2018
TID #:	Project Title: EVM430-FR6047	
Number: MCU018	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 6 of 6
Drawn By:	File: MCU018A_Hardware.SchDoc	Size: B
Engineer: Eddie LaCost	Contact: http://www.ti.com/support	

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.



IMPORTANT NOTICE FOR TI DESIGN INFORMATION AND RESOURCES

Texas Instruments Incorporated ("TI") technical, application or other design advice, services or information, including, but not limited to, reference designs and materials relating to evaluation modules, (collectively, "TI Resources") are intended to assist designers who are developing applications that incorporate TI products; by downloading, accessing or using any particular TI Resource in any way, you (individually or, if you are acting on behalf of a company, your company) agree to use it solely for this purpose and subject to the terms of this Notice.

TI's provision of TI Resources does not expand or otherwise alter TI's applicable published warranties or warranty disclaimers for TI products, and no additional obligations or liabilities arise from TI providing such TI Resources. TI reserves the right to make corrections, enhancements, improvements and other changes to its TI Resources.

You understand and agree that you remain responsible for using your independent analysis, evaluation and judgment in designing your applications and that you have full and exclusive responsibility to assure the safety of your applications and compliance of your applications (and of all TI products used in or for your applications) with all applicable regulations, laws and other applicable requirements. You represent that, with respect to your applications, you have all the necessary expertise to create and implement safeguards that (1) anticipate dangerous consequences of failures, (2) monitor failures and their consequences, and (3) lessen the likelihood of failures that might cause harm and take appropriate actions. You agree that prior to using or distributing any applications that include TI products, you will thoroughly test such applications and the functionality of such TI products as used in such applications. TI has not conducted any testing other than that specifically described in the published documentation for a particular TI Resource.

You are authorized to use, copy and modify any individual TI Resource only in connection with the development of applications that include the TI product(s) identified in such TI Resource. NO OTHER LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE TO ANY OTHER TI INTELLECTUAL PROPERTY RIGHT, AND NO LICENSE TO ANY TECHNOLOGY OR INTELLECTUAL PROPERTY RIGHT OF TI OR ANY THIRD PARTY IS GRANTED HEREIN, including but not limited to any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information regarding or referencing third-party products or services does not constitute a license to use such products or services, or a warranty or endorsement thereof. Use of TI Resources may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

TI RESOURCES ARE PROVIDED "AS IS" AND WITH ALL FAULTS. TI DISCLAIMS ALL OTHER WARRANTIES OR REPRESENTATIONS, EXPRESS OR IMPLIED, REGARDING TI RESOURCES OR USE THEREOF, INCLUDING BUT NOT LIMITED TO ACCURACY OR COMPLETENESS, TITLE, ANY EPIDEMIC FAILURE WARRANTY AND ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

TI SHALL NOT BE LIABLE FOR AND SHALL NOT DEFEND OR INDEMNIFY YOU AGAINST ANY CLAIM, INCLUDING BUT NOT LIMITED TO ANY INFRINGEMENT CLAIM THAT RELATES TO OR IS BASED ON ANY COMBINATION OF PRODUCTS EVEN IF DESCRIBED IN TI RESOURCES OR OTHERWISE. IN NO EVENT SHALL TI BE LIABLE FOR ANY ACTUAL, DIRECT, SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF TI RESOURCES OR USE THEREOF, AND REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

You agree to fully indemnify TI and its representatives against any damages, costs, losses, and/or liabilities arising out of your non-compliance with the terms and provisions of this Notice.

This Notice applies to TI Resources. Additional terms apply to the use and purchase of certain types of materials, TI products and services. These include; without limitation, TI's standard terms for semiconductor products (<http://www.ti.com/sc/docs/stdterms.htm>), [evaluation modules](#), and [samples](http://www.ti.com/sc/docs/sampterm.htm) (<http://www.ti.com/sc/docs/sampterm.htm>).

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2018, Texas Instruments Incorporated