

## ***BQ79606A-Q1 Evaluation Module***

This user's guide describes the general features, theory of operation, hardware setup, and use of the BQ79606EVM. Throughout this user's guide, the abbreviations *EVM*, *BQ79606EVM*, and the term *evaluation module* are synonymous with the *BQ79606A-Q1 Evaluation Module*, unless otherwise noted. This EVM is an evaluation board of the *BQ79606A-Q1* device for use in large format lithium-ion batteries that provides monitoring, balancing, and communications.

### **Contents**

1	General Description .....	5
1.1	Key Features .....	5
1.2	Key Electrical Parameters .....	5
2	Theory of Operation .....	6
2.1	Single Board .....	7
2.2	Stacked Systems .....	7
3	Connectors .....	7
3.1	Primary Input and Output Connectors .....	7
4	BQ79606EVM Quick Start Guide .....	11
4.1	Required Devices .....	12
4.2	Power Connections .....	12
4.3	Connecting the BQ79606EVM to TMS570 LaunchPad .....	12
4.4	Stacking BQ79606EVMs .....	13
4.5	Software .....	13
4.6	GUI .....	14
5	Physical Dimensions .....	15
5.1	Board Dimensions .....	15
5.2	Board Mounting .....	15
6	Schematic, Assembly, Layout, and BOM .....	17
6.1	Schematic .....	17
6.2	Assembly .....	19
6.3	Layout .....	21
6.4	Bill of Materials (BOM) .....	29

### **List of Figures**

1	System Stack Diagram .....	6
2	Molex 39502-1009 (Reference Image Only) .....	7
3	Molex 22-12-4062 (Reference Image Only) .....	8
4	Sullins Connector Solutions PEC06DAAN (Reference Image Only) .....	9
5	Basic EVM Setup .....	11
6	Advanced EVM Setup .....	11
7	Board Dimensions .....	16
8	BQ79606EVM Schematic Part 1 .....	17
9	BQ79606EVM Schematic Part 2 .....	17
10	BQ79606EVM Schematic Part 3 .....	18
11	BQ79606EVM Schematic Part 4 .....	18
12	BQ79606EVM Assembly Top .....	19
13	BQ79606EVM Assembly Bottom .....	20

14	BQ79606EVM Top Overlay .....	21
15	BQ79606EVM Top Solder .....	22
16	BQ79606EVM Top Layer .....	23
17	BQ79606EVM Internal Signal Layer 1 - GND Plane .....	24
18	BQ79606EVM Internal Signal Layer 2 .....	25
19	BQ79606EVM Bottom Layer .....	26
20	BQ79606EVM Bottom Solder .....	27
21	BQ79606EVM Bottom Overlay .....	28
22	BQ79606EVM Drill Drawing .....	29

### List of Tables

1	Connector Information .....	7
2	Pin Description .....	8
3	Connector Information .....	8
4	Pin Description .....	8
5	Connector Information .....	9
6	Pin Description - J16 .....	9
7	Pin Description - J15 .....	10
8	Connector Information .....	10
9	Pin Description - J5 .....	10
10	Pin Description - J6 .....	10
11	Connections Between BQ79606EVM and TMS570 LaunchPad .....	12
12	Connections Between High-Side/Low-Side BQ79606EVMS .....	13

### Trademarks

LaunchPad, *Code Composer Studio* are trademarks of Texas Instruments.  
All other trademarks are the property of their respective owners.

### General Texas Instruments High Voltage Evaluation (TI HV EVM) User Safety Guidelines



### **WARNING**

**Warning: To minimize risk of fire hazard, always verify and follow any specific safety instructions and application considerations related to the batteries being used in conjunction with this EVM.**

Always follow TI's set-up and application instructions, including use of all interface components within their recommended electrical rated voltage and power limits. Always use electrical safety precautions to help ensure your personal safety and the safety of those working around you. Contact TI's Product Information Center <http://support.ti.com> for further information.

**Save all warnings and instructions for future reference.**

**Failure to follow warnings and instructions may result in personal injury, property damage, or death due to electrical shock and/or burn hazards.**

The term TI HV EVM refers to an electronic device typically provided as an open framed, unenclosed printed-circuit-board assembly. It is intended strictly for use in development laboratory environments, solely for qualified professional users having training, expertise, and knowledge of electrical safety risks in development and application of high-voltage electrical circuits. Any other use or application are strictly prohibited by Texas Instruments. If you are not suitably qualified, you should immediately stop from further use of the HV EVM.

**1. Work Area Safety:**

1. Keep work area clean and orderly.
2. Qualified observer(s) must be present any time circuits are energized.
3. Effective barriers and signage must be present in the area where the TI HV EVM and its interface electronics are energized, indicating operation of accessible high voltages may be present, for the purpose of protecting inadvertent access.
4. All interface circuits, power supplies, evaluation modules, instruments, meters, scopes and other related apparatus used in a development environment exceeding 50 V<sub>RMS</sub> or 75 VDC must be electrically located within a protected Emergency Power Off (EPO) protected power strip.
5. Use a stable and non-conductive work surface.
6. Use adequately insulated clamps and wires to attach measurement probes and instruments. No freehand testing whenever possible.

**2. Electrical Safety:** As a precautionary measure, it is always a good engineering practice to assume that the entire EVM may have fully accessible and active high voltages.

1. De-energize the TI HV EVM and all its inputs, outputs, and electrical loads before performing any electrical or other diagnostic measurements. Revalidate that TI HV EVM power has been safely de-energized.
2. With the EVM confirmed de-energized, proceed with required electrical circuit configurations, wiring, measurement equipment hook-ups and other application needs, while still assuming the EVM circuit and measuring instruments are electrically live.
3. Once EVM readiness is complete, energize the EVM as intended.

**WARNING: while the EVM is energized, never touch the EVM or its electrical circuits as they could be at high voltages capable of causing electrical shock hazard.**

### 3. Personal Safety:

1. Wear personal protective equipment, for example, latex gloves or safety glasses with side shields or protect EVM in an adequate lucent plastic box with interlocks from accidental touch.

### 4. Limitation for Safe Use:

1. EVMs are not to be used as all or part of a production unit.

The following warnings and cautions are noted for the safety of anyone using or working close to the BQ76920 EVM. Observe all safety precautions.



#### Caution

Do not leave EVM powered when unattended.



#### Danger High Voltage

The BQ79606EVM is not rated as a high voltage EVM, has smaller clearances than normally used on high voltage boards and does not have an isolation boundary. If you apply high voltage to this board, all terminals should be considered high voltage.

Electric shock is possible when connecting the board to live wire. The board should be handled with care by a professional.

For safety, use of isolated test equipment with overvoltage and overcurrent protection is highly recommended.

#### CAUTION

The circuit module has signal traces, components, and component leads on the bottom of the board. This may result in exposed voltages, hot surfaces, or sharp edges. Do not reach under the board during operation.

#### CAUTION

The circuit module may be damaged by overtemperature. To avoid damage, monitor the temperature during evaluation and provide cooling, as needed, for your system environment.

#### CAUTION

Some power supplies can be damaged by application of external voltages. If using more than 1 power supply, check your equipment requirements and use blocking diodes or other isolation techniques, as needed, to prevent damage to your equipment.

#### CAUTION

The communication interface is not isolated on the EVM. Be sure no ground potential exists between the computer and the EVM. Also be aware that the computer will be referenced to the Battery- potential of the EVM.

## 1 General Description

TI's *BQ79606EVM Battery Management System* (BMS) is an evaluation board of the *BQ79606A-Q1* device for use in large format lithium-ion batteries that provide monitoring, balancing, and communications. Each BQ79606EVM can manage up to 6 cells (30-V max) for Li-ion battery applications. Up to 64 BQ79606EVM modules can be stacked, for packs up to 1300 V. The system provides fast cell balancing, diagnostics, and module to controller communication. Independent protection circuitry is also provided. Please see the BQ79606A-Q1 data sheet for more details.

The BQ79606EVM is equipped with precision measurement and synchronous communication to enable a master controller to perform *State of Charge* (SOC) and *State of Health* (SOH) estimation. Highly-accurate cell voltages and a fast sampling time for the entire battery pack allows more efficient operation of battery modules and more accurate SOC and SOH calculations. Communication with stacked BQ79606EVMs is via an isolated daisy-chain differential bus.

Control a single EVM or multiple stacked BQ79606EVMs using a PC-hosted GUI. Communication between the PC and the lowest in a stack of BQ79606EVMs is via an FTDI USB-to-TTL (5 V) serial interface cable. Communication between all other EVMs in the stack is via the isolated, daisy-chain differential communication bus. The PC GUI allows configuration of the BQ79606EVMs to monitor cells and other analog data channels, control balancing, and monitor details of any faults.

### 1.1 Key Features

This EVM includes the following features:

- Passive cell balancing
- Isolated communications (1 kV)
- Flexible architecture for up to 6 cells
- UART interface
- High-accuracy cell voltage measurement
- Diagnostics
- Resistor ladder to simulate cell voltages

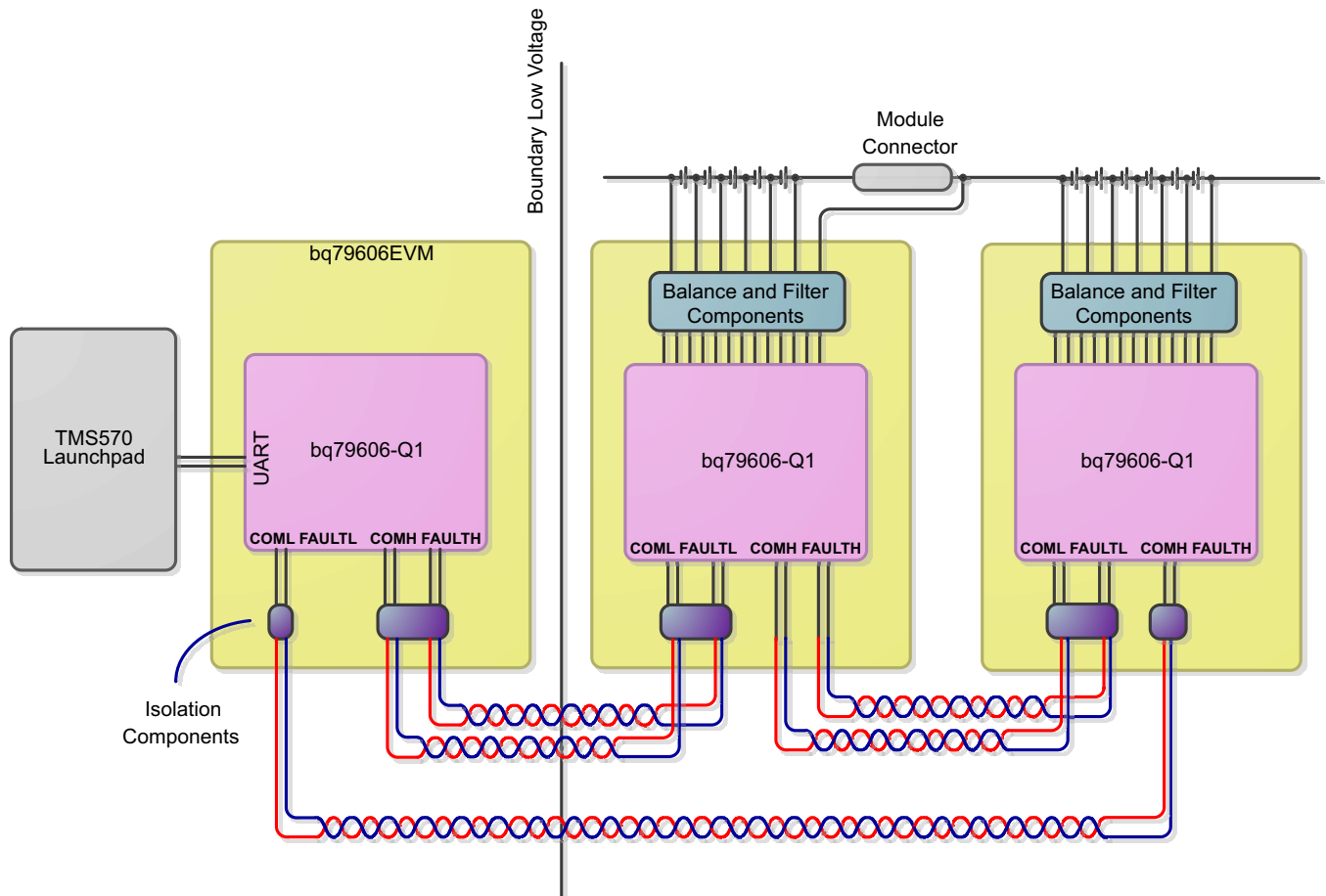
### 1.2 Key Electrical Parameters

The following table identifies the key electrical parameters:

Maximum battery pack voltage (stacked EVMs)	1300 V
Maximum operating voltage	30 V
Minimum operating voltage	5.5 V
Maximum cell open circuit voltage	5 V
Ambient temperature	–40 °C to 105 °C
Nominal operating temperature	–20 °C to 60 °C
Cell balancing current	Approximately 100 mA

## 2 Theory of Operation

Figure 1 shows the system stack diagram.



Copyright © 2017, Texas Instruments Incorporated

**Figure 1. System Stack Diagram**

The BMS system is designed to prolong the useful life of lithium-ion cells in battery packs through passive balancing. The battery pack is broken into a series of modules, each of which contains up to 6 cells. This system will monitor voltages of individual battery cells and transfer charge from the module stack to an under-charged cell or take charge from an over-charged cell and transfer it to the module stack. The BMS allows battery-powered electric machines to use smaller battery packs and use fewer charging cycles to perform the same amount of work. It also improves the overall lifetime of Li-ion battery packs by preventing under- and overvoltage damage from occurring.

The typical BMS system with stacked modules has three main sub-systems, as shown in Figure 1:

- Host controller - in this case a TMS570 LaunchPad™
- A BQ79606A-Q1 configured as an isolated communication bridge device - another BQ79606EVM can support this
- BQ79606EVM based modules attached to cells - these can be stacked up to 64 total (including the bridge device)

All commands and data are communicated with a host via either a UART or daisy-chain communication connection. The BQ79606A will remain idle until a command is received from the host. The BQ79606A can support a host PC or microcontroller (via the UART connection header) or a daisy-chain interface from a BQ79606A-Q1 implemented as a communication bridge.

The typical flow is for the host to go through the following simplified sequence:

1. Wakeup the BQ79606EVM board by sending a WAKEUP pulse when using the UART interface, or sending a WAKE tone when using the BQ79606EVM in a stack of other BQ79606EVM boards for a large battery pack or a BQ79606-Q1 configured as a bridge. Initialize the BQ79606A-Q1 to be ready for use.
2. Send a sample command to the BQ79606A-Q1 to read the cell measurement results.
3. The host will use the cell measurement data to calculate an average and determine the highest or lowest cells and determine the cells that should be balanced.
4. If no stop command is sent, the BQ79606A-Q1 has a built-in timeout (set by the user), after which time the discharge will be stopped automatically.
5. The host can then decide to repeat the process (back to step 2) or send commands to shutdown the BQ79606EVM and return later.

## 2.1 Single Board

As a single board the BMS can passively balance up to 6 cells, up to 30 V of total voltage. Communication to the BQ79606EVM is handled by the daisy-chain communications bus from another BQ79606A-Q1 or the UART host interface.

## 2.2 Stacked Systems

The BMS boards may be stacked up to 1300 V. Communication to the BQ79606EVM is handled by the daisy-chain communications bus from another BQ79606A-Q1.

## 3 Connectors

### 3.1 Primary Input and Output Connectors

#### 3.1.1 Battery Connector

The battery cell connections are made from connector J1. Cell voltage measurements and balancing currents use these connections. Short unused channels to the top cell connection in the wiring harness to support fewer than 6 cells.

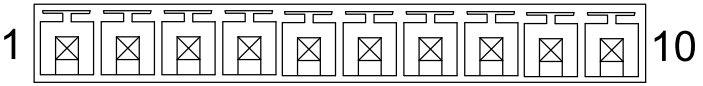


**Figure 2. Molex 39502-1009 (Reference Image Only)**

**Table 1. Connector Information**

Designator	Manufacturer	Part Number	Mating Connector
J1	Molex	Manufacturer: 39502-1009 Digi-Key: WM7777-ND	Manufacturer: 39500-0009 Digi-Key: WM7739-ND

**Table 2. Pin Description**

		
Pin	Name	Comments
1	PWR	Positive terminal of BAT6, connect LDOIN and BAT6 by placing J2
2	CELL6	Positive terminal of BAT6
3	CELL5	Positive terminal of BAT5, negative terminal of BAT6
4	CELL4	Positive terminal of BAT4, negative terminal of BAT5
5	CELL3	Positive terminal of BAT3, negative terminal of BAT4
6	CELL2	Positive terminal of BAT2, negative terminal of BAT3
7	CELL1	Positive terminal of BAT1, negative terminal of BAT2
8	CELL0	Negative terminal of BAT1, local ground for module
9	GND	EVM GND, connect BAT0 and GND by placing J3

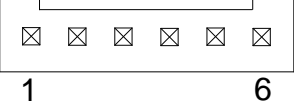
### 3.1.2 Host Interface

The 6-pin J8 - Serial connector is used to connect the BQ79606EVM to a PC running the GUI or to a host controller. Signals at the J8 - Serial connector are 5-V TTL signals. Texas Instruments recommends using FTDI's USB-to-TTL serial converter cable to allow connection with the USB port of a PC. The recommended cable is available from FTDI (<http://www.ftdichip.com>), and the appropriate part number for the 1.8-meter cable is TTL-232R-5V.

**Figure 3. Molex 22-12-4062 (Reference Image Only)****Table 3. Connector Information**

Designator	Manufacturer	Part Number	Mating Connector
J8	Molex	Manufacturer: 22-12-4062	Manufacturer: 22-01-2061 Digi-Key: WM1579-ND Contacts: Manufacturer: 08-55-0101 (22-30 AWG) Digi-Key: WM2312CT-ND

**Table 4. Pin Description**

		
Pin	Name	Mating Cable Color
1	Ground	Black
2	FAULT signal from BQ79606A-Q1	Black
3	5-V input from PC	Red
4	TX from PC (BQ79606A-Q1 RX)	Orange

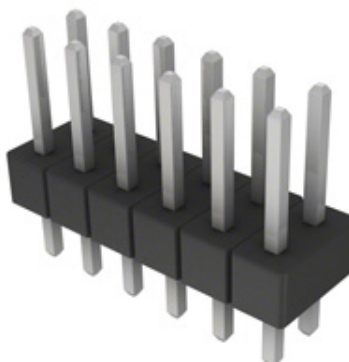


**Table 4. Pin Description (continued)**

5	TX from BQ79606A-Q1 (PC RX)	Yellow
6	WAKE signal from PC	Green

### 3.1.3 GPIO or Thermistor Inputs

There are 6 GPIO pins (with a parallel set of 6 connected to the same signal) which can also be used as thermistor connections. The first two columns of pins (J15 and the left column of J16) are tied together by row with the GPIO in ascending order (top row is GPIO1, second row is GPIO2, etc). The third column of pins (the right column of J16) are all GND. The board provides a 10-k $\Omega$  pullup to TSREF.


**Figure 4. Sullins Connector Solutions PEC06DAAN (Reference Image Only)**
**Table 5. Connector Information**

Designator	Manufacturer	Part Number	Mating Connector
J16	Tyco Electronic	Manufacturer: 43045-1601	Manufacturer: 43025-1600 Digi-Key: WM2490-ND Contacts: Manufacturer: 43030-0012 (22-30 AWG) Digi-Key: WM2779CT-ND

**Table 6. Pin Description - J16**

Pin	Name	Comments
1	GPIO1	Thermistor 1 connection (on-board 10-k $\Omega$ pullup to TSREF)
2	GND	Ground
3	GPIO2	Thermistor 2 connection (on-board 10-k $\Omega$ pullup to TSREF)
4	GND	Ground
5	GPIO3	Thermistor 3 connection (on-board 10-k $\Omega$ pullup to TSREF)
6	GND	Ground
7	GPIO4	Thermistor 4 connection (on-board 10-k $\Omega$ pullup to TSREF)
8	GND	Ground
9	GPIO5	Thermistor 5 connection (on-board 10-k $\Omega$ pullup to TSREF)
10	GND	Ground
11	GPIO6	Thermistor 6 connection (on-board 10-k $\Omega$ pullup to TSREF)
12	GND	Ground

**Table 7. Pin Description - J15**

Pin	Name	Comments
1	GPIO1	Tied to J16 Pin 1
2	GPIO2	Tied to J16 Pin 3
3	GPIO3	Tied to J16 Pin 5
4	GPIO4	Tied to J16 Pin 7
5	GPIO5	Tied to J16 Pin 9
6	GPIO6	Tied to J16 Pin 11

### 3.1.4 High-Side and Low-Side Communications

There are two sets of 4-position molex connectors available on each board. These provide high-side (J6) and low-side (J5) communications between stacked EVM devices.

**Table 8. Connector Information**

Designator	Manufacturer	Part Number	Mating Connector
J5/J6	Molex	Manufacturer: 0705510038 Digi-Key: WM14059-ND	Manufacturer: 0050579404 Digi-Key: WM2902-ND

**Table 9. Pin Description - J5**

Pin	Name	Comments
1	COML_N	COM lowside negative
2	COML_P	COM lowside positive
3	FAULTL_N	FAULT lowside negative
4	FAULTL_P	FAULT lowside positive

**Table 10. Pin Description - J6**

Pin	Name	Comments
1	FAULTH_P	FAULT highside positive
2	FAULTH_N	FAULT highside negative
3	COMH_P	COM highside positive
4	COMH_N	COM highside negative

## 4 BQ79606EVM Quick Start Guide

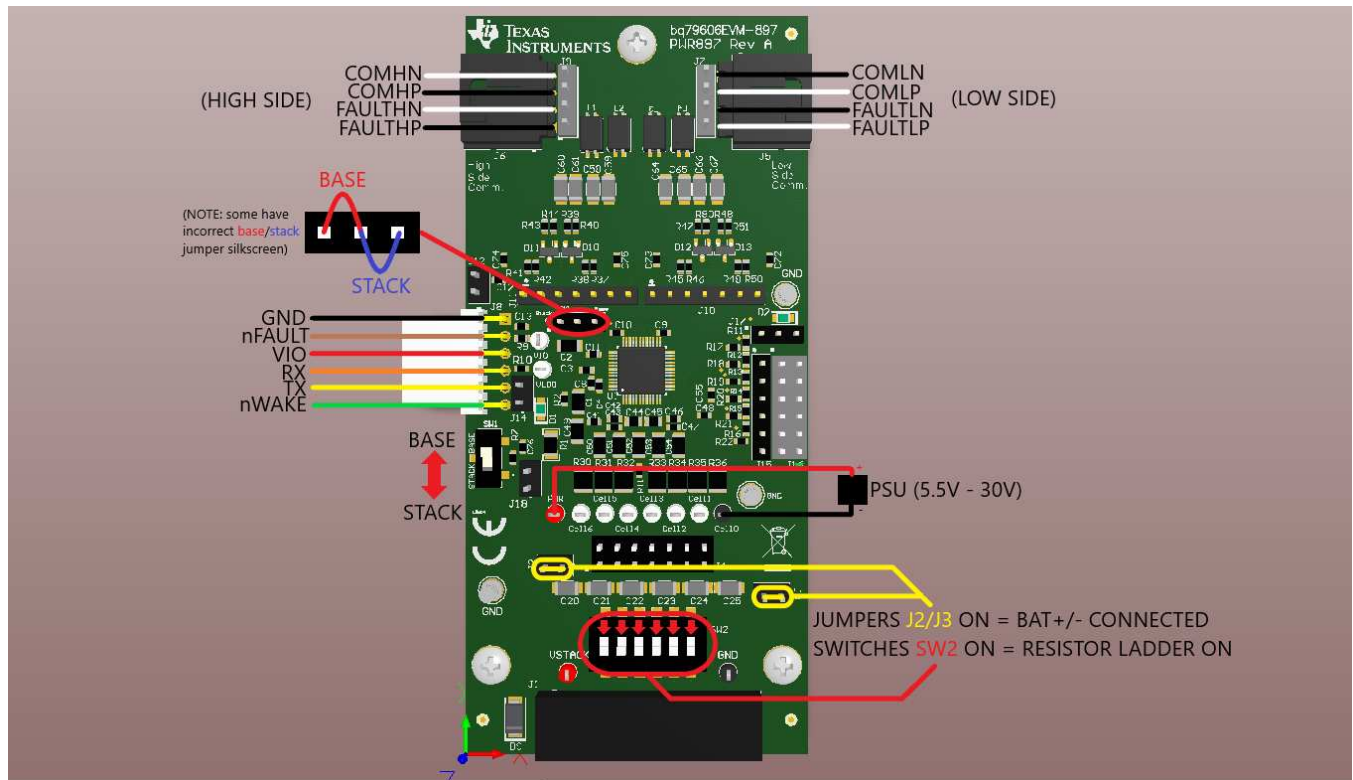


Figure 5. Basic EVM Setup

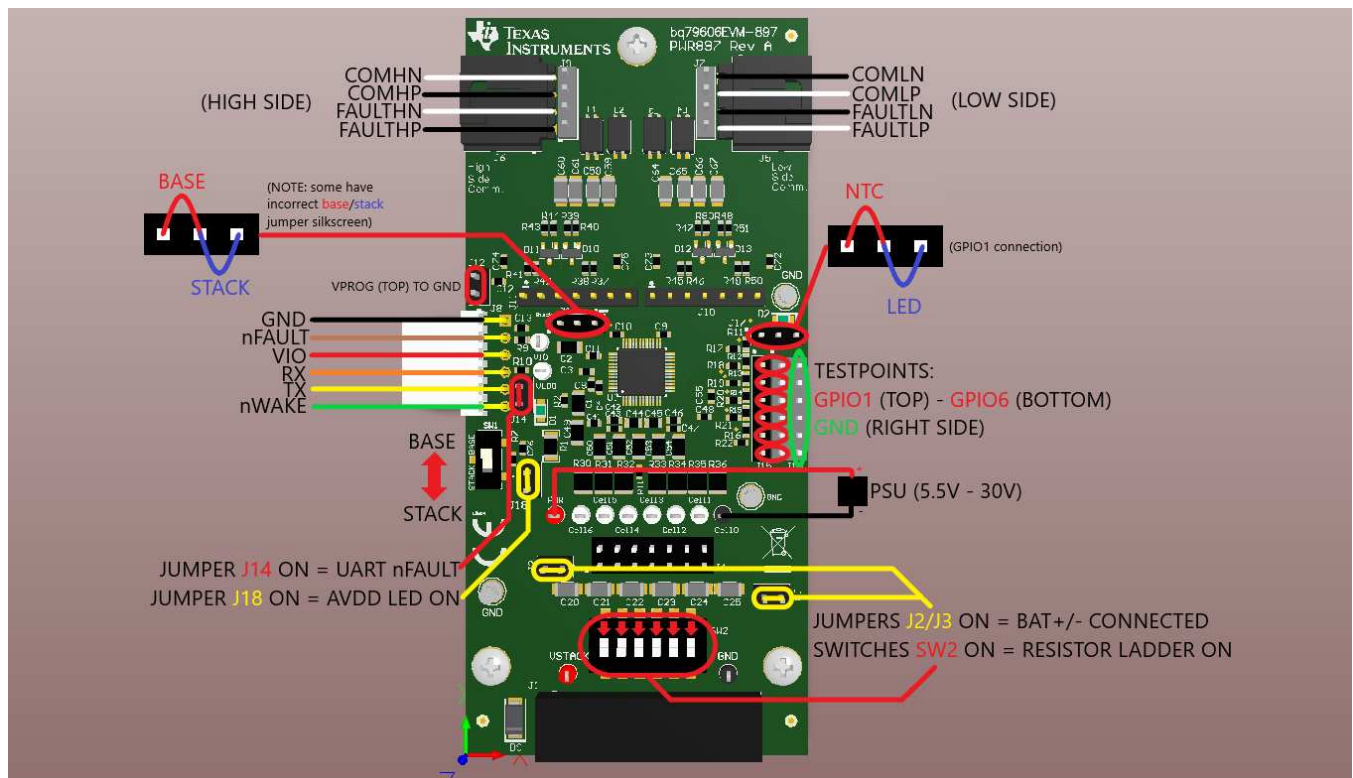


Figure 6. Advanced EVM Setup

This section includes hardware setup instructions, connection procedures, and software and GUI instructions.

## 4.1 Required Devices

The system is implemented using the TMS570LS04 LaunchPad™ board (TMS570LS0432 MCU) and the BQ79606EVM.

The part numbers of the evaluation modules are LAUNCHXL-TMS57004 and BQ79606EVM. These boards are available from the TI eStore (<https://estore.ti.com/>) or from your local TI sales representative. For more details and information related to the LaunchPad modules, see the specific module user's guide.

### 4.1.1 LaunchPad Required Modification

To allow use of the SCI1 UART pin on the LaunchPad with the BQ79606EVM, remove resistor R8.

## 4.2 Power Connections

If powering the BQ79606EVM using the included resistor ladder as cells, simply ensure that the power supply positive terminal is connected to the "VSTACK" or "PWR" testpoints provided on the board, and the power supply negative terminal is connected to the "GND" or "Cell0" testpoints provided on the board, or any "GND" standoff provided.

If not using the resistor ladder, the power supply positive terminal must be connected to the "PWR" testpoint, and the power supply negative terminal must be connected to the "Cell0" testpoint.

Also ensure that headers J2 and J3 are both jumpered, to allow for power and ground to be supplied from the "battery stack" (resistor ladder).

### 4.2.1 On-Board Resistor Ladder - Power Supply

The BQ79606EVM utilizes an on-board resistor ladder to simplify the evaluation process. Each of the six resistors is nominally 1 kΩ, resulting in roughly one-sixth of the module voltage at each cell connection. By default, all actuators of SW2 are positioned closest to the IC, which is the "off" or "open" state. In this state, none of the resistors are connected to the BQ79606A sense and balance connections.

To simulate connected cells to the sense and balance connections (using a power supply), all switch actuators on SW2 must be moved towards the module connector J1. Moving the actuators in this manner connects the BQ79606A to the resistor ladder, and allows for simulated cell measurements.

### 4.2.2 On-Board Resistor Ladder - Using Actual Battery Cells

When using actual battery cells, disconnect the resistor ladder by moving all switch actuators on SW2 to the "OFF" position (away from J1).

## 4.3 Connecting the BQ79606EVM to TMS570 LaunchPad

The EVMs are connected using a standard wire jumper; [Table 11](#) shows the connections between the two EVMs. By default, the TMS570 LaunchPad is powered by the USB port on the host computer. VIO on the BQ79606EVM can be powered by 3.3 V or 5 V supplies from the TMS570 (3.3 V used in this example).

**Table 11. Connections Between BQ79606EVM and TMS570 LaunchPad**

Connection Name	BQ79606EVM	TMS570 LaunchPad
TX	J8 pin 5	J1 pin 3 (SCI1_RX)
RX	J8 pin 4	J1 pin 4 (SCI1_TX)
nWAKE	J8 pin 6	J2 pin 3 (GIOA0)
nFAULT	J8 pin 2	J2 pin 4 (GIOA1)
VIO	J8 pin 3	J1 pin 1 (+3V3)
GND	J8 pin 1	J2 pin 1 (DGND)

## 4.4 Stacking BQ79606EVMs

The EVMs are connected using 4-position Molex connectors. There is a high side (J6) and low side (J5) communication connector available on each device. A jumper and a switch must be configured for proper stack operation (see [Section 4.4.1](#) and [Section 4.4.2](#)).

**Table 12. Connections Between High-Side/Low-Side BQ79606EVMs**

Connection Name	BQ79606EVM High Side	BQ79606EVM Low Side
COMH_N/COML_N	J6 pin 4	J5 pin 1
COMH_P/COML_P	J6 pin 3	J5 pin 2
FAULTH_N/FAULTL_N	J6 pin 2	J5 pin 3
FAULTH_P/FAULTL_P	J6 pin 1	J5 pin 4

### 4.4.1 Base/Stack Jumper Setup - J13

**NOTE:** Silkscreen for this jumper may be **INCORRECT** on some EVMs. Please place the jumper as defined below.

For base devices, ensure that the jumper is placed on the left side of J13. For stack devices, ensure that the jumper is placed on the right side of J13.

### 4.4.2 Base/Stack Switch Setup - SW1

For base devices, ensure that the switch is placed in the "BASE" position (up). For stack devices, ensure that the switch is placed in the "STACK" position (down).

## 4.5 Software

The software provides a command API and drivers that are capable of implementing the examples provided in *BQ79606A-Q1 Software Design Reference*.

The example code only provides a control interface to the BQ79606A-Q1 and does not provide any other communications interface to the outside world. The customer is expected to develop their own communication implementation. Examples of communications interfaces available to the TMS570 are SPI, CAN, or UART. For the TMS570 example code, UART is the communication protocol used between the microcontroller and BQ79606A-Q1 device.

This firmware provided with this application note provides source code examples of the command sequences described in the *BQ79606A-Q1 Software Design Reference*.

Importing a project into *Code Composer Studio*™:

1. Launch the provided file: *BQ79606A-Q1 Example Code 0.1 Installer.exe* and extract files to the default path provided (C:\ti\BQ79606A-Q1 Example Code 0.1).
2. Launch *Code Composer Studio* (CCS):  
**Start → Programs → Texas Instruments → Code Composer Studio v8 → Code Composer Studio v8**
3. When it launches, CCS requests a workspace is selected, choose "C:\myWorkspace". Once CCS loads, go to:  
**Project → Import CSS Projects... → Select search-directory**
4. In Select search-directory, browse to the folder:  
C:\ti\BQ79606A-Q1 Example Code 0.1
5. In *Discovered projects*:, check BQ79606A-Q1 example code

## 4.6 GUI

For initial evaluation, it may be more beneficial to use the graphical user's interface (GUI), which provides a "point and click" interface to become familiar with the BQ79606A. During the initial sampling phase, please contact your local TI FAE to get the latest BQStudio file supporting the BQ79606A (the BQStudio file will be publicly available after GUI validation and release).

To get started with the GUI, please refer to the *Getting Started with the BQ79606A GUI* document.

### 4.6.1 GUI UART Connection

Physical setup for the GUI is the same as for the microcontroller, but will instead use an FTDI cable for the UART connections on J8. This FTDI cable must have the metal contacts facing upwards when connecting the 6 pin side of the cable to the EVM, with the other side of the cable connected via USB to the host PC.

## 5 Physical Dimensions

### 5.1 Board Dimensions

Board dimensions: 4.304 in × 2 in

Board height:

- Top - Tallest component (GPIO, Shunts) is 0.35 in (8.8 mm) above PCB.
- Bottom - Tallest component (Transformers) is 0.41 in (10.5 mm) above PCB.

### 5.2 Board Mounting

[Figure 7](#) illustrates the EVM dimension drawing.

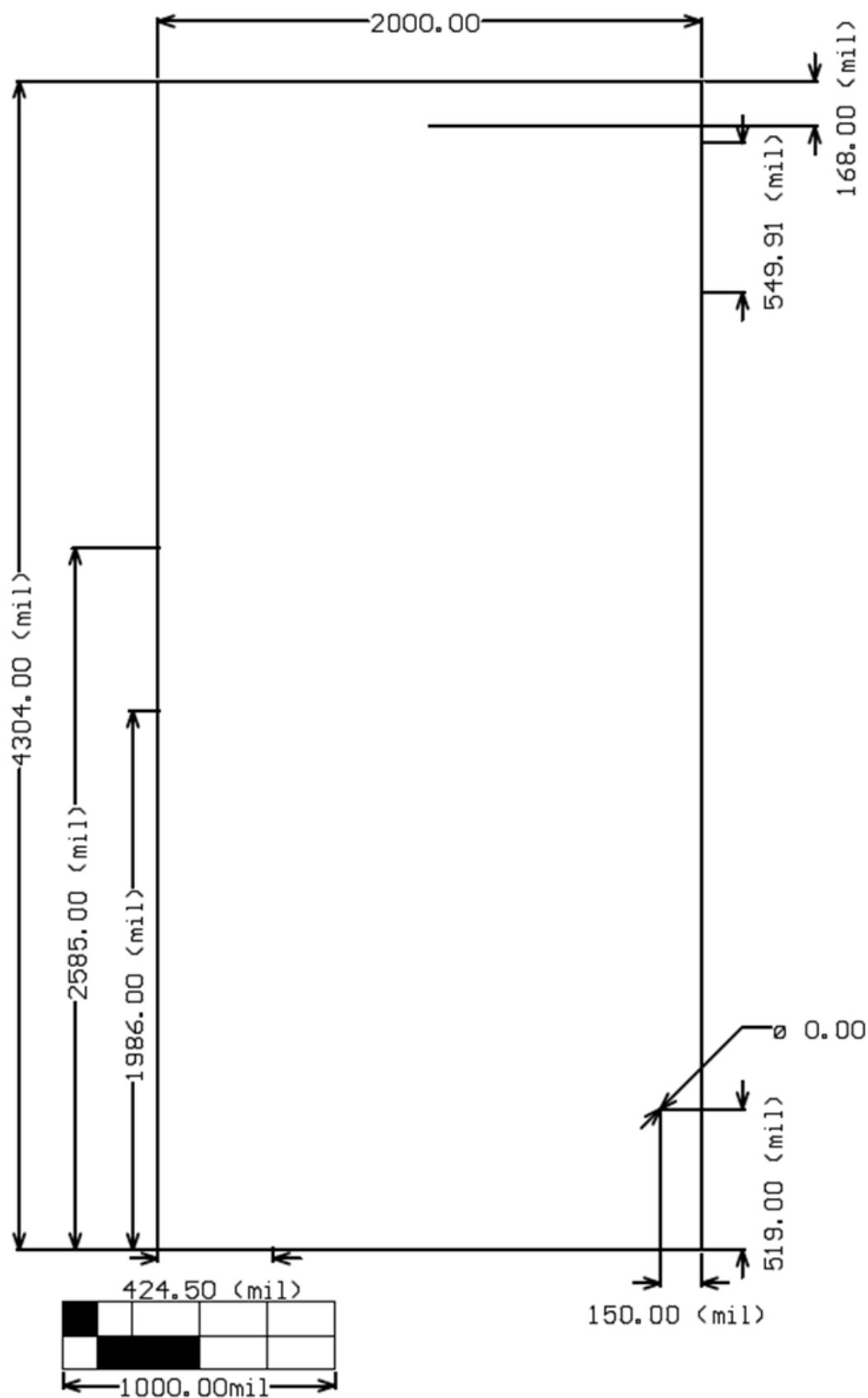


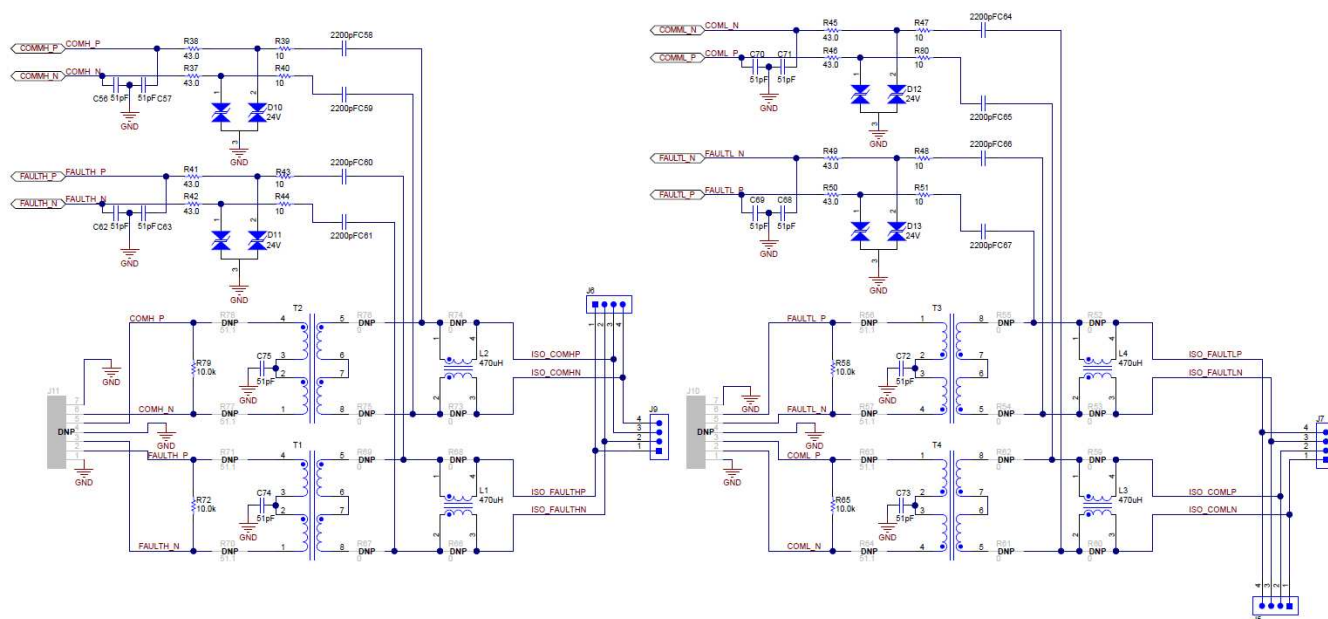
Figure 7. Board Dimensions



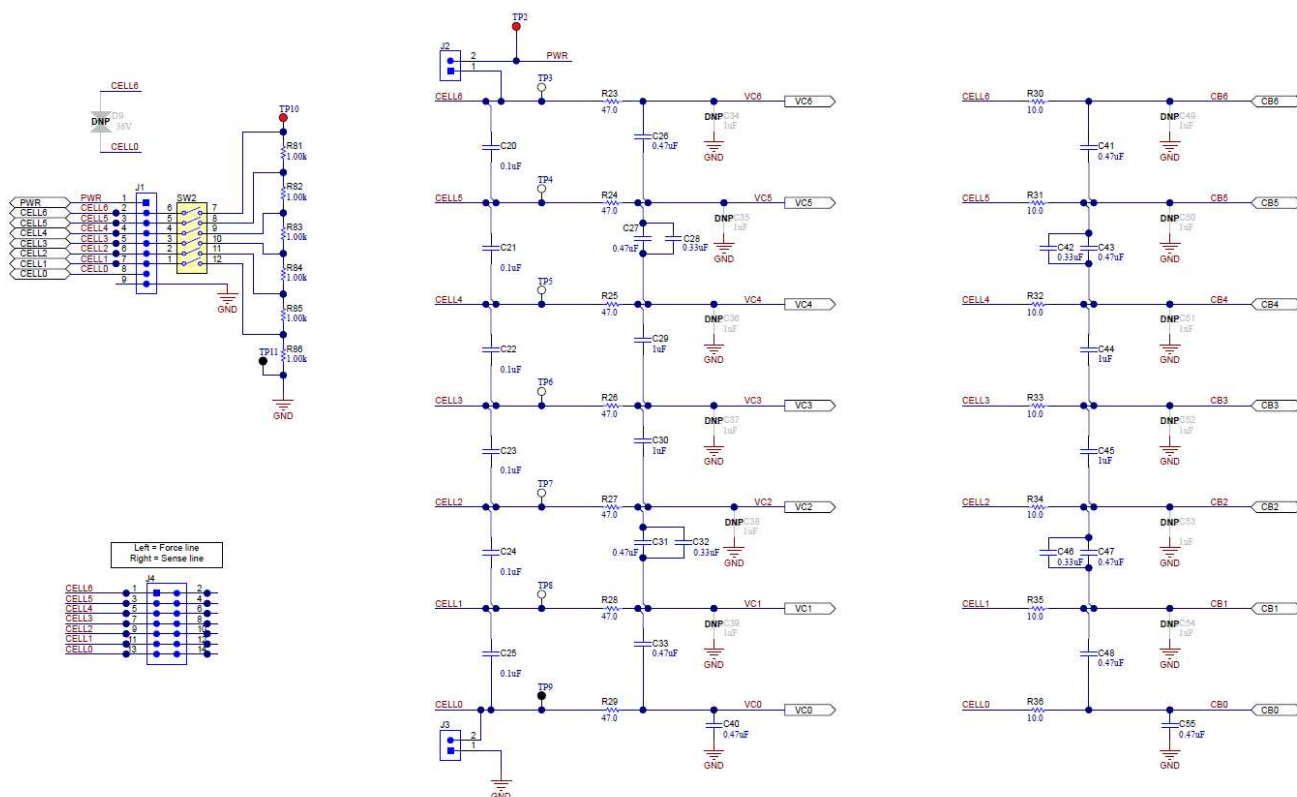
## 6 Schematic, Assembly, Layout, and BOM

Provided are the BQ79606EVM schematic, assembly, layout and BOM in their respective sections.

### 6.1 Schematic



**Figure 8. BQ79606EVM Schematic Part 1**



**Figure 9. BQ79606EVM Schematic Part 2**



## 6.2 Assembly

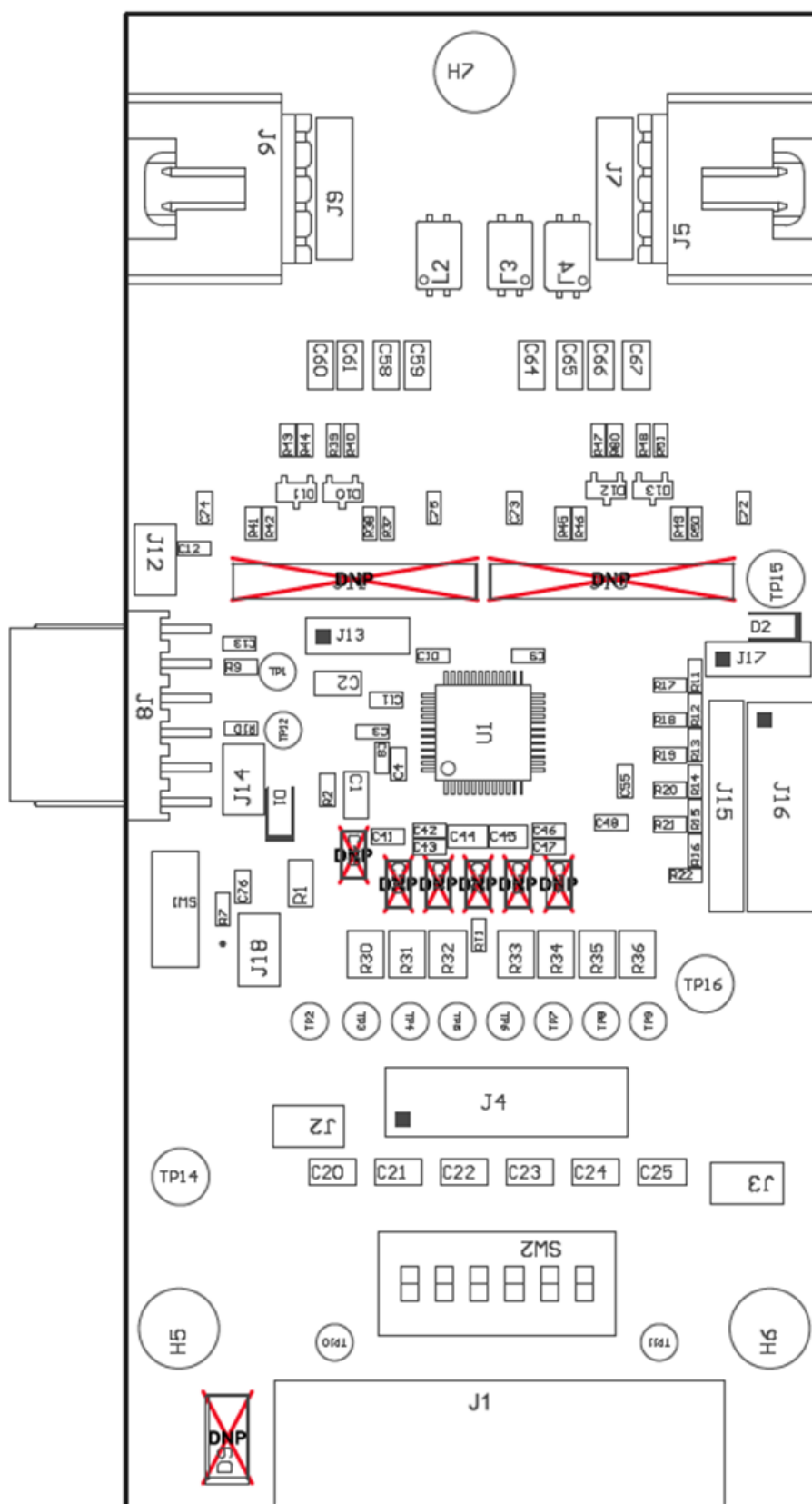


Figure 12. BQ79606EVM Assembly Top



## 6.3 Layout

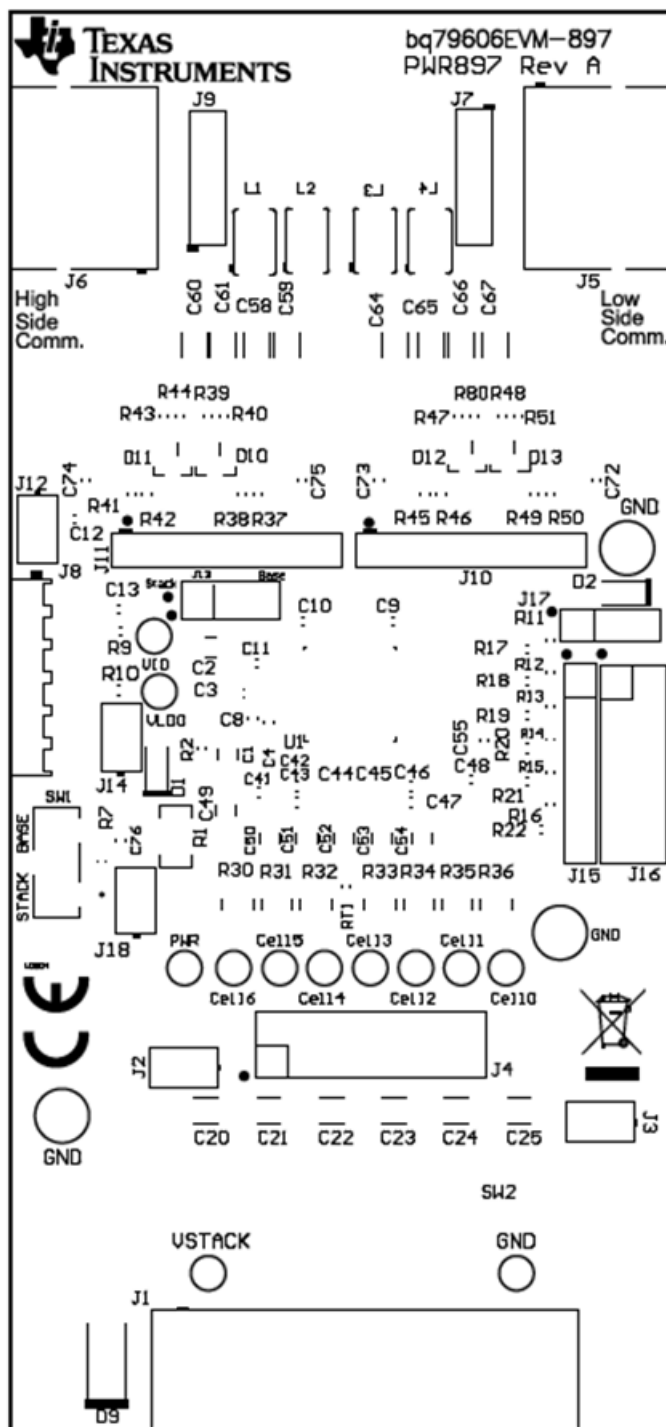


Figure 14. BQ79606EVM Top Overlay

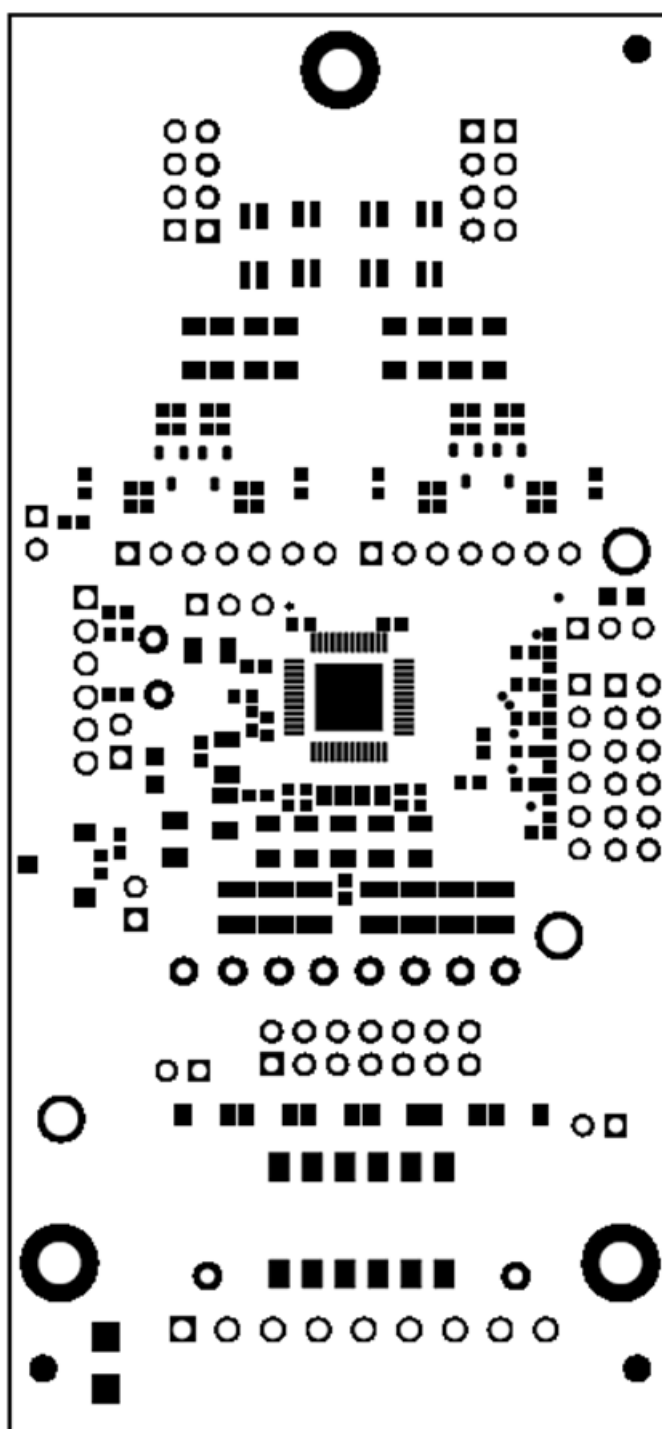
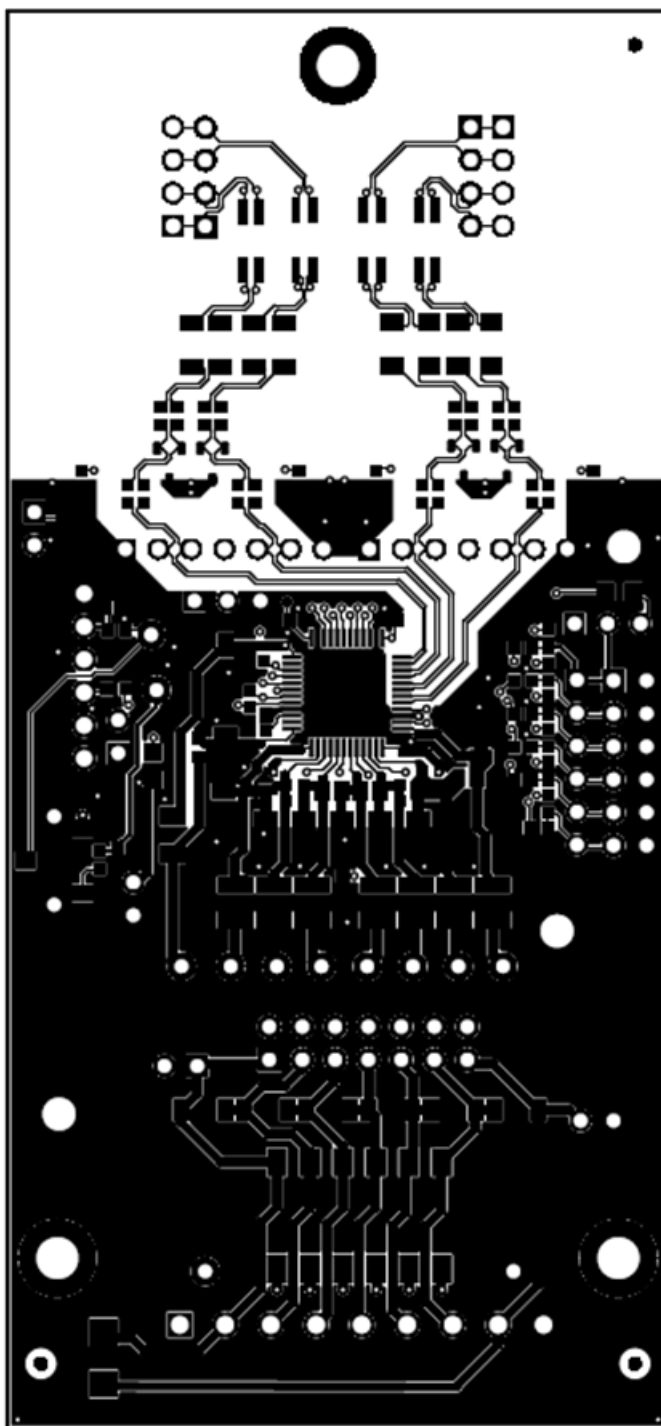
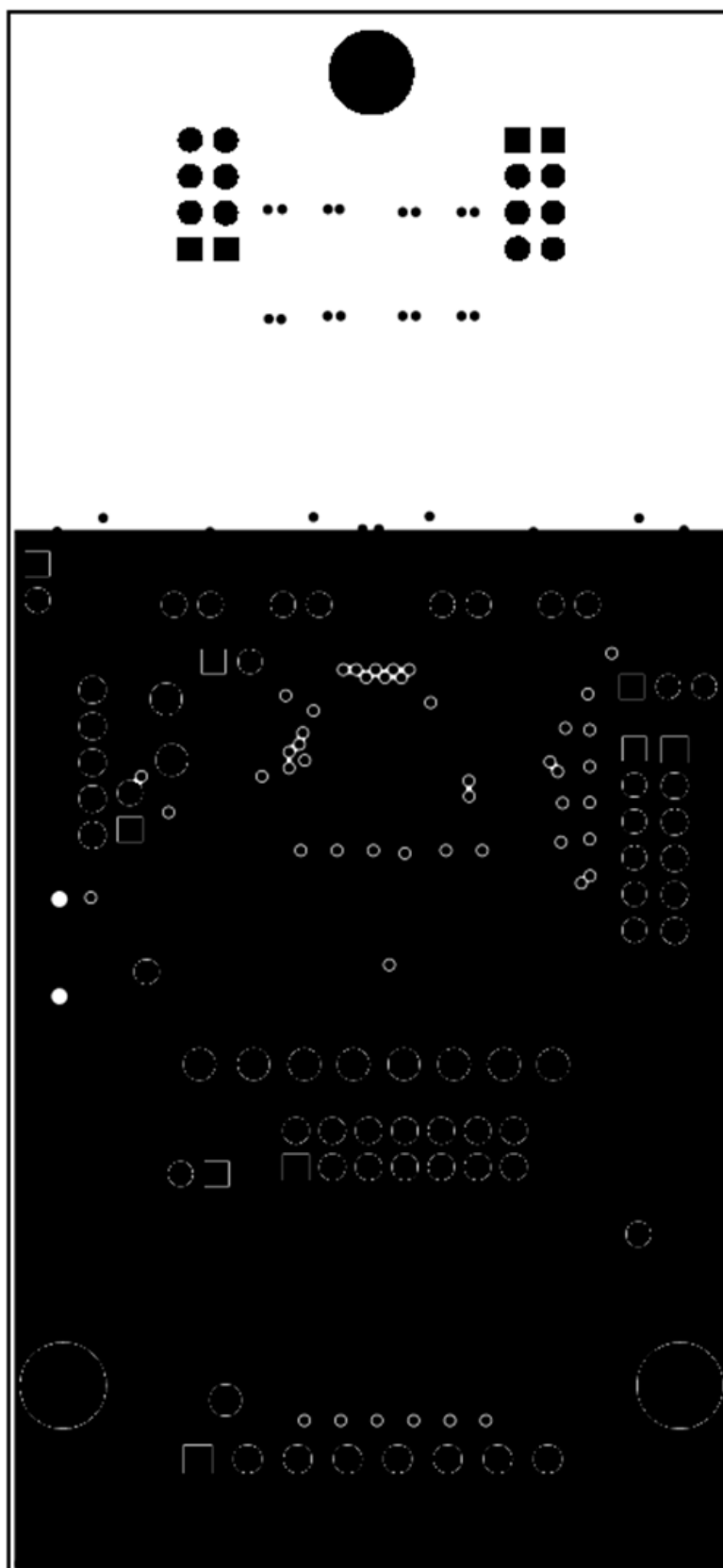


Figure 15. BQ79606EVM Top Solder

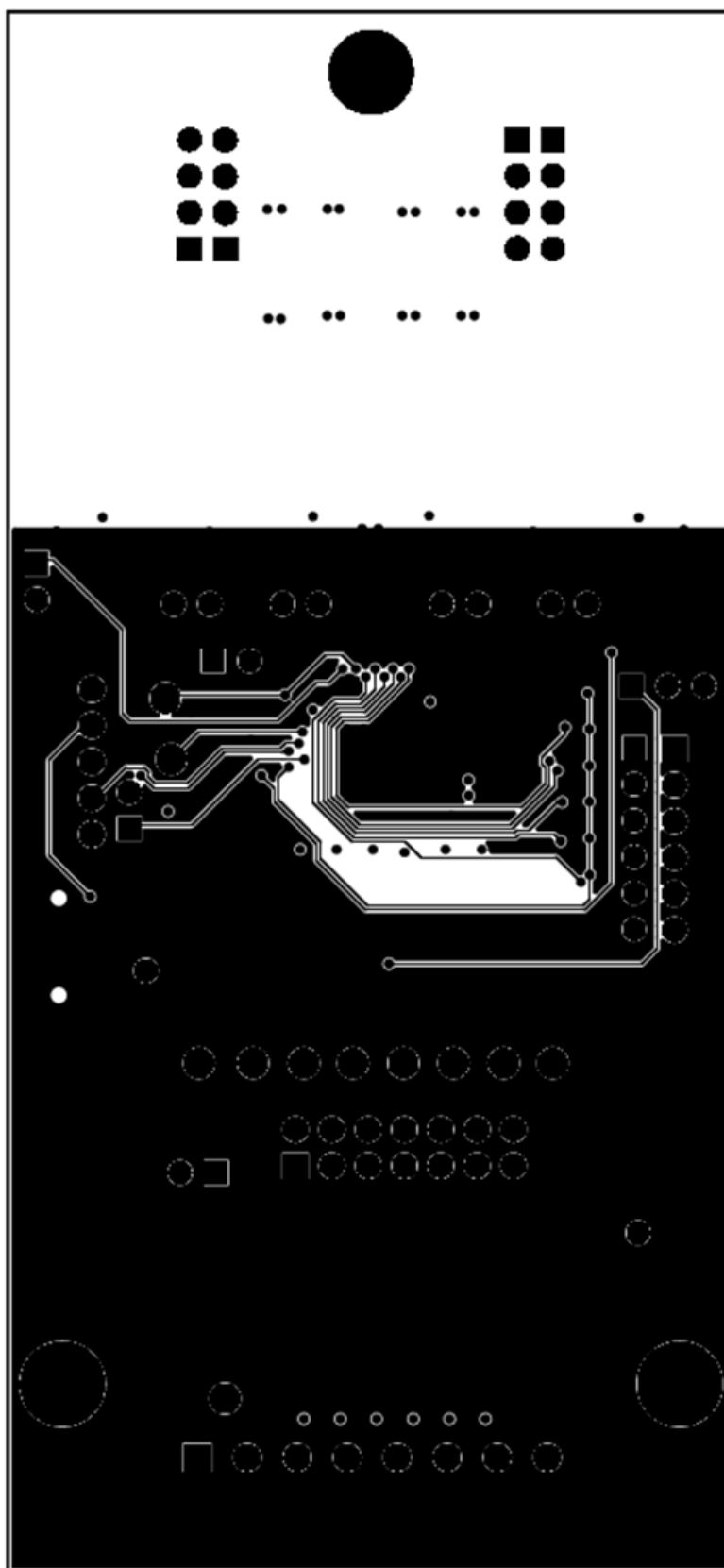


**Figure 16. BQ79606EVM Top Layer**

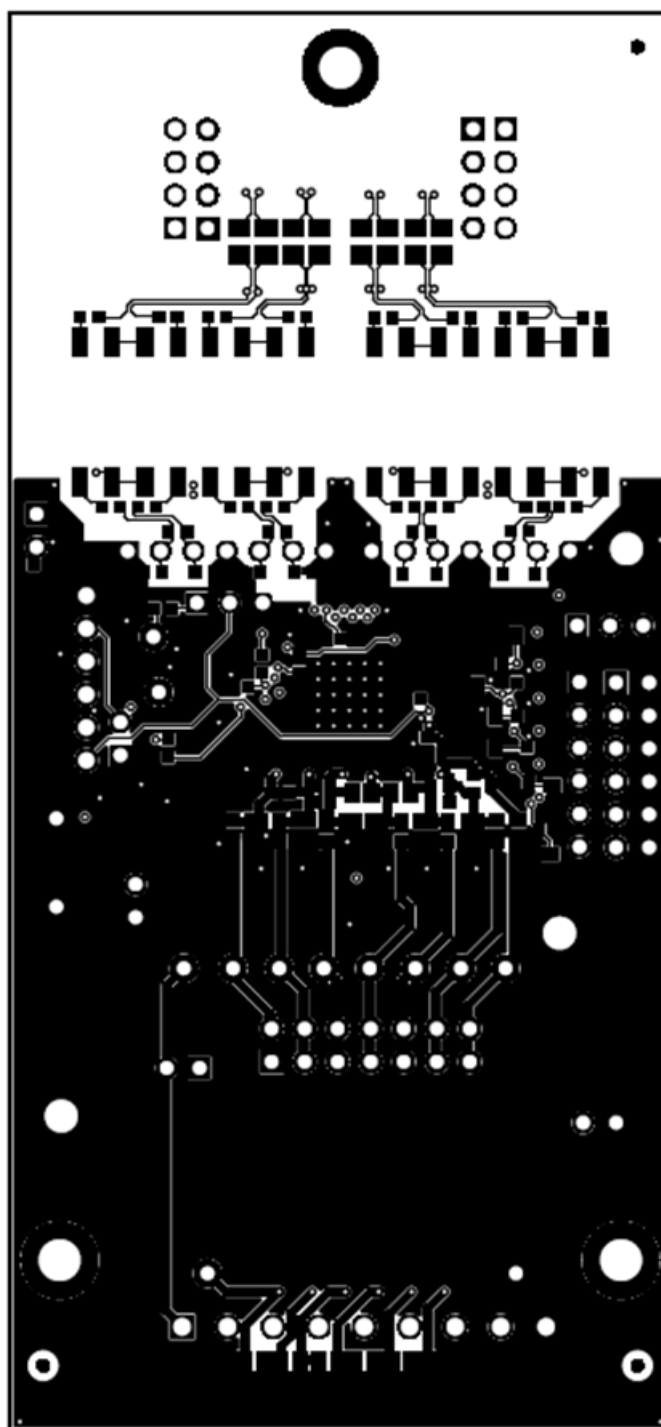


**Figure 17. BQ79606EVM Internal Signal Layer 1 - GND Plane**





**Figure 18. BQ79606EVM Internal Signal Layer 2**



**Figure 19. BQ79606EVM Bottom Layer**

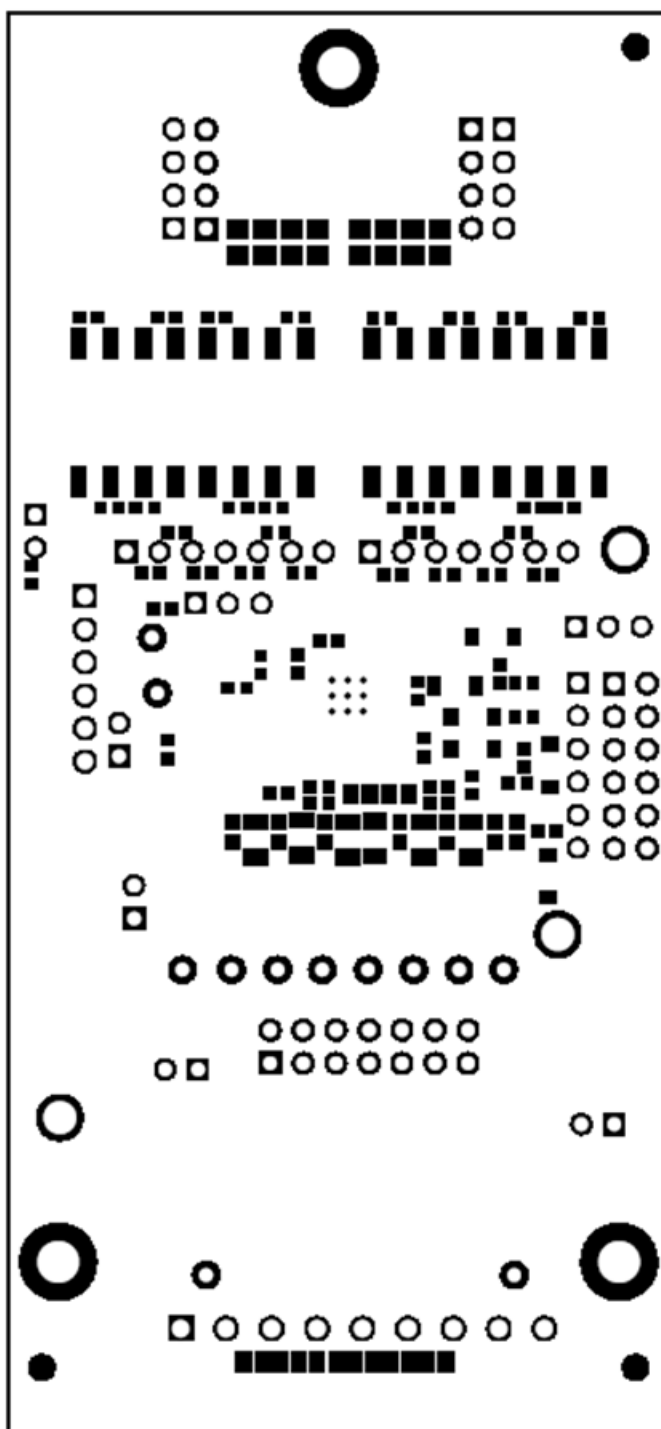


Figure 20. BQ79606EVM Bottom Solder

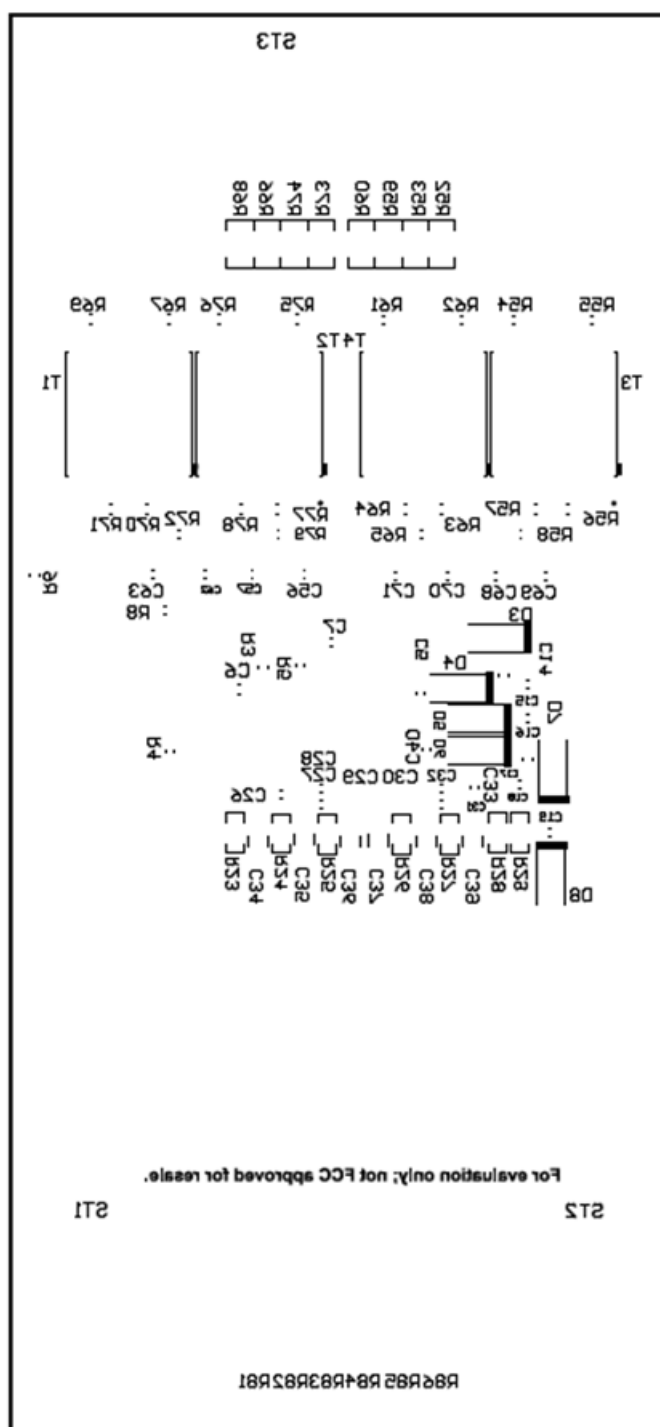
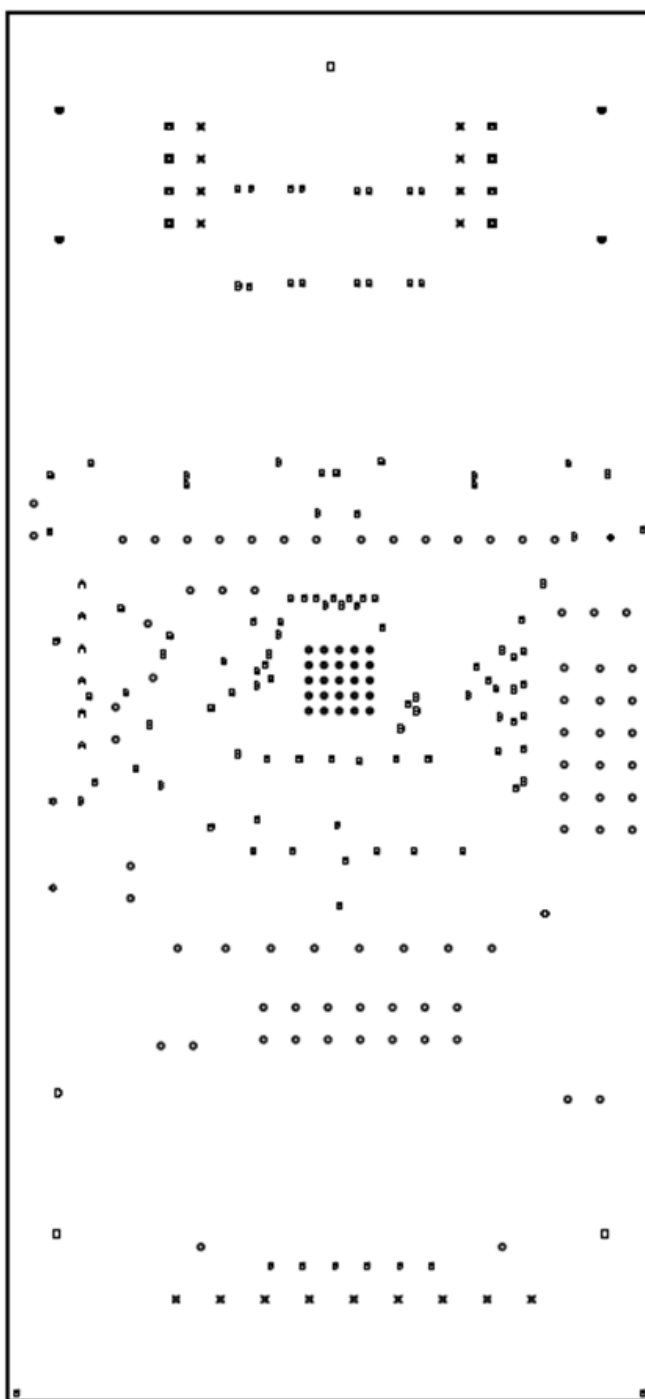


Figure 21. BQ79606EVM Bottom Overlay



**Figure 22. BQ79606EVM Drill Drawing**

## 6.4 Bill of Materials (BOM)

Designator	Quantity	Value	Description	PackageReference	PartNumber	Manufacturer
PCB	1		Printed Circuit Board		PWR897	Any

Designator	Quantity	Value	Description	PackageReference	PartNumber	Manufacturer
C1, C2	2	0.33 uF	CAP, CERM, 0.33 uF, 50 V, +/- 10%, X7R, AEC-Q200 Grade 1, 1206	1206	GCM31MR71H334KA37L	MuRata
C3, C4, C7, C8, C9, C10, C11	7	2.2 uF	CAP, CERM, 2.2 uF, 16 V, +/- 20%, X7S, AEC-Q200 Grade 1, 0603	0603	CGA3E1X7S1C225M080AC	TDK
C5, C13	2	1000 pF	CAP, CERM, 1000 pF, 25 V, +/- 10%, C0G/NP0, 0603	0603	C0603C102K3GACTU	Kemet
C6, C56, C57, C62, C63, C68, C69, C70, C71, C72, C73, C74, C75	13	51 pF	CAP, CERM, 51 pF, 50 V, +/- 5%, C0G/NP0, 0603	0603	GRM1885C1H510JA01D	MuRata
C12, C14, C15, C16, C17, C18, C19, C76	8	1 uF	CAP, CERM, 1 uF, 16 V, +/- 10%, X7R, AEC-Q200 Grade 1, 0603	0603	GCM188R71C105KA64D	MuRata
C20, C21, C22, C23, C24, C25	6	0.1 uF	CAP, CERM, 0.1 uF, 100 V, +/- 10%, X7R, AEC-Q200 Grade 1, 1206	1206	CGA5L2X7R2A104K160AA	TDK
C26, C27, C31, C33, C40, C41, C43, C47, C48, C55	10	0.47 uF	CAP, CERM, 0.47 uF, 16 V, +/- 10%, X7R, AEC-Q200 Grade 1, 0603	0603	GCM188R71C474KA55D	MuRata
C28, C32, C42, C46	4	0.33 uF	CAP, CERM, 0.33 uF, 50 V, +/- 10%, X7R, AEC-Q200 Grade 1, 0603	0603	CGA3E3X7R1H334K080AB	TDK
C29, C30, C44, C45	4	1 uF	CAP, CERM, 1 uF, 16 V, +/- 20%, X8R, AEC-Q200 Grade 0, 0805	0805	CGA4J3X8R1C105K125AB	TDK
C58, C59, C60, C61, C64, C65, C66, C67	8	2200 pF	CAP, CERM, 2200 pF, 2000 V, +/- 10%, X7R, AEC-Q200 Grade 1, 1206	1206	1206J2K00222KXR	Knowles Capacitors
D1, D2	2	Green	LED, Green, SMD	LED_0805	LTST-C170KGKT	Lite-On
D3, D4, D5, D6, D7, D8	6	6.2 V	Diode, Zener, 6.2 V, 500 mW, AEC-Q101, SOD-123	SOD-123	DDZ6V2B-7	Diodes Inc.
D10, D11, D12, D13	4	24 V	Diode, TVS, Bi, 24 V, 70 Vc, AEC-Q101, SOT-23	SOT-23	PESD1CAN,215	NXP Semiconductor
H1	1		Terminal Block, 9 Pos, 3.5 mm		39500-0009	Molex

Designator	Quantity	Value	Description	PackageReference	PartNumber	Manufacturer
H2, H3	2		Rectangular Housing Connector, 4 Pos, 2.54 mm		50-57-9404	Molex
H4	1		USB to TTL Serial 5 V Cable 5.90' (1.80 m) Kitted Item		TTL-232R-5V	Future Technology Devices International Ltd
H5, H6, H7	3		MACHINE SCREW PAN PHILLIPS 4-40	Machine Screw, 4-40, 1/4 inch	PMSSS 440 0025 PH	B&F Fastener Supply
J1	1		Header(Shrouded), 3.5 mm, 9 x 1, R/A, TH	Header(Shrouded), 3.5 mm, 9 x 1, R/A, TH	39502-1009	Molex
J2, J3, J12, J14, J18	5		Header, 2.54 mm, 1 x 2, Tin, Black, TH	Header, 2.54 mm, 2 x 1, TH	PEC01DAAN	Sullins Connector Solutions
J4	1		Header, 100 mil, 7 x 2, Tin, TH	Header, 7 x 2, 100 mil, Tin	PEC07DAAN	Sullins Connector Solutions
J5, J6	2		Header(shrouded), 2.54 mm, 4 x 1, R/A, Gold, TH	Header(shrouded), 2.54 mm, 4 x 1, R/A, TH	70551-0038	Molex
J7, J9	2		Header, 100 mil, 4 x 1, Tin, TH	Header, 4 x 1, 100 mil, TH	PEC04SAAN	Sullins Connector Solutions
J8	1		Header, 0.5 mm, 6 x 1, R/A, Gold, TH	Header, 0.5 mm, 6 x 1, R/A, TH	22/12/4062	Molex
J13, J17	2		Header, 100 mil, 3 x 1, Tin, TH	Header, 3 PIN, 100 mil, Tin	PEC03SAAN	Sullins Connector Solutions
J15	1		Header, 100 mil, 6 x 1, Tin, TH	TH, 6-Leads, Body 608 x 100 mil, Pitch 100 mil	PEC06SAAN	Sullins Connector Solutions
J16	1		Header, 100 mil, 6 x 2, Tin, TH	Header, 6 x 2, 100 mil, Tin	PEC06DAAN	Sullins Connector Solutions
L1, L2, L3, L4	4	470 uH	Coupled inductor, 470 uH, 0.4 A, 0.35 ohm, SMD	5 x 3.3 mm	744242471	Würth Elektronik
R1	1	47	RES, 47, 5%, 0.25 W, AEC-Q200 Grade 0, 1206	1206	CRCW120647R0JNEA	Vishay-Dale
R2, R7, R10	3	100	RES, 100, 5%, 0.25 W, AEC-Q200 Grade 0, 0603	0603	ESR03EZPJ101	Rohm
R3, R9, R11, R12, R13, R14, R15, R16, R58, R65, R72, R79	12	10.0 k	RES, 10.0 k, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	0603	CRCW060310K0FKEA	Vishay-Dale
R4, R17, R18, R19, R20, R21, R22	7	1.0 k	RES, 1.0 k, 5%, 0.1 W, AEC-Q200 Grade 0, 0603	0603	CRCW06031K00JNEA	Vishay-Dale
R5	1	0	RES, 0, 5%, 0.1 W, AEC-Q200 Grade 0, 0603	0603	CRCW06030000Z0EA	Vishay-Dale

Designator	Quantity	Value	Description	PackageReference	PartNumber	Manufacturer
R6, R8	2	100 k	RES, 100 k, 5%, 0.1 W, AEC-Q200 Grade 0, 0603	0603	CRCW0603100KJNEA	Vishay-Dale
R23, R24, R25, R26, R27, R28, R29	7	47.0	RES, 47.0, 1%, 0.1 W, 0603	0603	RC0603FR-0747RL	Yageo America
R30, R31, R32, R33, R34, R35, R36	7	10.0	RES, 10.0, 1%, 0.5 W, AEC-Q200 Grade 0, 1210	1210	ERJ-14NF10R0U	Panasonic
R37, R38, R41, R42, R45, R46, R49, R50	8	43.0	RES, 43.0, 1%, 0.1 W, 0603	0603	RC0603FR-0743RL	Yageo America
R39, R40, R43, R44, R47, R48, R51, R80	8	10	RES, 10, 5%, 0.1 W, AEC-Q200 Grade 0, 0603	0603	CRCW060310R0JNEA	Vishay-Dale
R81, R82, R83, R84, R85, R86	6	1.00 k	RES, 1.00 k, 0.1%, 0.25 W, 0805	0805	RP73PF2A1K0BTDF	TE Connectivity
RT1	1	10 k	Thermistor NTC, 10 k ohm, 2%, 0603	0603	ERT-J1VG103GA	Panasonic
SH1, SH2, SH3, SH4, SH5	5		Shunt, 100 mil, Gold plated, Black	Shunt 2 pos. 100 mil	881545-2	TE Connectivity
ST1, ST2, ST3	3		Standoff, Hex, 0.5"L #4-40 Nylon	Standoff	1902C	Keystone
SW1	1		Switch, Slide, SPDT, 0.2 A, GULL, 12 V, SMD	SMD, 3-Leads, Body 8.5 x 3.5 mm, Pitch 2.5 mm	CL-SB-12B-01T	Copal Electronics
SW2	1		Switch, SPST 6Pos, SMT	10.4 x 3.9 x 9.3 mm	SDA06H1SBD	C&K Components
T1, T2, T3, T4	4		Transformer, 618 uH, SMT	9.7 x 9.5 mm	CEP99	Sumida
TP1, TP3, TP4, TP5, TP6, TP7, TP8, TP12	8		Test Point, Miniature, White, TH	White Miniature Testpoint	5002	Keystone
TP2, TP10	2		Test Point, Miniature, Red, TH	Red Miniature Testpoint	5000	Keystone
TP9, TP11	2		Test Point, Miniature, Black, TH	Black Miniature Testpoint	5001	Keystone
TP14, TP15, TP16	3		Terminal, Turret, TH, Triple	Keystone1598-2	1598-2	Keystone



Designator	Quantity	Value	Description	PackageReference	PartNumber	Manufacturer
U1	1		SafeTI Precision Monitor With Integrated Hardware Protector for Multi-Cell Lithium-Ion and Lithium Phosphate Battery Pack Automotive Applications SYSTEMS SPECIFICATION , PHP0048G (HTQFP-48)	PHP0048G	BQ79606APHP Q1	Texas Instruments
ZZ4	1		Screws H5, H6 and H7 are for the standoffs ST1, ST2 and ST3			
C34, C35, C36, C37, C38, C39, C49, C50, C51, C52, C53, C54	0	1 uF	CAP, CERM, 1 uF, 50 V, +/- 10%, X7R, AEC-Q200 Grade 1, 1206	1206	GCM31MR71H1 05KA55L	MuRata
D9	0	36 V	Diode, TVS, Bi, 36 V, SMA	SMA	SMAJ36CA	Littelfuse
FID1, FID2, FID3, FID4, FID5, FID6	0		Fiducial mark. There is nothing to buy or mount.	N/A	N/A	N/A
J10, J11	0		Header, 2.54 mm, 7 x 1, Gold, TH	Header, 2.54 mm, 7 x 1, TH	PBC07SAAN	Sullins Connector Solutions
R52, R53, R59, R60, R66, R68, R73, R74	0	0	RES, 0, 5%, 0.125 W, AEC-Q200 Grade 0, 0805	0805	CRCW08050000 Z0EA	Vishay-Dale
R54, R55, R61, R62, R67, R69, R75, R76	0	0	RES, 0, 5%, 0.1 W, AEC-Q200 Grade 0, 0603	0603	CRCW06030000 Z0EA	Vishay-Dale
R56, R57, R63, R64, R70, R71, R77, R78	0	51.1	RES, 51.1, 1%, 0.1 W, 0603	0603	RC0603FR-0751R1L	Yageo America

# Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from D Revision (December 2018) to E Revision	Page
<ul style="list-style-type: none"> <li>Changed instances of BQ79606/BQ79606-Q1 to BQ79606A/BQ79606A-Q1. ....</li> </ul>	1

## STANDARD TERMS FOR EVALUATION MODULES

1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an "EVM" or "EVMs") to the User ("User") in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.
  - 1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductors products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM ("Software") shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms that accompany such Software
  - 1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.
2. *Limited Warranty and Related Remedies/Disclaimers:*
  - 2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.
  - 2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.
  - 2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

### **WARNING**

**Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.**

**User shall operate the Evaluation Kit within TI's recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI's recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI's instructions for electrical ratings of interface circuits such as input, output and electrical loads.**

**NOTE:**

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGRADATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

### 3 Regulatory Notices:

#### 3.1 United States

##### 3.1.1 Notice applicable to EVMs not FCC-Approved:

**FCC NOTICE:** This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

##### 3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

#### **CAUTION**

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

#### **FCC Interference Statement for Class A EVM devices**

*NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.*

#### **FCC Interference Statement for Class B EVM devices**

*NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:*

- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

#### 3.2 Canada

##### 3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

#### **Concerning EVMs Including Radio Transmitters:**

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

#### **Concernant les EVMs avec appareils radio:**

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

#### **Concerning EVMs Including Detachable Antennas:**

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

### Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

#### 3.3 Japan

3.3.1 *Notice for EVMs delivered in Japan:* Please see [http://www.tij.co.jp/sds/ti\\_ja/general/eStore/notice\\_01.page](http://www.tij.co.jp/sds/ti_ja/general/eStore/notice_01.page) 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。

<https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html>

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

【無線電波を送信する製品の開発キットをお使いになる際の注意事項】 開発キットの中には技術基準適合証明を受けていないものがあります。技術適合証明を受けていないもののご使用に際しては、電波法遵守のため、以下のいずれかの措置を取っていただく必要がありますのでご注意ください。

1. 電波法施行規則第6条第1項第1号に基づく平成18年3月28日総務省告示第173号で定められた電波暗室等の試験設備でご使用いただく。
2. 実験局の免許を取得後ご使用いただく。
3. 技術基準適合証明を取得後ご使用いただく。

なお、本製品は、上記の「ご使用にあたっての注意」を譲渡先、移転先に通知しない限り、譲渡、移転できないものとします。

上記を遵守頂けない場合は、電波法の罰則が適用される可能性があることをご留意ください。 日本テキサス・インスツルメンツ株式会社  
東京都新宿区西新宿 6 丁目 2 4 番 1 号  
西新宿三井ビル

3.3.3 *Notice for EVMs for Power Line Communication:* Please see [http://www.tij.co.jp/sds/ti\\_ja/general/eStore/notice\\_02.page](http://www.tij.co.jp/sds/ti_ja/general/eStore/notice_02.page)

電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。<https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-for-power-line-communication.html>

#### 3.4 European Union

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):*

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

---

4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

5. *Accuracy of Information:* To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

6. *Disclaimers:*

6.1 EXCEPT AS SET FORTH ABOVE, EVMS AND ANY MATERIALS PROVIDED WITH THE EVM (INCLUDING, BUT NOT LIMITED TO, REFERENCE DESIGNS AND THE DESIGN OF THE EVM ITSELF) ARE PROVIDED "AS IS" AND "WITH ALL FAULTS." TI DISCLAIMS ALL OTHER WARRANTIES, EXPRESS OR IMPLIED, REGARDING SUCH ITEMS, INCLUDING BUT NOT LIMITED TO ANY EPIDEMIC FAILURE WARRANTY OR IMPLIED WARRANTIES OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF ANY THIRD PARTY PATENTS, COPYRIGHTS, TRADE SECRETS OR OTHER INTELLECTUAL PROPERTY RIGHTS.

6.2 EXCEPT FOR THE LIMITED RIGHT TO USE THE EVM SET FORTH HEREIN, NOTHING IN THESE TERMS SHALL BE CONSTRUED AS GRANTING OR CONFERRING ANY RIGHTS BY LICENSE, PATENT, OR ANY OTHER INDUSTRIAL OR INTELLECTUAL PROPERTY RIGHT OF TI, ITS SUPPLIERS/LICENSORS OR ANY OTHER THIRD PARTY, TO USE THE EVM IN ANY FINISHED END-USER OR READY-TO-USE FINAL PRODUCT, OR FOR ANY INVENTION, DISCOVERY OR IMPROVEMENT, REGARDLESS OF WHEN MADE, CONCEIVED OR ACQUIRED.

7. *USER'S INDEMNITY OBLIGATIONS AND REPRESENTATIONS.* USER WILL DEFEND, INDEMNIFY AND HOLD TI, ITS LICENSORS AND THEIR REPRESENTATIVES HARMLESS FROM AND AGAINST ANY AND ALL CLAIMS, DAMAGES, LOSSES, EXPENSES, COSTS AND LIABILITIES (COLLECTIVELY, "CLAIMS") ARISING OUT OF OR IN CONNECTION WITH ANY HANDLING OR USE OF THE EVM THAT IS NOT IN ACCORDANCE WITH THESE TERMS. THIS OBLIGATION SHALL APPLY WHETHER CLAIMS ARISE UNDER STATUTE, REGULATION, OR THE LAW OF TORT, CONTRACT OR ANY OTHER LEGAL THEORY, AND EVEN IF THE EVM FAILS TO PERFORM AS DESCRIBED OR EXPECTED.

8. *Limitations on Damages and Liability:*

8.1 *General Limitations.* IN NO EVENT SHALL TI BE LIABLE FOR ANY SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL, OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF THESE TERMS OR THE USE OF THE EVMS, REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. EXCLUDED DAMAGES INCLUDE, BUT ARE NOT LIMITED TO, COST OF REMOVAL OR REINSTALLATION, ANCILLARY COSTS TO THE PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES, RETESTING, OUTSIDE COMPUTER TIME, LABOR COSTS, LOSS OF GOODWILL, LOSS OF PROFITS, LOSS OF SAVINGS, LOSS OF USE, LOSS OF DATA, OR BUSINESS INTERRUPTION. NO CLAIM, SUIT OR ACTION SHALL BE BROUGHT AGAINST TI MORE THAN TWELVE (12) MONTHS AFTER THE EVENT THAT GAVE RISE TO THE CAUSE OF ACTION HAS OCCURRED.

8.2 *Specific Limitations.* IN NO EVENT SHALL TI'S AGGREGATE LIABILITY FROM ANY USE OF AN EVM PROVIDED HEREUNDER, INCLUDING FROM ANY WARRANTY, INDEMNITY OR OTHER OBLIGATION ARISING OUT OF OR IN CONNECTION WITH THESE TERMS, EXCEED THE TOTAL AMOUNT PAID TO TI BY USER FOR THE PARTICULAR EVM(S) AT ISSUE DURING THE PRIOR TWELVE (12) MONTHS WITH RESPECT TO WHICH LOSSES OR DAMAGES ARE CLAIMED. THE EXISTENCE OF MORE THAN ONE CLAIM SHALL NOT ENLARGE OR EXTEND THIS LIMIT.

9. *Return Policy.* Except as otherwise provided, TI does not offer any refunds, returns, or exchanges. Furthermore, no return of EVM(s) will be accepted if the package has been opened and no return of the EVM(s) will be accepted if they are damaged or otherwise not in a resalable condition. If User feels it has been incorrectly charged for the EVM(s) it ordered or that delivery violates the applicable order, User should contact TI. All refunds will be made in full within thirty (30) working days from the return of the components(s), excluding any postage or packaging costs.

10. *Governing Law:* These terms and conditions shall be governed by and interpreted in accordance with the laws of the State of Texas, without reference to conflict-of-laws principles. User agrees that non-exclusive jurisdiction for any dispute arising out of or relating to these terms and conditions lies within courts located in the State of Texas and consents to venue in Dallas County, Texas. Notwithstanding the foregoing, any judgment may be enforced in any United States or foreign court, and TI may seek injunctive relief in any United States or foreign court.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
Copyright © 2023, Texas Instruments Incorporated

## IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you fully indemnify TI and its representatives against any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#), [TI's General Quality Guidelines](#), or other applicable terms available either on [ti.com](#) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products. Unless TI explicitly designates a product as custom or customer-specified, TI products are standard, catalog, general purpose devices.

TI objects to and rejects any additional or different terms you may propose.

Copyright © 2026, Texas Instruments Incorporated

Last updated 10/2025