TPS92682EVM-069 CV 2-Phase Boost Controller Evaluation Module

User's Guide

Literature Number: SLUUBX9A
March 2019–Revised August 2019

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General Texas Instruments High Voltage Evaluation (TI HV EMV) User Safety Guidelines

Always follow TI's set-up and application instructions, including use of all interface components within their recommended electrical rated voltage and power limits. Always use electrical safety precautions to help ensure your personal safety and those working around you. Contact TI's Product Information Center http://ti.com/customer support for further information.

Save all warnings and instructions for future reference.

WARNING

Failure to follow warnings and instructions may result in personal injury, property damage or death due to electrical shock and burn hazards.

The term TI HV EVM refers to an electronic device typically provided as an open framed, unenclosed printed circuit board assembly. It is *intended strictly for use in development laboratory environments, solely for qualified professional users having training, expertise and knowledge of electrical safety risks in development and application of high voltage electrical circuits. Any other use and/or application are strictly prohibited by Texas Instruments.* If you are not suitable qualified, you should immediately stop from further use of the HV EVM.

1. Work Area Safety:
   a. Keep work area clean and orderly.
   b. Qualified observer(s) must be present anytime circuits are energized.
   c. Effective barriers and signage must be present in the area where the TI HV EVM and its interface electronics are energized, indicating operation of accessible high voltages may be present, for the purpose of protecting inadvertent access.
   d. All interface circuits, power supplies, evaluation modules, instruments, meters, scopes, and other related apparatus used in a development environment exceeding 50Vrms/75VDC must be electrically located within a protected Emergency Power Off EPO protected power strip.
   e. Use stable and non-conductive work surface.
   f. Use adequately insulated clamps and wires to attach measurement probes and instruments. No freehand testing whenever possible.
2. Electrical Safety:

   As a precautionary measure, it is always good engineering practice to assume that the entire EVM may have fully accessible and active high voltages.
   a. De-energize the TI HV EVM and all its inputs, outputs and electrical loads before performing any electrical or other diagnostic measurements. Revalidate that TI HV EVM power has been safely de-energized.
   b. With the EVM confirmed de-energized, proceed with required electrical circuit configurations, wiring, measurement equipment hook-ups and other application needs, while still assuming the EVM circuit and measuring instruments are electrically live.
   c. Once EVM readiness is complete, energize the EVM as intended.

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WARNING

While the EVM is energized, never touch the EVM or its electrical circuits, as they could be at high voltages capable of causing electrical shock hazard.

3. Personal Safety
   a. Wear personal protective equipment e.g. latex gloves or safety glasses with side shields or protect EVM in an adequate lucent plastic box with interlocks from accidental touch.

Limitation for safe use:

EVMs are not to be used as all or part of a production unit.

User's Guide

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TPS92682EVM-069 CV 2-Phase Boost Controller Evaluation Module

This user's guide describes the specifications, board connection description, characteristics, operation, and use of the TPS92682-Q1 constant-voltage 2-phase boost evaluation module (EVM). The TPS92682-Q1 device implements a fixed-frequency peak current mode control technique with programmable switching frequency, slope compensation, and soft-start. Additional features include wide input voltage range (4.5 V to 65 V), programmable spread spectrum frequency modulation, programmable fault handling, adjustable switching frequency, and adjustable output voltage setting. A complete schematic diagram, printed circuit board layouts, and bill of materials are included in this document.

1 Trademarks

SimpleLink, LaunchPad are trademarks of Texas Instruments.

2 Description

The TPS92682EVM-069 CV boost solution provides a 2-phase , constant voltage boost regulator which is configurable via SPI serial peripheral interface (SPI). It is designed to operate with an input voltage range of 6.5 V to 44 V. The EVM is specified for maximum output voltage of 60 V, maximum input current of I_{IN} = 10 A, and maximum output power of P_{OUT} = 100 W. The CV TPS92682EVM-069 provides high efficiency, SPI programmable fault handling, V_{OUT} setting, and spread-spectrum.

2.1 Typical Applications

This document outlines the operation and implementation of the TPS92682-Q1 as a 2-phase boost voltage regulator with the specifications listed in Table 3. For applications with a different input voltage range or different output voltage range, refer to the TPS92682-Q1 data sheet (SLUSCX8). The MSP-EXP432E401Y SimpleLink™ Ethernet MSP432E401Y MCU LaunchPad™ Development Kit controls the TPS92682EVM-069 evaluation board. The MSP-EXP432E401Y is available on TI website. Alternatively, any SPI controller board can control the TPS92682EVM. After the LaunchPad board is obtained from the TI website, the board must be programmed according to the instructions provided in this design guide. The program instructions are provided in Section 7.

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2.2 Warnings

Observe the following precaution when using the TPS92682EVM-069 evaluation module.

Caution hot surface. Contact may cause burns. Do not touch.

2.3 Connector Description

Table 1 describes the connectors and Table 2 lists the test points on the EVM and how to properly connect, set up, and use the TPS92682EVM-069.

Figure 1 shows the connection diagram and the default jumper locations of the TPS92682EVM-069.

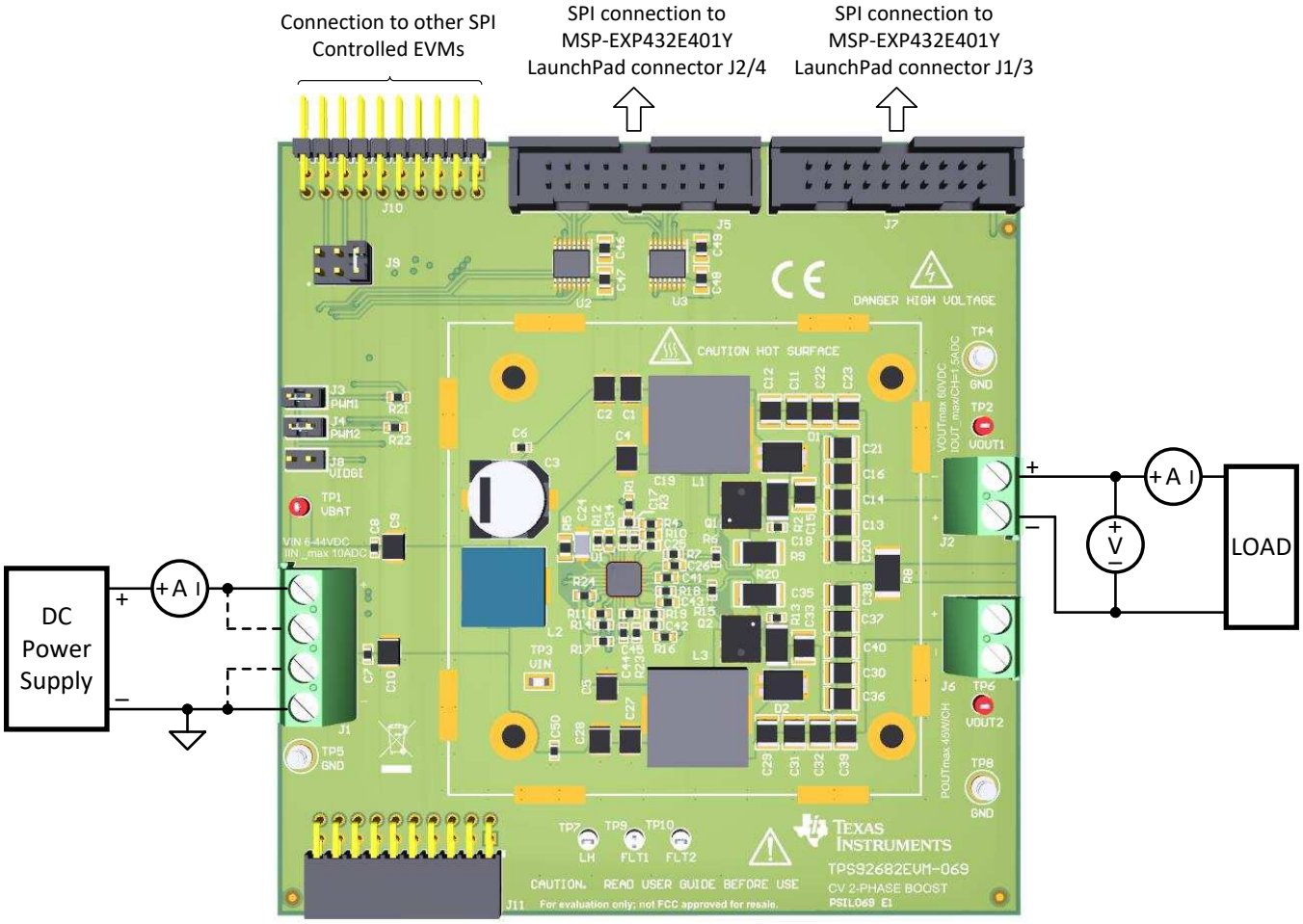

Figure 1. Connection diagram

Table 1. Connector Descriptions

Connector	Label	Description
J5	SPI control from the MSP-EXP432E401Y LaunchPad	J5 and J7 allow attachment of a header cable for SPI control of the TPS92682-Q1 to the TI SimpleLink™ Ethernet MSP-EXP432E401Y MCU LaunchPad™ Development Kit, part number MSP-EXP432E401Y
J7		
J10	SPI control signals to other SPI controlled EVM	J10 and J11 allow star connection of TPS92682EVM-069 boards to each other with one MSP-EXP432E401Y control board.
J11		

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Table 1. Connector Descriptions (continued)

Connector	Label	Description
J1	VIN, GND	J1 connects the input power to the TPS92682EVM-069 board. The board silkscreen identifies VIN pins with "+" and the GND pins with "–" markings.
J2	VOUT1, VOUT2 and GND	J2 is connected to the channel-1 output and J6 is connected to the channel-2 output of the TPS92682-Q1 device. By default the board is configured as 2-phase . Therefore, VOUT1 and VOUT2 are shorted together with a zero-Ω resistor, R8.
J6		
J8	VDD jumper	J8 is a jumper provided to share VDD with other SPI controlled EVM, in case a digital supply is needed. For the operation of this EVM, leave this jumper open.
J3	PWM1 jumper	J3 and J4 are jumpers to allow for PWM signals to be applied to the two channels. When the jumpers are removed and the R21 and R22 zero-Ω resistors are installed, the PWM signals can be generated from the MSP-EXP432E401Y controller board. When the jumpers are populated (by default), the PWM1 and PWM2 pins of the TPS92682-Q1 are connected to VDD. The PWM signals can be used to disable the associated channels in CV mode.
J4	PWM2 jumper	
J9	SSN configuration jumper	J9 allows configuration of the SSN chip select line, when multiple chips on the same SPI bus are used. By default, evaluation module is configured to be connected to the SSN0 of the MSP-EXP432E401Y controller board.

Table 2. Test Points

Test Point	Description
Metal turrets	All metal turrets are grounds.
VBAT	The VBAT test point allows for voltage measurement of the external power supply applied to the evaluation board.
VIN	The VIN test point allows for voltage measurement of the power applied to the BOOST voltage regulator after the EMI filter.
LH	The LH test point allows for external application of voltage to the LH pin and place the TPS92682-Q1 in Limp Home mode
FLT1	The FLT1 test point can be used to monitor the fault occurrence of the channel-1. When a fault occurs, FLT1 voltage level goes low. Note that during power up, FLT1 is low (due to POR). The Fault pins can be reset by setting bit-7 of the EN register 0x00.
FLT2	The FLT2 test point can be used to monitor the fault occurrence of the channel-2. When a fault occurs, FLT2 voltage level goes low. Note that during power up, FLT2 is low (due to POR). The Fault pins can be reset by setting bit-7 of the EN register 0x00. The FLT2 test point can also be used for synchronizing of the TPS92682-Q1 with an external clock.
VOUT1	The VOUT1 test point allows for voltage measurement of the channel-1 output.
VOUT2	The VOUT2 test point allows for voltage measurement of the channel-2 output.

3 REACH Compliance

In compliance with the Article 33 provision of the EU REACH regulation we are notifying you that this EVM includes component(s) containing at least one substance of very high concern (SVHC) above 0.1%. These uses from Texas Instruments do not exceed 1 ton per year. The SVHC's are:

Component Manufacturer	Component part number	SVHC Substance	SVHC CAS (when available)
Phoenix Contact GmbH & Co. KG	1715721 and 1715747	Lead (Pb)	7439-92-1

4 Performance Specifications

Table 3 provides the EVM electrical performance specifications.

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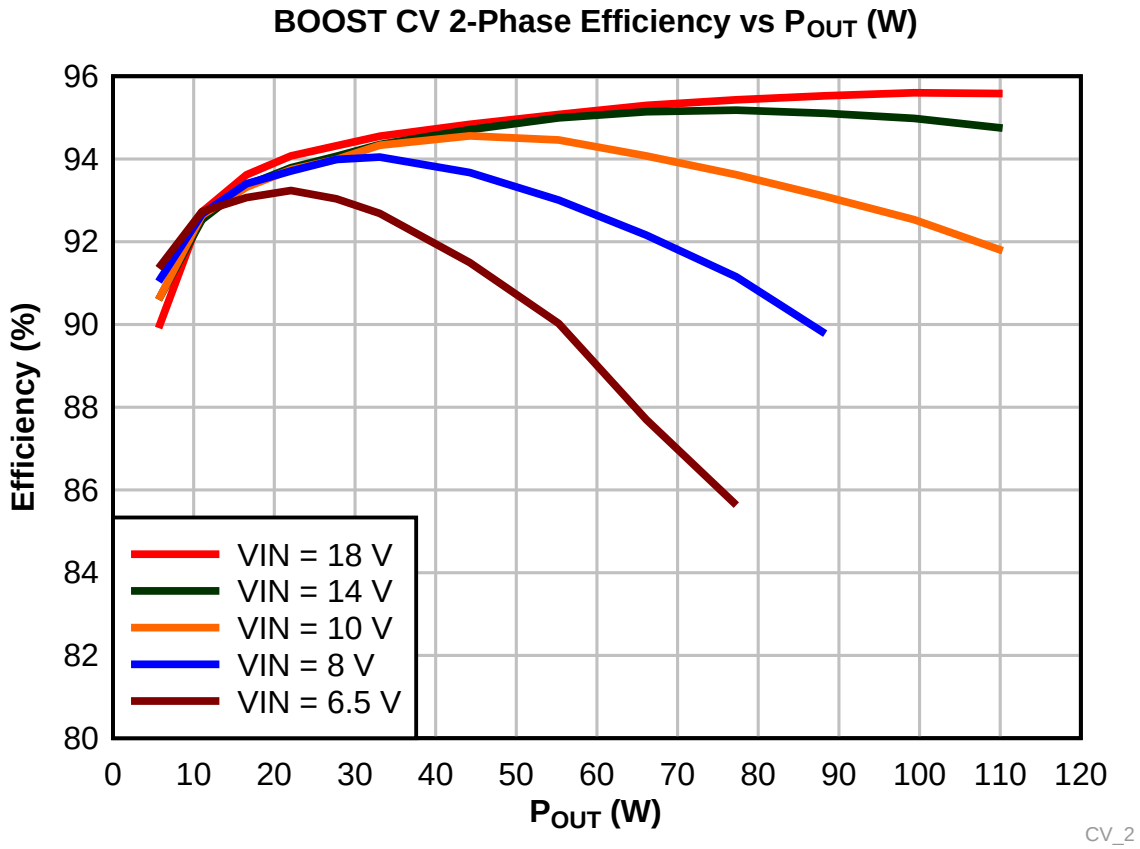
Table 3. TPS92682EVM Performance Specifications

Parameter	Description	Min	Typ	Max	Units
Input Characteristics					
Voltage, V_{IN}		6.5	14	28	V
Maximum Input Current, I_{IN}				10	A
Output Characteristics					
Output Voltage, V_{OUT}	Maximum voltage configured by the output voltage divider and programmable by the SPI	V_{IN}		61	V
Maximum Output Current, I_{OUT}	Total output current in 2-phase mode			3	A
Maximum Output Power, P_{OUT}	Total output power in 2-phase mode			90	W
Systems Characteristics					
Switching frequency	Switching Frequency (f_{SW}) Range	100		800	kHz
Peak efficiency				95	%
Operating temperature		–40	25	125	ºC

5 Performance Data and Typical Characteristic Curves

Figure 2 illustrates the efficiency results for the TPS92682EVM-069 vs output power for different input voltage V_{IN}. It is important to note that the efficiency results include the power loss in the input EMI filter.



Conditions: V_{OUT} 55 V.

Figure 2. Efficiency vs Output Power

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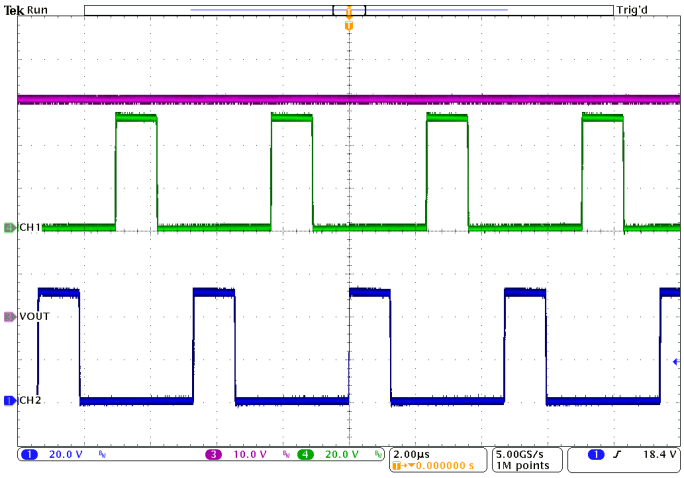
5.1 2-phase CV BOOST SW-Node Voltage Waveform

Figure 2 shows the switch node voltage waveforms of the Phase-1 (channel-1) and Phase-2 (channel-2) of the TPS92682EVM-069 2-phase CV BOOST, together with the output voltage (V_{OUT}) set to 50 V. The switch turn-on of the two phases are 180° phase shifted in order to reduce input current and output voltage ripple.

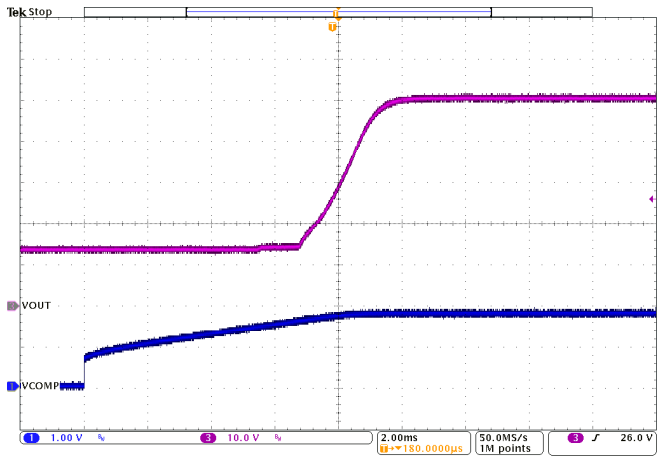


V_{OUT} = 50 V

Figure 3. CV boost 2-phase SW-node voltage

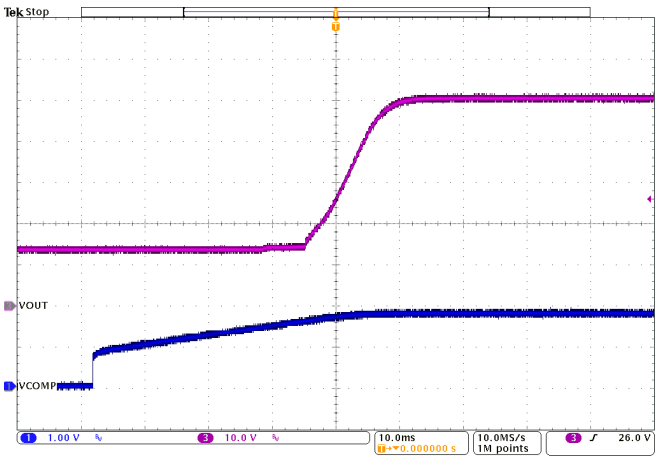
5.2 Startup Waveforms

Figure 4 and Figure 5 show the startup waveforms, V_{OUT} and compensator voltage of the TPS92682EVM-069 2-phase CV boost for two different soft-start register (0x06) settings.



CHxSS3:0 = 7

Figure 4. Soft-start timing



CHxSS3:0 = 31

Figure 5. Softstart waveforms for

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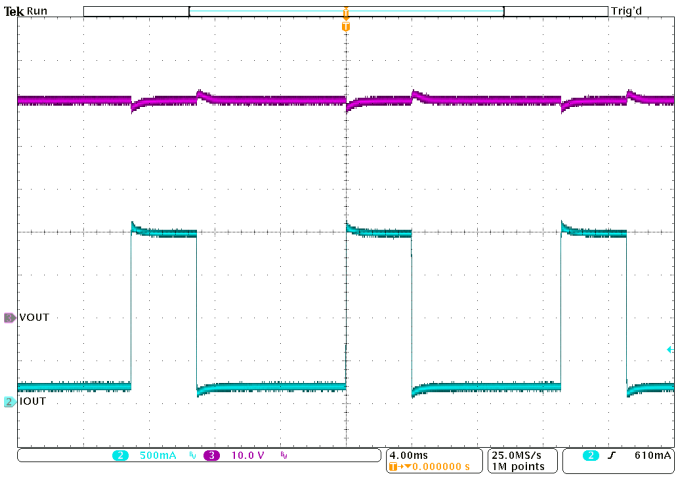
5.3 Load Transient

Figure 6 shows the load transient on the output of the TPS92682EVM-069 2-phase CV boost. The output current is stepped up and down from 0.2 A to 2 A (P_{OUT} from 10 W to 100 W). The V_{OUT} waveform (in purple) shows the resulting undershoot and overshoot.



V_{IN} = 14 V, V_{OUT} = 50 V

Figure 6. CV 2-phase boost load transient

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6 Schematic, PCB Layout, and Bill of Materials

This section contains TPS92682EVM-069 schematics, PCB layouts, and bill of materials (BOM).

6.1 Schematic

Figure 7 illustrates the TPS92682EVM-069 schematic.

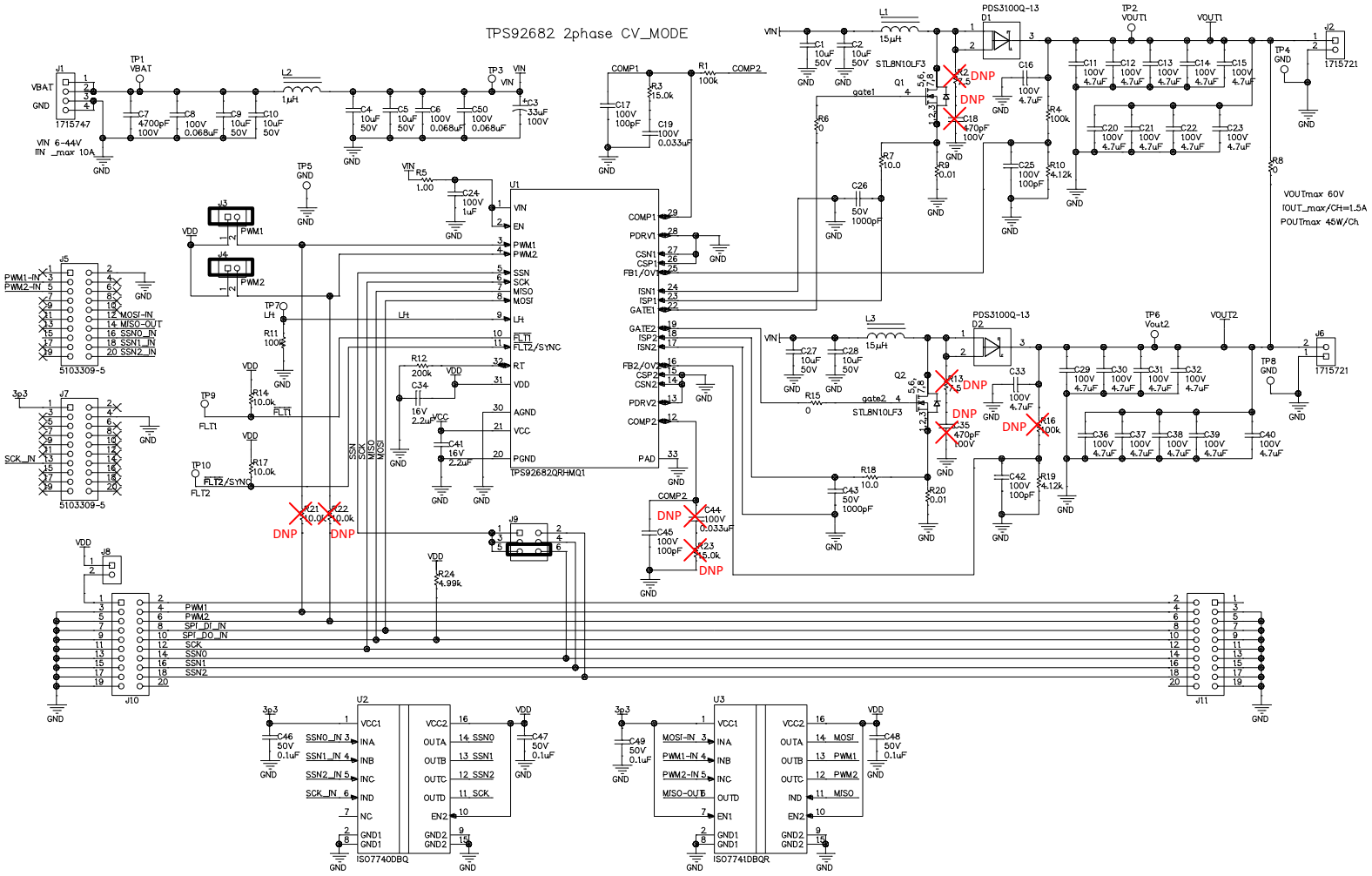

Figure 7. TPS92682EVM-069 Schematic

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6.2 Layout

The TPS92682EVM-069 is a 4-layer board. Figure 8, Figure 9, Figure 10, Figure 11 and Figure 12 illustrate the assembly, the top, the inner-layer1, the inner-layer2 and the bottom side of the TPS92682EVM-069 PCB layout. The Inner-layer 1 is a ground plane and there is no routing on this layer.

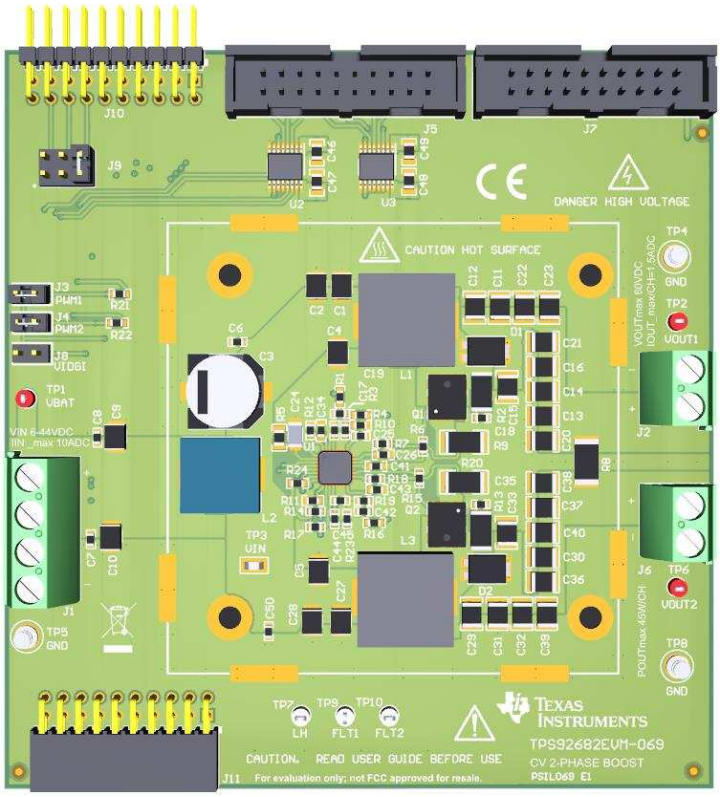

Figure 8. TPS92682EVM-069 assembly drawing

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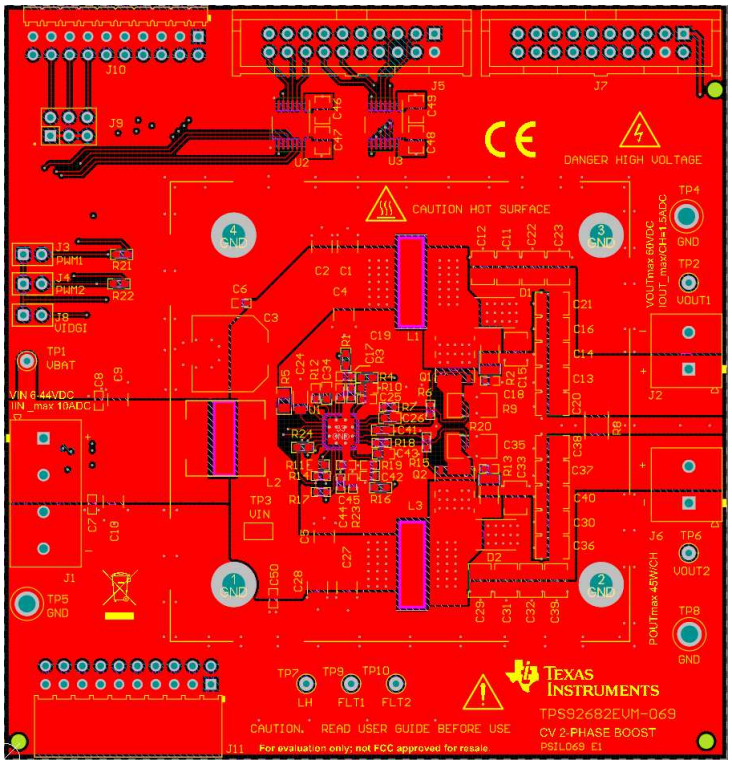

Figure 9. TPS92682EVM-069 Top Layer and Top Overlay (Top View)

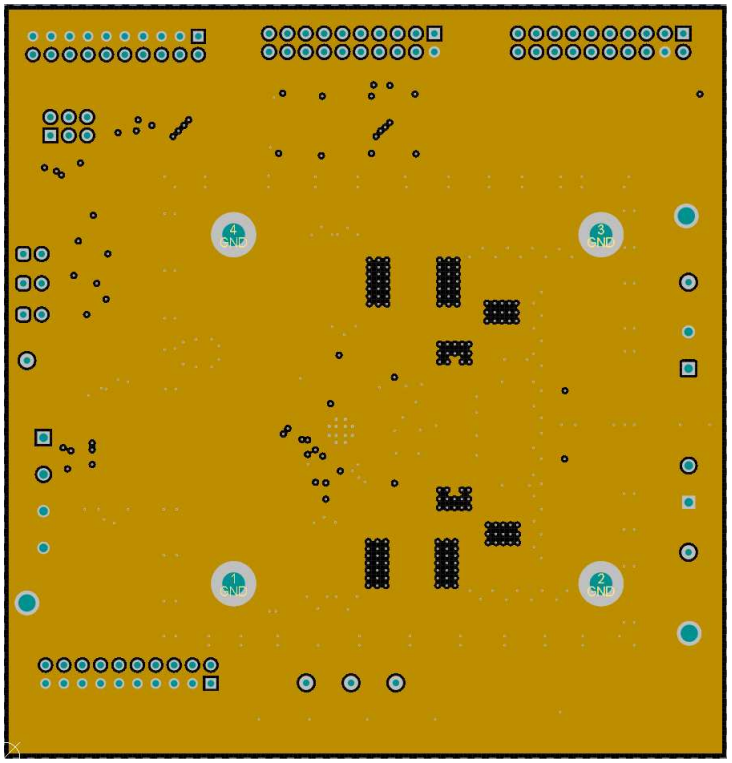

Figure 10. TPS92682EVM-069 inner-layer 1

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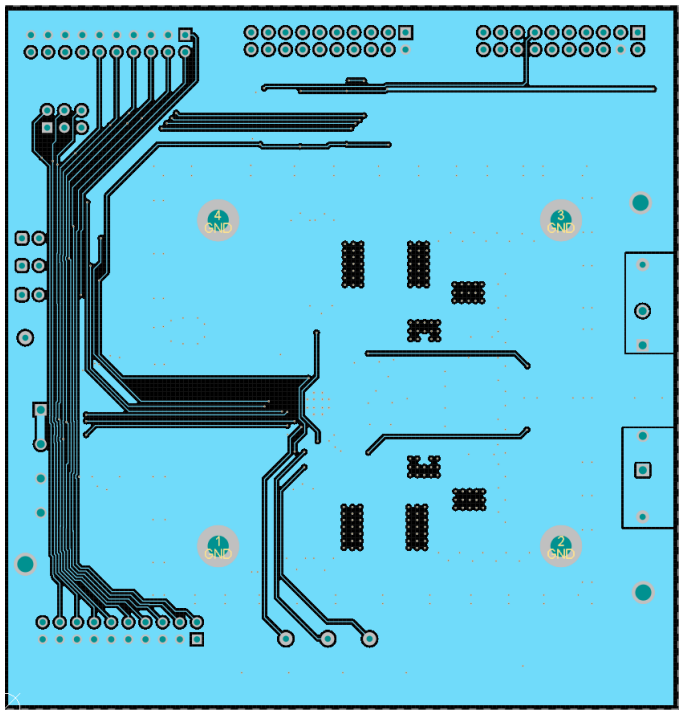

Figure 11. TPS92682EVM-069 inner-layer 2

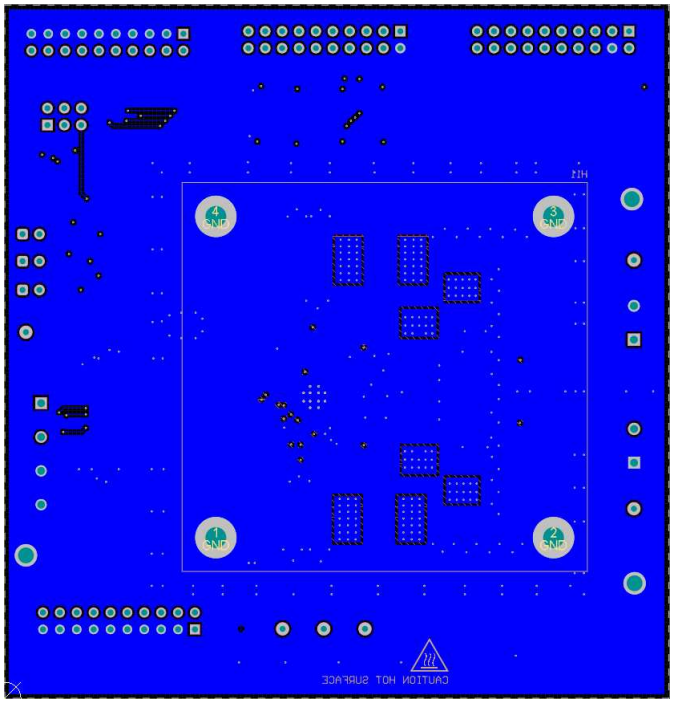

Figure 12. TPS92682EVM-069 bottom layer and bottom overlay (Bottom View)

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6.3 Bill of Materials

Table 4 lists the TPS92682EVM-069 bill of materials.

Table 4. TPS92682EVM-069 Bill of Materials

Designator	Qty	Value	Description	Package	Part Number	Manufacturer
C1, C2, C4, C5, C9, C10, C27, C28	8	10 µF	CAP, CERM, 10 µF, 50 V, ±10%, X7S, AEC-Q200 Grade 1	1210	CGA6P3X7S1H106K250AB	TDK
C3	1	33 µF	CAP, AL, 33 µF, 100 V, ±20%, AEC-Q200 Grade 2, SMD	D10xL10mm	MAL215097904E3	Vishay
C6, C8, C50	3	0.068 µF	CAP, CERM, 0.068 µF, 100 V, ±10%, X7S, AEC-Q200 Grade 1	0603	CGA3E3X7S2A683K080AB	TDK
C7	1	4700 pF	CAP, CERM, 4700 pF, 100 V, ±10%, X7R, AEC-Q200 Grade 1	0603	CGA3E2X7R2A472K080AA	TDK
C11, C12, C13, C14, C15, C16, C20, C21, C22, C23, C29, C30, C31, C32, C33, C36, C37, C38, C39, C40	20	4.7 µF	CAP, CERM, 4.7 µF, 100 V, ±10%, X7S, AEC-Q200 Grade 1	1210	CGA6M3X7S2A475K200AB	TDK
C17, C25, C42, C45	4	100 pF	CAP, CERM, 100 pF, 100 V, ±5%, C0G/NP0, AEC-Q200 Grade 1	0603	GCM1885C2A101JA16D	MuRata
C19	1	0.033 µF	CAP, CERM, 0.033 µF, 100 V, ±10%, X7S, AEC-Q200 Grade 1	0603	CGA3E3X7S2A333K080AB	TDK
C24	1	1 µF	CAP, CERM, 1 µF, 100 V, ±10%, X7R	1206	GCM31CR72A105KA03	MuRata
C26, C43	2	1000 pF	CAP, CERM, 1000 pF, 50 V, ±5%, X7R, AEC-Q200 Grade 1	0603	C0603C102J5RACAUTO	Kemet
C34, C41	2	2.2 µF	CAP, CERM, 2.2 µF, 16 V, ±20%, X7S, AEC-Q200 Grade 1	0603	CGA3E1X7S1C225M080AC	TDK
C46, C47, C48, C49	4	0.1 µF	CAP, CERM, 0.1 µF, 50 V, ±10%, X7R	0805	C0805C104K5RACTU	Kemet
D1, D2	2	100 V	Diode, Schottky, 100 V, 3 A, AEC-Q101	POWERDI5	PDS3100Q-13	Diodes Inc
H1, H2, H5, H6	4		MACHINE SCREW PAN PHILLIPS 4-40		PMS 440 0038 PH	B&F Fastener Supply
H3, H4, H7, H8	4				HNSS440	B&F Fastener Supply
H9	1		Sil-Pad K-10 high performance insulator		SPK10-0.006-AC-1212-NA	BERGQUIST
H10, H12, H13, H14, H15, H16, H17, H18	8		RFI SHIELD CLIP TIN SMD		S2711-46R	Harwin
H11	1		Heatsink DC/DC half brick vert		518-95AB	Wakefield Solutions
J1	1		Terminal Block, 5.08mm, 4x1, TH	4POS Terminal Block	1715747	Phoenix Contact
J2, J6	2		Terminal Block, 5.08 mm, 2x1, TH	2POS Terminal Block	1715721	Phoenix Contact

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Table 4. TPS92682EVM-069 Bill of Materials (continued)

Designator	Qty	Value	Description	Package	Part Number	Manufacturer
J3, J4, J8	3		Header, 100 mil, 2x1, Gold, TH	2x1 Header	TSW-102-07-G-S	Samtec
J1, J4	2		Header (shrouded), 100 mil, 10x2, Gold, TH	10x2 Header	5103309-5	TE Connectivity
J9	1		Header, 100 mil, 3x2, Gold, TH	3x2 Header	TSW-103-07-G-D	Samtec
J10	1		Header, 2.54 mm, 10x2, Tin, R/A, TH	10 x 2 Header	TSW-110-08-x-D-RA	Samtec
J11	1		Receptacle, 2.54mm, 10x2, Gold, R/A, TH	10 x 2 Header	SSW-110-02-G-D-RA	Samtec
L1, L3	2	15 µH	Inductor, Shielded, 15 µH, 8.5 A, 0.02299 Ω, AEC-Q200 Grade 0, SMD	13x12.5mm	SPM12565VT-150M-D	TDK
L5	1	1 µH	Inductor, Shielded, 1 µH, 22.5 A, 0.0026 Ω, AEC-Q200 Grade 0, SMD	10.5x10mm	SPM10065VT-1R0M-D	TDK
Q1, Q2	2	100 V	MOSFET, N-CH, 100 V, 20 A, AEC-Q101, 8-PowerVDFN	PowerFLAT5x6_R	STL8N10LF3	STMicroelectronics
R1, R6, R15	3	0	RES, 0, 1%, 0.1 W, AEC-Q200 Grade 0	0603	RMCF0603ZT0R00	Stackpole Electronics
R3	1	15.0 kΩ	RES, 15.0 k, 1%, 0.1 W	0603	RC0603FR-0715KL	Yageo
R4, R11	2	100 kΩ	RES, 100 k, 1%, 0.1 W, AEC-Q200 Grade 0	0603	RC0603FR-07100KL	Yageo
R5	1	1.00 Ω	RES, 1.00, 1%, 0.125 W, AEC-Q200 Grade 0	0805	CRCW08051R00FKEA	Vishay-Dale
R7, R18	2	10.0 Ω	RES, 10.0, 1%, 0.1 W, AEC-Q200 Grade 0	0603	CRCW060310R0FKEA	Vishay-Dale
R8	1	0 Ω	RES, 0, 0.05%, 2 W, AEC-Q200 Grade 0	2512	HCJ2512ZT0R00	Stackpole Electronics
R9, R20	2	0.01 Ω	RES, 0.01, 1%, 1 W	2010	PMR50HZPFU10L0	Rohm
R10, R19	2	4.12 kΩ	RES, 4.12 k, 1%, 0.1 W, AEC-Q200 Grade 0	0603	CRCW06034K12FKEA	Vishay-Dale
R12	1	200 kΩ	RES, 200 k, 1%, 0.1 W, AEC-Q200 Grade 0	0603	CRCW0603200KFKEA	Vishay-Dale
R14, R17, R21, R22	4	10.0 kΩ	RES, 10.0 k, 1%, 0.1 W, AEC-Q200 Grade 0	0603	CRCW060310K0FKEA	Vishay-Dale
R24	1	4.99 kΩ	RES, 4.99 k, 0.1%, 0.1 W	0603	RT0603BRD074K99L	Yageo
SH-J1, SH-J2, SH-J3, SH-J4, SH-J5, SH-J6, SH-J7	7		Shunt, 2.54 mm, Gold, Black	2x1, 2.54mm	60900213421	Wurth Elektronik
TP1, TP2, TP6	3		Test Point, Miniature, Red, TH	TH	5000	keystone
TP3	1		Test Point, Miniature, SMT	SMT	5015	keystone
TP4, TP5, TP8	3		Terminal, Turret, TH, Double	Turret	1502-2	keystone
TP7, TP9, TP10	3		Test Point, Miniature, White, TH	TH	5002	keystone
U1	1		Dual Channel Constant Voltage and Constant Current Controller with SPI Interface, RHM0032A (VQFNP-32)	RHM0032A	TPS92682QRHMQ1	Texas Instruments
U2	1		High-Speed, Low-Power, Robust EMC Quad-Channel Digital Isolator, DBQ0016A (SSOP-16)	DBQ0016A	ISO7740DBQ	Texas Instruments

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Table 4. TPS92682EVM-069 Bill of Materials (continued)

Designator	Qty	Value	Description	Package	Part Number	Manufacturer
U3	1		High Speed, Robust EMC Quad-Channel Digital Isolators, DBQ0016A (SSOP-16)	DBQ0016A	ISO7741DBQR	Texas Instruments

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7 Software

This section describes the installation of the GUI software, the necessary drivers to operate the TPS92682EVM-069.

7.1 Demonstration Kit Software Installation for MSP-EXP432E401Y LaunchPad Board

7.1.1 Installation overview

This is a summary of the installation steps. To see step-by step instructions with screen shots, see Section 7.1.2.

1. Click on *TPS92682 LaunchPad Evaluation Software Installer.exe*,
2. Right click, and choose **Run As Administrator**
3. Click **yes** when *Windows Account Control* asks to allow the program to make changes to the computer
4. Click **I Agree** to the installation license terms and install in the recommended location

Installation will take a few minutes, as it may need to install Microsoft .NET Framework. If the installer asks if you wish to reboot after installing Microsoft .NET, you must click **Restart Later** and allow the driver installation to complete.

After running the *TPS92682 LaunchPad Evaluation Software Installer.exe*, the evaluation software window appears as shown in Figure 13.

7.1.2 Step-by-step installation instructions

This section shows the detailed installation instructions with screen shots.

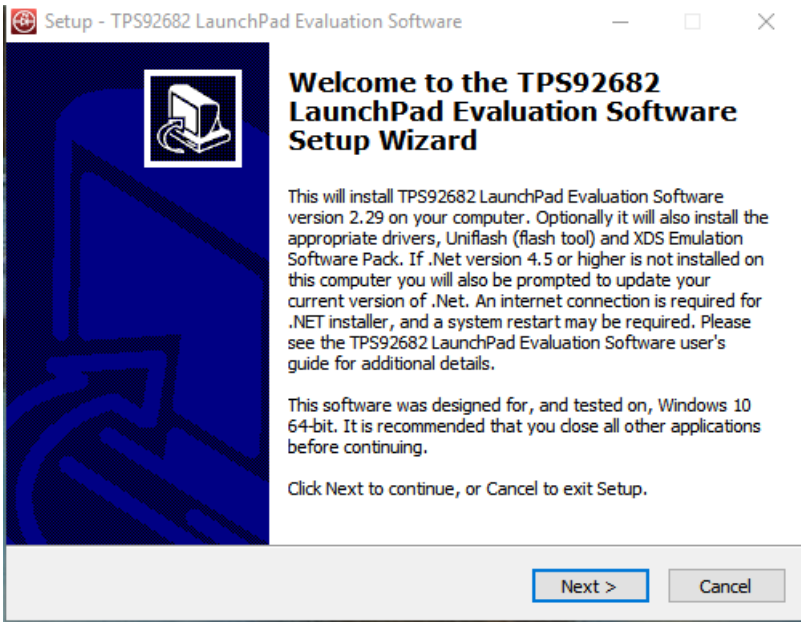

Figure 13. Setup Screen 1

Click **Next >** to install.

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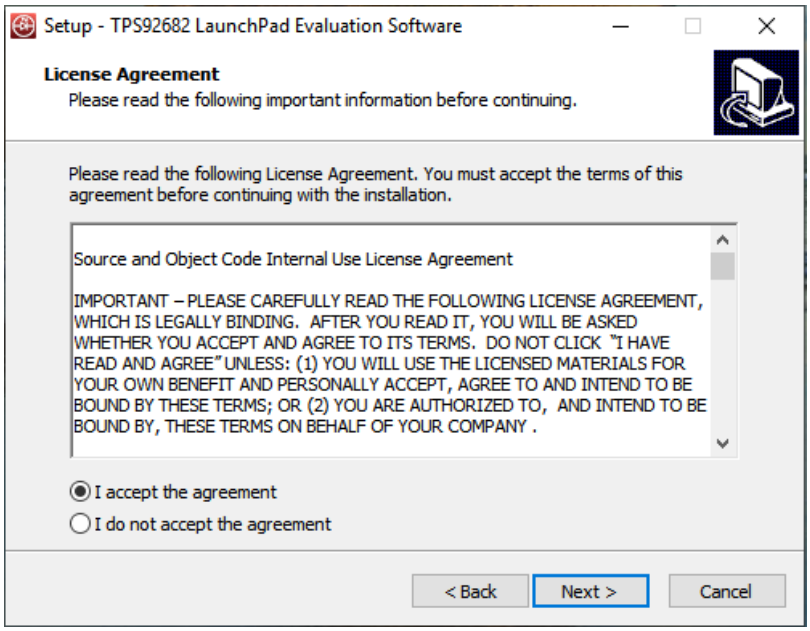

Figure 14. Setup Screen 2

Click **Next >** to accept the License Agreement.

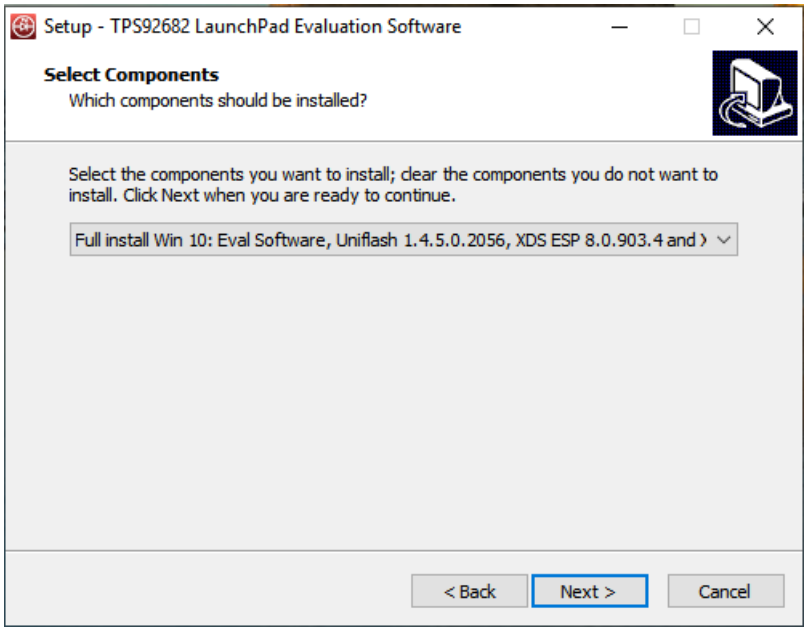

Figure 15. Setup Screen 3

Select **Full Install** and click **Next >** to install the evaluation software, the UniFlash, and the required XDS drivers. Full installation for both Windows 10 and 7 are provided.

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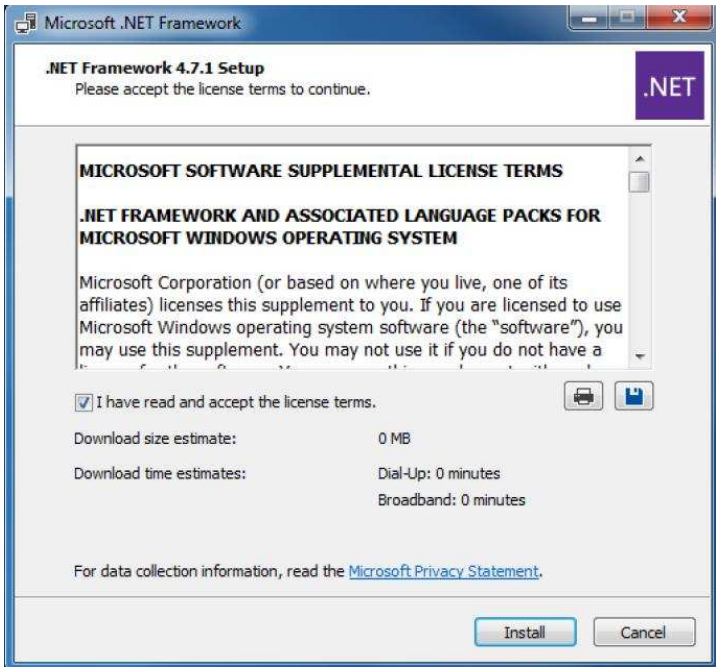

Figure 16. Setup Screen 4

If .NET Framework 4.5 or higher does not exist on the computer, the .NET Framework installation begins. Installation of .NET Framework will take several minutes. If .NET Framework 4.5 or higher exists on the computer, the installation jumps to the XDS driver installation.

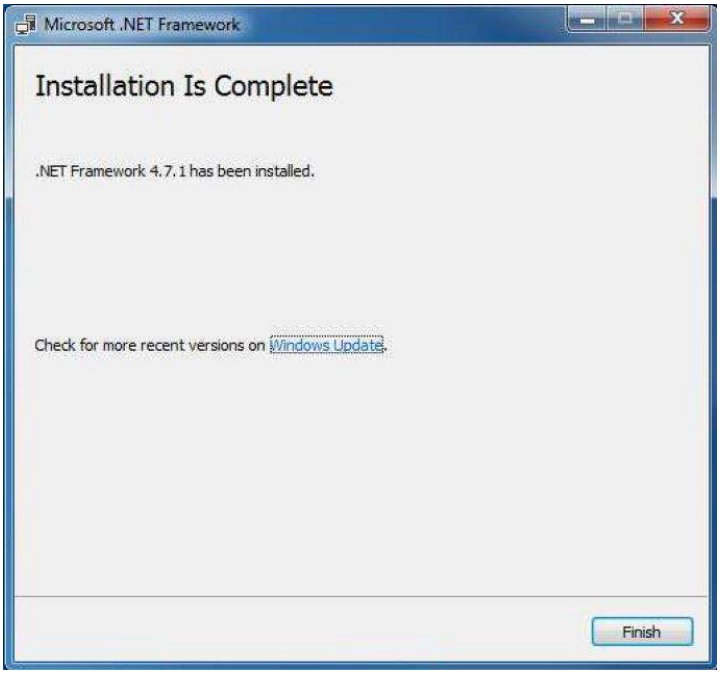

Figure 17. Setup Screen 5

A window appears indicating the completion of the .NET Framework installation.

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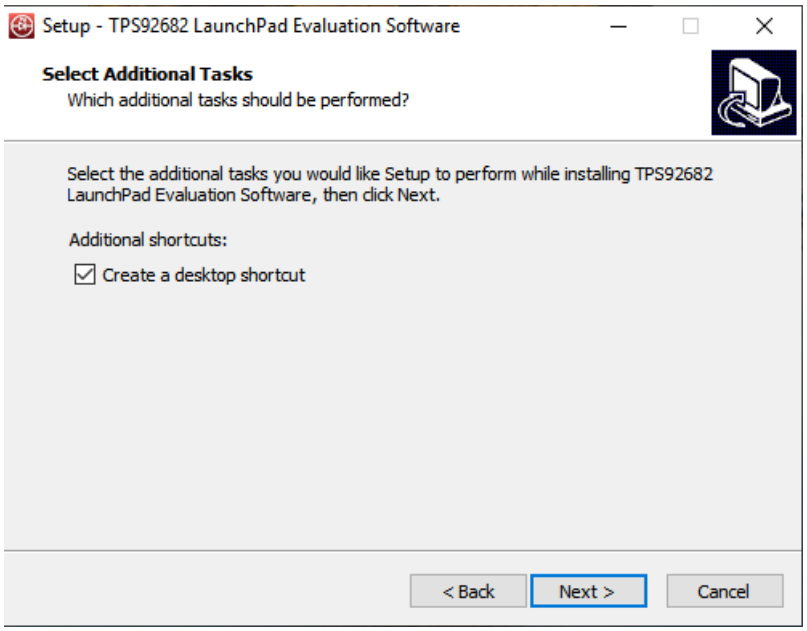

Figure 18. Setup Screen 6

Click next to proceed.

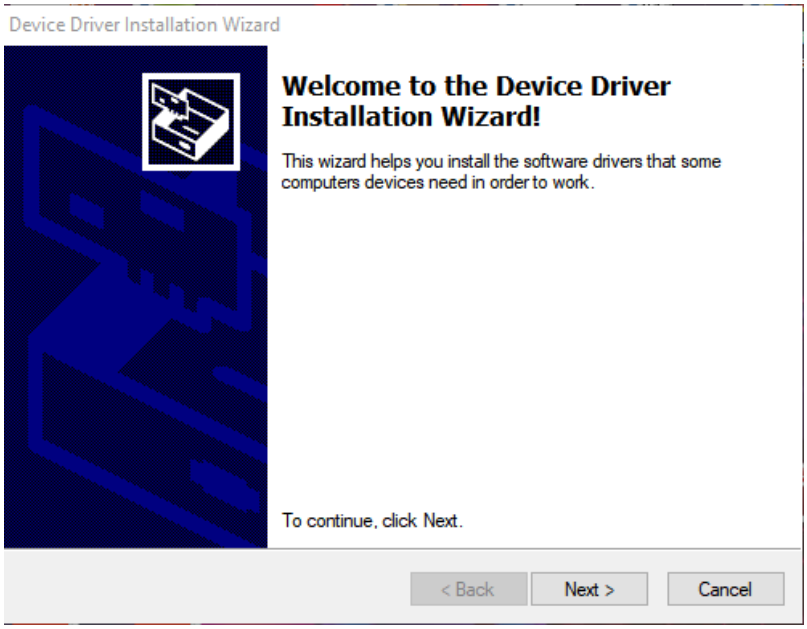

Figure 19. Setup Screen 7

Click the **Next >** button to install the XDS driver.

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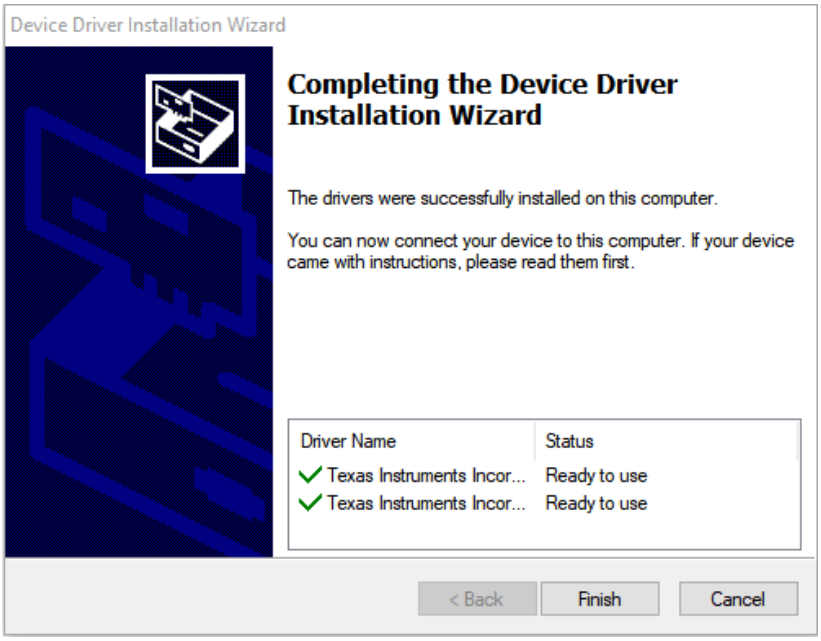

Figure 20. Setup Screen 8

The completion of the XDS driver installation is shown in Figure 20.

The TI-Emulators installation starts at this point. This will install the necessary drivers for running the application. In the next few steps as shown in Figure 21, Figure 22 and Figure 23 click **Next >** to perform the installation.

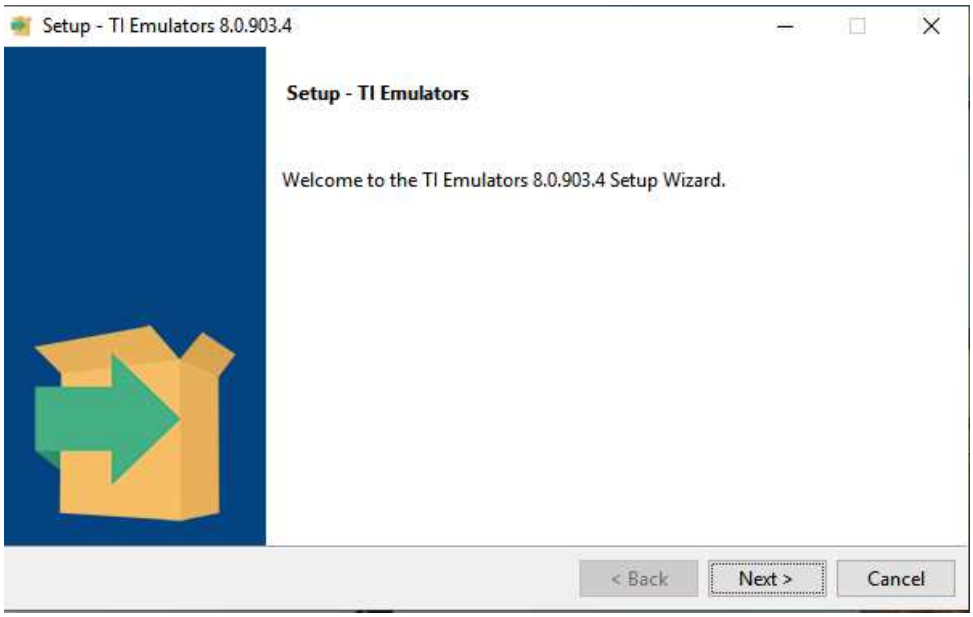

Figure 21. Setup Screen 9

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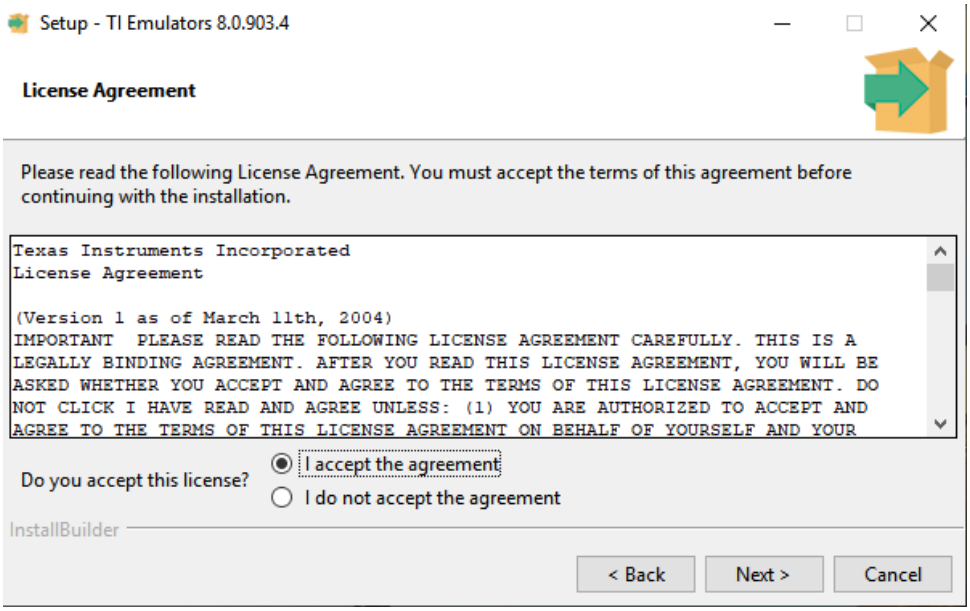

Figure 22. Setup Screen 10

Accept the license agreement in Figure 22.

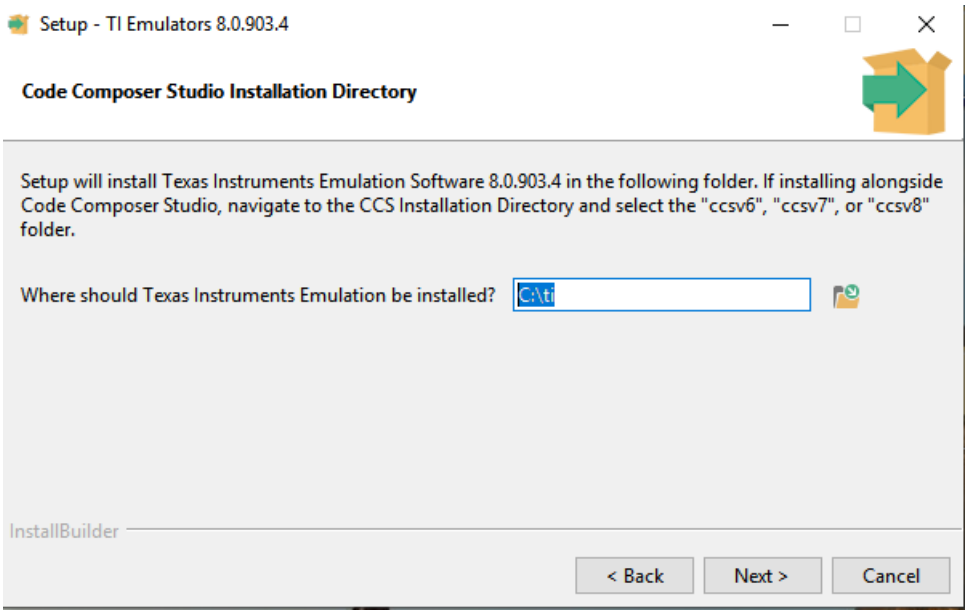

Figure 23. Setup Screen 11

In the next few windows click **Next >**, and if prompted by Windows Security about software installation as shown in Figure 24, select **Install**.

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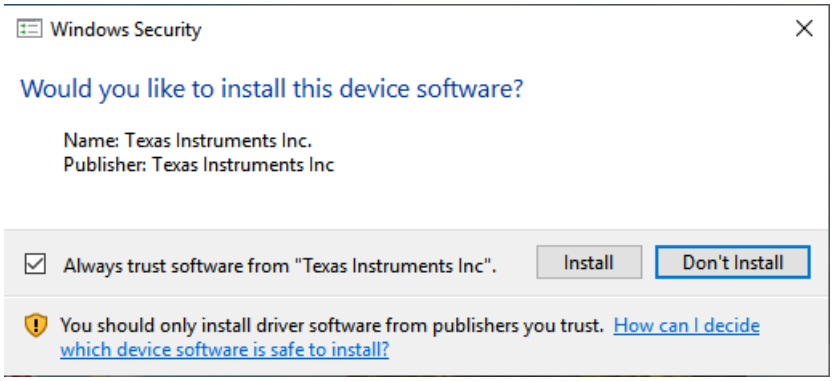

Figure 24. Setup Screen 12

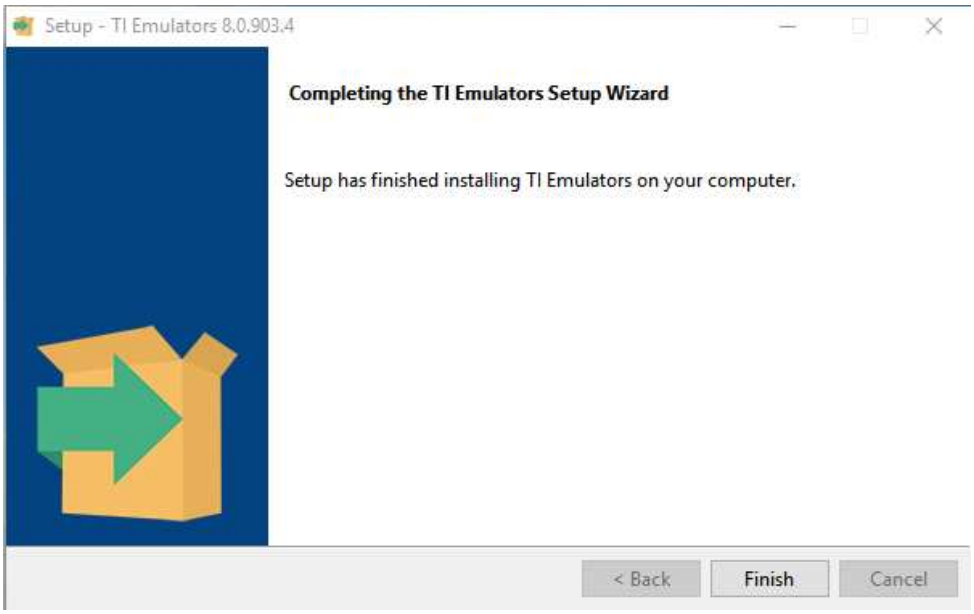

Figure 25. Setup Screen 13

The screen showing the completion of the TI Emulators installation is shown in Figure 25. Click on **Finish** to move to the next step.

The UniFlash installation starts at this point. UniFlash is required to program the LaunchPad. In the next few steps as shown in Figure 26, Figure 27 and Figure 28 click **Next >** to proceed and start the installation.

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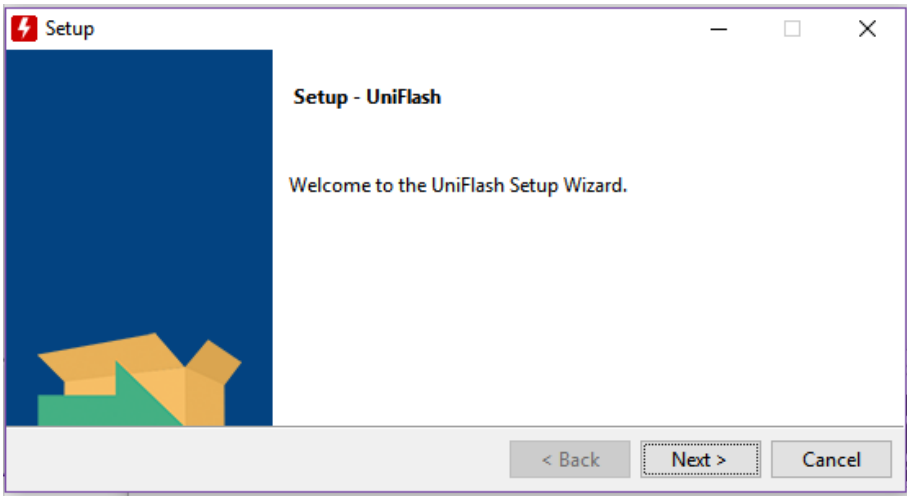

Figure 26. Setup Screen 14

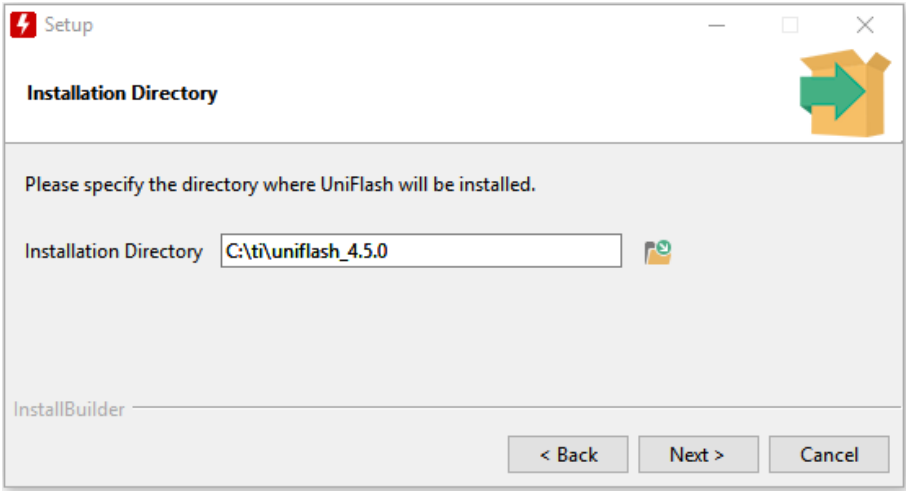

Figure 27. Setup Screen 15

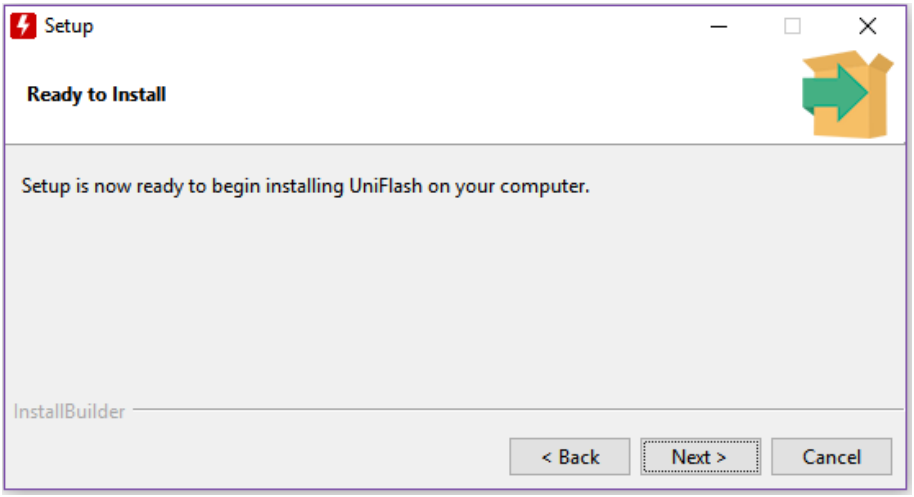

Figure 28. Setup Screen 16

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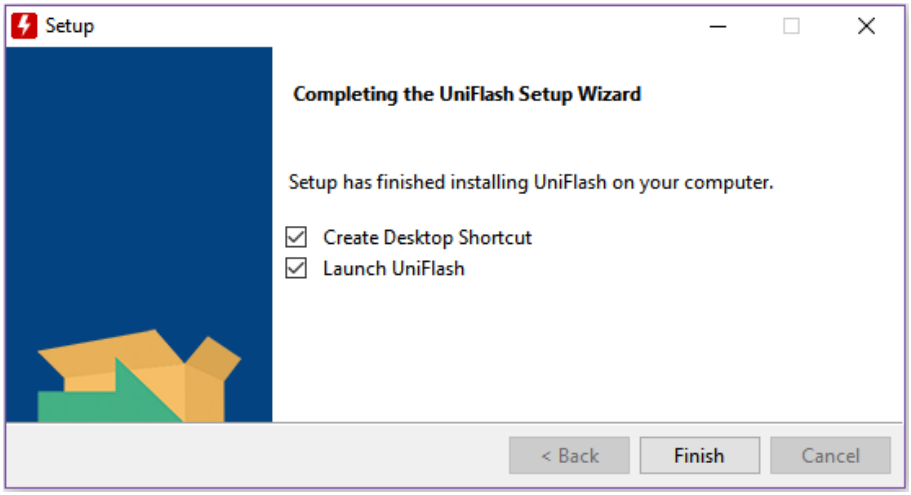

Figure 29. Setup Screen 17

When UniFlash installation is complete, click **Finish** to launch the UniFlash and program the LaunchPad.

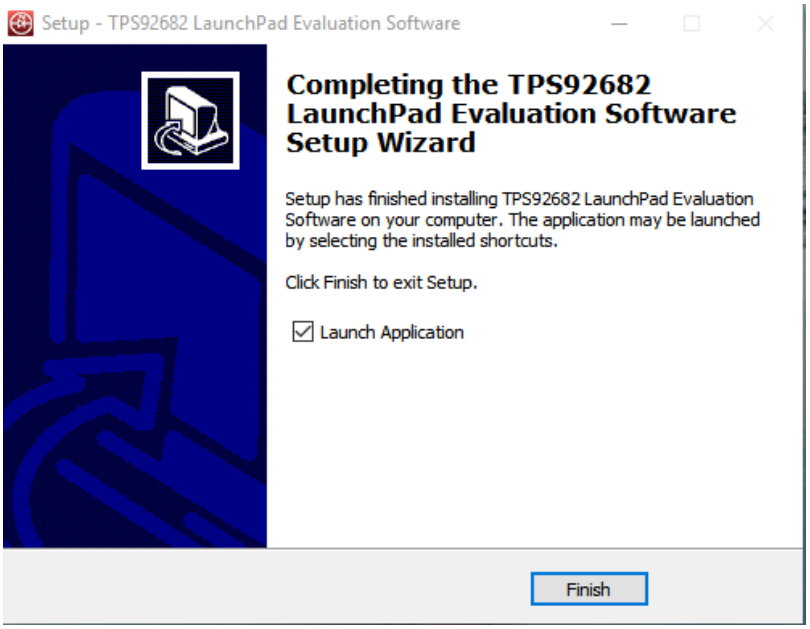

Figure 30. Setup Screen 18

Figure 30 shows the completion of the TPS92682-Q1 Evaluation Software . Un-check the **Launch Application** and click **Finish**.

7.2 Installation Error Recovery

If the screen shown in Figure 31 appears, follow the steps below to install an unsigned driver one time.

1. Click **Start** and select **Settings**.
2. Click **Update and Security**.
3. Click **Recovery**.
4. Click **Restart Now** under **Advanced Startup**.
5. Click **Troubleshoot**.
6. Select **Advanced Options**.

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7. Select **Startup Settings**.
8. Click **Restart**.
9. On the **Startup Settings** screen, press F7 during reboot to disable driver signature enforcement. The host computer restarts.
10. Repeat the entire reinstallation process.
11. A message appears informing that installing .NET failed. Close that window and continue.
12. Double-click **Install unsigned drivers**.

After restarting a second time, the host computer resets. The reset requires all drivers to be digitally signed the next time a default installation executes, unless these steps are repeated.

Figure 31. Setup Screen 9

7.3 Programming the MSP-EXP432E401Y LaunchPad Board

Use UniFlash to program the LaunchPad board before starting the GUI. Connect the included Micro-USB cable to the USB port of the PC and the LaunchPad as shown in Figure 32. Connect a jumper between pin 3 and pin 4 of the connector JP1 as shown in Figure 32.

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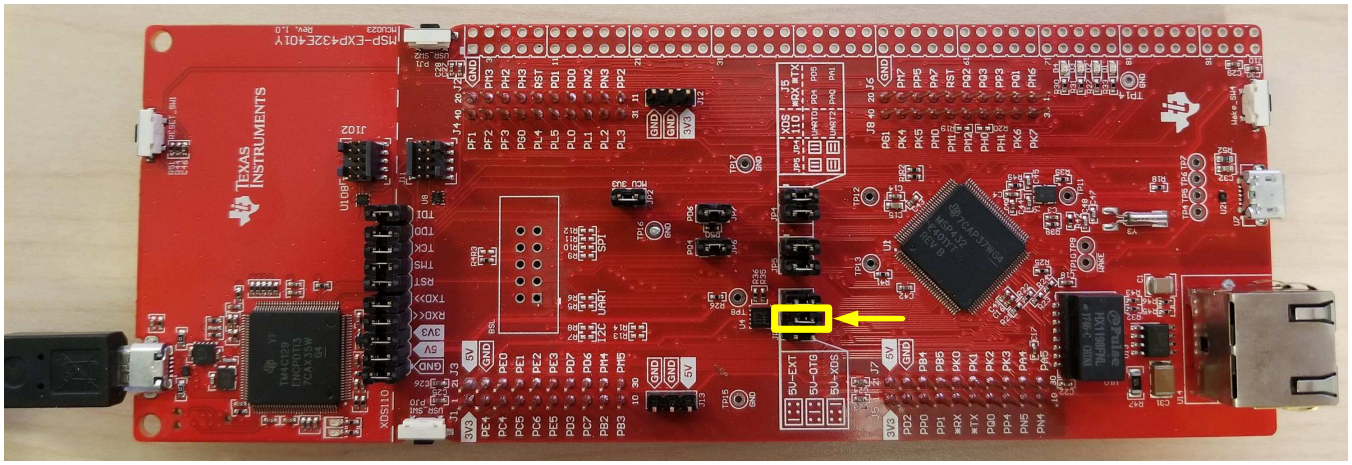

Figure 32. LaunchPad Connection for Programming

The installed UniFlash program should have been already opened at the end of the software setup shown in Figure 29. If the UniFlash program is not open, launch the program. The window in Figure 33 will be opened.

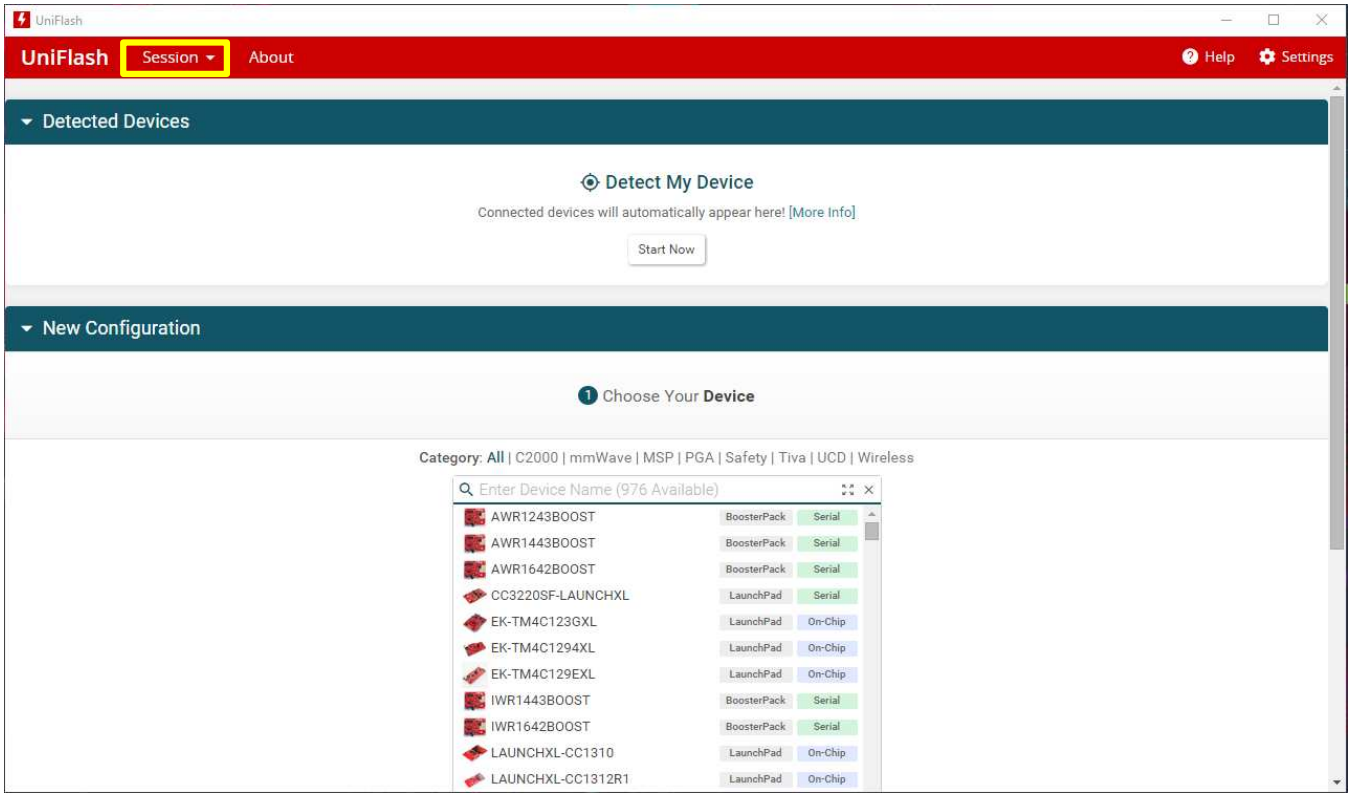

Figure 33. UniFlash Programming, Step 1

1. Click on **Session** as shown in Figure 33
2. Select **Load Session**

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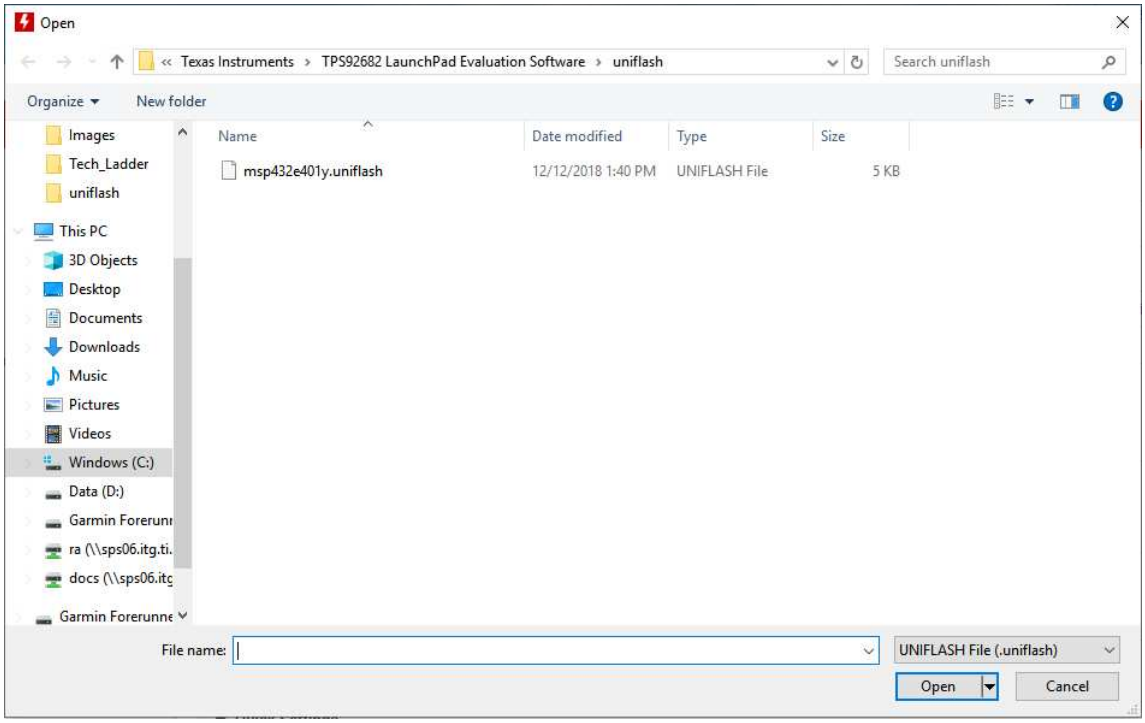

Figure 34. UniFlash Programming, Step 2

3. Navigate to *":\Texas Instruments\TPS92682 LaunchPad Evaluation Software\uniflash"*, as shown in Figure 34,
4. Select the file **msp432e401y.uniflash**.

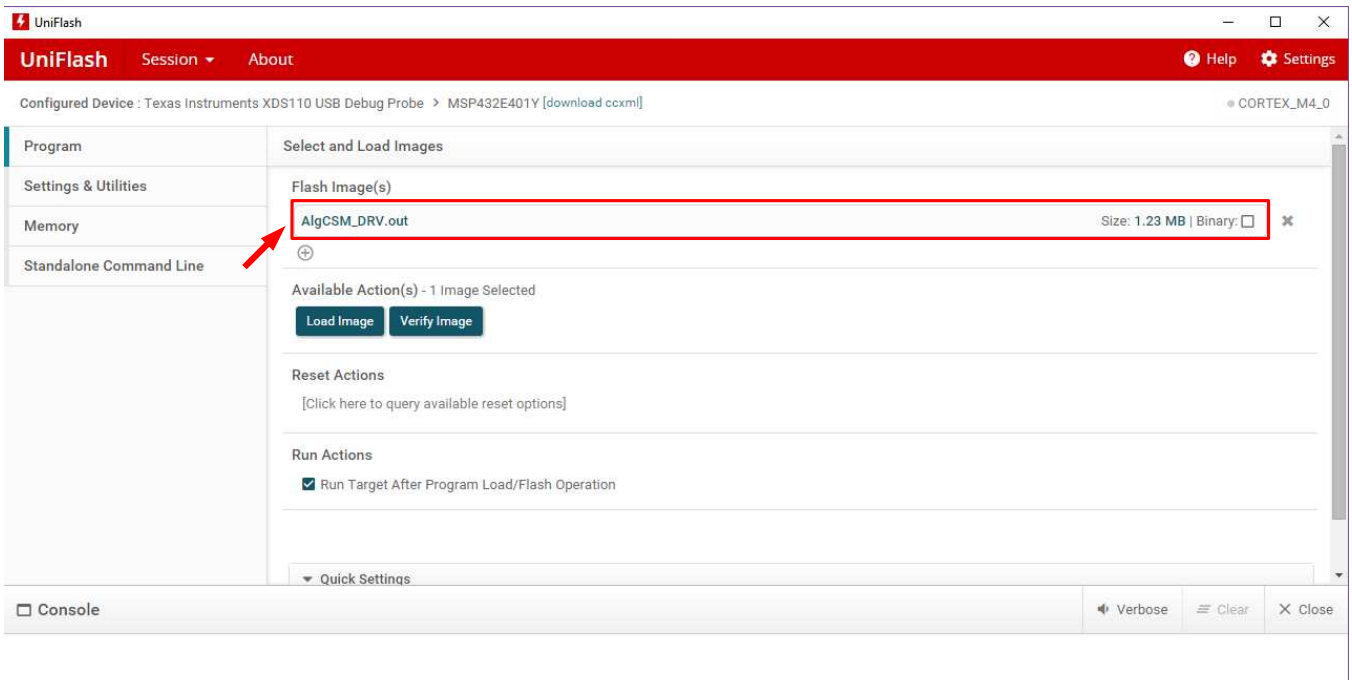

Figure 35. UniFlash Programming, Step 3

5. Click on the Flash Image file shown as the red box in Figure 35.
6. Navigate to *":\Texas Instruments\TPS92682 LaunchPad Evaluation Software\uniflash"*

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7. Select the **AlgCSM_DRV.out** file as shown in Figure 36

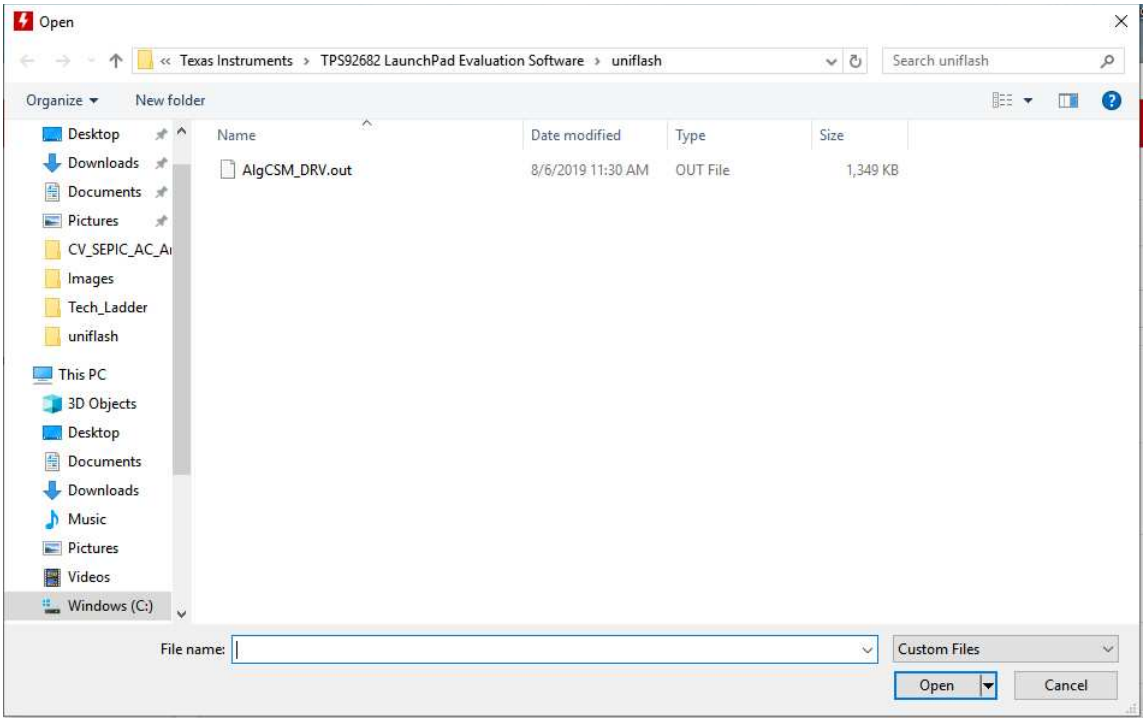

Figure 36. UniFlash Programming, Step 4

8. Click on **Load Image**. After the program loads into the LaunchPad, the *Program Load completed successfully* message appears in the Console, as shown in Figure 37

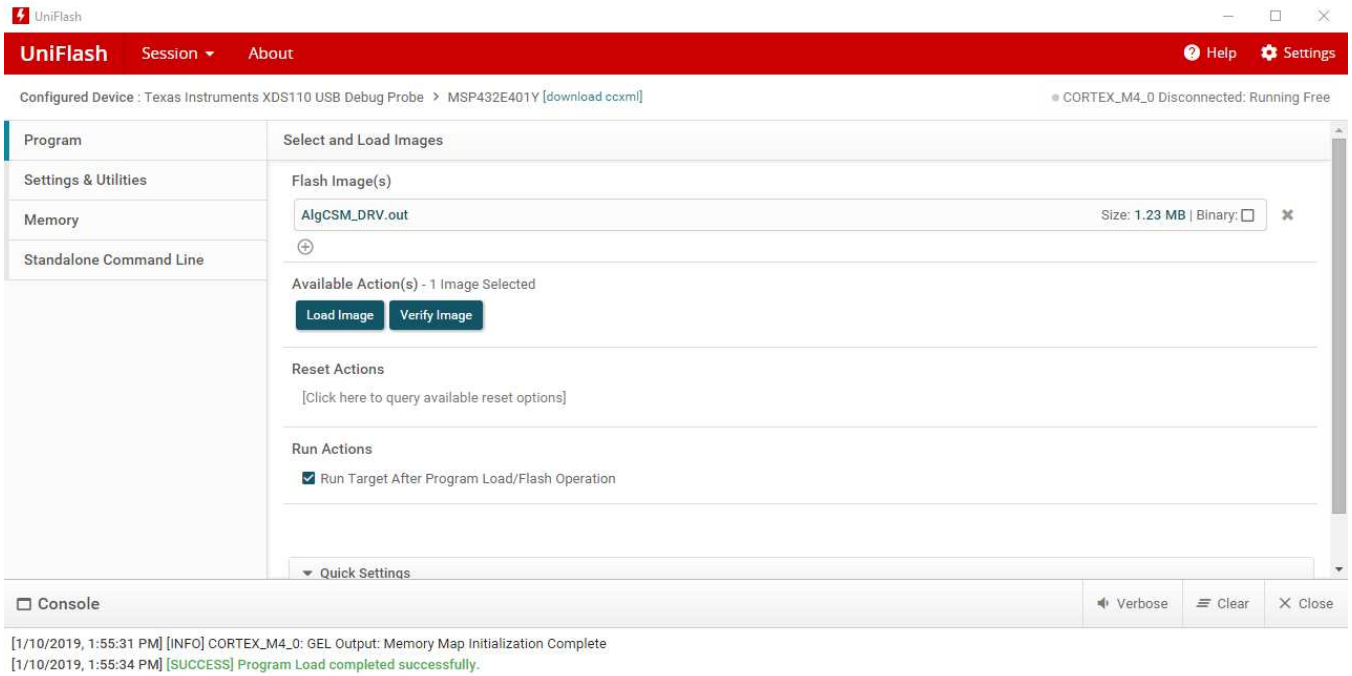

Figure 37. UniFlash Programming, Step 5

9. Close the UniFlash program, disconnect the Micro-USB from the LaunchPad and connect it to the USB port U7 on the other side of the LaunchPad, as shown in Figure 38.

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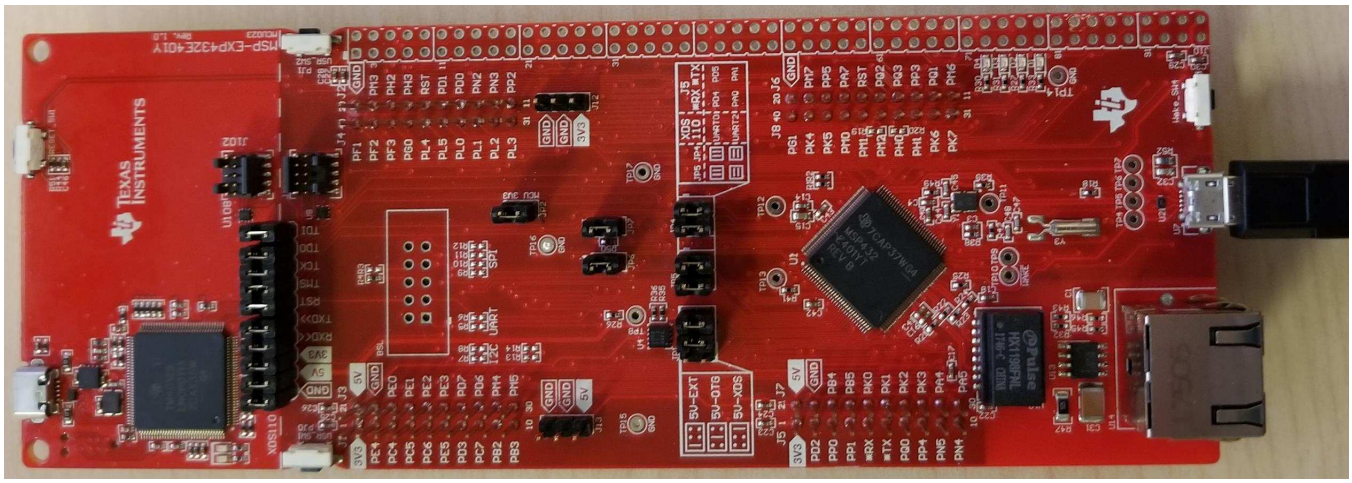

Figure 38. LaunchPad Connection for GUI Operation

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8 TPS92682EVM-069 Power UP and Operation

To start the EVM operation, connect the header J5 on TPS92682EVM-069 to the header J2/J4 on the LaunchPad, and the header J7 to the header J1/J3, using two included ribbon cables as shown in Figure 39.

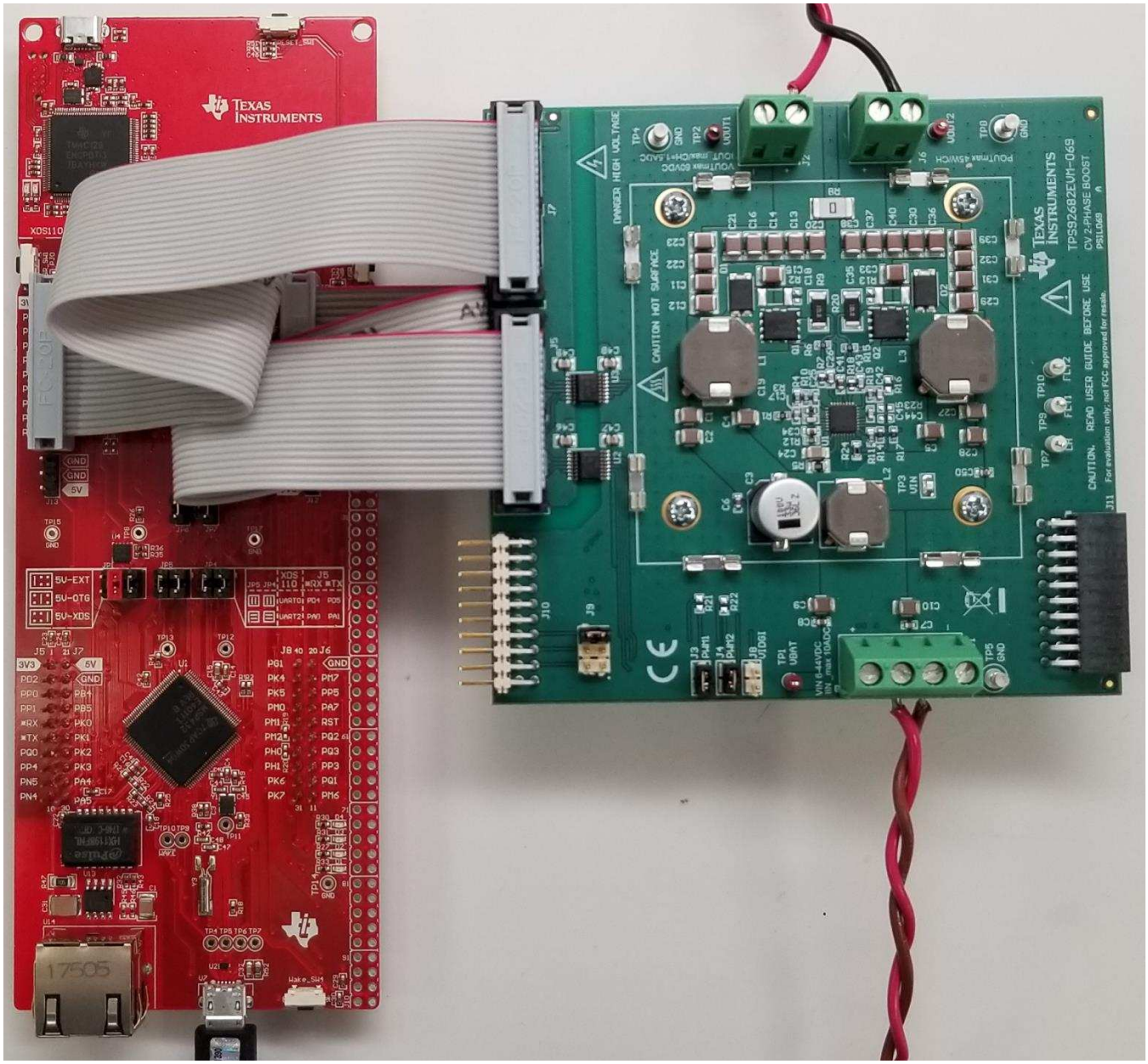

Figure 39. LaunchPad Connection to TPS92682EVM-069

Apply power (12 V) to the TPS92682EVM-069 board (terminal J1). Connect a resistive or a current sink load to the output of the EVM (terminal J2). The load should not exceed maximum output current of 1.5 A and the maximum output power of 90 W for 2-phase operation. The following steps then provide the necessary setup to enable and turn on the TPS92682EVM-069.

Run the program **LED_Controller_GUI_LP.exe**, located at the *":\Texas Instruments\TPS92682 LaunchPad Evaluation Software"*, to start the GUI. The window shown in Figure 40 opens.

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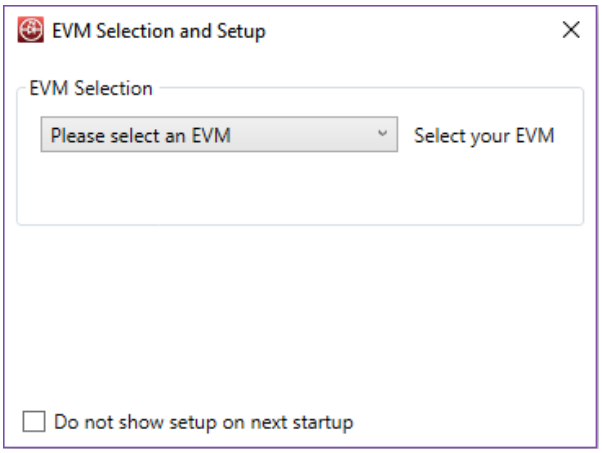

Figure 40. GUI Setup Screen 1

Click on the EVM selection option to select the TPS92682 CV - PSIL069.

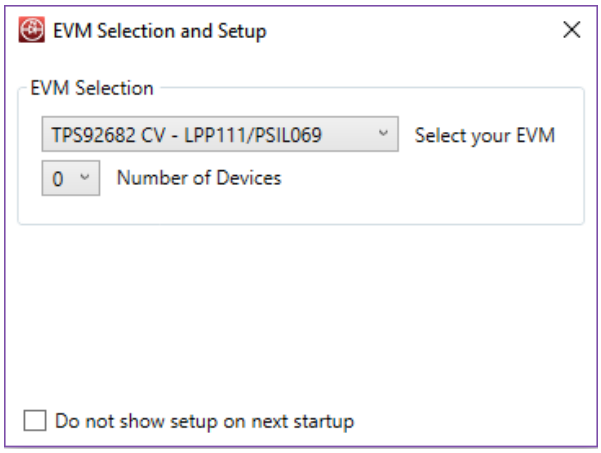

Figure 41. GUI Setup Screen 2

In Figure 41, select 1 for Number of Devices. A new tab will be shown as in Figure 42. Select 682 for Device Type and 0 for Desired Address. Click on Add Device.

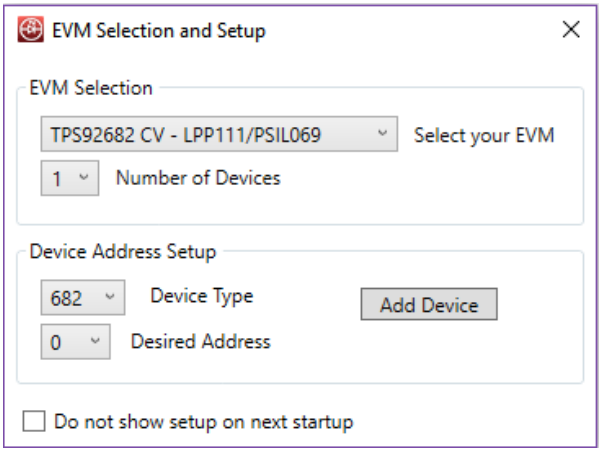

Figure 42. GUI Setup Screen 3

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The main GUI window should now appear as shown in Figure 43. This window include three sub-windows:

- MCU Control Box (1): is mostly used for Constant Current (CC) Mode EVM and is not used for the TPS92682EVM-069.
- SPI Command Box (2): is used to manually read from and write to the registers on the SPI BUS.
- Devices Box (3): this is the main GUI control window to configure the TPS92682-Q1 device.

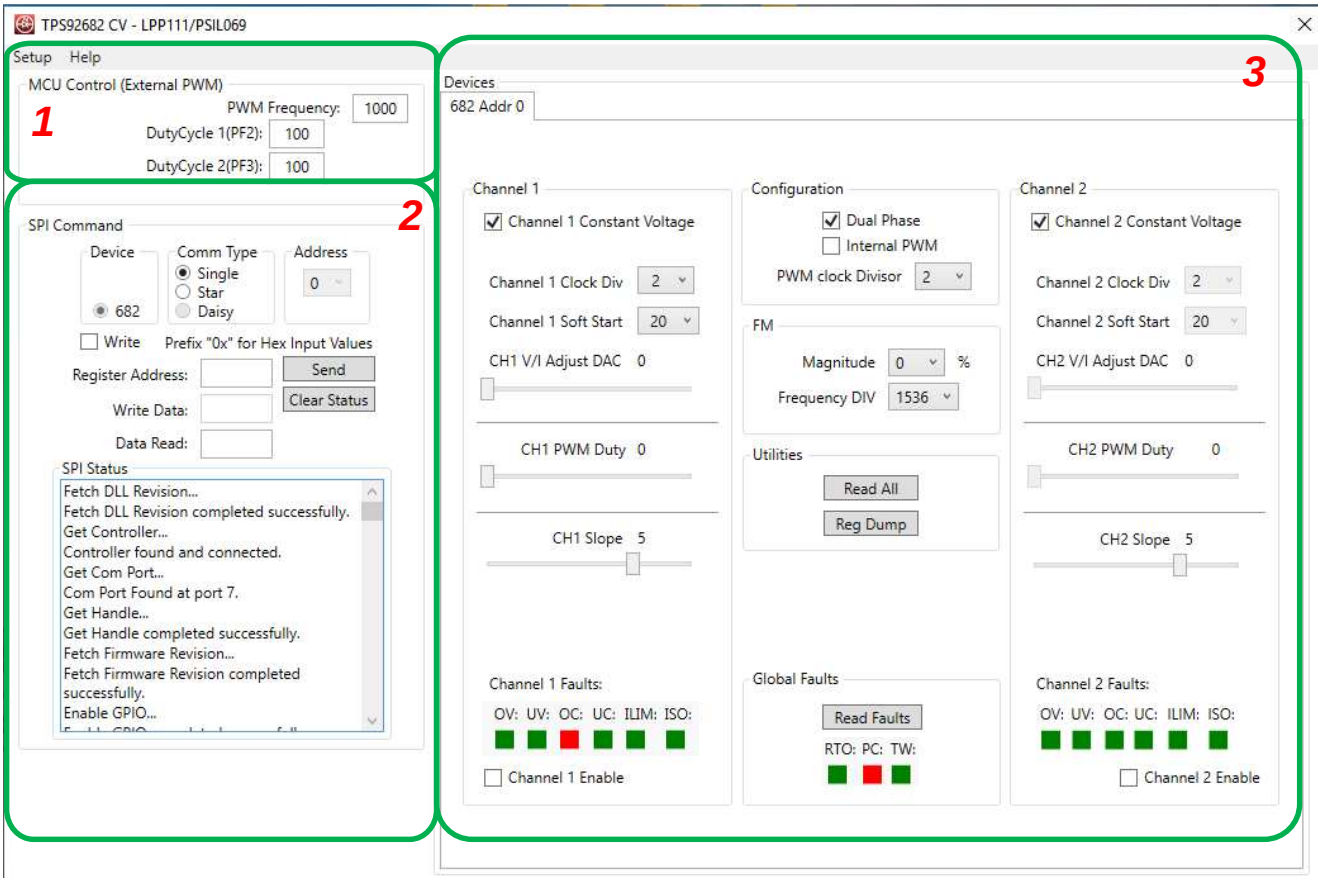

Figure 43. GUI, Main Window

8.1 SPI Command

The SPI command box allows register *read* and *write* actions.

To ensure a connection from the board to the TPS92682-Q1 exists, perform the following steps as shown in Figure 44.

1. Write the register address zero in the *Register Address* box: 0x00.
2. Double-click **Send**.

The default value of 0x3C for the register 0 will be shown in the SPI Status window.

To write data to associated register address:

- Click the check box next to **Write**
- Write the desired data in the box next to **Write Data:** as shown in Figure 44.
- Click **Send**.

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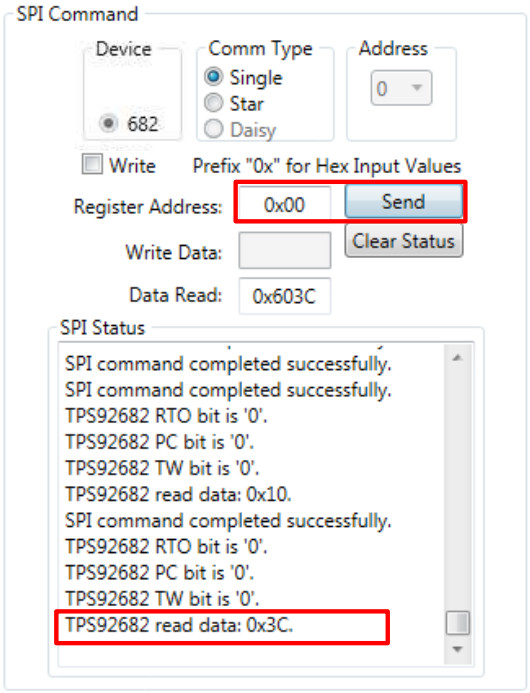

Figure 44. SPI Command Window

8.2 GUI Devices Window

Because TPS92682EVM-069 is configured as a 2-phase CV boost converter, ensure the settings shown in red in Figure 45 are correctly set to turn-on and regulate the output voltage.

The settings shown in Figure 43 are sent to the TPS92682-Q1 when the GUI is started. However, if the power to the TPS92682EVM-069 is off when the GUI is started, the settings are not applied after the power-on of the EVM. To make sure that all the required settings are applied, uncheck and re-check the check-boxes in the GUI control box shown in Figure 43 and Figure 45.

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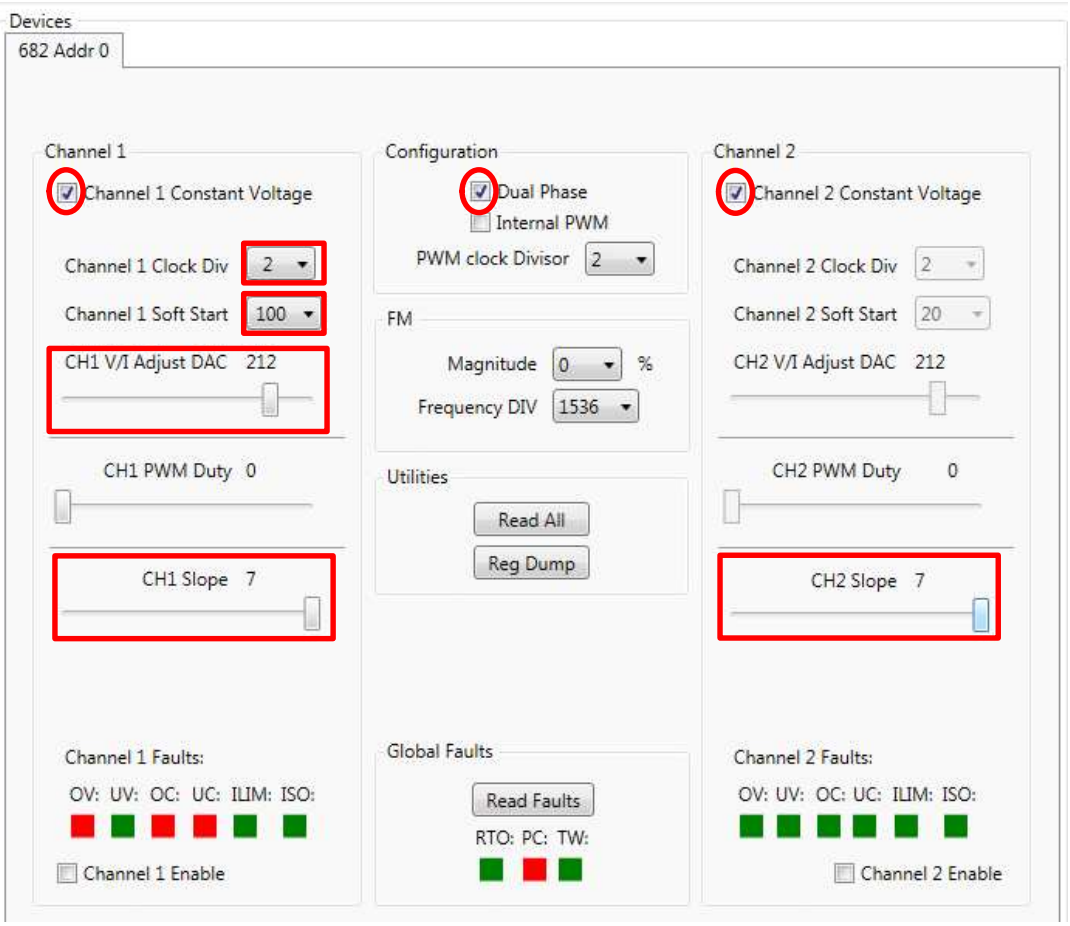

Figure 45. SPI Command Window

1. Set both channels 1 and 2 to constant voltage by clicking on the associated Check boxes
2. Set the configuration to 2-phase by setting the Dual Phase check box
3. By default, the Clock Div is set to “2”, which corresponds with switching frequency of F_{SW}=200kHz.
4. Set the channel-1 soft-start time to “100”. Two-phase mode does not require a soft-start time setting for channel-2. Channel-1 soft-start time is used for both channels in 2-phase mode
5. In 2-phase mode, the output voltage V_{OUT} is set by channel-1 V/I Adjust DAC. Set the DAC to 211 or 212 for VOUT ≈ 50 V. The feedback resistors are set on the TPS92682EVM-069, such that the maximum setting of V/IADJ-DAC corresponds to VOUT ≈ 60 V.

$$V_O = \left(1 + \frac{R9}{R6}\right) \times \frac{VIADJ \times 2.4}{255} \quad (1)$$

6. By default CHx-Slope is set to code “5”, which corresponds with 250mV of peak slope. For the TPS92682EVM-069, it is recommended to set the slopes for both channels to code "7” as shown in Figure 45

NOTE: The PWM settings in the main GUI window are for the internal PWM setting and are used in CC mode. These settings are not relevant for the TPS92682EVM-069. Do **NOT** select any PWM settings in the main GUI window

After the SPI settings are applied the fault status must be obtained. The GUI shows the fault status registers FLT1 (0x11) and FLT2 (0x12). Before enabling and turning on the output, the fault registers must be read (cleared). The power cycle (PC) bit must be cleared in order for the TPS92682-Q1 to turn on. The fault status can be obtained by clicking **Read Faults** (as shown in Figure 46).

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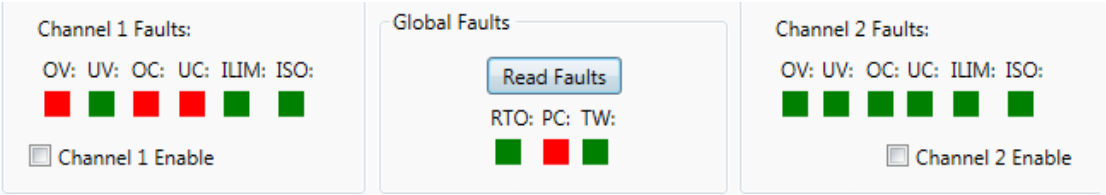

Figure 46. Fault Status after Pushing the Read Faults Once

The first time **Read Fault** is clicked, the previous status of the fault registers are shown and the faults are cleared. The second time **Read Fault** is clicked, the cleared faults change to green as shown in Figure 47.

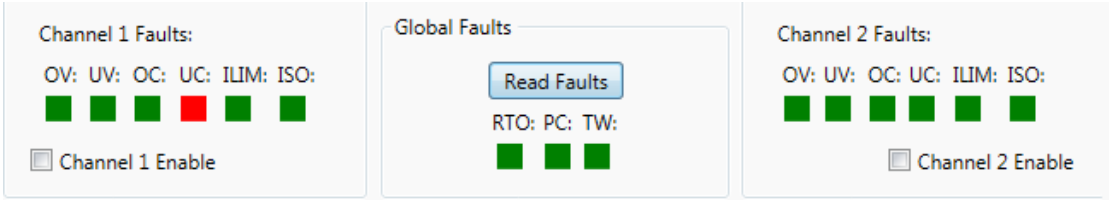

Figure 47. Fault Status after Pushing the Read Faults Twice

Some faults, (such as as undercurrent (UC) and overcurrent (OC))are related to the CC mode and are not relevant in CV mode. Clear the following faults (change status to green) before enabling the TPS92682EVM-069 2-phase CV BOOST.

- output overvoltage (OV)
- output undervoltage (UV)
- cycle-by-cycle current limit (ILIM)
- IS open (ISO)
- RT Open (RTO)
- power cycle (PC)
- thermal warning (TW)

Click the check box next to **Channel 1 Enable**,to turn on the TPS92682EVM-069 and to regulate the output to the desired voltage (50 V, in this example).

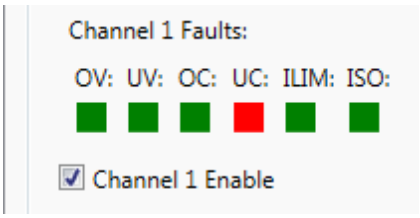

Figure 48. Enabling the EVM

To turn off the TPS92682EVM-069, click **Channel 1 Enable** again to deslelect.

NOTE: If a power cycle occurs, all the registers reset to their default values. Be sure to apply all the preliminary steps listed in Section 8before re-enabling the converter.

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Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Original (March 2019) to A Revision	Page
• *Figures 13 to 15*	20
• Updated *Figure 18*	23
• Updated *Figures 21 to 25*	24
• Updated *Figure 30*	28
• Updated *Figure 34 and Figure 36*	31
• Updated *Figure 43 and Figure 44*	36

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