Texas
Instruments
Incorporated

UC1825A-SP
5962-8768105VEA

Radiation Test Report

5962-8768105VEA (UC1825AJ-SP) Radiation Testing

Note: The following radiation test results are provided for information only, as these devices are not Radiation Hardness Assured (RHA) at this time.

Samples of the 5962-8768105VEA, UC1825AJ-SP, device have been evaluated to determine performance effects after Total Ionizing Dose (TID) radiation exposure. Since the purpose of this new device qualification was to improve low dose rate TID performance for the existing device function based on customer request, the initial radiation test plan for this device involved testing 40 units at a dose rate of 10 mrad/second, with samples pulled at various total dose intervals. Twenty samples were exposed under unbiased conditions, and 20 samples were exposed under biased conditions. For the biased samples, the bias conditions were the same as the circuit used for burn-in.

Radiation exposure for all samples was performed at Radiation Assured Device, Inc. in Colorado Springs, CO. Pre and post radiation electrical testing was performed by Texas Instruments in Sherman, TX.

Lot Information

Device Traceability Information Confirmed By: Date:	Kevin Treece
Full Device Name:	5962-8768105VEA
Datecode or Lot Trace Code:	0832A
A/T Lot #:	8020431ALP
Full Die Name (Alias with Die Rev):	SMJJARC1825VS
Die Lot #:	8177365SHE

The TID samples were pulled from the initial qualification lot after completing normal class-V processing (assembly, burn-in, full-temp testing), and serialized datalogs were collected at 25C before and after radiation exposure. The electrical testing results are included in the release documentation. The test results are summarized below.

Summary: Units pass up to 40 krad(Si)

Dose rate requirement: 10 mrad(Si)/sec

Exposure groups by S/N:

Control – SN 266 (no radiation exposure)

Biased samples:
10krad(Si) – SN 246, 247, 248, 249, 250
20krad(Si) – SN 251, 252*, 253*, 254, 255
30krad(Si) – SN 256, 257, 258, 259, 260
40krad(Si) – SN 261, 262, 263, 264, 265

Uniased samples:
10krad(Si) – SN 226, 227, 228, 229, 230
20krad(Si) – SN 231, 232, 233, 234, 235
30krad(Si) – SN 236, 237, 238, 239, 240
40krad(Si) – SN 241, 242, 243, 244, 245

*** Note:** Unit #252 and 253 from the 20krad sample failed post radiation electrical testing with multiple gross parametric test failures. Due to the significant nature of the failures and the fact that higher dose samples did not exhibit similar characteristics, these failures are determined to be due to electrical overstress rather than radiation exposure. These devices were removed from the electrical test data.

Units: 41

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
25'C QA				34	34	34	34	0	#DIV/0!	#DIV/0!	34		
ICC @VIN 12V	m a	31.5841182	4	23.9731254	24.9072	25.3233854	25.6952	0.22504333	0.0524	-0.9938	26.6736453	35.9	15.6660418
VREF @VIN15V; ILOAD 1MA	v	0.86666891	5.055	4.99357064	5.0651	5.10197561	5.1448	0.0180675	0.4933	0.0964	5.21038058	5.145	0.79377153
VREF LINE REG; VIN12-20V	m v	9.78593709	-14	-4.4754226	-2.8677	-2.0288122	-1.1358	0.4077684	-0.091	-0.2392	0.41779822	14	13.1028725
VREF LOAD REG; IL1-10MA	m v	24.5674796	-19	0.01704783	0.9559	1.70239756	2.4471	0.28089162	0.2723	1.4139	3.38774729	19	20.5270184
VREF VARIANCE MIN	v	1.20149429	5.035	4.9917706	5.0635	5.10004634	5.1424	0.01804596	0.4958	0.067	5.20832208	5.165	1.19978231
VREF VARIANCE MIN	v	1.23435666	5.035	4.99359574	5.0649	5.10175122	5.1445	0.01802591	0.4841	0.0877	5.2099067	5.165	1.16958992
VREF ISC; VPIN 160V	m a	2.57827805	-89	-84.864716	-74.1008	-70.562654	-64.1155	2.38367701	0.9216	0.3482	-56.260592	-31	5.5324405
OSC ACCURACY PRE TRIM	k hz			381.639886	396.813	401.948293	413.61	3.38473437	1.1836	2.4001	422.256699		
CT DISCAHRGE PRE-TRIM	m a			9.46113281	9.781	9.93953659	10.086	0.07973396	-0.0888	-0.5317	10.4179404		
CT DISCHARGE CURRENT	m a	3.84666253	9.02	9.46145602	9.7814	9.93956829	10.0859	0.07968538	-0.0856	-0.5355	10.4176806	10.98	4.35224844
OSC FREQ MEASURE - DATA PURPOSE ONLY	k hz	4.88224133	350.8	380.914895	396.7516	401.811754	414.3602	3.48280979	1.3552	3.109	422.708612	449.2	4.53544209
OSC 1MHZ ACCURACY; RT6.6K,CT220PF	k hz	1.51414612	901	878.408202	940.9689	971.406527	1011.378	15.4997209	0.4157	0.2554	1064.40485	1099	2.74399508
OSC V.STABILITY; VIN12-20V	%	5.84719178	-0.99	-0.3130938	0	0.03880244	0.2921	0.05864938	2.6419	8.1811	0.39069869	0.99	5.40612498
OSC RAMP PEAK VOLTAGE	v	4.70128575	2.605	2.72401746	2.7813	2.81213659	2.8499	0.01468652	0.6233	0.268	2.90025571	2.995	4.15036853
OSC RAMP VALLEY VOLTAGE	v	7.429131	0.71	1.02857075	1.1146	1.14592683	1.2012	0.01955935	0.8489	0.5056	1.2632829	1.24	1.60320921
RAMP VALLEY TO PEAK	v	1.29900512	1.61	1.57966698	1.6273	1.66620976	1.702	0.0144238	-0.3343	0.8929	1.75275253	1.99	7.48277904
CALCULATED CHARGE CURRENT	u a			792.236355	811.315	817.919488	828.054	4.28052216	0.9536	0.1994	843.602621		
CHARGE CURRENT PRE TRIM	u a			723.680754	742.262	751.981098	766.853	4.71672397	0.6287	1.1641	780.281441		
CHARGE CURRENT ERROR THROUGH TRIM	%			-0.0177257	-0.004	0.00143902	0.016	0.00319413	2.2203	10.151	0.02060378		
OSC ACTUAL CHARGE CURRENT	u a			723.67336	742.251	751.970537	766.857	4.71619602	0.6284	1.175	780.267713		
CURRENT MIRROR ERROR	%			-12.288028	-10.132	-8.7723902	-7.752	0.58593967	0.2009	-0.4249	-5.2567522		
OSC CLOCK OUT HIGH LEVEL	v	4.91071444	3.72	3.92507786	3.9948	4.06599024	4.1093	0.0234854	-0.5386	0.7119	4.20690263	5.5	20.3532109
OSC CLOCK OUT LOW LEVEL	v	11.7014021	-0.05	0.00285488	0.0118	0.01375122	0.0186	0.00181606	1.0289	0.3076	0.02464756	0.195	33.2678319
ILIMIT FULL SOFTSTART THRESHOLD	v	10.3240083	4.31	4.75569605	4.8355	4.86278293	4.9087	0.01784781	0.7405	0.0222	4.96986981	5.5	11.9009362
ILIMIT RESTART THRESHOLD	v	7.00360327	0	0.23872333	0.3118	0.3341439	0.3817	0.01590343	1.0008	0.6463	0.42956447	0.495	3.37151833
E/A VIO PRETRIM	m v			-5.7867056	-1.373	-0.2706341	2.629	0.91934525	1.4832	2.7578	5.24543735		
ERR AMP VIO	m v	2.36745498	-6.8	-5.7865731	-1.3728	-0.2706488	2.6293	0.91932072	1.4834	2.7589	5.24527552	6.8	2.5637222
E/A IIBIAS	u a	10.5049124	0.001	0.41710153	0.47	0.51495122	0.5422	0.01630828	-0.8476	0.2905	0.61280091	2.95	49.77121
E/A IIO	n a	18.0682146	-975	95.8563812	165.7384	229.145163	262.3385	22.214797	-0.9547	1.2863	362.433946	975	11.191562
E/A AVOL; VO1-4V	db	3.47118139	62	80.4796461	96.5167	105.601832	121.8602	4.18703094	1.5105	5.0353	130.724017	250	11.4956692
E/A CMRR; CMV 1.5-5.5V	db	2.67841615	77	82.5100284	94.1213	98.7538293	106.8577	2.70730014	0.5836	0.5726	114.99763	250	18.6220174
E/A PSRR; VIN12-20V	db	1.77918428	87	82.6969627	111.2947	121.670976	136.4001	6.49566882	0.7613	-0.0289	160.644989	250	6.58536367
E/A OUTPUT HIGH LEVEL; IOUT0.5MA	v	5.76704921	4.505	4.70780972	4.7855	4.81548537	4.8521	0.01794594	0.2962	-0.6334	4.92316101	4.995	3.33435918
E/A OUTPUT LOW LEVEL; IOUT1MA	v	8.60006817	0	0.35675276	0.4388	0.46485854	0.4968	0.01801763	0.3806	-1.0109	0.57296432	0.995	9.80782833
E/A OUTPUT SINK I; VOUT1V	m a	9.11837006	1.01	2.63174839	2.9403	3.0874	3.2078	0.07594194	-0.2413	-1.0854	3.54305161	9.99	30.2977092
E/A OUTPUT SOURCE I; VOUT4V	m a	114.524044	-10	-1.471476	-1.374	-1.3198902	-1.274	0.0252643	-0.2236	-0.5965	-1.1683044	-0.65	8.8384297
E/A GAIN BANDWITH PRODUCT	m hz	10.3968211	7	12.9926662	13.8143	14.4200317	14.7645	0.23789424	-0.8134	0.1106	15.8473972	29	20.4292015
PWM COMP IIBIAS(IP3) @0V	u a	86.1967287	-7.95	-0.7229272	-0.6077	-0.5512561	-0.4949	0.02861185	-0.1193	-0.8453	-0.379585	0	6.42223503
OUTPUT 'A' MAX DUTY CYCLE	%	7.21678404	86	90.8853605	92.0979	92.7583	93.4321	0.31215659	0.0016	-0.0621	94.6312395	100	7.73297797
OUTPUT 'B' MAX DUTY CYCLE	%	5.83746273	86	90.1328553	91.5553	92.2868073	92.9719	0.358992	-0.3413	-0.5399	94.4407593	100	7.16189833
OUTPUT 'A' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
OUTPUT 'B' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
ZER DUTY CYCLE THOLD @P3; VP70V	v	4.70175496	1.11	1.17011859	1.1993	1.21462195	1.2308	0.00741723	0.0506	-0.4536	1.25912531	1.4	8.33096831
ILIMIT THOLD TO OUTPUTS	v	1.43031522	0.952	0.92943815	0.9915	1.00864634	1.0442	0.01320137	0.9878	0.4863	1.08785453	1.048	0.99367647

Units: 41

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
OVER CURRENT THRESHOLD	v	1.20672543	1.142	1.11322288	1.1682	1.18577561	1.2186	0.01209212	0.9208	0.4609	1.25832834	1.258	1.99094904
ILIMIT/SHUTDOWN IIBIAS @1.5V	u a	47.9554549	-14.5	3.27262102	3.7394	4.04609268	4.326	0.12891194	-0.0268	0.1429	4.81956434	14.5	27.0311321
OUTPUT A PWM LEADING EDGE BLANKING	n s	4.18864956	303	336.540083	347.2874	367.189195	372.9649	5.10818536	-1.702	4.4386	397.838307	447	5.20803373
OUTPUT B PWM LEADING EDGE BLANKING	n s	4.32747375	303	336.756065	348.0007	365.762678	371.5384	4.83443549	-1.4727	3.122	394.769291	447	5.60129665
LEADING EDGE BLANKING RESISTOR	k ohm	23.4218255	5.02	10.289436	10.6292	10.7814049	10.9526	0.08199482	0.1492	-0.6623	11.2733738	14.98	17.0685388
SOFTSTART DISCHARGE I; VPIN82.5V	u a	7.08128077	101	179.705647	199.5583	210.684312	222.742	5.16311082	0.0137	-0.0704	241.662977	347	8.80061733
SOFTSTART CHARGE I; VPIN82.5V	u a	7.61402459	-19.9	-15.085614	-14.1792	-13.370485	-12.9486	0.28585472	-1.1968	1.4802	-11.655357	-8.1	6.14587874
INITIAL VCC OF 8.5V, +PEAK DETECTOR	v			8.49924584	8.4998	8.50141707	8.5017	0.00036187	-3.3396	12.252	8.5035883		
INITIAL VCC OF 8.5V, -PEAK DETECTOR	v			8.50147457	8.5018	8.50198049	8.5022	8.4319E-05	0.1294	0.3096	8.5024864		
UV L.O. TURN ON THOLD (A VERSION)	v	1.37035923	8.42	8.30408995	8.5681	8.67226829	8.8076	0.06136306	0.2181	-0.335	9.04044663	9.58	4.93093487
UV L.O. STOP THRESHOLD (A VERSION)	v	17.4397396	5.02	7.69309234	7.9347	8.03935366	8.1647	0.05771022	0.2601	-0.2249	8.38561498	14.98	40.089065
UVLO HYST (A VERSION)	v	7.26084904	0.415	0.57288654	0.6126	0.63290976	0.6517	0.01000387	-0.2419	-0.7744	0.69293297	1.185	18.3958901
VSAT 'A' LOW @20MA	m v	3.30412995	10	119.424876	239.4578	287.237717	344.2404	27.9688069	0.0535	-0.9858	455.050558	390	1.2247249
VSAT 'A' LOW @200MA	v	1.63620406	0.1	-0.0390512	0.526	0.72539512	1.1021	0.12740773	1.2138	2.2652	1.48984149	2.14	3.70099184
VSAT 'B' LOW @20MA	m v	3.19128252	10	112.184451	236.6169	283.738134	353.1647	28.5922805	0.1727	-0.7108	455.291817	390	1.23881766
VSAT 'B' LOW @200MA	v	1.81513335	0.1	0.03867841	0.515	0.70209268	0.9748	0.11056904	0.7809	0.5809	1.36550695	2.19	4.48560541
VHIGH 'A' @20MA	v	105.552014	0.04	2.16664316	2.1948	2.20771707	2.223	0.00684565	0.4693	-0.1083	2.24879099	2.9	33.7091304
VHIGH 'A' @200MA	v	23.4709295	0	2.16080114	2.3206	2.36207805	2.4439	0.03354615	0.8476	-0.0531	2.56335496	3.02	6.53748074
VHIGH 'B' @20MA	v	87.4874158	0.04	2.17336499	2.215	2.22327561	2.2476	0.00831844	1.3947	1.6072	2.27318623	2.9	27.1174504
VHIGH 'B' @200MA	v	14.8245122	0	2.07179788	2.3377	2.39489756	2.5611	0.05384995	1.8937	3.3837	2.71799724	3.02	3.86940922
OUTPUT A VSAT @ VIN UVLO-300MV	v	9.87980821	0	0.63305804	0.7698	0.79373659	0.9075	0.02677976	3.9133	15.366	0.95441513	1.19	4.93237506
OUTPUT B VSAT @ VIN UVLO-300MV	v	47.568822	0	0.75035265	0.769	0.78328537	0.7964	0.00548879	-0.144	0.3717	0.81621809	1.19	24.6997287
ICC START I @ VIN UVLO-300MV	u a	5.83601279	0	75.4479487	110.812	114.7846	153.1489	6.55610855	5.3514	31.06	154.121251	270	7.89164277
COLLECTOR LEAKAGE @ 20V	u a			-35.009423	0.2881	2.54905122	39.2472	6.25974577	5.4807	31.505	40.1075259	148	7.74530649

Units: 5

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
25'C QA				34	34	34	34	0	#DIV/0!	#DIV/0!	34		
ICC @VIN 12V	m a	32.454337	4	23.8870555	24.8851	25.19308	25.4228	0.21767075	-0.435	-0.723	26.4991045	35.9	16.3962006
VREF @VIN15V; ILOAD 1MA	v	0.78172932	5.055	5.01204966	5.0688	5.08256	5.0972	0.01175172	0.0341	-1.892	5.15307034	5.145	1.77108777
VREF LINE REG; VIN12-20V	m v	12.0813758	-14	-3.8580225	-2.4115	-1.846	-1.5311	0.33533708	-1.603	3.1506	0.1660225	14	15.7513149
VREF LOAD REG; IL1-10MA	m v	14.1571586	-19	-0.9806256	1.1866	1.98378	2.4332	0.4940676	-1.324	1.5475	4.94818559	19	11.4803589
VREF VARIANCE MIN	v	1.30600166	5.035	5.01092796	5.0669	5.0803	5.0944	0.01156201	0.0193	-2.109	5.14967204	5.165	2.44190597
VREF VARIANCE MIN	v	1.36206493	5.035	5.01278106	5.0688	5.08244	5.0969	0.01160982	0.0232	-1.874	5.15209894	5.165	2.37040642
VREF ISC; VPIN 160V	m a	1.69023299	-89	-91.843354	-75.8146	-73.48534	-68.4345	3.05966892	1.5462	1.9608	-55.127326	-31	4.62853347
OSC ACCURACY PRE TRIM	k hz			392.087129	398.791	400.3866	402.01	1.38324521	-0.076	-2.364	408.686071		
CT DISCAHRGE PRE-TRIM	m a			9.44512768	9.773	9.8658	9.951	0.07011205	-0.117	-1.033	10.2864723		
CT DISCHARGE CURRENT	m a	4.02795663	9.02	9.44582513	9.7729	9.86578	9.9506	0.06999248	-0.131	-1.023	10.2857349	10.98	5.30637972
OSC FREQ MEASURE - DATA PURPOSE ONLY	k hz	11.1026843	350.8	391.389091	398.6546	400.30714	401.9469	1.48634146	-0.097	-2.746	409.225189	449.2	10.9649232
OSC 1MHZ ACCURACY; RT6.6K,CT220PF	k hz	0.90841401	901	828.623077	941.5226	961.23182	995.644	22.1014572	1.0774	0.6587	1093.84056	1099	2.07781443
OSC V.STABILITY; VIN12-20V	%	4.84683721	-0.99	-0.3829989	0.0032	0.04344	0.17	0.07107315	2.1859	4.8169	0.46987891	0.99	4.43936971
OSC RAMP PEAK VOLTAGE	v	7.49068444	2.605	2.74525255	2.7868	2.79634	2.8073	0.00851458	0.1552	-1.798	2.84742745	2.995	7.77725186
OSC RAMP VALLEY VOLTAGE	v	25.769616	0.71	1.09817828	1.1264	1.13084	1.1394	0.00544362	1.2114	0.6645	1.16350172	1.24	6.68427735
RAMP VALLEY TO PEAK	v	2.2151841	1.61	1.61539324	1.6539	1.66552	1.6749	0.00835446	-0.533	-1.004	1.71564676	1.99	12.9463785
CALCULATED CHARGE CURRENT	u a			807.721142	812.89	813.776	815.344	1.00914295	1.0951	0.5244	819.830858		
CHARGE CURRENT PRE TRIM	u a			733.962909	745.955	747.9756	751.209	2.33544852	0.7196	-1.787	761.988291		
CHARGE CURRENT ERROR THROUGH TRIM	%			-0.006623	0	0.0012	0.003	0.00130384	0.5414	-1.488	0.00902304		
OSC ACTUAL CHARGE CURRENT	u a			733.937181	745.958	747.9656	751.202	2.33806978	0.7231	-1.798	761.994019		
CURRENT MIRROR ERROR	%			-11.29112	-9.291	-8.7996	-8.212	0.4152533	0.4321	-0.3	-6.3080802		
OSC CLOCK OUT HIGH LEVEL	v	10.5003496	3.72	3.98662685	4.0371	4.04936	4.0656	0.01045552	0.8532	1.6883	4.11209315	5.5	46.2479573
OSC CLOCK OUT LOW LEVEL	v	74.5735969	-0.05	0.01041507	0.0118	0.01208	0.0124	0.00027749	-0.009	-2.704	0.01374493	0.195	219.732641
ILIMIT FULL SOFTSTART THRESHOLD	v	16.1074304	4.31	4.77878123	4.83	4.84524	4.86	0.01107646	-0.097	0.4893	4.91169877	5.5	19.704247
ILIMIT RESTART THRESHOLD	v	13.0907338	0	0.27383851	0.3171	0.32322	0.3372	0.00823025	1.7119	3.1252	0.37260149	0.495	6.9572621
E/A VIO PRETRIM	m v			-6.4726138	-0.458	0.4978	2.5	1.16173564	1.8402	3.6771	7.46821385		
ERR AMP VIO	m v	2.09359651	-6.8	-6.4737412	-0.4584	0.49786	2.5004	1.16193354	1.8401	3.6776	7.46946122	6.8	1.80794621
E/A IIBIAS	u a	9.89525845	0.001	0.44945812	0.5307	0.56306	0.576	0.01893365	-1.793	3.2315	0.67666188	2.95	42.0228947
E/A IIO	n a	14.4295691	-975	46.3741872	164.7696	210.72006	231.3992	27.3909788	-1.614	2.6796	375.065933	975	9.30087172
E/A AVOL; VO1-4V	db	4.08456718	62	84.9799172	102.774	107.02758	112.3165	3.67461047	0.5646	-0.196	129.075243	250	12.9693946
E/A CMRR; CMV 1.5-5.5V	db	0.69731205	77	23.0145397	96.5744	105.89772	130.3783	13.8138634	2.1286	4.6584	188.7809	250	3.47723819
E/A PSRR; VIN12-20V	db	1.92353187	87	85.6297066	114.8152	121.4693	130.3783	5.97326556	0.6695	0.4048	157.308893	250	7.17255347
E/A OUTPUT HIGH LEVEL; IOUT0.5MA	v	11.2265151	4.505	4.74433925	4.7869	4.79622	4.8089	0.00864679	0.7419	-0.284	4.84810075	4.995	7.66295815
E/A OUTPUT LOW LEVEL; IOUT1MA	v	7.81372864	0	0.34082996	0.442	0.45808	0.4884	0.01954167	1.1663	0.2522	0.57533004	0.995	9.15854694
E/A OUTPUT SINK I; VOUT1V	m a	10.27617	1.01	2.70102969	3.0008	3.10968	3.1648	0.06810838	-1.286	1.1716	3.51833031	9.99	33.6733873
E/A OUTPUT SOURCE I; VOUT4V	m a	88.6056032	-10	-1.507499	-1.3417	-1.31138	-1.2696	0.0326865	0.6231	-2.669	-1.115261	-0.65	6.74468142
E/A GAIN BANDWITH PRODUCT	m hz	13.699237	7	13.3260187	14.1927	14.40746	14.6005	0.18024021	-0.08	-2.524	15.4889013	29	26.9872079
PWM COMP IIBIAS(IP3) @0V	u a	45.1795889	-7.95	-1.1600578	-0.9104	-0.84556	-0.7685	0.0524163	0.4858	0.8515	-0.5310622	0	5.37720822
OUTPUT 'A' MAX DUTY CYCLE	%	10.4419841	86	91.4031691	92.478	92.68324	93.0133	0.21334515	1.0816	0.4655	93.9633109	100	11.4318043
OUTPUT 'B' MAX DUTY CYCLE	%	7.62355349	86	90.8534906	92.192	92.57962	92.9458	0.28768824	-0.128	-0.395	94.3057494	100	8.59770987
OUTPUT 'A' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
OUTPUT 'B' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
ZER DUTY CYCLE THOLD @P3; VP70V	v	5.64130126	1.11	1.17851038	1.2068	1.21614	1.2222	0.0062716	-0.914	-0.406	1.25376962	1.4	9.77209015
ILIMIT THOLD TO OUTPUTS	v	6.2235819	0.952	0.98078798	0.9909	0.99442	0.9964	0.002272	-1.006	0.6508	1.00805202	1.048	7.86090331

Units: 5

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
OVER CURRENT THRESHOLD	v	3.43837484	1.142	1.15472559	1.1677	1.17242	1.1749	0.00294907	-1.337	1.08	1.19011441	1.258	9.6731137
ILIMIT/SHUTDOWN IIBIAS @1.5V	u a	63.5162463	-14.5	3.6510576	4.0913	4.24118	4.3572	0.09835373	-0.735	1.2489	4.8313024	14.5	34.7684478
OUTPUT A PWM LEADING EDGE BLANKING	n s	7.24514477	303	352.563391	367.4471	371.46216	375.3338	3.1497949	0.0637	-1.354	390.360929	447	7.99394273
OUTPUT B PWM LEADING EDGE BLANKING	n s	5.96792373	303	347.756066	365.2962	370.315	374.6168	3.7598224	-0.26	-1.332	392.873934	447	6.79863673
LEADING EDGE BLANKING RESISTOR	k ohm	53.3972275	5.02	10.5423509	10.7004	10.75724	10.7917	0.03581484	-1.203	1.1826	10.9721291	14.98	39.3017681
SOFTSTART DISCHARGE I; VPIN82.5V	u a	7.95381826	101	181.119161	201.0611	208.0327	213.0069	4.48558987	-0.912	1.3324	234.946239	347	10.3269435
SOFTSTART CHARGE I; VPIN82.5V	u a	7.66400887	-19.9	-14.967426	-13.6039	-13.2257	-12.9356	0.29028759	-0.193	-1.858	-11.483974	-8.1	5.88577233
INITIAL VCC OF 8.5V, +PEAK DETECTOR	v			8.50373137	8.504	8.50406	8.5041	5.4772E-05	-0.609	-3.333	8.50438863		
INITIAL VCC OF 8.5V, -PEAK DETECTOR	v			8.504578	8.505	8.50508	8.5052	8.3666E-05	0.5122	-0.612	8.505582		
UV L.O. TURN ON THOLD (A VERSION)	v	6.34662208	8.42	8.61050392	8.6783	8.69816	8.7145	0.01460935	-0.377	-1.249	8.78581608	9.58	20.120453
UV L.O. STOP THRESHOLD (A VERSION)	v	62.7516683	5.02	7.96899637	8.0447	8.06608	8.0855	0.01618061	-0.168	-1.162	8.16316363	14.98	142.432246
UVLO HYST (A VERSION)	v	14.3582623	0.415	0.60187679	0.6236	0.63212	0.637	0.00504054	-1.608	3.3262	0.66236321	1.185	36.5622515
VSAT 'A' LOW @20MA	m v	2.56717675	10	69.0499972	237.4358	277.27432	315.0963	34.7040538	-0.305	-2.726	485.498643	390	1.08273307
VSAT 'A' LOW @200MA	v	2.67370999	0.1	0.22893597	0.5383	0.6117	0.7026	0.063794	0.5325	-0.413	0.99446403	2.14	7.98559893
VSAT 'B' LOW @20MA	m v	2.21054974	10	35.6764055	235.723	279.57512	334.0922	40.6497857	0.2409	-1.425	523.473834	390	0.90549784
VSAT 'B' LOW @200MA	v	0.91465314	0.1	-0.5847517	0.5288	0.67706	1.0472	0.21030194	2.0564	4.3934	1.93887167	2.19	2.39804408
VHIGH 'A' @20MA	v	57.501069	0.04	2.1422835	2.2062	2.21804	2.2387	0.01262608	1.3616	2.1656	2.2937965	2.9	18.0039985
VHIGH 'A' @200MA	v	7.45851697	0	1.75875307	2.3442	2.40316	2.5946	0.10740115	2.1965	4.8578	3.04756693	3.02	1.91444249
VHIGH 'B' @20MA	v	177.23818	0.04	2.20259818	2.2221	2.22728	2.2325	0.00411364	-0.09	-1.208	2.25196182	2.9	54.5113879
VHIGH 'B' @200MA	v	156.201736	0	2.34326776	2.3699	2.37366	2.382	0.00506537	1.5089	1.9437	2.40405224	3.02	42.5332314
OUTPUT A VSAT @ VIN UVLO-300MV	v	53.7046211	0	0.7553424	0.7798	0.78456	0.7922	0.0048696	1.044	1.1142	0.8137776	1.19	27.7531375
OUTPUT B VSAT @ VIN UVLO-300MV	v	26.5277645	0	0.72504005	0.7779	0.78416	0.8014	0.00985332	2.0016	4.1375	0.84327995	1.19	13.7293766
ICC START I @ VIN UVLO-300MV	u a	2.32602004	0	16.9490947	110.7706	120.92488	151.81	17.3292975	2.1942	4.8578	224.900665	270	2.86749688
COLLECTOR LEAKAGE @ 20V	u a			-96.218215	-0.1324	8.53586	39.7444	17.4590126	2.2278	4.9705	113.289935	148	2.66269622

Units: 5

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
25'C QA				34	34	34	34	0	#DIV/0!	#DIV/0!	34		
ICC @VIN 12V	m a	37.5167258	4	23.9733507	24.8756	25.09808	25.3244	0.18745488	-0.163	-2.045	26.2228093	35.9	19.2080356
VREF @VIN15V; ILOAD 1MA	v	0.65528455	5.055	4.97981072	5.0679	5.09164	5.1156	0.01863821	-0.044	-0.964	5.20346928	5.145	0.95431178
VREF LINE REG; VIN12-20V	m v	12.8286718	-14	-3.8663863	-2.2966	-1.99476	-1.4928	0.31193772	1.289	1.5878	-0.1231337	14	17.0918304
VREF LOAD REG; IL1-10MA	m v	11.8828412	-19	-1.0532591	1.7825	2.57864	3.3353	0.60531651	-0.204	-0.917	6.21053909	19	9.04285037
VREF VARIANCE MIN	v	0.94858285	5.035	4.97519029	5.0648	5.08896	5.1139	0.01896162	0.0195	-0.694	5.20272971	5.165	1.33673535
VREF VARIANCE MIN	v	1.00416956	5.035	4.9789692	5.0675	5.0915	5.1156	0.01875513	-0.05	-0.923	5.2040308	5.165	1.30630908
VREF ISC; VPIN 160V	m a	2.07575063	-89	-88.424765	-75.917	-73.23718	-69.1077	2.53126424	1.2761	2.4755	-58.049595	-31	5.5620665
OSC ACCURACY PRE TRIM	k hz			375.87293	393.725	399.567	404.168	3.94901171	-0.635	0.4346	423.26107		
CT DISCAHRGE PRE-TRIM	m a			9.63945251	9.841	9.8794	9.925	0.03999125	0.1983	-2.884	10.1193475		
CT DISCHARGE CURRENT	m a	7.09829382	9.02	9.63717112	9.8405	9.87928	9.9255	0.04035148	0.197	-2.867	10.1213889	10.98	9.09276834
OSC FREQ MEASURE - DATA PURPOSE ONLY	k hz	4.07090733	350.8	375.548837	393.4951	399.45028	404.0532	3.98357386	-0.697	0.5981	423.351723	449.2	4.16290512
OSC 1MHZ ACCURACY; RT6.6K,CT220PF	k hz	1.22005049	901	854.459296	942.0076	973.80216	996.9624	19.8904774	-1.036	2.4636	1093.14502	1099	2.09812025
OSC V.STABILITY; VIN12-20V	%	4.70428296	-0.99	-0.355578	0.0158	0.11362	0.1987	0.07819966	-0.401	-2.33	0.58281797	0.99	3.73565131
OSC RAMP PEAK VOLTAGE	v	4.28779356	2.605	2.70955635	2.7833	2.80096	2.8194	0.01523394	-0.052	-2.147	2.89236365	2.995	4.24578211
OSC RAMP VALLEY VOLTAGE	v	17.0373902	0.71	1.08062607	1.1215	1.12992	1.1412	0.00821566	0.6205	-1.616	1.17921393	1.24	4.46626956
RAMP VALLEY TO PEAK	v	1.11328827	1.61	1.56136694	1.6477	1.67106	1.695	0.01828218	0.0813	-0.729	1.78075306	1.99	5.81513529
CALCULATED CHARGE CURRENT	u a			800.488665	812.094	814.853	817.691	2.39405587	-0.094	-2.306	829.217335		
CHARGE CURRENT PRE TRIM	u a			714.930905	741.52	747.7208	753.553	5.4649825	0.0765	-2.72	780.510695		
CHARGE CURRENT ERROR THROUGH TRIM	%			-0.0063727	-0.001	0.0002	0.002	0.00109545	1.2932	2.9167	0.00677267		
OSC ACTUAL CHARGE CURRENT	u a			714.972535	741.527	747.7204	753.535	5.45797754	0.0754	-2.722	780.468265		
CURRENT MIRROR ERROR	%			-13.481181	-10.109	-8.9824	-8.19	0.74979684	-0.878	0.1053	-4.483619		
OSC CLOCK OUT HIGH LEVEL	v	6.32362855	3.72	3.95235741	4.0356	4.05984	4.08	0.01791377	-0.438	-1.353	4.16732259	5.5	26.7980135
OSC CLOCK OUT LOW LEVEL	v	97.423601	-0.05	0.01072721	0.0117	0.012	0.0122	0.00021213	-0.524	-0.963	0.01327279	0.195	287.556758
ILIMIT FULL SOFTSTART THRESHOLD	v	10.4244594	4.31	4.75102009	4.8342	4.85572	4.8797	0.01744999	0.3017	-0.448	4.96041991	5.5	12.3071734
ILIMIT RESTART THRESHOLD	v	8.99034451	0	0.25243585	0.3098	0.32466	0.34	0.01203736	0.1636	-1.362	0.39688415	0.495	4.71698171
E/A VIO PRETRIM	m v			-4.4586389	-1.183	-0.528	0.355	0.65510648	0.2473	-1.429	3.40263888		
ERR AMP VIO	m v	3.19077176	-6.8	-4.4593298	-1.1835	-0.52798	0.355	0.65522497	0.2465	-1.431	3.40336982	6.8	3.72797148
E/A IIBIAS	u a	23.7505717	0.001	0.51061965	0.552	0.55748	0.5704	0.00781006	1.5638	2.0135	0.60434035	2.95	102.112776
E/A IIO	n a	12.5895285	-975	26.5262941	178.6236	215.68038	258.0172	31.525681	0.3729	-1.189	404.834466	975	8.0285828
E/A AVOL; VO1-4V	db	5.40565868	62	89.9508325	102.3044	106.36518	109.8177	2.73572458	-0.521	1.2565	122.779527	250	17.5011306
E/A CMRR; CMV 1.5-5.5V	db	2.56991679	77	81.8041549	95.4145	98.6633	101.1303	2.80985751	-0.455	-3.071	115.522445	250	17.9530693
E/A PSRR; VIN12-20V	db	1.29127992	87	67.0340137	116.3989	123.3778	136.3989	9.39063105	0.8292	-2.013	179.721586	250	4.49462872
E/A OUTPUT HIGH LEVEL; IOUT0.5MA	v	8.12890658	4.505	4.73263695	4.7878	4.80692	4.8189	0.01238051	-0.949	0.7157	4.88120305	4.995	5.0638737
E/A OUTPUT LOW LEVEL; IOUT1MA	v	7.2467483	0	0.32928163	0.442	0.4548	0.4917	0.02091973	2.085	4.444	0.58031837	0.995	8.60750535
E/A OUTPUT SINK I; VOUT1V	m a	9.50870809	1.01	2.6770965	2.9912	3.12114	3.1686	0.07400725	-2.037	4.2684	3.5651835	9.99	30.9377799
E/A OUTPUT SOURCE I; VOUT4V	m a	204.17034	-10	-1.3828465	-1.3104	-1.2976	-1.2762	0.01420774	1.0192	-0.365	-1.2123535	-0.65	15.1935917
E/A GAIN BANDWITH PRODUCT	m hz	13.511469	7	13.3446602	14.2741	14.44698	14.6911	0.18371997	0.569	-2.14	15.5492998	29	26.4043517
PWM COMP IIBIAS(IP3) @0V	u a	41.4994474	-7.95	-1.1763161	-0.9247	-0.83334	-0.7858	0.05716269	-1.333	1.0206	-0.4903639	0	4.8594635
OUTPUT 'A' MAX DUTY CYCLE	%	8.93781818	86	91.2878148	92.41	92.81216	93.0572	0.25405753	-1.174	1.1492	94.3365052	100	9.43072491
OUTPUT 'B' MAX DUTY CYCLE	%	9.94056495	86	91.0340161	92.0792	92.30194	92.6048	0.21132065	0.4933	-0.521	93.5698639	100	12.1427791
OUTPUT 'A' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
OUTPUT 'B' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
ZER DUTY CYCLE THOLD @P3; VP70V	v	4.55050187	1.11	1.17130618	1.211	1.21938	1.23	0.0080123	0.3384	-1.758	1.26745382	1.4	7.51427728
ILIMIT THOLD TO OUTPUTS	v	3.17402649	0.952	0.96775712	0.9906	0.9946	1.0017	0.00447381	1.2644	0.9435	1.02144288	1.048	3.97870926

Units: 5

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
OVER CURRENT THRESHOLD	v	2.07982597	1.142	1.14325199	1.1698	1.17462	1.183	0.005228	1.284	1.3611	1.20598801	1.258	5.3162443
ILIMIT/SHUTDOWN IIBIAS @1.5V	u a	45.2805151	-14.5	3.32907538	3.958	4.15296	4.3434	0.1373141	-0.08	1.7067	4.97684462	14.5	25.1176918
OUTPUT A PWM LEADING EDGE BLANKING	n s	3.85221194	303	335.779944	360.9944	371.1754	376.0508	5.89924274	-1.849	3.7457	406.570856	447	4.28442561
OUTPUT B PWM LEADING EDGE BLANKING	n s	4.42243664	303	340.500875	362.4283	371.46216	375.3338	5.16021412	-1.997	4.2174	402.423445	447	4.87950294
LEADING EDGE BLANKING RESISTOR	k ohm	41.4812795	5.02	10.5031029	10.7207	10.78086	10.8459	0.04629285	0.1975	0.5205	11.0586171	14.98	30.2360585
SOFTSTART DISCHARGE I; VPIN82.5V	u a	5.75285028	101	170.03081	197.5708	206.81928	213.8609	6.13141167	-0.712	0.8116	243.60775	347	7.62090513
SOFTSTART CHARGE I; VPIN82.5V	u a	14.8888998	-19.9	-14.079968	-13.4386	-13.17686	-13.0785	0.15051795	-1.942	3.9017	-12.273752	-8.1	11.2430888
INITIAL VCC OF 8.5V, +PEAK DETECTOR	v			8.50315005	8.5039	8.50406	8.5043	0.00015166	1.1181	1.4556	8.50496995		
INITIAL VCC OF 8.5V, -PEAK DETECTOR	v			8.5045	8.505	8.5051	8.5052	1E-04	0	-3	8.5057		
UV L.O. TURN ON THOLD (A VERSION)	v	1.44940956	8.42	8.32203095	8.6213	8.6779	8.7756	0.05931151	1.4395	2.3186	9.03376905	9.58	5.06984242
UV L.O. STOP THRESHOLD (A VERSION)	v	17.5086811	5.02	7.70487804	7.9965	8.05112	8.146	0.05770699	1.4214	2.2595	8.39736196	14.98	40.0233414
UVLO HYST (A VERSION)	v	12.8054437	0.415	0.59373719	0.6186	0.62682	0.6332	0.0055138	-0.694	0.5319	0.65990281	1.185	33.7444178
VSAT 'A' LOW @20MA	m v	2.54094117	10	65.9783484	244.1501	272.94484	318.9775	34.4944153	0.7266	-2.4	479.911332	390	1.13115084
VSAT 'A' LOW @200MA	v	2.82394504	0.1	0.25538552	0.5725	0.63256	0.7098	0.06286241	0.3913	-2.68	1.00973448	2.14	7.99332978
VSAT 'B' LOW @20MA	m v	2.48439382	10	61.2499921	243.0407	272.85464	319.4568	35.2674413	0.713	-2.502	484.459288	390	1.10720971
VSAT 'B' LOW @200MA	v	2.49128215	0.1	0.20621615	0.5499	0.63862	0.7212	0.07206731	-0.248	-2.16	1.07102385	2.19	7.17560674
VHIGH 'A' @20MA	v	101.305078	0.04	2.17580498	2.2111	2.21882	2.227	0.00716917	0.2953	-2.78	2.26183502	2.9	31.6717272
VHIGH 'A' @200MA	v	28.573091	0	2.20172884	2.3375	2.36744	2.4078	0.02761853	0.7521	-0.368	2.53315116	3.02	7.87587277
VHIGH 'B' @20MA	v	189.655969	0.04	2.20702429	2.2257	2.23012	2.2344	0.00384929	-0.136	-2.625	2.25321571	2.9	58.0090317
VHIGH 'B' @200MA	v	51.7789299	0	2.28018745	2.351	2.3718	2.3918	0.01526876	-0.132	0.0947	2.46341255	3.02	14.1508991
OUTPUT A VSAT @ VIN UVLO-300MV	v	103.749413	0	0.77059312	0.7827	0.78574	0.7884	0.00252448	-0.442	-2.697	0.80088688	1.19	53.3786465
OUTPUT B VSAT @ VIN UVLO-300MV	v	65.70951	0	0.75976896	0.7784	0.78362	0.7884	0.00397517	-0.278	-1.278	0.80747104	1.19	34.0765048
ICC START I @ VIN UVLO-300MV	u a	44.2525261	0	107.172375	111.3682	112.24532	113.5231	0.84549087	0.9228	0.1505	117.318265	270	62.1945137
COLLECTOR LEAKAGE @ 20V	u a			-1.699615	0.6036	1.14768	1.8706	0.47454916	0.798	0.9856	3.99497497	148	103.152165

Units: 5

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
25'C QA				34	34	34	34	0	#DIV/0!	#DIV/0!	34		
ICC @VIN 12V	m a	51.2888074	4	24.2853962	24.965	25.10852	25.2999	0.13718731	0.3688	-0.938	25.9316438	35.9	26.2207933
VREF @VIN15V; ILOAD 1MA	v	0.86887219	5.055	5.02268847	5.0682	5.07982	5.0914	0.00952192	0.1661	-1.825	5.13695153	5.145	2.2817522
VREF LINE REG; VIN12-20V	m v	10.2520385	-14	-4.6052359	-2.8542	-2.32828	-1.8539	0.37949266	-0.278	-0.173	-0.0513241	14	14.3422011
VREF LOAD REG; IL1-10MA	m v	27.6270487	-19	0.34493233	1.5145	1.85466	2.2035	0.25162128	0.0942	0.8297	3.36438767	19	22.7131559
VREF VARIANCE MIN	v	1.52916741	5.035	5.02178488	5.0664	5.07792	5.089	0.00935585	0.1249	-1.9	5.13405512	5.165	3.10251393
VREF VARIANCE MIN	v	1.57322003	5.035	5.02283589	5.0682	5.07984	5.0913	0.00950068	0.1494	-1.843	5.13684411	5.165	2.98785499
VREF ISC; VPIN 160V	m a	1.65133999	-89	-91.796645	-77.8528	-75.7544	-71.2745	2.67370743	1.5916	2.6884	-59.712155	-31	5.57956834
OSC ACCURACY PRE TRIM	k hz			383.65506	397.973	401.3316	404.666	2.94608992	0.0249	-2.577	419.00814		
CT DISCAHRGE PRE-TRIM	m a			9.18025877	9.704	9.8204	9.95	0.10669021	-0.114	-2.262	10.4605412		
CT DISCHARGE CURRENT	m a	2.50427561	9.02	9.18121754	9.7044	9.82062	9.9503	0.10656708	-0.109	-2.25	10.4600225	10.98	3.62644833
OSC FREQ MEASURE - DATA PURPOSE ONLY	k hz	5.90889865	350.8	384.152388	397.9976	401.21724	404.4873	2.84414197	0.0137	-2.543	418.282092	449.2	5.62357769
OSC 1MHZ ACCURACY; RT6.6K,CT220PF	k hz	1.07190858	901	835.794787	953.8714	976.30942	1008.271	23.4191054	0.4323	-1.504	1116.82405	1099	1.74630325
OSC V.STABILITY; VIN12-20V	%	7.80910955	-0.99	-0.2101367	0.0188	0.05836	0.1082	0.04474945	0.4336	-3.132	0.32685668	0.99	6.93967609
OSC RAMP PEAK VOLTAGE	v	16.2064561	2.605	2.77490114	2.7937	2.79882	2.8033	0.00398648	-0.296	-1.904	2.82273886	2.995	16.4037899
OSC RAMP VALLEY VOLTAGE	v	12.0735225	0.71	1.05486951	1.1057	1.12334	1.1358	0.01141175	-0.959	0.9923	1.19181049	1.24	3.40759939
RAMP VALLEY TO PEAK	v	1.60182373	1.61	1.59371822	1.6659	1.6755	1.6976	0.0136303	1.4442	1.3356	1.75728178	1.99	7.69119941
CALCULATED CHARGE CURRENT	u a			806.290723	811.351	812.7256	814.166	1.07247951	0.1672	-0.379	819.160477		
CHARGE CURRENT PRE TRIM	u a			719.876317	744.084	750.6964	755.318	5.13668057	-0.65	-2.663	781.516483		
CHARGE CURRENT ERROR THROUGH TRIM	%			-0.0097453	0	0.0034	0.006	0.00219089	-0.846	1.7448	0.01654534		
OSC ACTUAL CHARGE CURRENT	u a			719.842543	744.055	750.6706	755.315	5.13800942	-0.648	-2.654	781.498657		
CURRENT MIRROR ERROR	%			-12.483365	-9.164	-8.2702	-7.621	0.7021942	-0.611	-2.694	-4.0570348		
OSC CLOCK OUT HIGH LEVEL	v	7.80410871	3.72	3.96428383	4.0326	4.04846	4.0671	0.01402936	0.1528	-1.361	4.13263617	5.5	34.4881445
OSC CLOCK OUT LOW LEVEL	v	113.656159	-0.05	0.01085005	0.0117	0.01194	0.0122	0.00018166	0.2669	1.0744	0.01302995	0.195	335.904045
ILIMIT FULL SOFTSTART THRESHOLD	v	25.3620047	4.31	4.8028846	4.8374	4.84508	4.8536	0.00703257	-0.084	-2.297	4.8872754	5.5	31.0422444
ILIMIT RESTART THRESHOLD	v	17.1271949	0	0.28443431	0.3156	0.32204	0.3292	0.00626762	-0.067	-2.717	0.35964569	0.495	9.19860772
E/A VIO PRETRIM	m v			-3.625075	-1.155	-0.7484	0.025	0.47944583	1.2823	1.6237	2.12827496		
ERR AMP VIO	m v	4.20807566	-6.8	-3.6244686	-1.1546	-0.74818	0.0253	0.47938143	1.2839	1.6302	2.12810859	6.8	5.2485554
E/A IIBIAS	u a	5.7523443	0.001	0.39964315	0.5492	0.61212	0.632	0.03541281	-2.163	4.7223	0.82459685	2.95	22.0059738
E/A IIO	n a	46.6378836	-975	187.831389	229.9444	239.93204	247.1962	8.68344177	-0.552	-3.201	292.032691	975	28.2172277
E/A AVOL; VO1-4V	db	4.95998734	62	87.5076128	100.6413	104.74256	107.8759	2.8724912	-0.473	-0.502	121.977507	250	16.8561514
E/A CMRR; CMV 1.5-5.5V	db	2.8975491	77	83.3803369	95.4109	97.59758	100.5474	2.36954052	0.576	-2.827	111.814823	250	21.439096
E/A PSRR; VIN12-20V	db	1.87791772	87	84.6823751	114.1164	122.65062	130.3747	6.32804082	-0.194	-0.77	160.618865	250	6.70820472
E/A OUTPUT HIGH LEVEL; IOUT0.5MA	v	11.0406716	4.505	4.74212305	4.787	4.79458	4.8079	0.00874283	1.0059	0.0322	4.84703695	4.995	7.64131295
E/A OUTPUT LOW LEVEL; IOUT1MA	v	9.55370035	0	0.35802532	0.4393	0.45282	0.4777	0.01579911	1.2427	0.647	0.54761468	0.995	11.4390382
E/A OUTPUT SINK I; VOUT1V	m a	9.65744544	1.01	2.69386254	3.0104	3.13366	3.2005	0.07329958	-1.592	2.9799	3.57345746	9.99	31.1795341
E/A OUTPUT SOURCE I; VOUT4V	m a	123.560566	-10	-1.423054	-1.3043	-1.28194	-1.2446	0.02351899	1.23	1.115	-1.140826	-0.65	8.95644948
E/A GAIN BANDWITH PRODUCT	m hz	8.14803909	7	12.6249459	14.0268	14.45478	14.717	0.30497235	-0.81	-1.719	16.2846141	29	15.8978563
PWM COMP IIBIAS(IP3) @0V	u a	22.4373953	-7.95	-1.768373	-1.2505	-1.16344	-0.9935	0.10082216	1.6493	2.8929	-0.558507	0	3.84650887
OUTPUT 'A' MAX DUTY CYCLE	%	6.06362712	86	90.4165979	92.1911	92.59032	93.1454	0.36228701	0.8914	0.8429	94.7640421	100	6.81750455
OUTPUT 'B' MAX DUTY CYCLE	%	6.10744125	86	90.371718	92.1739	92.5004	93.0983	0.35478033	1.6075	3.073	94.629082	100	7.04623814
OUTPUT 'A' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
OUTPUT 'B' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
ZER DUTY CYCLE THOLD @P3; VP70V	v	3.18933028	1.11	1.14998331	1.2073	1.21722	1.2295	0.01120611	0.4751	-3.184	1.28445669	1.4	5.43691279
ILIMIT THOLD TO OUTPUTS	v	3.61908952	0.952	0.97131764	0.9899	0.99518	0.9999	0.00397706	-0.234	-1.203	1.01904236	1.048	4.42705671

Units: 5

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
OVER CURRENT THRESHOLD	v	2.78394465	1.142	1.15100541	1.1686	1.17398	1.1792	0.0038291	-0.094	1.2069	1.19695459	1.258	7.31416604
ILIMIT/SHUTDOWN IIBIAS @1.5V	u a	40.981262	-14.5	3.44517239	4.1083	4.36588	4.4997	0.15345127	-1.591	2.8366	5.28658761	14.5	22.0137638
OUTPUT A PWM LEADING EDGE BLANKING	n s	3.48214739	303	331.511163	361.4079	369.98394	375.7013	6.41212949	-0.665	-2.234	408.456717	447	4.00366525
OUTPUT B PWM LEADING EDGE BLANKING	n s	4.7531491	303	340.888156	362.1226	368.41166	373.5573	4.58725073	-0.299	-1.021	395.935164	447	5.71063473
LEADING EDGE BLANKING RESISTOR	k ohm	19.5176083	5.02	10.1496801	10.6237	10.73534	10.8724	0.09760998	0.5523	-0.761	11.3209999	14.98	14.4953076
SOFTSTART DISCHARGE I; VPIN82.5V	u a	6.37241681	101	173.924288	199.5679	207.2808	213.2529	5.55941872	-0.557	-1.236	240.637312	347	8.37732666
SOFTSTART CHARGE I; VPIN82.5V	u a	8.19087081	-19.9	-14.952456	-13.6568	-13.35412	-12.9228	0.26638926	1.1501	2.6791	-11.755784	-8.1	6.57448932
INITIAL VCC OF 8.5V, +PEAK DETECTOR	v			8.50375147	8.5044	8.5046	8.5047	0.00014142	-0.884	-1.75	8.50544853		
INITIAL VCC OF 8.5V, -PEAK DETECTOR	v			8.504978	8.5054	8.50548	8.5056	8.3666E-05	0.5122	-0.612	8.505982		
UV L.O. TURN ON THOLD (A VERSION)	v	0.81238117	8.42	8.10984361	8.5499	8.63216	8.7285	0.08705273	0.4927	-3.156	9.15447639	9.58	3.6293711
UV L.O. STOP THRESHOLD (A VERSION)	v	12.5620325	5.02	7.54084005	7.9378	8.01818	8.1061	0.07955666	0.4539	-3.043	8.49551995	14.98	29.1692323
UVLO HYST (A VERSION)	v	7.14857347	0.415	0.55831015	0.5999	0.61398	0.6224	0.00927831	-0.837	0.3267	0.66964985	1.185	20.5145161
VSAT 'A' LOW @20MA	m v	2.81133364	10	85.7157575	244.112	272.36094	309.3675	31.1075304	0.5726	-3.089	459.006122	390	1.26056358
VSAT 'A' LOW @200MA	v	2.80193273	0.1	0.24176977	0.5339	0.59534	0.6803	0.05892837	0.7117	-0.744	0.94891023	2.14	8.73750031
VSAT 'B' LOW @20MA	m v	3.11114849	10	103.87333	247.4887	272.83964	304.9483	28.1610516	0.4733	-3.123	441.80595	390	1.38678959
VSAT 'B' LOW @200MA	v	4.31357624	0.1	0.36895686	0.5613	0.60146	0.6419	0.03875052	0.1819	-2.972	0.83396314	2.19	13.6646759
VHIGH 'A' @20MA	v	125.853379	0.04	2.17939281	2.2038	2.21394	2.2177	0.00575786	-2.07	4.3763	2.24848719	2.9	39.7172735
VHIGH 'A' @200MA	v	34.9088993	0	2.22215134	2.3392	2.3572	2.3935	0.02250811	1.3654	1.342	2.49224866	3.02	9.81572139
VHIGH 'B' @20MA	v	121.722209	0.04	2.18870515	2.2177	2.2246	2.2323	0.00598247	0.2952	-1.763	2.26049485	2.9	37.6321432
VHIGH 'B' @200MA	v	41.3894596	0	2.24835953	2.3428	2.36252	2.3932	0.01902674	1.2063	2.0234	2.47668047	3.02	11.5185234
OUTPUT A VSAT @ VIN UVLO-300MV	v	4.35553713	0	0.43983352	0.7805	0.81328	0.9245	0.06224108	2.2238	4.9579	1.18672648	1.19	2.01753141
OUTPUT B VSAT @ VIN UVLO-300MV	v	99.9829438	0	0.76894452	0.7818	0.78464	0.7878	0.00261591	0.0783	-2.407	0.80033548	1.19	51.6530971
ICC START I @ VIN UVLO-300MV	u a	35.2753233	0	106.316414	111.2193	112.70652	114.0652	1.06501759	-0.292	0.157	119.096626	270	49.2303228
COLLECTOR LEAKAGE @ 20V	u a			-2.1851363	0.0073	0.89436	1.2341	0.51324938	-1.889	3.6563	3.97385629	148	95.5387674

Units: 3

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
25'C QA				34	34	34	34	0	#DIV/0!	#DIV/0!	34		
ICC @VIN 12V	m a	24.4626233	4	23.4738752	24.8796	25.2077667	25.4242	0.28898191	-1.48	#DIV/0!	26.9416581	35.9	12.3332211
VREF @VIN15V; ILOAD 1MA	v	0.74130264	5.055	4.99059097	5.0812	5.09293333	5.1125	0.01705706	1.6307	#DIV/0!	5.1952757	5.145	1.01749976
VREF LINE REG; VIN12-20V	m v	11.8574147	-14	-4.6267082	-3.0622	-2.7249333	-2.4332	0.31696248	-0.633	#DIV/0!	-0.8231585	14	17.5887625
VREF LOAD REG; IL1-10MA	m v	24.2361731	-19	0.10102007	1.4928	1.81903333	2.0287	0.28633554	-1.528	#DIV/0!	3.53704659	19	20.000971
VREF VARIANCE MIN	v	1.09396215	5.035	4.98873029	5.0792	5.09086667	5.1104	0.01702273	1.6355	#DIV/0!	5.19300305	5.165	1.45165383
VREF VARIANCE MIN	v	1.1387245	5.035	4.99128291	5.0811	5.0928	5.1122	0.01691952	1.6238	#DIV/0!	5.19431709	5.165	1.42242057
VREF ISC; VPIN 160V	m a	1.79115818	-89	-90.719647	-77.1943	-74.251233	-71.7612	2.74473556	-0.723	#DIV/0!	-57.78282	-31	5.25262906
OSC ACCURACY PRE TRIM	k hz			372.530725	403.264	407.304667	413.945	5.79565702	1.6124	#DIV/0!	442.078609		
CT DISCAHRGE PRE-TRIM	m a			9.62818278	9.849	9.87733333	9.925	0.04152509	1.641	#DIV/0!	10.1264839		
CT DISCHARGE CURRENT	m a	6.85405498	9.02	9.62718873	9.8487	9.87736667	9.9252	0.04169632	1.6314	#DIV/0!	10.1275446	10.98	8.81479277
OSC FREQ MEASURE - DATA PURPOSE ONLY	k hz	13.2690916	350.8	395.279525	402.0984	403.1736	404.6407	1.31567911	1.2215	#DIV/0!	411.067675	449.2	11.6609994
OSC 1MHZ ACCURACY; RT6.6K,CT220PF	k hz	2.86355336	901	925.983715	972.8661	983.8463	990.9422	9.64376419	-1.518	#DIV/0!	1041.70889	1099	3.98024733
OSC V.STABILITY; VIN12-20V	%	3.03010233	-0.99	-0.6260881	0.0032	0.08046667	0.216	0.11775913	1.6815	#DIV/0!	0.78702146	0.99	2.57455851
OSC RAMP PEAK VOLTAGE	v	4.93464646	2.605	2.72217621	2.7923	2.80203333	2.8172	0.01330952	1.5309	#DIV/0!	2.88189046	2.995	4.8327979
OSC RAMP VALLEY VOLTAGE	v	22.0844684	0.71	1.10178616	1.1344	1.1408	1.1474	0.00650231	0.1383	#DIV/0!	1.17981384	1.24	5.08537435
RAMP VALLEY TO PEAK	v	2.27494898	1.61	1.616188	1.656	1.6612	1.6698	0.007502	1.6205	#DIV/0!	1.706212	1.99	14.609438
CALCULATED CHARGE CURRENT	u a			800.926062	813.579	815.207333	817.939	2.38021183	1.6377	#DIV/0!	829.488604		
CHARGE CURRENT PRE TRIM	u a			738.661554	746.574	747.589	749.297	1.48790759	1.6412	#DIV/0!	756.516446		
CHARGE CURRENT ERROR THROUGH TRIM	%			-0.0128233	-0.002	-0.0003333	0.002	0.00208167	1.2933	#DIV/0!	0.01215666		
OSC ACTUAL CHARGE CURRENT	u a			738.593606	746.58	747.592	749.315	1.49973231	1.6539	#DIV/0!	756.590394		
CURRENT MIRROR ERROR	%			-9.6414747	-9.158	-9.044	-8.974	0.09957911	-1.6	#DIV/0!	-8.4465253		
OSC CLOCK OUT HIGH LEVEL	v	10.1618542	3.72	3.99683128	4.0555	4.06466667	4.0773	0.0113059	1.2501	#DIV/0!	4.13250206	5.5	42.3181279
OSC CLOCK OUT LOW LEVEL	v	33.2532799	-0.05	0.008553	0.0118	0.0123	0.013	0.0006245	1.2933	#DIV/0!	0.016047	0.195	97.5180457
ILIMIT FULL SOFTSTART THRESHOLD	v	11.6201163	4.31	4.762301	4.8464	4.85633333	4.8744	0.01567206	1.7064	#DIV/0!	4.95036567	5.5	13.6903262
ILIMIT RESTART THRESHOLD	v	11.6164248	0	0.26915512	0.3144	0.32513333	0.3313	0.0093297	-1.675	#DIV/0!	0.38111154	0.495	6.06902816
E/A VIO PRETRIM	m v			-5.2952055	-1.081	-0.2736667	0.59	0.83692313	0.3015	#DIV/0!	4.74787214		
ERR AMP VIO	m v	2.60013551	-6.8	-5.2936384	-1.0806	-0.2735667	0.5899	0.83667861	0.3021	#DIV/0!	4.74650502	6.8	2.81811381
E/A IIBIAS	u a	7.79158521	0.001	0.43117991	0.5526	0.57973333	0.6011	0.0247589	-0.991	#DIV/0!	0.72828676	2.95	31.9113029
E/A IIO	n a	46.1597674	-975	177.821761	221.1972	230.033167	238.5945	8.70190091	-0.142	#DIV/0!	282.244572	975	28.5365554
E/A AVOL; VO1-4V	db	3.0618109	62	77.0871424	102.7704	105.5049	110.9739	4.73629293	1.7321	#DIV/0!	133.922658	250	10.1693527
E/A CMRR; CMV 1.5-5.5V	db	2.78273193	77	83.0323701	95.7268	98.446	100.8322	2.56893831	-0.574	#DIV/0!	113.85963	250	19.6649331
E/A PSRR; VIN12-20V	db	1.04180323	87	53.5809445	114.1164	123.335	136.3953	11.6256759	1.3246	#DIV/0!	193.089055	250	3.63176016
E/A OUTPUT HIGH LEVEL; IOUT0.5MA	v	7.28358086	4.505	4.72409794	4.797	4.80703333	4.8228	0.01382257	1.5454	#DIV/0!	4.88996873	4.995	4.53284543
E/A OUTPUT LOW LEVEL; IOUT1MA	v	16.9027177	0	0.39496138	0.4407	0.44796667	0.4578	0.00883421	1.1971	#DIV/0!	0.50097195	0.995	20.6407099
E/A OUTPUT SINK I; VOUT1V	m a	17.9111022	1.01	2.92116911	3.1152	3.1614	3.186	0.04003848	-1.721	#DIV/0!	3.40163089	9.99	56.8503079
E/A OUTPUT SOURCE I; VOUT4V	m a	76.9067306	-10	-1.5028722	-1.3125	-1.276	-1.237	0.03781204	0.2962	#DIV/0!	-1.0491278	-0.65	5.51852515
E/A GAIN BANDWITH PRODUCT	m hz	17.8226814	7	13.4691891	14.1341	14.2869	14.3959	0.13628514	-1.297	#DIV/0!	15.1046109	29	35.98607
PWM COMP IIBIAS(IP3) @0V	u a	19.6380083	-7.95	-1.9772581	-1.3742	-1.3	-1.1701	0.11287635	1.6799	#DIV/0!	-0.6227419	0	3.83900915
OUTPUT 'A' MAX DUTY CYCLE	%	3.78074456	86	89.0938504	92.0528	92.5686333	93.1951	0.57913049	0.8283	#DIV/0!	96.0434163	100	4.27731275
OUTPUT 'B' MAX DUTY CYCLE	%	5.06472838	86	89.8760253	91.9959	92.4054667	92.8381	0.42157356	0.2455	#DIV/0!	94.934908	100	6.00490964
OUTPUT 'A' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
OUTPUT 'B' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
ZER DUTY CYCLE THOLD @P3; VP70V	v	9.52631579	1.11	1.1958	1.2154	1.2186	1.2228	0.0038	1.1022	#DIV/0!	1.2414	1.4	15.9122807
ILIMIT THOLD TO OUTPUTS	v	3.01259526	0.952	0.96738312	0.9945	0.99776667	1.0036	0.00506392	1.6949	#DIV/0!	1.02815021	1.048	3.30661403

20krad bias

Units: 3

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
OVER CURRENT THRESHOLD	v	2.32547919	1.142	1.14708529	1.1747	1.17833333	1.1843	0.00520801	1.6114	#DIV/0!	1.20958137	1.258	5.09898649
ILIMIT/SHUTDOWN IIBIAS @1.5V	u a	34.8811666	-14.5	3.30196076	4.2173	4.38476667	4.5759	0.18046765	0.58	#DIV/0!	5.46757258	14.5	18.683373
OUTPUT A PWM LEADING EDGE BLANKING	n s	18.2303015	303	363.271499	369.2692	370.698533	371.4132	1.23783898	-1.732	#DIV/0!	378.125567	447	20.5469554
OUTPUT B PWM LEADING EDGE BLANKING	n s	12.3704601	303	358.954529	367.8399	369.745667	371.4132	1.79852288	-0.586	#DIV/0!	380.536804	447	14.3181078
LEADING EDGE BLANKING RESISTOR	k ohm	54.0624301	5.02	10.5193084	10.6922	10.7305667	10.7614	0.03520971	-0.919	#DIV/0!	10.9418249	14.98	40.2297541
SOFTSTART DISCHARGE I; VPIN82.5V	u a	5.05987221	101	166.903309	202.8168	209.979167	217.1753	7.17930956	0.0212	#DIV/0!	253.055024	347	6.36183894
SOFTSTART CHARGE I; VPIN82.5V	u a	7.11545862	-19.9	-15.235228	-13.7612	-13.411433	-13.2112	0.30396573	-1.678	#DIV/0!	-11.587639	-8.1	5.82459672
INITIAL VCC OF 8.5V, +PEAK DETECTOR	v			8.50432026	8.5046	8.50466667	8.5047	5.7735E-05	-1.732	#DIV/0!	8.50501308		
INITIAL VCC OF 8.5V, -PEAK DETECTOR	v			8.5055	8.5055	8.5055	8.5055	0	#DIV/0!	#DIV/0!	8.5055		
UV L.O. TURN ON THOLD (A VERSION)	v	1.44660004	8.42	8.30431623	8.6442	8.7224	8.7779	0.06968063	-1.31	#DIV/0!	9.14048377	9.58	4.10252709
UV L.O. STOP THRESHOLD (A VERSION)	v	14.270807	5.02	7.67055653	8.0213	8.10256667	8.1584	0.07200169	-1.391	#DIV/0!	8.53457681	14.98	31.8392219
UVLO HYST (A VERSION)	v	22.9599734	0.415	0.60199069	0.6171	0.61983333	0.623	0.00297377	0.6418	#DIV/0!	0.63767598	1.185	63.3500974
VSAT 'A' LOW @20MA	m v	2.82076958	10	87.2190297	246.1073	275.381533	308.4778	31.3604173	0.5403	#DIV/0!	463.544037	390	1.21829232
VSAT 'A' LOW @200MA	v	1.52047682	0.1	-0.0770315	0.5877	0.66133333	0.8034	0.12306081	1.7286	#DIV/0!	1.3996982	2.14	4.00524653
VSAT 'B' LOW @20MA	m v	2.86386862	10	89.0202658	241.4728	271.965367	302.4545	30.4908501	-5E-04	#DIV/0!	454.910468	390	1.2903831
VSAT 'B' LOW @200MA	v	1.52486753	0.1	-0.0697435	0.5427	0.64476667	0.7756	0.11908503	1.0236	#DIV/0!	1.35927682	2.19	4.32529426
VHIGH 'A' @20MA	v	148.617392	0.04	2.18688118	2.211	2.21616667	2.2207	0.00488092	-0.574	#DIV/0!	2.24545216	2.9	46.7011686
VHIGH 'A' @200MA	v	40.0284518	0	2.23518028	2.3392	2.35273333	2.3752	0.01959218	1.6253	#DIV/0!	2.47028639	3.02	11.3526047
VHIGH 'B' @20MA	v	174.908345	0.04	2.20812172	2.2299	2.2332	2.2379	0.00417971	1.3382	#DIV/0!	2.25827828	2.9	53.177496
VHIGH 'B' @200MA	v	37.5187627	0	2.25748493	2.3624	2.3846	2.4046	0.02118584	-0.462	#DIV/0!	2.51171507	3.02	9.9972414
OUTPUT A VSAT @ VIN UVLO-300MV	v	74.9864306	0	0.76698286	0.7849	0.788	0.7918	0.00350286	0.8634	#DIV/0!	0.80901714	1.19	38.2544989
OUTPUT B VSAT @ VIN UVLO-300MV	v	116.192906	0	0.77086556	0.7818	0.78436667	0.786	0.00225019	-1.545	#DIV/0!	0.79786778	1.19	60.0888818
ICC START I @ VIN UVLO-300MV	u a	22.03816	0	103.195563	111.6587	113.495467	115.0593	1.71665068	-0.697	#DIV/0!	123.795371	270	30.3895129
COLLECTOR LEAKAGE @ 20V	u a			-4.4858965	0.3848	1.2475	2.2746	0.95556608	0.7513	#DIV/0!	6.98089647	148	51.1921688

Units: 5

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
25'C QA				34	34	34	34	0	#DIV/0!	#DIV/0!	34		
ICC @VIN 12V	m a	33.900958	4	23.8030469	24.8519	25.04458	25.293	0.20692218	0.4717	-2.921	26.2861131	35.9	17.487122
VREF @VIN15V; ILOAD 1MA	v	0.42732475	5.055	4.94930232	5.0609	5.08372	5.1183	0.02240295	0.969	0.7257	5.21813768	5.145	0.91178484
VREF LINE REG; VIN12-20V	m v	5.52400073	-14	-6.9985311	-3.6581	-3.02494	-2.0287	0.66226518	0.9274	-0.144	0.94865108	14	8.56904481
VREF LOAD REG; IL1-10MA	m v	17.3942056	-19	-0.797722	0.9404	1.5671	1.8756	0.394137	-1.32	0.812	3.931922	19	14.7435198
VREF VARIANCE MIN	v	0.70528461	5.035	4.94824318	5.0591	5.08226	5.1164	0.02233614	0.9067	0.5475	5.21627682	5.165	1.23477038
VREF VARIANCE MIN	v	0.7224039	5.035	4.94876627	5.0609	5.08376	5.1184	0.02249896	0.9573	0.6545	5.21875373	5.165	1.20361143
VREF ISC; VPIN 160V	m a	2.25339225	-89	-87.907405	-80.4472	-79.28366	-76.8316	1.43729082	1.763	3.2465	-70.659915	-31	11.1978405
OSC ACCURACY PRE TRIM	k hz			391.271857	396.003	397.6486	398.642	1.06279057	-0.984	0.703	404.025343		
CT DISCAHRGE PRE-TRIM	m a			9.41457629	9.704	9.7638	9.859	0.05820395	1.3152	2.4252	10.1130237		
CT DISCHARGE CURRENT	m a	4.27814779	9.02	9.41619658	9.7044	9.76402	9.8588	0.05797057	1.3114	2.4168	10.1118434	10.98	6.9919386
OSC FREQ MEASURE - DATA PURPOSE ONLY	k hz	14.1878534	350.8	391.010848	395.9294	397.60936	398.6173	1.09975199	-0.882	0.4495	404.207872	449.2	15.6370529
OSC 1MHZ ACCURACY; RT6.6K,CT220PF	k hz	4.36388125	901	936.407495	960.9609	966.36458	974.4956	4.99284744	1.2489	2.5286	996.321665	1099	8.85502856
OSC V.STABILITY; VIN12-20V	%	13.3212175	-0.99	-0.1278984	0.0063	0.0244	0.0687	0.02538307	1.9787	4.0391	0.17669839	0.99	12.6803703
OSC RAMP PEAK VOLTAGE	v	5.6391986	2.605	2.73106065	2.7889	2.80034	2.8195	0.01154656	1.4578	2.7283	2.86961935	2.995	5.61956793
OSC RAMP VALLEY VOLTAGE	v	33.5286134	0.71	1.09558089	1.1157	1.12004	1.1257	0.00407652	0.6091	-1.314	1.14449911	1.24	9.80902463
RAMP VALLEY TO PEAK	v	2.4772571	1.61	1.62354368	1.6732	1.6803	1.6968	0.00945939	1.96	4.0806	1.73705632	1.99	10.9133218
CALCULATED CHARGE CURRENT	u a			792.016491	809.902	814.106	818.164	3.68158478	0.251	-2.612	836.195509		
CHARGE CURRENT PRE TRIM	u a			738.03704	746.645	748.319	750.142	1.71365997	0.2579	-3.039	758.60096		
CHARGE CURRENT ERROR THROUGH TRIM	%			-0.01	-0.001	0.002	0.004	0.002	-0.938	-0.187	0.014		
OSC ACTUAL CHARGE CURRENT	u a			738.066968	746.641	748.3048	750.114	1.70630528	0.2449	-3.037	758.542632		
CURRENT MIRROR ERROR	%			-11.417359	-9.336	-8.7936	-8.256	0.43729315	0.0355	-1.619	-6.1698411		
OSC CLOCK OUT HIGH LEVEL	v	12.2091364	3.72	3.99724658	4.0381	4.05156	4.0614	0.00905224	-0.677	0.2224	4.10587342	5.5	53.3363541
OSC CLOCK OUT LOW LEVEL	v	157.943657	-0.05	0.0109977	0.0117	0.01178	0.012	0.00013038	1.7144	2.6644	0.0125623	0.195	468.411084
ILIMIT FULL SOFTSTART THRESHOLD	v	7.11542961	4.31	4.69686561	4.8184	4.84812	4.8842	0.02520907	0.5476	-0.196	4.99937439	5.5	8.61965036
ILIMIT RESTART THRESHOLD	v	7.39315061	0	0.23435253	0.3101	0.32126	0.3465	0.01448458	1.9471	4.0604	0.40816747	0.495	3.9982755
E/A VIO PRETRIM	m v			-4.6237175	-1.337	-0.5196	0.119	0.68401959	-0.359	-2.825	3.58451754		
ERR AMP VIO	m v	3.05934113	-6.8	-4.6253483	-1.3374	-0.51968	0.1195	0.68427806	-0.359	-2.825	3.58598833	6.8	3.56564603
E/A IIBIAS	u a	26.7662059	0.001	0.61331257	0.6525	0.66276	0.6749	0.00824124	0.5205	0.9269	0.71220743	2.95	92.5119933
E/A IIO	n a	21.2842125	-975	123.170014	213.0317	237.06318	260.3048	18.9821944	-2E-04	-1.464	350.956346	975	12.9584038
E/A AVOL; VO1-4V	db	4.95911941	62	87.9290071	101.8553	105.45382	109.8141	2.92080216	0.5925	1.1228	122.978633	250	16.4961738
E/A CMRR; CMV 1.5-5.5V	db	3.27815258	77	85.3183975	95.5674	98.33468	100.5474	2.16938041	-0.027	-1.931	111.350962	250	23.303938
E/A PSRR; VIN12-20V	db	1.935734	87	85.786308	116.3953	123.5572	130.3747	6.29514866	-0.372	-2.787	161.328092	250	6.69525094
E/A OUTPUT HIGH LEVEL; IOUT0.5MA	v	7.50245483	4.505	4.72110971	4.7836	4.79966	4.8178	0.01309171	0.2717	-0.33	4.87821029	4.995	4.97362902
E/A OUTPUT LOW LEVEL; IOUT1MA	v	10.9294211	0	0.36721228	0.4396	0.44946	0.4708	0.01370795	1.2291	0.1805	0.53170772	0.995	13.2657775
E/A OUTPUT SINK I; VOUT1V	m a	12.6635564	1.01	2.81574421	3.0787	3.15442	3.1994	0.05644597	-0.745	-2.369	3.49309579	9.99	40.3665108
E/A OUTPUT SOURCE I; VOUT4V	m a	86.7477814	-10	-1.4607612	-1.3151	-1.25924	-1.2343	0.03358687	-1.596	2.2157	-1.0577188	-0.65	6.04640996
E/A GAIN BANDWITH PRODUCT	m hz	17.5995528	7	13.6082271	14.3219	14.45546	14.6232	0.14120548	0.5435	-3.031	15.3026929	29	34.3342195
PWM COMP IIBIAS(IP3) @0V	u a	12.2904082	-7.95	-2.6228937	-1.8817	-1.58754	-1.4412	0.17255896	-1.727	3.3418	-0.5521863	0	3.06666206
OUTPUT 'A' MAX DUTY CYCLE	%	9.6917466	86	91.3862681	92.4979	92.7868	93.0149	0.23342198	-0.301	-2.519	94.1873319	100	10.3006581
OUTPUT 'B' MAX DUTY CYCLE	%	10.8814332	86	91.1431907	92.0777	92.30138	92.5766	0.19303156	0.552	-0.384	93.4595693	100	13.2942339
OUTPUT 'A' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
OUTPUT 'B' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
ZER DUTY CYCLE THOLD @P3; VP70V	v	6.23075593	1.11	1.18600857	1.2133	1.22194	1.23	0.00598857	-0.226	1.5272	1.25787143	1.4	9.91109882
ILIMIT THOLD TO OUTPUTS	v	2.64973301	0.952	0.96212214	0.9866	0.99328	0.9994	0.00519298	-0.099	-1.563	1.02443786	1.048	3.51243678

Units: 5

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
OVER CURRENT THRESHOLD	v	1.86204788	1.142	1.13968851	1.1668	1.1732	1.1813	0.00558525	0.6045	-0.12	1.20671149	1.258	5.06095065
ILIMIT/SHUTDOWN IIBIAS @1.5V	u a	50.4682126	-14.5	3.89473667	4.552	4.65378	4.8569	0.12650722	1.3485	1.1804	5.41282333	14.5	25.9437628
OUTPUT A PWM LEADING EDGE BLANKING	n s	4.26113349	303	338.392746	362.1226	369.69806	375.7013	5.21755226	-0.634	-0.107	401.003374	447	4.9385827
OUTPUT B PWM LEADING EDGE BLANKING	n s	4.49961065	303	340.051902	362.1226	369.69804	374.9866	4.94102307	-0.946	0.6636	399.344178	447	5.21497667
LEADING EDGE BLANKING RESISTOR	k ohm	31.9821283	5.02	10.3536325	10.6411	10.70942	10.7844	0.05929791	0.3341	-1.956	11.0652075	14.98	24.0063553
SOFTSTART DISCHARGE I; VPIN82.5V	u a	9.28212953	101	180.668319	197.3796	202.54882	207.0141	3.64675009	-0.419	-0.014	224.429321	347	13.2036449
SOFTSTART CHARGE I; VPIN82.5V	u a	6.53650418	-19.9	-15.43261	-13.9883	-13.46308	-13.1807	0.32825497	-1.214	1.4391	-11.49355	-8.1	5.44605104
INITIAL VCC OF 8.5V, +PEAK DETECTOR	v			8.50425167	8.5045	8.50452	8.5046	4.4721E-05	2.2361	5	8.50478833		
INITIAL VCC OF 8.5V, -PEAK DETECTOR	v			8.5047377	8.5054	8.50552	8.5057	0.00013038	0.5414	-1.488	8.5063023		
UV L.O. TURN ON THOLD (A VERSION)	v	0.95457167	8.42	8.19588225	8.5236	8.62464	8.6983	0.07145962	-0.597	-1.019	9.05339775	9.58	4.45640924
UV L.O. STOP THRESHOLD (A VERSION)	v	14.9043287	5.02	7.62070515	7.9273	8.02378	8.1002	0.06717914	-0.55	-0.316	8.42685485	14.98	34.5157732
UVLO HYST (A VERSION)	v	7.60389726	0.415	0.55197454	0.5922	0.60086	0.613	0.00814758	0.8324	-0.043	0.64974546	1.185	23.8983135
VSAT 'A' LOW @20MA	m v	2.61143776	10	69.5868129	238.9954	264.49402	301.3126	32.4845345	0.6038	-3.301	459.401227	390	1.28785366
VSAT 'A' LOW @200MA	v	4.44367436	0.1	0.33850118	0.5011	0.5337	0.5694	0.03253314	0.1003	-2.929	0.72889882	2.14	16.4580911
VSAT 'B' LOW @20MA	m v	2.55115169	10	64.6486455	237.7207	262.95574	300.6934	33.0511824	0.6094	-3.297	461.262835	390	1.2812881
VSAT 'B' LOW @200MA	v	4.34041923	0.1	0.33245559	0.4996	0.5311	0.568	0.0331074	0.3062	-3.021	0.72974441	2.19	16.7022071
VHIGH 'A' @20MA	v	169.191348	0.04	2.19081126	2.2092	2.21654	2.2201	0.00428812	-1.769	3.462	2.24226874	2.9	53.1281386
VHIGH 'A' @200MA	v	167.750854	0	2.30646944	2.3264	2.3343	2.3379	0.00463843	-1.742	3.1981	2.36213056	3.02	49.2767685
VHIGH 'B' @20MA	v	147.612892	0.04	2.20038698	2.222	2.23006	2.2354	0.0049455	-1.256	2.5406	2.25973302	2.9	45.1548271
VHIGH 'B' @200MA	v	107.709919	0	2.30557913	2.3395	2.3492	2.36	0.00727014	0.3757	2.0488	2.39282087	3.02	30.7559227
OUTPUT A VSAT @ VIN UVLO-300MV	v	179.513435	0	0.77666946	0.7836	0.78542	0.7875	0.00145842	0.3748	0.3087	0.79417054	1.19	92.4696919
OUTPUT B VSAT @ VIN UVLO-300MV	v	210.660773	0	0.77631903	0.7823	0.78376	0.7853	0.00124016	0.1596	-1.925	0.79120097	1.19	109.190099
ICC START I @ VIN UVLO-300MV	u a	25.0167733	0	103.170012	110.6429	112.13478	114.2325	1.49412794	0.4625	-0.994	121.099548	270	35.2190321
COLLECTOR LEAKAGE @ 20V	u a			-1.6642593	0.6113	1.35914	1.9538	0.50389989	-0.667	0.5578	4.38253933	148	97.003964

Units: 5

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
25'C QA				34	34	34	34	0	#DIV/0!	#DIV/0!	34		
ICC @VIN 12V	m a	25.9002613	4	23.4607621	24.6582	25.08926	25.3959	0.27141631	-1.037	2.0276	26.7177579	35.9	13.2769472
VREF @VIN15V; ILOAD 1MA	v	1.12060307	5.055	5.0177713	5.078	5.10244	5.1145	0.01411145	-1.868	3.9507	5.1871087	5.145	1.00533024
VREF LINE REG; VIN12-20V	m v	8.63462299	-14	-5.8031052	-3.6581	-3.33216	-2.6411	0.4118242	1.5939	2.7736	-0.8612148	14	14.0287694
VREF LOAD REG; IL1-10MA	m v	26.6731423	-19	0.32139183	1.6676	1.88758	2.3349	0.26103136	1.7834	3.4965	3.45376817	19	21.8523167
VREF VARIANCE MIN	v	1.56184997	5.035	5.01663633	5.0762	5.10046	5.1122	0.01397061	-1.897	4.0271	5.18428367	5.165	1.53989913
VREF VARIANCE MIN	v	1.59251249	5.035	5.01779482	5.0779	5.10224	5.1144	0.0140742	-1.851	3.913	5.18668518	5.165	1.48640815
VREF ISC; VPIN 160V	m a	1.10291318	-89	-99.382543	-79.5739	-76.2353	-69.8239	3.85787391	1.5306	2.4381	-53.088057	-31	3.90848267
OSC ACCURACY PRE TRIM	k hz			390.297624	397.96	399.8902	402.402	1.59876271	0.8751	2.2772	409.482776		
CT DISCAHRGE PRE-TRIM	m a			9.33986619	9.691	9.807	9.874	0.07785564	-1.005	-0.691	10.2741338		
CT DISCHARGE CURRENT	m a	3.37722076	9.02	9.34094446	9.6913	9.80702	9.8738	0.07767926	-1.005	-0.694	10.2730955	10.98	5.03343296
OSC FREQ MEASURE - DATA PURPOSE ONLY	k hz	10.6039714	350.8	390.568208	397.8739	399.81236	402.187	1.54069195	0.6928	2.188	409.056512	449.2	10.6851643
OSC 1MHZ ACCURACY; RT6.6K,CT220PF	k hz	2.70652978	901	921.867159	969.2563	980.9366	996.0314	9.84490676	0.7849	1.4176	1040.00604	1099	3.99744433
OSC V.STABILITY; VIN12-20V	%	2.74319757	-0.99	-0.6906942	0.0188	0.11476	0.3311	0.13424237	1.4064	1.1579	0.9202142	0.99	2.1732831
OSC RAMP PEAK VOLTAGE	v	10.0618376	2.605	2.76502149	2.7961	2.80472	2.8116	0.00661642	-0.135	-1.775	2.84441851	2.995	9.58625307
OSC RAMP VALLEY VOLTAGE	v	32.1251448	0.71	1.098057	1.1184	1.12382	1.1282	0.00429383	-0.069	-2.09	1.149583	1.24	9.01913712
RAMP VALLEY TO PEAK	v	3.05204678	1.61	1.63443248	1.668	1.68088	1.688	0.00774125	-1.523	2.5788	1.72732752	1.99	13.3105065
CALCULATED CHARGE CURRENT	u a			808.471774	814.364	815.7268	816.943	1.20917108	-0.164	-2.886	822.981826		
CHARGE CURRENT PRE TRIM	u a			731.069753	747.573	750.4588	755.646	3.23150781	1.2269	1.5643	769.847847		
CHARGE CURRENT ERROR THROUGH TRIM	%			-0.0200892	-0.001	0.0026	0.007	0.00378153	0.2386	-2.838	0.0252892		
OSC ACTUAL CHARGE CURRENT	u a			730.977783	747.578	750.4386	755.655	3.24346948	1.2421	1.6071	769.899417		
CURRENT MIRROR ERROR	%			-11.245063	-9.222	-8.7012	-8.111	0.42397724	0.2747	-0.237	-6.1573366		
OSC CLOCK OUT HIGH LEVEL	v	5.73584549	3.72	3.94082203	4.0298	4.05904	4.0786	0.01970299	-0.816	-0.272	4.17725797	5.5	24.3780201
OSC CLOCK OUT LOW LEVEL	v	107.232934	-0.05	0.01072588	0.0116	0.01188	0.0121	0.00019235	-0.59	-0.022	0.01303412	0.195	317.331849
ILIMIT FULL SOFTSTART THRESHOLD	v	14.2982056	4.31	4.78409942	4.8437	4.8612	4.8767	0.0128501	-0.288	-0.756	4.93830058	5.5	16.570562
ILIMIT RESTART THRESHOLD	v	16.4633195	0	0.27936867	0.311	0.318	0.3274	0.00643856	0.7453	-0.372	0.35663133	0.495	9.16354574
E/A VIO PRETRIM	m v			-5.9330892	-1.141	-0.185	1.202	0.95801487	0.814	-0.909	5.56308925		
ERR AMP VIO	m v	2.30200871	-6.8	-5.9321756	-1.1407	-0.18516	1.2018	0.95783593	0.8148	-0.907	5.56185561	6.8	2.43088256
E/A IIBIAS	u a	5.04848517	0.001	0.37628753	0.5738	0.6225	0.6752	0.04103541	-0.007	-1.501	0.86871247	2.95	18.9064348
E/A IIO	n a	15.2884608	-975	59.5706158	177.8737	215.28024	237.8676	25.951604	-0.853	-1.218	370.989864	975	9.7581606
E/A AVOL; VO1-4V	db	1.98867913	62	61.7297671	100.2717	109.47044	121.8553	7.95677882	0.8794	1.6403	157.211113	250	5.88720483
E/A CMRR; CMV 1.5-5.5V	db	2.07933288	77	77.8554599	96.0546	99.4218	104.3541	3.59439002	0.7291	-1.923	120.98814	250	13.9641867
E/A PSRR; VIN12-20V	db	1.55677639	87	78.0790558	112.8734	118.33388	126.8528	6.70913737	0.6674	-2.852	158.588704	250	6.54163184
E/A OUTPUT HIGH LEVEL; IOUT0.5MA	v	5.54723702	4.505	4.69999698	4.7879	4.80994	4.826	0.01832384	-0.571	-3.055	4.91988302	4.995	3.36647106
E/A OUTPUT LOW LEVEL; IOUT1MA	v	10.1718264	0	0.3660835	0.4404	0.45568	0.4806	0.01493275	1.469	3.086	0.5452765	0.995	12.0388637
E/A OUTPUT SINK I; VOUT1V	m a	12.1757651	1.01	2.7815834	3.0474	3.12978	3.2028	0.05803277	-0.303	0.3347	3.4779766	9.99	39.4042906
E/A OUTPUT SOURCE I; VOUT4V	m a	101.160404	-10	-1.4377744	-1.298	-1.26508	-1.223	0.02878241	0.6212	0.0472	-1.0923856	-0.65	7.12333269
E/A GAIN BANDWITH PRODUCT	m hz	10.1015024	7	12.9067209	13.9882	14.3649	14.5988	0.24302985	-0.993	0.7455	15.8230791	29	20.0731169
PWM COMP IIBIAS(IP3) @0V	u a	11.8206913	-7.95	-2.7481627	-1.8978	-1.6888	-1.4941	0.17656046	-0.321	-2.575	-0.6294373	0	3.18833188
OUTPUT 'A' MAX DUTY CYCLE	%	5.29926851	86	90.1067243	92.1137	92.5962	93.216	0.41491261	0.7102	0.5951	95.0856757	100	5.94807983
OUTPUT 'B' MAX DUTY CYCLE	%	5.79887226	86	90.1575213	91.9626	92.34634	92.8099	0.36480312	0.02	-1.958	94.5351587	100	6.99341615
OUTPUT 'A' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
OUTPUT 'B' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
ZER DUTY CYCLE THOLD @P3; VP70V	v	6.82278484	1.11	1.18779751	1.2152	1.22006	1.2282	0.00537708	1.0177	-0.197	1.25232249	1.4	11.1547511
ILIMIT THOLD TO OUTPUTS	v	2.97564771	0.952	0.96613807	0.9869	0.99512	0.9993	0.00483032	-1.721	3.2647	1.02410193	1.048	3.64917094

30krad bias

Units: 5

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
OVER CURRENT THRESHOLD	v	2.21186804	1.142	1.1452989	1.1678	1.17644	1.1803	0.00519018	-1.567	2.3776	1.2075811	1.258	5.238094
ILIMIT/SHUTDOWN IIBIAS @1.5V	u a	48.7431653	-14.5	3.75690187	4.4131	4.53806	4.6937	0.13019302	0.4731	-2.921	5.31921813	14.5	25.5055656
OUTPUT A PWM LEADING EDGE BLANKING	n s	5.55398547	303	344.033615	360.6932	367.12522	369.9839	3.84860088	-1.633	2.3746	390.216825	447	6.91807946
OUTPUT B PWM LEADING EDGE BLANKING	n s	5.4269647	303	342.771084	359.9786	365.98176	369.2692	3.86844602	-1.169	0.2604	389.192436	447	6.98111847
LEADING EDGE BLANKING RESISTOR	k ohm	20.4898885	5.02	10.1655591	10.6071	10.72214	10.8204	0.09276348	0.0312	-2.163	11.2787209	14.98	15.3000587
SOFTSTART DISCHARGE I; VPIN82.5V	u a	6.99415685	101	174.867036	200.6512	204.44842	212.7416	4.93023068	1.6579	2.6915	234.029804	347	9.63792497
SOFTSTART CHARGE I; VPIN82.5V	u a	5.19604246	-19.9	-15.922731	-14.1405	-13.43386	-13.0524	0.41481185	-1.699	3.4597	-10.944989	-8.1	4.28616811
INITIAL VCC OF 8.5V, +PEAK DETECTOR	v			8.50365502	8.5043	8.50446	8.5046	0.00013416	0.1656	-2.407	8.50526498		
INITIAL VCC OF 8.5V, -PEAK DETECTOR	v			8.504978	8.5054	8.50548	8.5056	8.3666E-05	0.5122	-0.612	8.505982		
UV L.O. TURN ON THOLD (A VERSION)	v	2.29375458	8.42	8.45714715	8.6416	8.71006	8.7528	0.04215214	-1.296	2.0954	8.96297285	9.58	6.87936585
UV L.O. STOP THRESHOLD (A VERSION)	v	22.1642076	5.02	7.82629574	8.0272	8.10464	8.1493	0.04639071	-1.508	2.8704	8.38298426	14.98	49.4018447
UVLO HYST (A VERSION)	v	7.74444582	0.415	0.55622928	0.5945	0.6054	0.6143	0.00819512	-0.178	-1.473	0.65457072	1.185	23.5750042
VSAT 'A' LOW @20MA	m v	4.80476921	10	169.792686	259.6616	283.73624	309.5547	18.9905923	0.1321	-0.297	397.679794	390	1.86520003
VSAT 'A' LOW @200MA	v	9.0785226	0.1	0.45549634	0.534	0.55594	0.5739	0.01674061	-0.193	-1.716	0.65638366	2.14	31.5412653
VSAT 'B' LOW @20MA	m v	4.5500997	10	162.203978	257.2568	281.57498	308.6237	19.895167	0.1932	-0.633	400.945982	390	1.81660568
VSAT 'B' LOW @200MA	v	8.38960215	0.1	0.44336411	0.5327	0.55084	0.5736	0.01791265	0.2821	-2.239	0.65831589	2.19	30.5028397
VHIGH 'A' @20MA	v	203.912986	0.04	2.1938258	2.2103	2.21516	2.2195	0.0035557	-0.253	-0.591	2.2364942	2.9	64.2011482
VHIGH 'A' @200MA	v	171.552782	0	2.30714552	2.3282	2.33436	2.3398	0.00453575	-0.278	-0.832	2.36157448	3.02	50.3878791
VHIGH 'B' @20MA	v	205.141226	0.04	2.20795568	2.2241	2.2293	2.233	0.00355739	-0.782	-0.53	2.25064432	2.9	62.8457592
VHIGH 'B' @200MA	v	181.313259	0	2.32611529	2.3471	2.35206	2.3586	0.00432412	0.7881	0.7767	2.37800471	3.02	51.4894935
OUTPUT A VSAT @ VIN UVLO-300MV	v	192.885588	0	0.77509892	0.7818	0.78322	0.7847	0.00135351	-0.25	-2.833	0.79134108	1.19	100.178749
OUTPUT B VSAT @ VIN UVLO-300MV	v	111.027118	0	0.76742236	0.7788	0.7815	0.7842	0.00234627	-0.238	-2.474	0.79557764	1.19	58.0352881
ICC START I @ VIN UVLO-300MV	u a	6.48659622	0	80.757036	112.6058	116.75628	127.2356	5.999874	1.9804	4.0894	152.755524	270	8.51371879
COLLECTOR LEAKAGE @ 20V	u a			-37.539026	0.6113	4.2006	16.6114	6.9566043	2.2062	4.8918	45.9402258	148	6.89030614

control (unexposed) unit #266 included with 40k unbiased sample data

Units: 6

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
25'C QA				34	34	34	34	0	#DIV/0!	#DIV/0!	34		
ICC @VIN 12V	m a	25.9405102	4	23.4744468	24.7302	25.10135	25.4511	0.27115054	-1E-03	-1.093	26.7282532	35.9	13.2750981
VREF @VIN15V; ILOAD 1MA	v	0.67885911	5.055	5.0072552	5.0559	5.07953333	5.0876	0.01204635	-2.077	4.373	5.15181146	5.145	1.81152078
VREF LINE REG; VIN12-20V	m v	4.67287858	-14	-7.6961117	-4.2346	-2.9791833	-1.7549	0.78615472	-0.088	2.4194	1.73774499	14	7.19925433
VREF LOAD REG; IL1-10MA	m v	21.4607045	-19	-0.3260751	1.1826	1.59306667	2.0544	0.31985695	0.2299	-0.81	3.51220839	19	18.1403313
VREF VARIANCE MIN	v	1.22254536	5.035	5.00743239	5.0552	5.07835	5.0865	0.0118196	-2.06	4.3185	5.14926761	5.165	2.44368063
VREF VARIANCE MIN	v	1.24047848	5.035	5.00759022	5.0563	5.07976667	5.0883	0.01202941	-2.026	4.172	5.15194312	5.165	2.36180452
VREF ISC; VPIN 160V	m a	0.78639295	-89	-103.48615	-82.2893	-79.613267	-71.8361	3.97881378	2.0416	4.2441	-55.740384	-31	4.07267671
OSC ACCURACY PRE TRIM	k hz			385.629215	396.739	400.48	402.744	2.47513087	-0.649	-1.295	415.330785		
CT DISCAHRGE PRE-TRIM	m a			9.43696678	9.742	9.79583333	9.903	0.05981109	1.3441	1.7257	10.1546999		
CT DISCHARGE CURRENT	m a	4.33238907	9.02	9.43768734	9.7421	9.79585	9.9028	0.05969378	1.3453	1.72	10.1540127	10.98	6.61235873
OSC FREQ MEASURE - DATA PURPOSE ONLY	k hz	6.64840131	350.8	385.488442	396.6409	400.413333	402.7184	2.48748188	-0.644	-1.251	415.338225	449.2	6.5376244
OSC 1MHZ ACCURACY; RT6.6K,CT220PF	k hz	1.49771635	901	877.938729	946.6056	969.764417	987.2734	15.3042813	-0.369	-0.707	1061.5901	1099	2.81480241
OSC V.STABILITY; VIN12-20V	%	5.3157704	-0.99	-0.3419019	0	0.04901667	0.1398	0.06515309	0.9741	-1.753	0.43993523	0.99	4.81421666
OSC RAMP PEAK VOLTAGE	v	4.56960067	2.605	2.71171991	2.7698	2.79478333	2.8087	0.0138439	-1.395	2.0106	2.87784675	2.995	4.82081433
OSC RAMP VALLEY VOLTAGE	v	19.4477348	0.71	1.08372706	1.1136	1.12656667	1.1352	0.00713993	-1.235	2.8943	1.16940627	1.24	5.2957223
RAMP VALLEY TO PEAK	v	1.38376329	1.61	1.58407414	1.6431	1.66821667	1.6837	0.01402375	-1.287	2.0608	1.75235919	1.99	7.64853075
CALCULATED CHARGE CURRENT	u a			802.915708	811.084	813.8915	815.861	1.82929875	-0.598	-0.921	824.867292		
CHARGE CURRENT PRE TRIM	u a			715.353336	743.451	749.270833	756.544	5.65291618	0.6113	-1.931	783.18833		
CHARGE CURRENT ERROR THROUGH TRIM	%			-0.0237331	-0.006	-0.0008333	0.004	0.00381663	-0.159	-1.511	0.02206645		
OSC ACTUAL CHARGE CURRENT	u a			715.294705	743.478	749.275833	756.587	5.66352146	0.6151	-1.934	783.256962		
CURRENT MIRROR ERROR	%			-13.420034	-9.328	-8.6285	-7.509	0.79858894	0.901	-1.698	-3.8369663		
OSC CLOCK OUT HIGH LEVEL	v	10.1102369	3.72	3.98838292	4.0413	4.05456667	4.0681	0.01103062	-0.049	-1.929	4.12075041	5.5	43.6794065
OSC CLOCK OUT LOW LEVEL	v	163.120823	-0.05	0.01114105	0.0118	0.0119	0.0121	0.00012649	0.8894	-0.781	0.01265895	0.195	482.510866
ILIMIT FULL SOFTSTART THRESHOLD	v	17.598765	4.31	4.78700707	4.8296	4.84816667	4.8583	0.01019327	-1.411	2.3271	4.90932626	5.5	21.3158161
ILIMIT RESTART THRESHOLD	v	11.235647	0	0.25740777	0.3015	0.31315	0.3257	0.00929037	-0.124	-1.176	0.36889223	0.495	6.52467639
E/A VIO PRETRIM	m v			-4.232403	-1.275	-0.3011667	0.566	0.65520605	-0.148	-0.257	3.63006963		
ERR AMP VIO	m v	3.30617273	-6.8	-4.2325789	-1.2751	-0.3013667	0.566	0.65520204	-0.147	-0.257	3.62984556	6.8	3.61281267
E/A IIBIAS	u a	2.79609052	0.001	0.18869874	0.5012	0.66025	0.6995	0.07859188	-2.361	5.6292	1.13180126	2.95	9.71156355
E/A IIO	n a	29.3944593	-975	129.099232	188.238	209.706717	227.7424	13.4345808	-0.447	0.7454	290.314202	975	18.9881444
E/A AVOL; VO1-4V	db	4.35852146	62	86.4366639	104.3734	107.158683	113.9158	3.45366991	2.0045	4.4205	127.880703	250	13.7864282
E/A CMRR; CMV 1.5-5.5V	db	3.20623	77	85.3312247	95.1254	99.1448833	101.4508	2.30227644	-1.104	1.371	112.958542	250	21.8414427
E/A PSRR; VIN12-20V	db	1.28808698	87	65.7567177	116.4146	125.43615	144.1688	9.94657206	1.6641	3.2164	185.115582	250	4.17443146
E/A OUTPUT HIGH LEVEL; IOUT0.5MA	v	8.69741976	4.505	4.73107289	4.7803	4.79858333	4.8123	0.01125174	-0.776	0.3704	4.86609378	4.995	5.81885279
E/A OUTPUT LOW LEVEL; IOUT1MA	v	42.1613333	0	0.42431926	0.4411	0.44545	0.4512	0.00352179	0.6103	0.6248	0.46658074	0.995	52.0142793
E/A OUTPUT SINK I; VOUT1V	m a	50.9718024	1.01	3.08222086	3.1471	3.16685	3.1836	0.01410486	-0.432	-1.42	3.25147914	9.99	161.248234
E/A OUTPUT SOURCE I; VOUT4V	m a	80.9005876	-10	-1.4750761	-1.3032	-1.2589833	-1.2162	0.03601546	-0.14	-2.262	-1.0428905	-0.65	5.63631342
E/A GAIN BANDWITH PRODUCT	m hz	20.0601487	7	13.7161618	14.2385	14.4599167	14.5819	0.12395915	-1.313	1.7728	15.2036715	29	39.0991277
PWM COMP IIBIAS(IP3) @0V	u a	3.67014392	-7.95	-5.0915189	-2.2018	-1.6684833	-0.5689	0.57050593	1.8511	3.9972	1.75455228	0	0.97485596
OUTPUT 'A' MAX DUTY CYCLE	%	15.1208071	86	91.6639177	92.3899	92.5272667	92.7737	0.1438915	1.1957	0.6527	93.3906156	100	17.3110377
OUTPUT 'B' MAX DUTY CYCLE	%	7.10909128	86	90.3968596	91.6396	92.11805	92.4086	0.28686507	-0.871	0.4355	93.8392404	100	9.1587192
OUTPUT 'A' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
OUTPUT 'B' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
ZER DUTY CYCLE THOLD @P3; VP70V	v	5.27413485	1.11	1.17538997	1.2064	1.21533333	1.2233	0.00665723	-0.382	-1.606	1.25527669	1.4	9.2464263
ILIMIT THOLD TO OUTPUTS	v	3.87049619	0.952	0.97236179	0.9891	0.99413333	0.9996	0.00362859	0.2037	0.0636	1.01590487	1.048	4.94835589

control (unexposed) unit #266 included with 40k unbiased sample data

Units: 6

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
OVER CURRENT THRESHOLD	v	2.82311542	1.142	1.15116479	1.1687	1.17343333	1.1778	0.00371142	-0.151	-2.198	1.19570188	1.258	7.59516842
ILIMIT/SHUTDOWN IIBIAS @1.5V	u a	17.9505118	-14.5	2.43433993	3.8431	4.5577	4.7955	0.35389335	-2.328	5.5741	6.68106007	14.5	9.36468584
OUTPUT A PWM LEADING EDGE BLANKING	n s	7.25761459	303	354.995134	370.4373	374.774117	378.3882	3.29649712	-0.171	-2.334	394.553099	447	7.30329607
OUTPUT B PWM LEADING EDGE BLANKING	n s	5.74677957	303	348.381443	366.1004	372.605683	377.6653	4.03737331	-0.599	0.3341	396.829923	447	6.14213838
LEADING EDGE BLANKING RESISTOR	k ohm	48.5542648	5.02	10.5025976	10.686	10.7381333	10.7896	0.03925596	-0.124	-1.398	10.9736691	14.98	36.0188728
SOFTSTART DISCHARGE I; VPIN82.5V	u a	4.10042248	101	152.884498	192.6863	202.288367	215.7203	8.23397809	0.7717	0.255	251.692235	347	5.85831181
SOFTSTART CHARGE I; VPIN82.5V	u a	17.4742199	-19.9	-13.907601	-13.3482	-13.1331	-12.9939	0.12908349	-0.798	0.5843	-12.358599	-8.1	12.9970143
INITIAL VCC OF 8.5V, +PEAK DETECTOR	v			8.50288262	8.5036	8.50393333	8.5041	0.00017512	-1.763	3.5586	8.50498405		
INITIAL VCC OF 8.5V, -PEAK DETECTOR	v			8.504265	8.5046	8.50471667	8.5048	7.5277E-05	-0.313	-0.104	8.50516833		
UV L.O. TURN ON THOLD (A VERSION)	v	1.00531356	8.42	8.18684105	8.5761	8.65565	8.78	0.07813483	0.6775	-0.454	9.12445895	9.58	3.94339737
UV L.O. STOP THRESHOLD (A VERSION)	v	12.0796422	5.02	7.54938646	7.9405	8.05126667	8.1687	0.0836467	0.0893	-0.979	8.55314688	14.98	27.6111041
UVLO HYST (A VERSION)	v	3.41157641	0.415	0.49335939	0.5819	0.60438333	0.6356	0.01850399	0.8294	1.084	0.71540728	1.185	10.4593054
VSAT 'A' LOW @20MA	m v	4.54608834	10	148.454883	235.733	257.213783	276.6612	18.1264833	-0.342	-2.05	365.972683	390	2.44184553
VSAT 'A' LOW @200MA	v	6.4090906	0.1	0.39575827	0.5026	0.52991667	0.5579	0.02235973	-0.118	-1.892	0.66407507	2.14	24.0027214
VSAT 'B' LOW @20MA	m v	4.55259951	10	147.696861	234.1055	255.584417	275.4099	17.9812593	-0.417	-2.053	363.471972	390	2.49177178
VSAT 'B' LOW @200MA	v	7.67761021	0.1	0.41529912	0.5026	0.52636667	0.5465	0.01851126	-0.549	-1.882	0.63743421	2.19	29.9571455
VHIGH 'A' @20MA	v	105.630883	0.04	2.18228873	2.2112	2.22363333	2.2314	0.00689077	-1.271	2.3709	2.26497793	2.9	32.7185004
VHIGH 'A' @200MA	v	115.604887	0	2.30161371	2.3296	2.34213333	2.3494	0.00675327	-1.498	3.0423	2.38265296	3.02	33.4586842
VHIGH 'B' @20MA	v	130.130086	0.04	2.202003	2.229	2.23575	2.2423	0.0056245	-0.352	-1.781	2.269497	2.9	39.3664624
VHIGH 'B' @200MA	v	177.543091	0	2.33021799	2.3484	2.35676667	2.3608	0.00442478	-1.704	3.3073	2.38331534	3.02	49.9635783
OUTPUT A VSAT @ VIN UVLO-300MV	v	4.45002307	0	0.44701219	0.7781	0.81191667	0.9357	0.06081741	2.4168	5.8825	1.17682114	1.19	2.07223183
OUTPUT B VSAT @ VIN UVLO-300MV	v	45.2342789	0	0.7503396	0.7733	0.78505	0.7879	0.00578507	-2.396	5.7905	0.8197604	1.19	23.3330632
ICC START I @ VIN UVLO-300MV	u a	20.7130901	0	101.200625	109.6655	112.01665	114.3039	1.80267083	0.1522	-1.517	122.832675	270	29.21283
COLLECTOR LEAKAGE @ 20V	u a			-2.4980664	0.1676	1.06891667	1.6889	0.59449717	-0.537	-1.206	4.63589969	148	82.3839544

Units: 5

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
25'C QA				34	34	34	34	0	#DIV/0!	#DIV/0!	34		
ICC @VIN 12V	m a	25.9064204	4	23.4810449	24.6922	25.11082	25.3789	0.27162919	-0.939	0.6835	26.7405951	35.9	13.2400841
VREF @VIN15V; ILOAD 1MA	v	0.85143871	5.055	5.00222848	5.0724	5.09412	5.1075	0.01531525	-0.741	-1.343	5.18601152	5.145	1.10739268
VREF LINE REG; VIN12-20V	m v	11.9502595	-14	-5.0011641	-3.5041	-3.1924	-2.7411	0.30146068	0.8927	-0.088	-1.3836359	14	19.0101079
VREF LOAD REG; IL1-10MA	m v	26.5684874	-19	0.1057358	1.3677	1.66104	2.0162	0.25921737	0.505	-1.196	3.2163442	19	22.2965514
VREF VARIANCE MIN	v	1.25212442	5.035	5.00072777	5.0706	5.09238	5.106	0.01527537	-0.743	-1.224	5.18403223	5.165	1.58468587
VREF VARIANCE MIN	v	1.28567199	5.035	5.00211919	5.0724	5.09418	5.1077	0.01534347	-0.741	-1.321	5.18624081	5.165	1.53854834
VREF ISC; VPIN 160V	m a	0.90577215	-89	-101.54564	-80.87	-78.61506	-71.8182	3.82176318	2.1719	4.7687	-55.684481	-31	4.15297493
OSC ACCURACY PRE TRIM	k hz			382.021643	397.158	400.7082	405.397	3.11442622	0.7639	0.6407	419.394757		
CT DISCAHRGE PRE-TRIM	m a			9.20243908	9.623	9.7598	9.885	0.09289349	-0.31	2.004	10.3171609		
CT DISCHARGE CURRENT	m a	2.65666369	9.02	9.20285097	9.6231	9.75976	9.8849	0.09281817	-0.308	2.0045	10.316669	10.98	4.38218788
OSC FREQ MEASURE - DATA PURPOSE ONLY	k hz	5.27444635	350.8	381.68258	396.9977	400.54536	405.307	3.14379663	0.7999	0.7222	419.40814	449.2	5.15879849
OSC 1MHZ ACCURACY; RT6.6K,CT220PF	k hz	1.67904778	901	885.119083	963.4927	984.08034	1003.0562	16.4935429	-0.256	-2.039	1083.0416	1099	2.32251818
OSC V.STABILITY; VIN12-20V	%	5.54821697	-0.99	-0.3049421	0.0063	0.0812	0.1376	0.06435701	-0.576	-3.188	0.46734207	0.99	4.70707579
OSC RAMP PEAK VOLTAGE	v	5.81416114	2.605	2.73301424	2.7882	2.80014	2.8135	0.01118763	0.3294	-2.642	2.86726576	2.995	5.80581859
OSC RAMP VALLEY VOLTAGE	v	16.2860385	0.71	1.07129925	1.1088	1.12188	1.1312	0.00843012	-0.945	1.1997	1.17246075	1.24	4.67055179
RAMP VALLEY TO PEAK	v	1.40240956	1.61	1.58091326	1.6645	1.67826	1.7048	0.01622446	1.3763	2.0459	1.77560674	1.99	6.40473418
CALCULATED CHARGE CURRENT	u a			801.031584	812.326	815.2228	817.947	2.36520268	-0.298	-2.184	829.414016		
CHARGE CURRENT PRE TRIM	u a			708.247751	740.153	749.6516	757.617	6.90064155	-0.337	-0.871	791.055449		
CHARGE CURRENT ERROR THROUGH TRIM	%			-0.0055331	0.003	0.0052	0.007	0.00178885	-0.052	-2.324	0.01593313		
OSC ACTUAL CHARGE CURRENT	u a			708.144366	740.103	749.6128	757.584	6.9114057	-0.337	-0.881	791.081234		
CURRENT MIRROR ERROR	%			-13.081445	-9.758	-8.7578	-7.968	0.72060752	-0.332	-0.9	-4.4341549		
OSC CLOCK OUT HIGH LEVEL	v	3.51734466	3.72	3.85444677	3.9884	4.03166	4.067	0.02953554	-0.585	0.4014	4.20887323	5.5	16.5714492
OSC CLOCK OUT LOW LEVEL	v	188.599134	-0.05	0.01132273	0.0118	0.01198	0.0121	0.00010954	-1.293	2.9167	0.01263727	0.195	556.912125
ILIMIT FULL SOFTSTART THRESHOLD	v	14.23719	4.31	4.78017618	4.8409	4.85702	4.8745	0.0128073	0.1492	-0.308	4.93386382	5.5	16.7347234
ILIMIT RESTART THRESHOLD	v	13.565757	0	0.26358053	0.3024	0.30916	0.3211	0.00759658	1.1408	0.8693	0.35473947	0.495	8.1545487
E/A VIO PRETRIM	m v			-8.96506	-1.122	0.3594	2.639	1.55407667	0.8644	-0.74	9.68386003		
ERR AMP VIO	m v	1.53580397	-6.8	-8.9639555	-1.1218	0.3595	2.6387	1.55390925	0.8641	-0.741	9.68295552	6.8	1.38156931
E/A IIBIAS	u a	5.34143291	0.001	0.40095105	0.5752	0.64034	0.6737	0.03989816	-1.36	1.9944	0.87972895	2.95	19.2962961
E/A IIO	n a	19.9518435	-975	84.1109657	171.5079	202.10564	222.3994	19.665779	-1.052	0.871	320.100314	975	13.1004957
E/A AVOL; VO1-4V	db	2.50325566	62	71.2802767	101.8746	108.16124	118.3528	6.14682722	1.4214	2.8521	145.042203	250	7.69170582
E/A CMRR; CMV 1.5-5.5V	db	2.65928203	77	82.7955579	97.929	100.37698	105.2885	2.93023702	1.5811	2.7603	117.958402	250	17.0205822
E/A PSRR; VIN12-20V	db	1.04912783	87	58.0742769	110.8395	118.91468	136.4145	10.1400672	1.8604	3.686	179.755083	250	4.30915357
E/A OUTPUT HIGH LEVEL; IOUT0.5MA	v	7.73988751	4.505	4.72033049	4.7774	4.79536	4.8118	0.01250492	-0.292	1.0745	4.87038951	4.995	5.32163915
E/A OUTPUT LOW LEVEL; IOUT1MA	v	8.55145643	0	0.35822319	0.4466	0.46758	0.4917	0.01822614	0.1244	-1.331	0.57693681	0.995	9.64585557
E/A OUTPUT SINK I; VOUT1V	m a	8.20125968	1.01	2.57572879	2.9576	3.0807	3.162	0.08416187	-0.634	-0.342	3.58567121	9.99	27.3651246
E/A OUTPUT SOURCE I; VOUT4V	m a	195.366073	-10	-1.3418519	-1.2701	-1.2523	-1.235	0.01492531	-0.248	-2.29	-1.1627481	-0.65	13.4514199
E/A GAIN BANDWITH PRODUCT	m hz	6.88845178	7	12.0895892	13.6157	14.17188	14.5282	0.34704847	-1.212	1.7097	16.2541708	29	14.242122
PWM COMP IIBIAS(IP3) @0V	u a	11.0288251	-7.95	-3.0353563	-2.0805	-1.9467	-1.6883	0.18144272	0.8979	-1.629	-0.8580437	0	3.57633531
OUTPUT 'A' MAX DUTY CYCLE	%	5.80859107	86	90.2319055	92.0475	92.4542	93.0432	0.37038242	1.0916	1.851	94.6764945	100	6.79099911
OUTPUT 'B' MAX DUTY CYCLE	%	5.27312662	86	89.8080204	91.501	92.13486	92.5005	0.38780661	-1.387	2.0435	94.4616996	100	6.76036276
OUTPUT 'A' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
OUTPUT 'B' MIN DUTY CYCLE	%	Infinite	-0.001	0	0	0	0	0	#DIV/0!	#DIV/0!	0	0.001	Infinite
ZER DUTY CYCLE THOLD @P3; VP70V	v	4.57441676	1.11	1.17203025	1.2111	1.22022	1.2332	0.00803162	1.1397	2.611	1.26840975	1.4	7.46133774
ILIMIT THOLD TO OUTPUTS	v	2.35621322	0.952	0.95795348	0.9843	0.99138	0.999	0.00557109	0.2437	-0.218	1.02480652	1.048	3.38772963

Units: 5

Parameter		Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma	Upper Limit	Cpk (UL)
OVER CURRENT THRESHOLD	v	1.84601173	1.142	1.13940073	1.1665	1.17316	1.1811	0.00562654	0.4386	-0.36	1.20691927	1.258	5.02617572
ILIMIT/SHUTDOWN IIBIAS @1.5V	u a	58.760241	-14.5	3.98670376	4.5228	4.6381	4.8027	0.10856604	0.9078	0.3436	5.28949624	14.5	30.279266
OUTPUT A PWM LEADING EDGE BLANKING	n s	2.31056757	303	311.170558	349.476	363.7875	371.1601	8.76949036	-1.423	1.7733	416.404442	447	3.16295461
OUTPUT B PWM LEADING EDGE BLANKING	n s	2.59489214	303	316.803277	350.1988	363.20926	369.7145	7.73433046	-1.641	2.7826	409.615243	447	3.6112042
LEADING EDGE BLANKING RESISTOR	k ohm	17.5368477	5.02	10.082503	10.6404	10.73418	10.9075	0.10861283	1.1802	1.4157	11.385857	14.98	13.0304433
SOFTSTART DISCHARGE I; VPIN82.5V	u a	6.11151257	101	167.338895	193.1835	199.60872	207.089	5.37830413	0.3526	-0.507	231.878545	347	9.13492905
SOFTSTART CHARGE I; VPIN82.5V	u a	6.19346063	-19.9	-15.45858	-13.9574	-13.34032	-13.1003	0.35304334	-1.997	4.0793	-11.22206	-8.1	4.94775897
INITIAL VCC OF 8.5V, +PEAK DETECTOR	v			8.50305005	8.5038	8.50396	8.5042	0.00015166	1.1181	1.4556	8.50486995		
INITIAL VCC OF 8.5V, -PEAK DETECTOR	v			8.50402273	8.5045	8.50468	8.5048	0.00010954	-1.293	2.9167	8.50533727		
UV L.O. TURN ON THOLD (A VERSION)	v	1.41382837	8.42	8.27855545	8.6533	8.76116	8.8681	0.08043409	-0.068	0.0635	9.24376455	9.58	3.39342017
UV L.O. STOP THRESHOLD (A VERSION)	v	13.3178799	5.02	7.68835149	8.0494	8.15988	8.2616	0.07858808	-0.249	0.4632	8.63140851	14.98	28.9277103
UVLO HYST (A VERSION)	v	7.36687924	0.415	0.55069313	0.5903	0.60126	0.6107	0.00842781	-0.397	-1.853	0.65182687	1.185	23.0878454
VSAT 'A' LOW @20MA	m v	5.91014543	10	195.433032	263.9241	290.28016	302.0332	15.8078547	-1.616	2.29	385.127288	390	2.10274875
VSAT 'A' LOW @200MA	v	9.69574414	0.1	0.46327157	0.5337	0.55768	0.5709	0.01573474	-1.074	-0.147	0.65208843	2.14	33.5207347
VSAT 'B' LOW @20MA	m v	5.65607624	10	190.148854	261.6201	288.6965	302.2489	16.4246077	-1.497	2.0686	387.244146	390	2.05592937
VSAT 'B' LOW @200MA	v	9.16311587	0.1	0.45740862	0.5318	0.5572	0.5721	0.0166319	-1.085	-0.074	0.65699138	2.19	32.7242686
VHIGH 'A' @20MA	v	270.682952	0.04	2.20885565	2.2215	2.225	2.2276	0.00269072	-0.353	-2.089	2.24114435	2.9	83.6205915
VHIGH 'A' @200MA	v	296.949121	0	2.32851076	2.3412	2.3443	2.3478	0.00263154	0.3581	-1.297	2.36008924	3.02	85.5899505
VHIGH 'B' @20MA	v	236.480523	0.04	2.22044194	2.2337	2.23904	2.2413	0.00309968	-1.842	3.6104	2.25763806	2.9	71.0783644
VHIGH 'B' @200MA	v	254.381428	0	2.33944107	2.3527	2.35798	2.3607	0.00308982	-1.743	3.4752	2.37651893	3.02	71.4194323
OUTPUT A VSAT @ VIN UVLO-300MV	v	91.264647	0	0.77118146	0.7843	0.78846	0.7909	0.00287976	-0.911	-1.306	0.80573854	1.19	46.4784597
OUTPUT B VSAT @ VIN UVLO-300MV	v	93.8305924	0	0.77024466	0.7841	0.78702	0.7906	0.00279589	0.082	-1.932	0.80379534	1.19	48.0443345
ICC START I @ VIN UVLO-300MV	u a	24.413762	0	103.015091	110.6845	112.20722	114.7803	1.53202143	1.5405	3.1896	121.399349	270	34.3321524
COLLECTOR LEAKAGE @ 20V	u a			-3.2694828	-0.4345	0.696	1.2938	0.66091381	-1.741	3.5752	4.66148284	148	74.2930967

Units: 1

Parameter	Scale Factor			Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma
25'C QA		1				#DIV/0!	34	34	34	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
ICC @VIN 12V	m	1000	a	#DIV/0!	4	#DIV/0!	25.0601	25.0601	25.0601	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
VREF @VIN15V; ILOAD 1MA		1	v	#DIV/0!	5.055	#DIV/0!	5.0846	5.0846	5.0846	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
VREF LINE REG; VIN12-20V	m	1000	v	#DIV/0!	-14	#DIV/0!	-1.7549	-1.7549	-1.7549	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
VREF LOAD REG; IL1-10MA	m	1000	v	#DIV/0!	-19	#DIV/0!	1.5203	1.5203	1.5203	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
VREF VARIANCE MIN		1	v	#DIV/0!	5.035	#DIV/0!	5.0831	5.0831	5.0831	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
VREF VARIANCE MIN		1	v	#DIV/0!	5.035	#DIV/0!	5.0846	5.0846	5.0846	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
VREF ISC; VPIN 160V	m	1000	a	#DIV/0!	-89	#DIV/0!	-71.8361	-71.8361	-71.8361	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OSC ACCURACY PRE TRIM	k	0.001	hz			#DIV/0!	396.739	396.739	396.739	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
CT DISCAHRGE PRE-TRIM	m	1000	a			#DIV/0!	9.817	9.817	9.817	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
CT DISCHARGE CURRENT	m	1000	a	#DIV/0!	9.02	#DIV/0!	9.8171	9.8171	9.8171	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OSC FREQ MEASURE - DATA PURPOSE ONLY	k	0.001	hz	#DIV/0!	350.8	#DIV/0!	396.6409	396.6409	396.6409	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OSC 1MHZ ACCURACY; RT6.6K,CT220PF	k	0.001	hz	#DIV/0!	901	#DIV/0!	946.6056	946.6056	946.6056	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OSC V.STABILITY; VIN12-20V		1	%	#DIV/0!	-0.99	#DIV/0!	0	0	0	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OSC RAMP PEAK VOLTAGE		1	v	#DIV/0!	2.605	#DIV/0!	2.8014	2.8014	2.8014	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OSC RAMP VALLEY VOLTAGE		1	v	#DIV/0!	0.71	#DIV/0!	1.1299	1.1299	1.1299	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
RAMP VALLEY TO PEAK		1	v	#DIV/0!	1.61	#DIV/0!	1.6715	1.6715	1.6715	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
CALCULATED CHARGE CURRENT	u	1000000	a			#DIV/0!	815.336	815.336	815.336	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
CHARGE CURRENT PRE TRIM	u	1000000	a			#DIV/0!	745.79	745.79	745.79	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
CHARGE CURRENT ERROR THROUGH TRIM		1	%			#DIV/0!	0.002	0.002	0.002	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OSC ACTUAL CHARGE CURRENT	u	1000000	a			#DIV/0!	745.773	745.773	745.773	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
CURRENT MIRROR ERROR		1	%			#DIV/0!	-9.328	-9.328	-9.328	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OSC CLOCK OUT HIGH LEVEL		1	v	#DIV/0!	3.72	#DIV/0!	4.0581	4.0581	4.0581	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OSC CLOCK OUT LOW LEVEL		1	v	#DIV/0!	-0.05	#DIV/0!	0.0119	0.0119	0.0119	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
ILIMIT FULL SOFTSTART THRESHOLD		1	v	#DIV/0!	4.31	#DIV/0!	4.8451	4.8451	4.8451	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
ILIMIT RESTART THRESHOLD		1	v	#DIV/0!	0	#DIV/0!	0.3257	0.3257	0.3257	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
E/A VIO PRETRIM	m	1000	v			#DIV/0!	-0.414	-0.414	-0.414	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
ERR AMP VIO	m	1000	v	#DIV/0!	-6.8	#DIV/0!	-0.4141	-0.4141	-0.4141	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
E/A IIBIAS	u	1000000	a	#DIV/0!	0.001	#DIV/0!	0.5012	0.5012	0.5012	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
E/A IIO	n	1000000000	a	#DIV/0!	-975	#DIV/0!	209.7309	209.7309	209.7309	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
E/A AVOL; VO1-4V		1	db	#DIV/0!	62	#DIV/0!	104.9726	104.9726	104.9726	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
E/A CMRR; CMV 1.5-5.5V		1	db	#DIV/0!	77	#DIV/0!	99.7644	99.7644	99.7644	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
E/A PSRR; VIN12-20V		1	db	#DIV/0!	87	#DIV/0!	118.3528	118.3528	118.3528	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
E/A OUTPUT HIGH LEVEL; IOUT0.5MA		1	v	#DIV/0!	4.505	#DIV/0!	4.8039	4.8039	4.8039	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
E/A OUTPUT LOW LEVEL; IOUT1MA		1	v	#DIV/0!	0	#DIV/0!	0.4466	0.4466	0.4466	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
E/A OUTPUT SINK I; VOUT1V	m	1000	a	#DIV/0!	1.01	#DIV/0!	3.1536	3.1536	3.1536	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
E/A OUTPUT SOURCE I; VOUT4V	m	1000	a	#DIV/0!	-10	#DIV/0!	-1.3032	-1.3032	-1.3032	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
E/A GAIN BANDWITH PRODUCT	m	1000	hz	#DIV/0!	7	#DIV/0!	14.5522	14.5522	14.5522	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
PWM COMP IIBIAS(IP3) @0V	u	1000000	a	#DIV/0!	-7.95	#DIV/0!	-0.5689	-0.5689	-0.5689	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OUTPUT 'A' MAX DUTY CYCLE		1	%	#DIV/0!	86	#DIV/0!	92.7737	92.7737	92.7737	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OUTPUT 'B' MAX DUTY CYCLE		1	%	#DIV/0!	86	#DIV/0!	92.3801	92.3801	92.3801	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OUTPUT 'A' MIN DUTY CYCLE		1	%	#DIV/0!	-0.001	#DIV/0!	0	0	0	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OUTPUT 'B' MIN DUTY CYCLE		1	%	#DIV/0!	-0.001	#DIV/0!	0	0	0	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
ZER DUTY CYCLE THOLD @P3; VP70V		1	v	#DIV/0!	1.11	#DIV/0!	1.2188	1.2188	1.2188	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
ILIMIT THOLD TO OUTPUTS		1	v	#DIV/0!	0.952	#DIV/0!	0.9924	0.9924	0.9924	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!

Units: 1

Parameter	Scale Factor			Cpk (LL)	Lower Limit	-6 Sigma	Min	Ave	Max	Sigma	Skew	Kurt	+6 Sigma
OVER CURRENT THRESHOLD		1	v	#DIV/0!	1.142	#DIV/0!	1.1701	1.1701	1.1701	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
ILIMIT/SHUTDOWN IIBIAS @1.5V	u	1000000	a	#DIV/0!	-14.5	#DIV/0!	3.8431	3.8431	3.8431	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OUTPUT A PWM LEADING EDGE BLANKING	n	1000000000	s	#DIV/0!	303	#DIV/0!	372.6057	372.6057	372.6057	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OUTPUT B PWM LEADING EDGE BLANKING	n	1000000000	s	#DIV/0!	303	#DIV/0!	371.1601	371.1601	371.1601	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
LEADING EDGE BLANKING RESISTOR	k	0.001	ohm	#DIV/0!	5.02	#DIV/0!	10.7896	10.7896	10.7896	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
SOFTSTART DISCHARGE I; VPIN82.5V	u	1000000	a	#DIV/0!	101	#DIV/0!	215.7203	215.7203	215.7203	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
SOFTSTART CHARGE I; VPIN82.5V	u	1000000	a	#DIV/0!	-19.9	#DIV/0!	-13.3482	-13.3482	-13.3482	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
INITIAL VCC OF 8.5V, +PEAK DETECTOR		1	v			#DIV/0!	8.504	8.504	8.504	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
INITIAL VCC OF 8.5V, -PEAK DETECTOR		1	v			#DIV/0!	8.5048	8.5048	8.5048	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
UV L.O. TURN ON THOLD (A VERSION)		1	v	#DIV/0!	8.42	#DIV/0!	8.5761	8.5761	8.5761	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
UV L.O. STOP THRESHOLD (A VERSION)		1	v	#DIV/0!	5.02	#DIV/0!	7.9405	7.9405	7.9405	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
UVLO HYST (A VERSION)		1	v	#DIV/0!	0.415	#DIV/0!	0.6356	0.6356	0.6356	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
VSAT 'A' LOW @20MA	m	1000	v	#DIV/0!	10	#DIV/0!	274.2332	274.2332	274.2332	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
VSAT 'A' LOW @200MA		1	v	#DIV/0!	0.1	#DIV/0!	0.5579	0.5579	0.5579	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
VSAT 'B' LOW @20MA	m	1000	v	#DIV/0!	10	#DIV/0!	269.5452	269.5452	269.5452	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
VSAT 'B' LOW @200MA		1	v	#DIV/0!	0.1	#DIV/0!	0.5388	0.5388	0.5388	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
VHIGH 'A' @20MA		1	v	#DIV/0!	0.04	#DIV/0!	2.2112	2.2112	2.2112	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
VHIGH 'A' @200MA		1	v	#DIV/0!	0	#DIV/0!	2.3296	2.3296	2.3296	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
VHIGH 'B' @20MA		1	v	#DIV/0!	0.04	#DIV/0!	2.2291	2.2291	2.2291	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
VHIGH 'B' @200MA		1	v	#DIV/0!	0	#DIV/0!	2.3608	2.3608	2.3608	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OUTPUT A VSAT @ VIN UVLO-300MV		1	v	#DIV/0!	0	#DIV/0!	0.7781	0.7781	0.7781	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
OUTPUT B VSAT @ VIN UVLO-300MV		1	v	#DIV/0!	0	#DIV/0!	0.7733	0.7733	0.7733	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
ICC START I @ VIN UVLO-300MV	u	1000000	a	#DIV/0!	0	#DIV/0!	111.1152	111.1152	111.1152	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!
COLLECTOR LEAKAGE @ 20V	u	1000000	a			#DIV/0!	1.5196	1.5196	1.5196	#DIV/0!	#DIV/0!	#DIV/0!	#DIV/0!

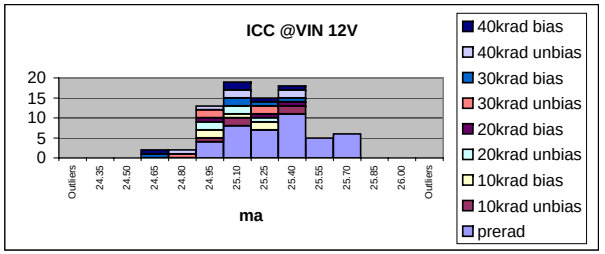
Upper Limit	Cpk (UL)
35.9	#DIV/0!
5.145	#DIV/0!
14	#DIV/0!
19	#DIV/0!
5.165	#DIV/0!
5.165	#DIV/0!
-31	#DIV/0!
10.98	#DIV/0!
449.2	#DIV/0!
1099	#DIV/0!
0.99	#DIV/0!
2.995	#DIV/0!
1.24	#DIV/0!
1.99	#DIV/0!
5.5	#DIV/0!
0.195	#DIV/0!
5.5	#DIV/0!
0.495	#DIV/0!
6.8	#DIV/0!
2.95	#DIV/0!
975	#DIV/0!
250	#DIV/0!
250	#DIV/0!
250	#DIV/0!
4.995	#DIV/0!
0.995	#DIV/0!
9.99	#DIV/0!
-0.65	#DIV/0!
29	#DIV/0!
0	#DIV/0!
100	#DIV/0!
100	#DIV/0!
0.001	#DIV/0!
0.001	#DIV/0!
1.4	#DIV/0!
1.048	#DIV/0!

Upper Limit	Cpk (UL)
1.258	#DIV/0!
14.5	#DIV/0!
447	#DIV/0!
447	#DIV/0!
14.98	#DIV/0!
347	#DIV/0!
-8.1	#DIV/0!
9.58	#DIV/0!
14.98	#DIV/0!
1.185	#DIV/0!
390	#DIV/0!
2.14	#DIV/0!
390	#DIV/0!
2.19	#DIV/0!
2.9	#DIV/0!
3.02	#DIV/0!
2.9	#DIV/0!
3.02	#DIV/0!
1.19	#DIV/0!
1.19	#DIV/0!
270	#DIV/0!
148	#DIV/0!

control (unexposed) unit #266 included with 40k unbiased sample data

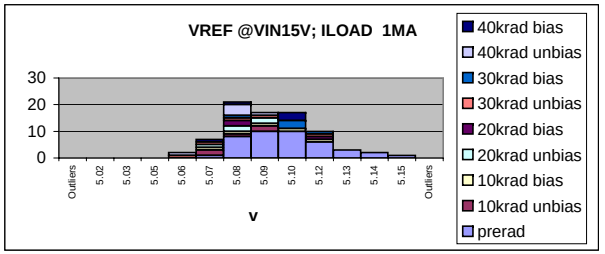
ICC @VIN 12V

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
24.35	0	0	0	0	0	0	0	0	0
24.50	0	0	0	0	0	0	0	0	0
24.65	0	0	0	0	0	0	1	0	1
24.80	0	0	0	0	0	1	0	1	0
24.95	4	1	2	2	1	2	0	1	0
25.10	8	2	1	2	0	0	2	2	2
25.25	7	0	2	1	1	2	1	0	1
25.40	11	2	0	0	1	0	1	2	1
25.55	5	0	0	0	0	0	0	0	0
25.70	6	0	0	0	0	0	0	0	0
25.85	0	0	0	0	0	0	0	0	0
26.00	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



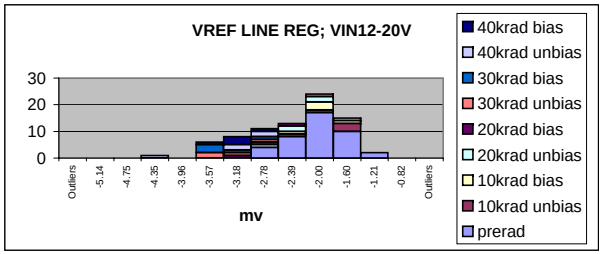
VREF @VIN15V; ILOAD 1MA

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
5.02	0	0	0	0	0	0	0	0	0
5.03	0	0	0	0	0	0	0	0	0
5.05	0	0	0	0	0	0	0	0	0
5.06	0	0	0	0	0	1	0	1	0
5.07	1	2	1	1	0	1	0	0	1
5.08	8	1	1	2	2	1	1	4	1
5.09	10	2	1	2	0	1	0	1	0
5.10	10	0	1	0	0	0	3	0	3
5.12	6	0	1	0	1	1	1	0	0
5.13	3	0	0	0	0	0	0	0	0
5.14	2	0	0	0	0	0	0	0	0
5.15	1	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



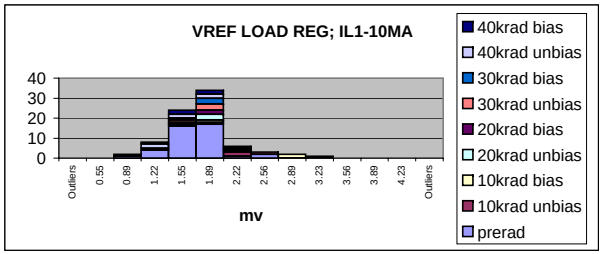
VREF LINE REG; VIN12-20V

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
-5.14	0	0	0	0	0	0	0	0	0
-4.75	0	0	0	0	0	0	0	0	0
-4.35	0	0	0	0	0	0	0	1	0
-3.96	0	0	0	0	0	0	0	0	0
-3.57	0	0	0	0	0	2	3	0	1
-3.18	0	0	0	0	1	1	1	2	3
-2.78	4	0	0	1	1	1	1	2	1
-2.39	8	1	1	2	1	0	0	0	0
-2.00	17	1	3	2	0	1	0	0	0
-1.60	10	3	1	0	0	0	0	1	0
-1.21	2	0	0	0	0	0	0	0	0
-0.82	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



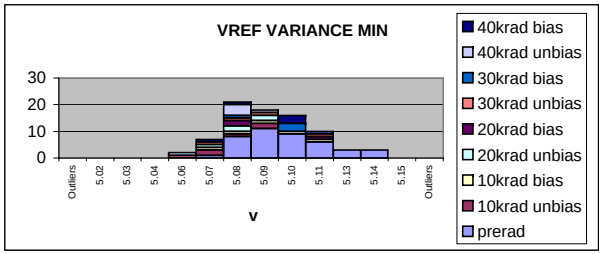
VREF LOAD REG; IL1-10MA

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
0.55	0	0	0	0	0	0	0	0	0
0.89	1	0	0	0	0	1	0	0	0
1.22	4	1	0	0	0	0	0	2	1
1.55	16	0	0	1	1	1	1	2	2
1.89	17	1	1	3	2	3	3	2	2
2.22	1	2	1	1	0	0	1	0	0
2.56	2	1	0	0	0	0	0	0	0
2.89	0	0	2	0	0	0	0	0	0
3.23	0	0	1	0	0	0	0	0	0
3.56	0	0	0	0	0	0	0	0	0
3.89	0	0	0	0	0	0	0	0	0
4.23	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



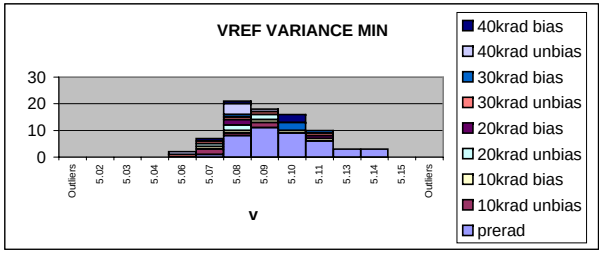
VREF VARIANCE MIN

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
5.02	0	0	0	0	0	0	0	0	0
5.03	0	0	0	0	0	0	0	0	0
5.04	0	0	0	0	0	0	0	0	0
5.06	0	0	0	0	0	1	0	1	0
5.07	1	2	1	1	0	1	0	0	1
5.08	8	1	1	2	2	1	1	4	1
5.09	11	2	1	2	0	1	0	1	0
5.10	9	0	1	0	0	0	3	0	3
5.11	6	0	1	0	1	1	1	0	0
5.13	3	0	0	0	0	0	0	0	0
5.14	3	0	0	0	0	0	0	0	0
5.15	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



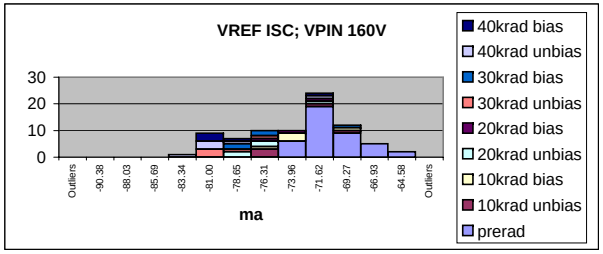
VREF VARIANCE MIN

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
5.02	0	0	0	0	0	0	0	0	0
5.03	0	0	0	0	0	0	0	0	0
5.04	0	0	0	0	0	0	0	0	0
5.06	0	0	0	0	0	1	0	1	0
5.07	1	2	1	1	0	1	0	0	1
5.08	8	1	1	2	2	1	1	4	1
5.09	11	2	1	2	0	1	0	1	0
5.10	9	0	1	0	0	0	3	0	3
5.11	6	0	1	0	1	1	1	0	0
5.13	3	0	0	0	0	0	0	0	0
5.14	3	0	0	0	0	0	0	0	0
5.15	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



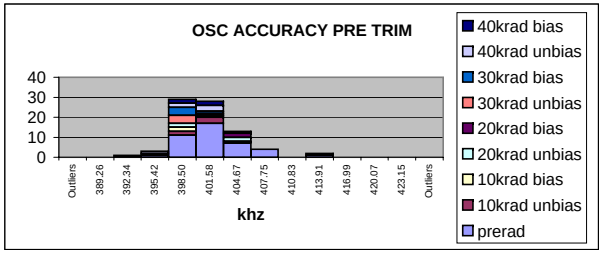
VREF ISC; VPIN 160V

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
-90.38	0	0	0	0	0	0	0	0	0
-88.03	0	0	0	0	0	0	0	0	0
-85.69	0	0	0	0	0	0	0	0	0
-83.34	0	0	0	0	0	0	0	1	0
-81.00	0	0	0	0	0	3	0	3	3
-78.65	0	0	0	2	0	1	2	1	1
-76.31	0	3	1	2	1	1	2	0	0
-73.96	6	0	3	0	1	0	0	0	0
-71.62	19	1	0	1	1	0	0	1	1
-69.27	9	1	1	0	0	0	1	0	0
-66.93	5	0	0	0	0	0	0	0	0
-64.58	2	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



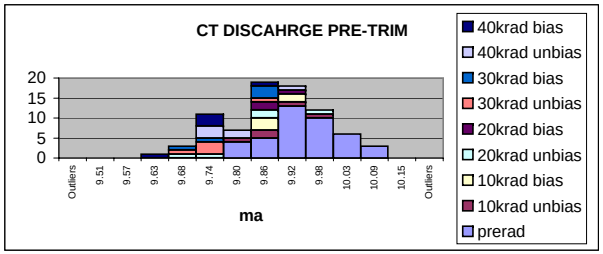
OSC ACCURACY PRE TRIM

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
389.26	0	0	0	0	0	0	0	0	0
392.34	0	0	1	0	0	0	0	0	0
395.42	1	0	0	0	0	1	0	1	0
398.50	11	2	2	2	0	4	4	2	2
401.58	17	3	1	1	0	0	1	3	2
404.67	7	0	1	2	2	0	0	0	1
407.75	4	0	0	0	0	0	0	0	0
410.83	0	0	0	0	0	0	0	0	0
413.91	1	0	0	0	1	0	0	0	0
416.99	0	0	0	0	0	0	0	0	0
420.07	0	0	0	0	0	0	0	0	0
423.15	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



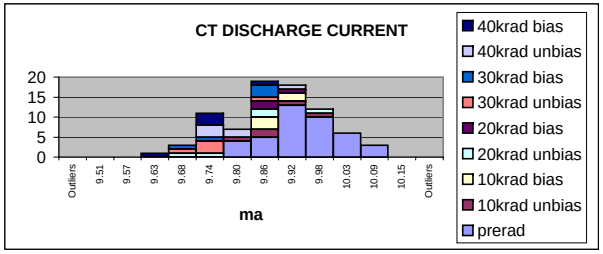
CT DISCAHRGE PRE-TRIM

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
9.51	0	0	0	0	0	0	0	0	0
9.57	0	0	0	0	0	0	0	0	0
9.63	0	0	0	0	0	0	0	0	1
9.68	0	0	0	1	0	1	1	0	0
9.74	0	0	0	1	0	3	1	3	3
9.80	4	1	0	0	0	0	0	2	0
9.86	5	2	3	2	2	1	3	0	1
9.92	13	1	2	0	1	0	0	1	0
9.98	10	1	0	1	0	0	0	0	0
10.03	6	0	0	0	0	0	0	0	0
10.09	3	0	0	0	0	0	0	0	0
10.15	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



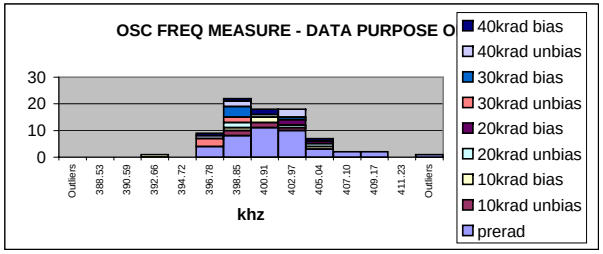
CT DISCHARGE CURRENT

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
9.51	0	0	0	0	0	0	0	0	0
9.57	0	0	0	0	0	0	0	0	0
9.63	0	0	0	0	0	0	0	0	1
9.68	0	0	0	1	0	1	1	0	0
9.74	0	0	0	1	0	3	1	3	3
9.80	4	1	0	0	0	0	0	2	0
9.86	5	2	3	2	2	1	3	0	1
9.92	13	1	2	0	1	0	0	1	0
9.98	10	1	0	1	0	0	0	0	0
10.03	6	0	0	0	0	0	0	0	0
10.09	3	0	0	0	0	0	0	0	0
10.15	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



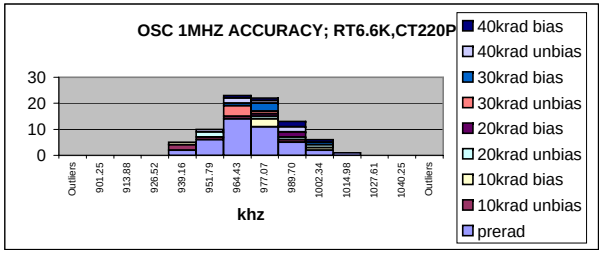
OSC FREQ MEASURE - DATA PURPOSE ONLY

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
388.53	0	0	0	0	0	0	0	0	0
390.59	0	0	0	0	0	0	0	0	0
392.66	0	0	1	0	0	0	0	0	0
394.72	0	0	0	0	0	0	0	0	0
396.78	4	0	0	0	0	3	0	1	1
398.85	8	2	1	2	0	2	4	2	1
400.91	11	2	2	1	0	0	0	0	2
402.97	10	1	0	1	2	0	1	3	0
405.04	3	0	1	1	1	0	0	0	1
407.10	2	0	0	0	0	0	0	0	0
409.17	2	0	0	0	0	0	0	0	0
411.23	0	0	0	0	0	0	0	0	0
Outliers	1	0	0	0	0	0	0	0	0



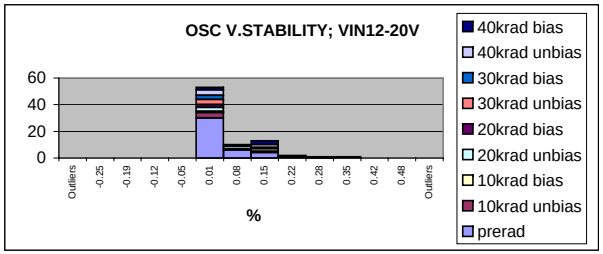
OSC 1MHZ ACCURACY; RT6.6K,CT220PF

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
901.25	0	0	0	0	0	0	0	0	0
913.88	0	0	0	0	0	0	0	0	0
926.52	0	0	0	0	0	0	0	0	0
939.16	2	2	1	0	0	0	0	0	0
951.79	6	1	0	2	0	0	0	1	0
964.43	14	1	0	0	0	4	1	2	1
977.07	11	0	3	1	1	1	3	1	1
989.70	5	1	0	1	2	0	0	2	2
1002.34	2	0	1	1	0	0	1	0	1
1014.98	1	0	0	0	0	0	0	0	0
1027.61	0	0	0	0	0	0	0	0	0
1040.25	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



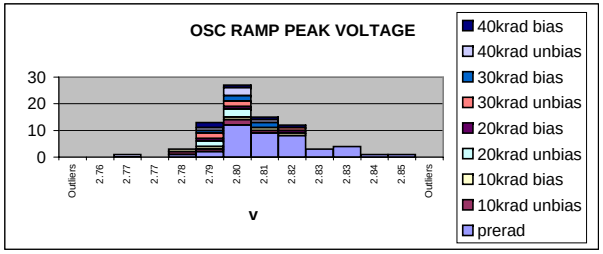
OSC V.STABILITY; VIN12-20V

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
-0.25	0	0	0	0	0	0	0	0	0
-0.19	0	0	0	0	0	0	0	0	0
-0.12	0	0	0	0	0	0	0	0	0
-0.05	0	0	0	0	0	0	0	0	0
0.01	30	4	1	3	2	4	3	4	2
0.08	6	0	1	2	0	1	0	0	0
0.15	4	1	2	0	0	0	1	2	3
0.22	0	0	1	0	1	0	0	0	0
0.28	1	0	0	0	0	0	0	0	0
0.35	0	0	0	0	0	0	1	0	0
0.42	0	0	0	0	0	0	0	0	0
0.48	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



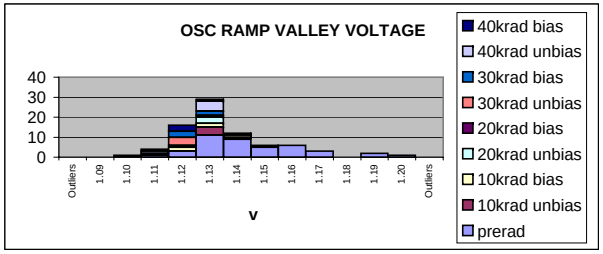
OSC RAMP PEAK VOLTAGE

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
2.76	0	0	0	0	0	0	0	0	0
2.77	0	0	0	0	0	0	0	1	0
2.77	0	0	0	0	0	0	0	0	0
2.78	1	1	1	0	0	0	0	0	0
2.79	2	1	1	2	1	2	1	1	2
2.80	12	2	1	3	1	2	2	3	1
2.81	9	1	1	0	0	0	2	1	1
2.82	8	0	1	0	1	1	0	0	1
2.83	3	0	0	0	0	0	0	0	0
2.83	4	0	0	0	0	0	0	0	0
2.84	1	0	0	0	0	0	0	0	0
2.85	1	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



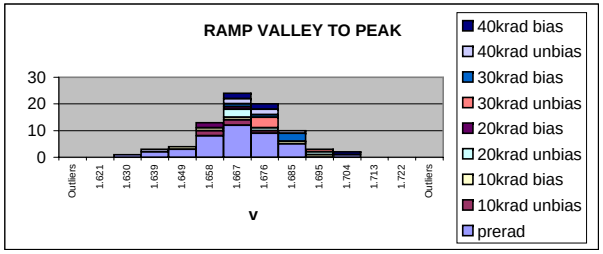
OSC RAMP VALLEY VOLTAGE

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
1.09	0	0	0	0	0	0	0	0	0
1.10	0	0	0	1	0	0	0	0	0
1.11	1	0	0	0	0	1	0	1	1
1.12	3	0	2	1	0	4	3	0	3
1.13	11	4	2	3	1	0	2	5	1
1.14	9	1	1	0	1	0	0	0	0
1.15	5	0	0	0	1	0	0	0	0
1.16	6	0	0	0	0	0	0	0	0
1.17	3	0	0	0	0	0	0	0	0
1.18	0	0	0	0	0	0	0	0	0
1.19	2	0	0	0	0	0	0	0	0
1.20	1	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



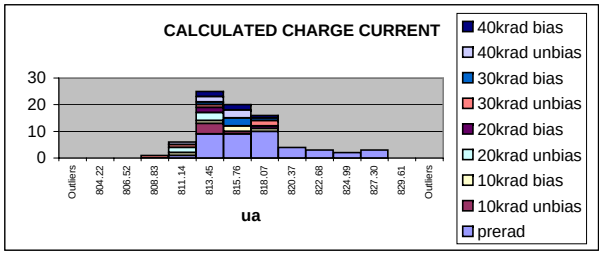
RAMP VALLEY TO PEAK

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
1.621	0	0	0	0	0	0	0	0	0
1.630	1	0	0	0	0	0	0	0	0
1.639	2	0	0	0	0	0	0	1	0
1.649	3	0	1	0	0	0	0	0	0
1.658	8	2	1	0	2	0	0	0	0
1.667	12	2	1	3	1	0	1	2	2
1.676	9	1	0	1	0	4	1	2	2
1.685	5	0	1	0	0	0	3	1	0
1.695	0	0	1	1	0	1	0	0	0
1.704	1	0	0	0	0	0	0	0	1
1.713	0	0	0	0	0	0	0	0	0
1.722	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



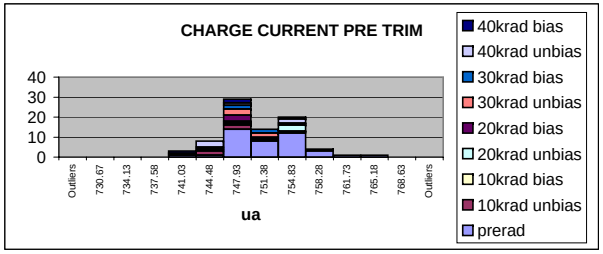
CALCULATED CHARGE CURRENT

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
804.22	0	0	0	0	0	0	0	0	0
806.52	0	0	0	0	0	0	0	0	0
808.83	0	0	0	0	0	1	0	0	0
811.14	1	0	1	2	0	1	0	1	0
813.45	9	4	1	3	2	1	1	2	2
815.76	9	1	2	0	0	0	3	3	2
818.07	10	0	1	0	1	2	1	0	1
820.37	4	0	0	0	0	0	0	0	0
822.68	3	0	0	0	0	0	0	0	0
824.99	2	0	0	0	0	0	0	0	0
827.30	3	0	0	0	0	0	0	0	0
829.61	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



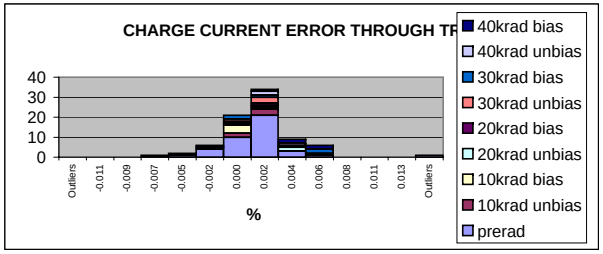
CHARGE CURRENT PRE TRIM

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
730.67	0	0	0	0	0	0	0	0	0
734.13	0	0	0	0	0	0	0	0	0
737.58	0	0	0	0	0	0	0	0	0
741.03	1	0	1	0	0	0	0	0	1
744.48	1	2	1	1	0	0	0	3	0
747.93	14	2	1	1	3	3	2	1	2
751.38	8	1	1	0	0	2	2	0	0
754.83	12	0	1	3	0	0	1	2	1
758.28	3	0	0	0	0	0	0	0	1
761.73	1	0	0	0	0	0	0	0	0
765.18	1	0	0	0	0	0	0	0	0
768.63	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



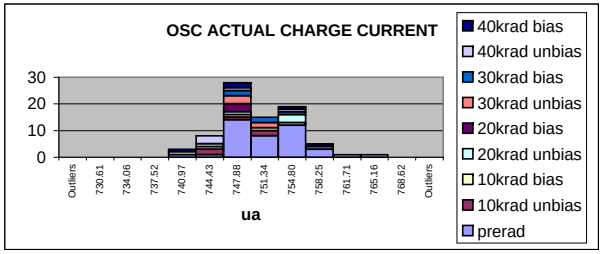
CHARGE CURRENT ERROR THROUGH TRIM

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
-0.011	0	0	0	0	0	0	0	0	0
-0.009	0	0	0	0	0	0	0	0	0
-0.007	0	0	0	0	0	0	0	1	0
-0.005	1	0	0	0	0	0	0	1	0
-0.002	4	0	0	0	1	0	0	1	0
0.000	10	2	4	1	1	1	2	0	0
0.002	21	3	1	1	1	3	1	2	1
0.004	3	0	0	2	0	1	0	1	2
0.006	1	0	0	1	0	0	2	0	2
0.008	0	0	0	0	0	0	0	0	0
0.011	0	0	0	0	0	0	0	0	0
0.013	0	0	0	0	0	0	0	0	0
Outliers	1	0	0	0	0	0	0	0	0



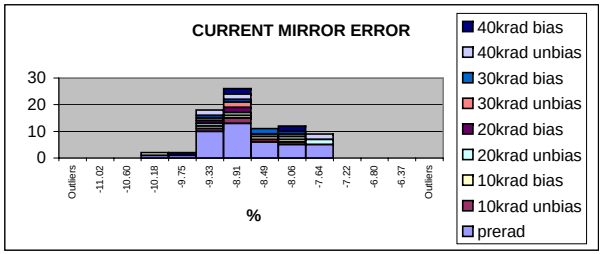
OSC ACTUAL CHARGE CURRENT

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
730.61	0	0	0	0	0	0	0	0	0
734.06	0	0	0	0	0	0	0	0	0
737.52	0	0	0	0	0	0	0	0	0
740.97	1	0	1	0	0	0	0	0	1
744.43	1	2	1	1	0	0	0	3	0
747.88	14	1	1	1	3	3	2	1	2
751.34	8	2	1	0	0	2	2	0	0
754.80	12	0	1	3	0	0	1	1	1
758.25	3	0	0	0	0	0	0	1	1
761.71	1	0	0	0	0	0	0	0	0
765.16	1	0	0	0	0	0	0	0	0
768.62	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



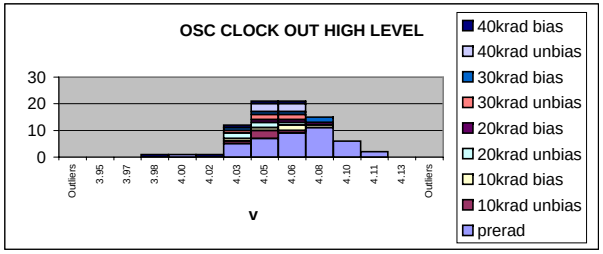
CURRENT MIRROR ERROR

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
-11.02	0	0	0	0	0	0	0	0	0
-10.60	0	0	0	0	0	0	0	0	0
-10.18	1	0	1	0	0	0	0	0	0
-9.75	1	0	0	0	0	0	0	0	1
-9.33	10	1	1	1	1	1	1	2	0
-8.91	13	2	1	1	2	2	1	2	2
-8.49	6	1	1	0	0	1	2	0	0
-8.06	5	1	1	1	0	1	1	0	2
-7.64	5	0	0	2	0	0	0	2	0
-7.22	0	0	0	0	0	0	0	0	0
-6.80	0	0	0	0	0	0	0	0	0
-6.37	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



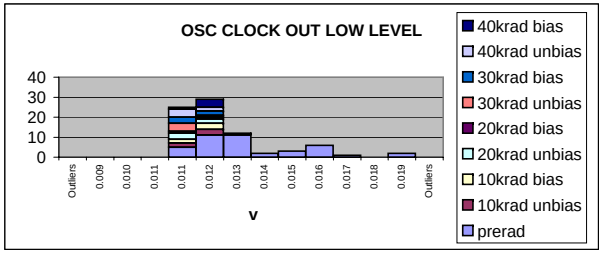
OSC CLOCK OUT HIGH LEVEL

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
3.95	0	0	0	0	0	0	0	0	0
3.97	0	0	0	0	0	0	0	0	0
3.98	0	0	0	0	0	0	0	0	1
4.00	1	0	0	0	0	0	0	0	0
4.02	0	0	0	0	0	0	0	0	1
4.03	5	1	1	2	0	1	1	0	1
4.05	7	3	1	2	1	2	1	3	1
4.06	9	1	2	1	1	2	1	3	1
4.08	11	0	1	0	1	0	2	0	0
4.10	6	0	0	0	0	0	0	0	0
4.11	2	0	0	0	0	0	0	0	0
4.13	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



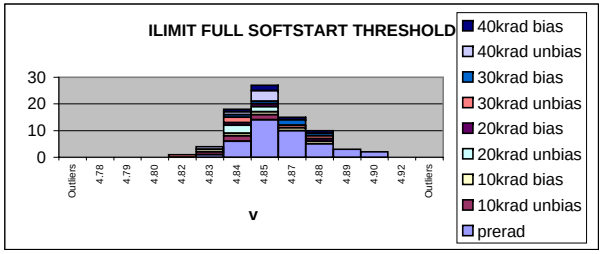
OSC CLOCK OUT LOW LEVEL

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
0.009	0	0	0	0	0	0	0	0	0
0.010	0	0	0	0	0	0	0	0	0
0.011	0	0	0	0	0	0	0	0	0
0.011	5	2	2	3	1	4	3	4	1
0.012	11	3	3	2	1	1	2	2	4
0.013	11	0	0	0	1	0	0	0	0
0.014	2	0	0	0	0	0	0	0	0
0.015	3	0	0	0	0	0	0	0	0
0.016	6	0	0	0	0	0	0	0	0
0.017	1	0	0	0	0	0	0	0	0
0.018	0	0	0	0	0	0	0	0	0
0.019	2	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



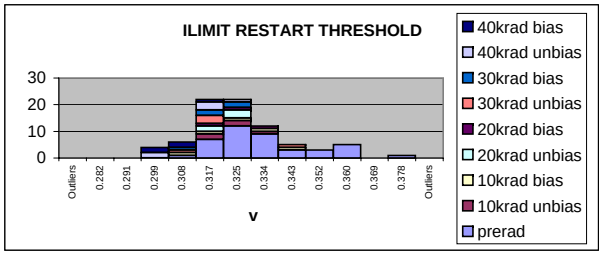
ILIMIT FULL SOFTSTART THRESHOLD

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
4.78	0	0	0	0	0	0	0	0	0
4.79	0	0	0	0	0	0	0	0	0
4.80	0	0	0	0	0	0	0	0	0
4.82	0	0	0	0	0	1	0	0	0
4.83	1	1	1	0	0	0	0	1	0
4.84	6	2	1	3	1	2	1	1	1
4.85	14	2	1	2	1	0	1	4	2
4.87	10	0	1	0	0	1	2	0	1
4.88	5	0	1	0	1	1	1	0	1
4.89	3	0	0	0	0	0	0	0	0
4.90	2	0	0	0	0	0	0	0	0
4.92	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



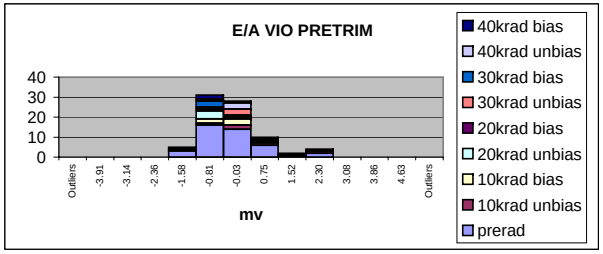
ILIMIT RESTART THRESHOLD

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
0.282	0	0	0	0	0	0	0	0	0
0.291	0	0	0	0	0	0	0	0	0
0.299	0	0	0	0	0	0	0	2	2
0.308	1	0	1	0	0	1	1	0	2
0.317	7	2	1	2	1	3	2	3	1
0.325	12	2	1	3	1	0	2	1	0
0.334	9	1	1	0	1	0	0	0	0
0.343	3	0	1	0	0	1	0	0	0
0.352	3	0	0	0	0	0	0	0	0
0.360	5	0	0	0	0	0	0	0	0
0.369	0	0	0	0	0	0	0	0	0
0.378	1	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



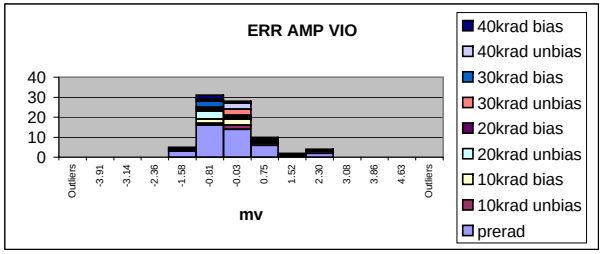
E/A VIO PRETRIM

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
-3.91	0	0	0	0	0	0	0	0	0
-3.14	0	0	0	0	0	0	0	0	0
-2.36	0	0	0	0	0	0	0	0	0
-1.58	3	0	0	0	0	1	0	1	0
-0.81	16	1	2	4	1	1	3	1	2
-0.03	14	2	3	1	1	3	0	3	1
0.75	6	1	0	0	1	0	1	1	0
1.52	0	0	0	0	0	0	1	0	1
2.30	2	1	0	0	0	0	0	0	1
3.08	0	0	0	0	0	0	0	0	0
3.86	0	0	0	0	0	0	0	0	0
4.63	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



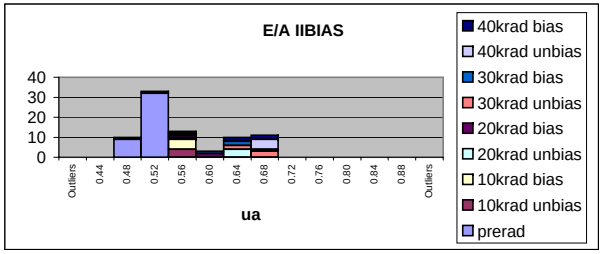
ERR AMP VIO

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
-3.91	0	0	0	0	0	0	0	0	0
-3.14	0	0	0	0	0	0	0	0	0
-2.36	0	0	0	0	0	0	0	0	0
-1.58	3	0	0	0	0	1	0	1	0
-0.81	16	1	2	4	1	1	3	1	2
-0.03	14	2	3	1	1	3	0	3	1
0.75	6	1	0	0	1	0	1	1	0
1.52	0	0	0	0	0	0	1	0	1
2.30	2	1	0	0	0	0	0	0	1
3.08	0	0	0	0	0	0	0	0	0
3.86	0	0	0	0	0	0	0	0	0
4.63	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



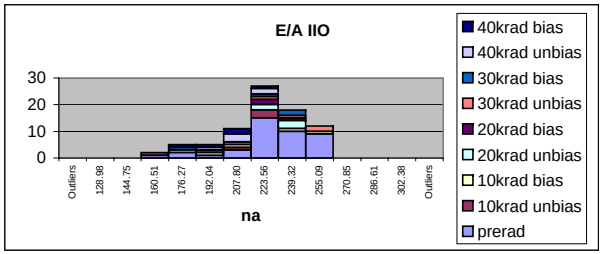
E/A IIBIAS

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
0.44	0	0	0	0	0	0	0	0	0
0.48	9	0	0	0	0	0	0	1	0
0.52	32	1	0	0	0	0	0	0	0
0.56	0	4	5	1	1	0	1	0	1
0.60	0	0	0	0	2	0	1	0	0
0.64	0	0	0	4	0	2	2	0	2
0.68	0	0	0	0	0	3	1	5	2
0.72	0	0	0	0	0	0	0	0	0
0.76	0	0	0	0	0	0	0	0	0
0.80	0	0	0	0	0	0	0	0	0
0.84	0	0	0	0	0	0	0	0	0
0.88	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



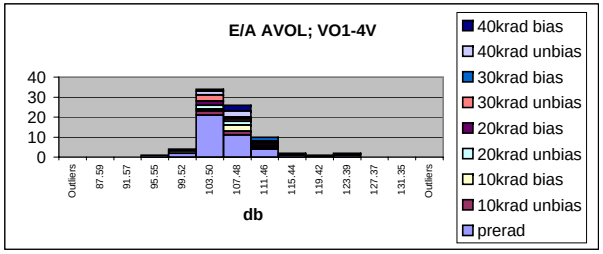
E/A IIO

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
128.98	0	0	0	0	0	0	0	0	0
144.75	0	0	0	0	0	0	0	0	0
160.51	1	1	0	0	0	0	0	0	0
176.27	2	0	1	0	0	0	1	0	1
192.04	1	0	1	0	0	0	1	1	1
207.80	3	1	1	0	0	1	0	3	2
223.56	15	3	0	2	2	1	1	2	1
239.32	10	0	1	3	1	1	2	0	0
255.09	9	0	1	0	0	2	0	0	0
270.85	0	0	0	0	0	0	0	0	0
286.61	0	0	0	0	0	0	0	0	0
302.38	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



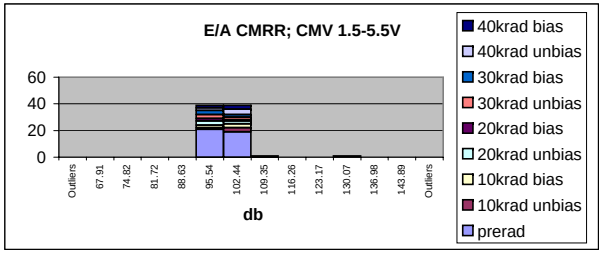
E/A AVOL; VO1-4V

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
87.59	0	0	0	0	0	0	0	0	0
91.57	0	0	0	0	0	0	0	0	0
95.55	1	0	0	0	0	0	0	0	0
99.52	2	0	0	1	0	0	1	0	0
103.50	21	2	1	2	2	3	0	2	1
107.48	11	2	3	2	0	1	1	3	3
111.46	4	1	1	0	1	1	2	0	0
115.44	1	0	0	0	0	0	0	1	0
119.42	0	0	0	0	0	0	0	0	1
123.39	1	0	0	0	0	0	1	0	0
127.37	0	0	0	0	0	0	0	0	0
131.35	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



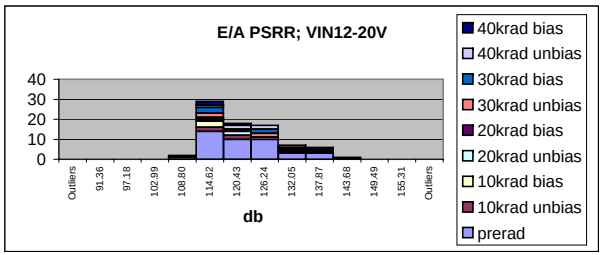
E/A CMRR; CMV 1.5-5.5V

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
67.91	0	0	0	0	0	0	0	0	0
74.82	0	0	0	0	0	0	0	0	0
81.72	0	0	0	0	0	0	0	0	0
88.63	0	0	0	0	0	0	0	0	0
95.54	21	1	2	3	2	3	3	2	2
102.44	19	3	3	2	1	2	2	4	3
109.35	1	0	0	0	0	0	0	0	0
116.26	0	0	0	0	0	0	0	0	0
123.17	0	0	0	0	0	0	0	0	0
130.07	0	1	0	0	0	0	0	0	0
136.98	0	0	0	0	0	0	0	0	0
143.89	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



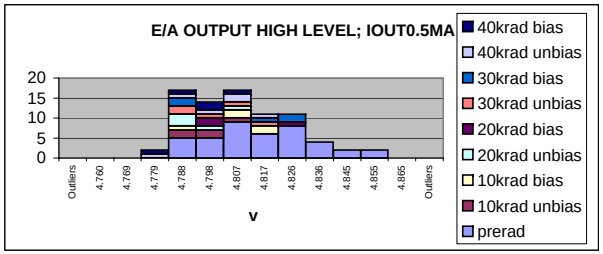
E/A PSRR; VIN12-20V

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
91.36	0	0	0	0	0	0	0	0	0
97.18	0	0	0	0	0	0	0	0	0
102.99	0	0	0	0	0	0	0	0	0
108.80	1	0	0	0	0	0	0	0	1
114.62	14	2	3	1	1	2	3	1	2
120.43	10	2	0	2	1	0	0	2	1
126.24	10	0	0	1	0	2	2	2	0
132.05	3	1	1	1	0	1	0	0	0
137.87	3	0	1	0	1	0	0	0	1
143.68	0	0	0	0	0	0	0	1	0
149.49	0	0	0	0	0	0	0	0	0
155.31	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



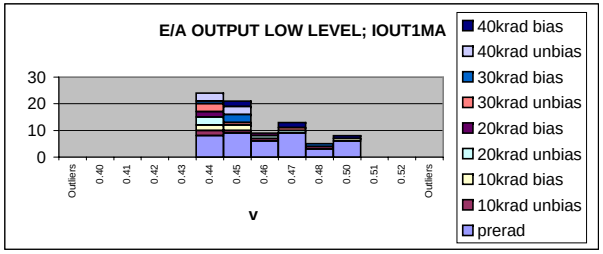
E/A OUTPUT HIGH LEVEL; IOUT0.5MA

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
4.760	0	0	0	0	0	0	0	0	0
4.769	0	0	0	0	0	0	0	0	0
4.779	0	0	0	0	0	0	0	1	1
4.788	5	2	1	3	0	2	2	1	1
4.798	5	2	0	1	2	1	0	1	2
4.807	9	1	2	1	0	1	0	2	1
4.817	6	0	2	0	0	1	1	1	0
4.826	8	0	0	0	1	0	2	0	0
4.836	4	0	0	0	0	0	0	0	0
4.845	2	0	0	0	0	0	0	0	0
4.855	2	0	0	0	0	0	0	0	0
4.865	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



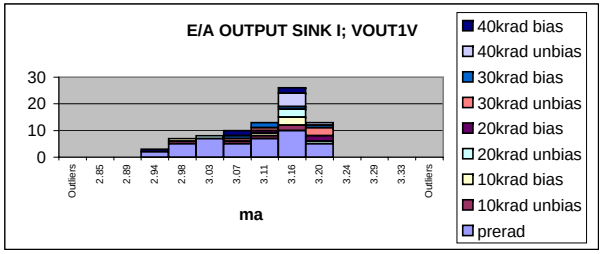
E/A OUTPUT LOW LEVEL; IOUT1MA

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
0.40	0	0	0	0	0	0	0	0	0
0.41	0	0	0	0	0	0	0	0	0
0.42	0	0	0	0	0	0	0	0	0
0.43	0	0	0	0	0	0	0	0	0
0.44	8	2	2	3	2	3	1	3	0
0.45	9	1	2	0	0	1	3	3	2
0.46	6	1	0	1	1	0	0	0	0
0.47	9	0	0	1	0	1	0	0	2
0.48	3	1	0	0	0	0	1	0	0
0.50	6	0	1	0	0	0	0	0	1
0.51	0	0	0	0	0	0	0	0	0
0.52	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



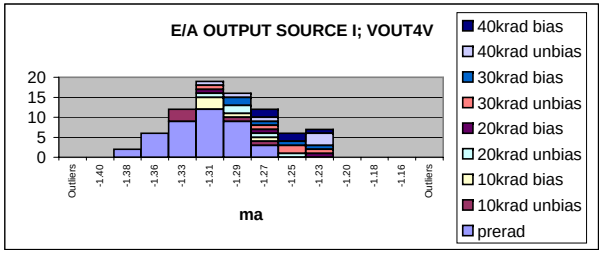
E/A OUTPUT SINK I; VOUT1V

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
2.85	0	0	0	0	0	0	0	0	0
2.89	0	0	0	0	0	0	0	0	0
2.94	2	0	0	0	0	0	0	0	1
2.98	5	1	1	0	0	0	0	0	0
3.03	7	0	0	1	0	0	0	0	0
3.07	5	1	0	0	0	1	1	0	2
3.11	7	1	1	0	1	1	2	0	0
3.16	10	2	3	3	0	0	1	5	2
3.20	5	0	0	1	2	3	1	1	0
3.24	0	0	0	0	0	0	0	0	0
3.29	0	0	0	0	0	0	0	0	0
3.33	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



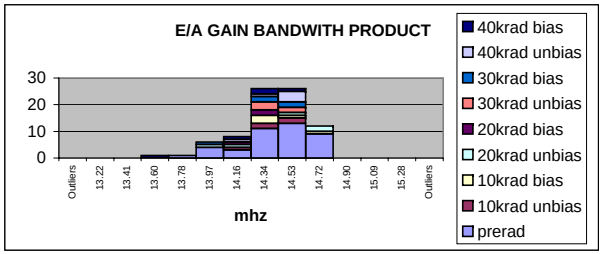
E/A OUTPUT SOURCE I; VOUT4V

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
-1.40	0	0	0	0	0	0	0	0	0
-1.38	2	0	0	0	0	0	0	0	0
-1.36	6	0	0	0	0	0	0	0	0
-1.33	9	3	0	0	0	0	0	0	0
-1.31	12	0	3	1	1	1	0	1	0
-1.29	9	1	1	2	0	0	2	1	0
-1.27	3	1	1	1	1	1	1	1	2
-1.25	0	0	0	1	0	2	1	0	2
-1.23	0	0	0	0	1	1	1	3	1
-1.20	0	0	0	0	0	0	0	0	0
-1.18	0	0	0	0	0	0	0	0	0
-1.16	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



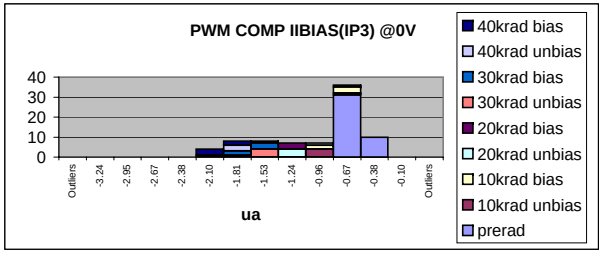
E/A GAIN BANDWITH PRODUCT

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
13.22	0	0	0	0	0	0	0	0	0
13.41	0	0	0	0	0	0	0	0	0
13.60	0	0	0	0	0	0	0	0	1
13.78	1	0	0	0	0	0	0	0	0
13.97	4	0	0	1	0	0	1	0	0
14.16	3	1	0	1	1	0	0	1	1
14.34	11	2	3	0	2	3	2	1	2
14.53	13	2	1	1	0	2	2	4	1
14.72	9	0	1	2	0	0	0	0	0
14.90	0	0	0	0	0	0	0	0	0
15.09	0	0	0	0	0	0	0	0	0
15.28	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



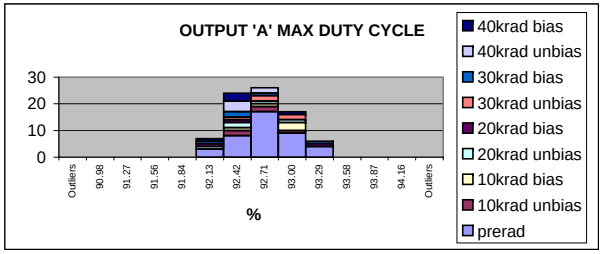
PWM COMP IIBIAS(IP3) @0V

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
-3.24	0	0	0	0	0	0	0	0	0
-2.95	0	0	0	0	0	0	0	0	0
-2.67	0	0	0	0	0	0	0	0	0
-2.38	0	0	0	0	0	0	0	0	0
-2.10	0	0	0	0	0	0	0	1	3
-1.81	0	0	0	0	0	1	2	3	2
-1.53	0	0	0	0	0	4	3	1	0
-1.24	0	0	0	4	3	0	0	0	0
-0.96	0	4	2	1	0	0	0	0	0
-0.67	31	1	3	0	0	0	0	1	0
-0.38	10	0	0	0	0	0	0	0	0
-0.10	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



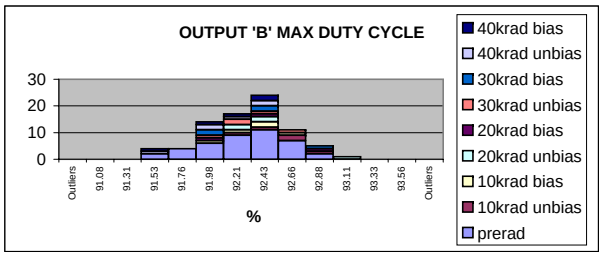
OUTPUT 'A' MAX DUTY CYCLE

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
90.98	0	0	0	0	0	0	0	0	0
91.27	0	0	0	0	0	0	0	0	0
91.56	0	0	0	0	0	0	0	0	0
91.84	0	0	0	0	0	0	0	0	0
92.13	3	0	0	1	1	0	1	0	1
92.42	8	2	1	2	1	1	2	4	3
92.71	17	2	1	1	0	2	1	2	0
93.00	9	1	3	1	0	2	0	0	1
93.29	4	0	0	0	1	0	1	0	0
93.58	0	0	0	0	0	0	0	0	0
93.87	0	0	0	0	0	0	0	0	0
94.16	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



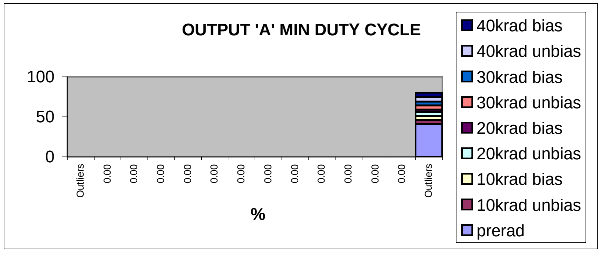
OUTPUT 'B' MAX DUTY CYCLE

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
91.08	0	0	0	0	0	0	0	0	0
91.31	0	0	0	0	0	0	0	0	0
91.53	2	0	0	0	0	0	0	1	1
91.76	4	0	0	0	0	0	0	0	0
91.98	6	0	1	0	1	1	2	2	1
92.21	9	1	1	2	0	2	0	1	1
92.43	11	1	2	2	1	1	2	2	2
92.66	7	2	1	0	0	1	0	0	0
92.88	2	1	0	0	1	0	1	0	0
93.11	0	0	0	1	0	0	0	0	0
93.33	0	0	0	0	0	0	0	0	0
93.56	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



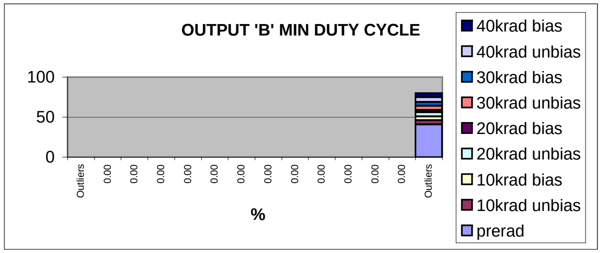
OUTPUT 'A' MIN DUTY CYCLE

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
Outliers	41	5	5	5	3	5	5	6	5



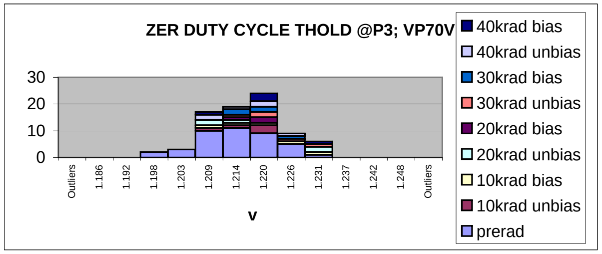
OUTPUT 'B' MIN DUTY CYCLE

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
0.00	0	0	0	0	0	0	0	0	0
Outliers	41	5	5	5	3	5	5	6	5



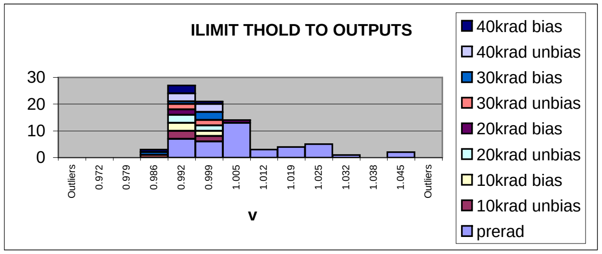
ZER DUTY CYCLE THOLD @P3; VP70V

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
1.186	0	0	0	0	0	0	0	0	0
1.192	0	0	0	0	0	0	0	0	0
1.198	2	0	0	0	0	0	0	0	0
1.203	3	0	0	0	0	0	0	0	0
1.209	10	1	1	2	0	0	0	2	1
1.214	11	1	1	1	1	1	2	1	0
1.220	9	3	1	0	2	2	2	2	3
1.226	5	0	1	0	0	1	1	1	0
1.231	1	0	1	2	0	1	0	0	1
1.237	0	0	0	0	0	0	0	0	0
1.242	0	0	0	0	0	0	0	0	0
1.248	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



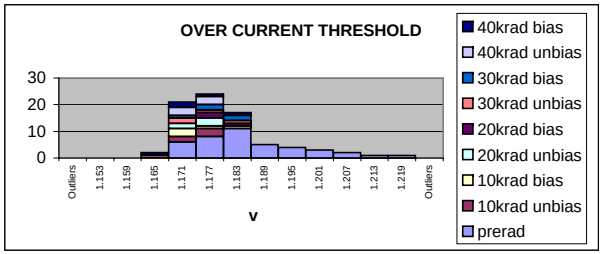
ILIMIT THOLD TO OUTPUTS

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
0.972	0	0	0	0	0	0	0	0	0
0.979	0	0	0	0	0	0	0	0	0
0.986	0	0	0	0	0	1	1	0	1
0.992	7	3	3	3	2	2	1	3	3
0.999	6	2	2	2	0	2	3	3	1
1.005	13	0	0	0	1	0	0	0	0
1.012	3	0	0	0	0	0	0	0	0
1.019	4	0	0	0	0	0	0	0	0
1.025	5	0	0	0	0	0	0	0	0
1.032	1	0	0	0	0	0	0	0	0
1.038	0	0	0	0	0	0	0	0	0
1.045	2	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



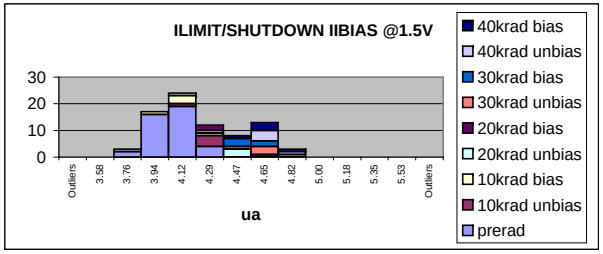
OVER CURRENT THRESHOLD

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
1.153	0	0	0	0	0	0	0	0	0
1.159	0	0	0	0	0	0	0	0	0
1.165	0	0	0	0	0	1	0	0	1
1.171	6	2	3	2	0	2	1	3	2
1.177	8	3	1	3	2	1	2	3	1
1.183	11	0	1	0	1	1	2	0	1
1.189	5	0	0	0	0	0	0	0	0
1.195	4	0	0	0	0	0	0	0	0
1.201	3	0	0	0	0	0	0	0	0
1.207	2	0	0	0	0	0	0	0	0
1.213	1	0	0	0	0	0	0	0	0
1.219	1	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



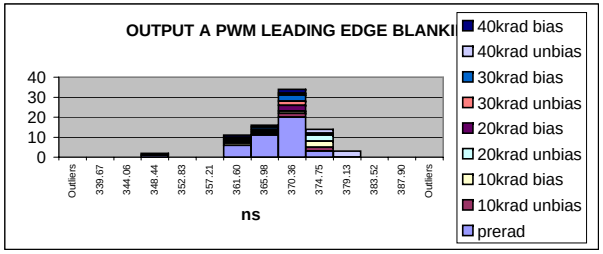
ILIMIT/SHUTDOWN IIBIAS @1.5V

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
3.58	0	0	0	0	0	0	0	0	0
3.76	2	0	0	0	0	0	0	1	0
3.94	16	0	1	0	0	0	0	0	0
4.12	19	1	3	1	0	0	0	0	0
4.29	4	4	1	1	2	0	0	0	0
4.47	0	0	0	3	0	1	3	0	1
4.65	0	0	0	0	1	3	2	4	3
4.82	0	0	0	0	0	1	0	1	1
5.00	0	0	0	0	0	0	0	0	0
5.18	0	0	0	0	0	0	0	0	0
5.35	0	0	0	0	0	0	0	0	0
5.53	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



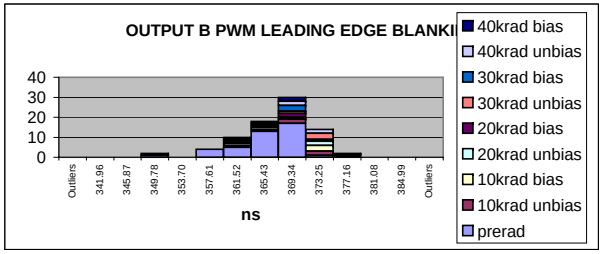
OUTPUT A PWM LEADING EDGE BLANKING

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
339.67	0	0	0	0	0	0	0	0	0
344.06	0	0	0	0	0	0	0	0	0
348.44	1	0	0	0	0	0	0	0	1
352.83	0	0	0	0	0	0	0	0	0
357.21	0	0	0	0	0	0	0	0	0
361.60	6	0	1	1	0	1	1	0	1
365.98	11	1	0	1	0	1	1	0	1
370.36	20	2	1	0	3	2	3	1	2
374.75	3	2	3	3	0	1	0	2	0
379.13	0	0	0	0	0	0	0	3	0
383.52	0	0	0	0	0	0	0	0	0
387.90	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



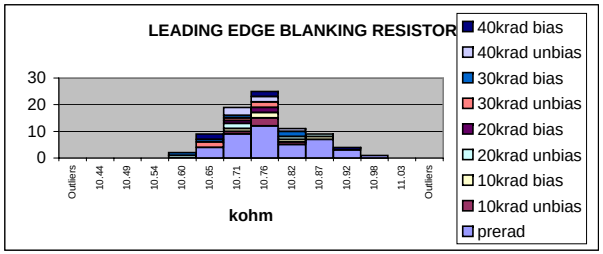
OUTPUT B PWM LEADING EDGE BLANKING

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
341.96	0	0	0	0	0	0	0	0	0
345.87	0	0	0	0	0	0	0	0	0
349.78	1	0	0	0	0	0	0	0	1
353.70	0	0	0	0	0	0	0	0	0
357.61	4	0	0	0	0	0	0	0	0
361.52	5	0	1	1	0	1	1	0	1
365.43	13	1	0	1	0	0	1	1	1
369.34	17	2	0	1	2	1	3	2	2
373.25	1	2	3	2	1	3	0	2	0
377.16	0	0	1	0	0	0	0	1	0
381.08	0	0	0	0	0	0	0	0	0
384.99	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



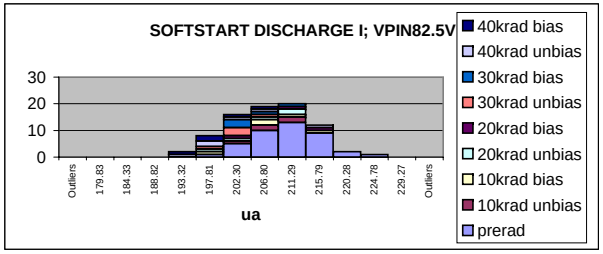
LEADING EDGE BLANKING RESISTOR

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
10.44	0	0	0	0	0	0	0	0	0
10.49	0	0	0	0	0	0	0	0	0
10.54	0	0	0	0	0	0	0	0	0
10.60	0	0	0	1	0	0	1	0	0
10.65	4	0	0	0	0	2	1	0	2
10.71	9	1	1	2	1	1	1	3	0
10.76	12	3	2	0	2	2	0	2	2
10.82	5	1	1	1	0	0	2	1	0
10.87	7	0	1	1	0	0	0	0	0
10.92	3	0	0	0	0	0	0	0	1
10.98	1	0	0	0	0	0	0	0	0
11.03	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



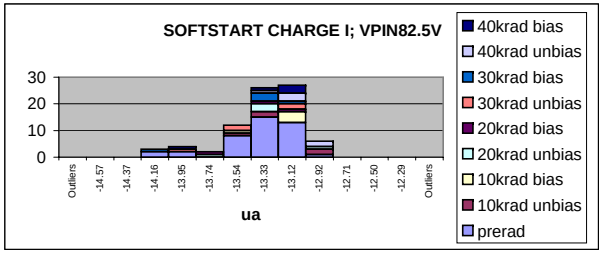
SOFTSTART DISCHARGE I; VPIN82.5V

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
179.83	0	0	0	0	0	0	0	0	0
184.33	0	0	0	0	0	0	0	0	0
188.82	0	0	0	0	0	0	0	0	0
193.32	0	0	0	0	0	0	0	1	1
197.81	1	0	1	1	0	1	0	2	2
202.30	5	1	0	1	1	3	3	1	1
206.80	10	2	2	1	0	1	1	1	1
211.29	13	2	1	2	1	0	1	0	0
215.79	9	0	1	0	1	0	0	1	0
220.28	2	0	0	0	0	0	0	0	0
224.78	1	0	0	0	0	0	0	0	0
229.27	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



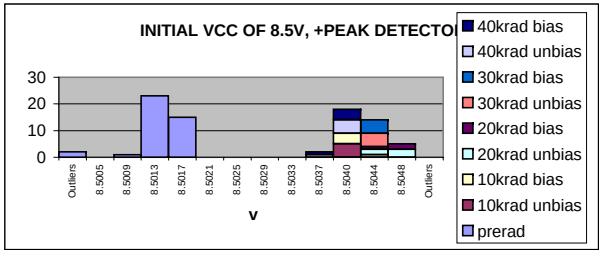
SOFTSTART CHARGE I; VPIN82.5V

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
-14.57	0	0	0	0	0	0	0	0	0
-14.37	0	0	0	0	0	0	0	0	0
-14.16	2	0	0	0	0	0	1	0	0
-13.95	2	0	0	0	0	1	0	0	1
-13.74	0	0	0	1	1	0	0	0	0
-13.54	8	1	1	0	0	2	0	0	0
-13.33	15	2	0	3	1	0	3	1	1
-13.12	13	0	4	0	1	2	1	3	3
-12.92	1	2	0	1	0	0	0	2	0
-12.71	0	0	0	0	0	0	0	0	0
-12.50	0	0	0	0	0	0	0	0	0
-12.29	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



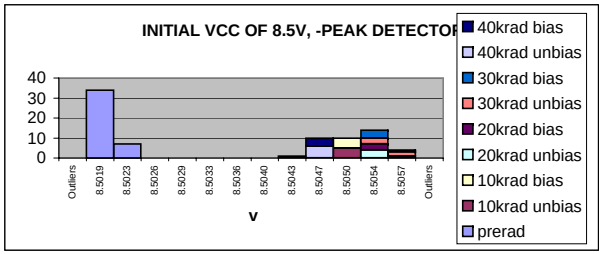
INITIAL VCC OF 8.5V, +PEAK DETECTOR

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	2	0	0	0	0	0	0	0	0
8.5005	0	0	0	0	0	0	0	0	0
8.5009	1	0	0	0	0	0	0	0	0
8.5013	23	0	0	0	0	0	0	0	0
8.5017	15	0	0	0	0	0	0	0	0
8.5021	0	0	0	0	0	0	0	0	0
8.5025	0	0	0	0	0	0	0	0	0
8.5029	0	0	0	0	0	0	0	0	0
8.5033	0	0	0	0	0	0	0	0	0
8.5037	0	0	0	0	0	0	0	1	1
8.5040	0	5	4	0	0	0	0	5	4
8.5044	0	0	1	2	1	5	5	0	0
8.5048	0	0	0	3	2	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



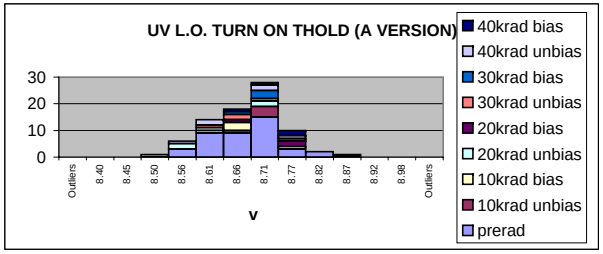
INITIAL VCC OF 8.5V, -PEAK DETECTOR

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
8.5019	34	0	0	0	0	0	0	0	0
8.5023	7	0	0	0	0	0	0	0	0
8.5026	0	0	0	0	0	0	0	0	0
8.5029	0	0	0	0	0	0	0	0	0
8.5033	0	0	0	0	0	0	0	0	0
8.5036	0	0	0	0	0	0	0	0	0
8.5040	0	0	0	0	0	0	0	0	0
8.5043	0	0	0	0	0	0	0	0	1
8.5047	0	0	0	0	0	0	0	6	4
8.5050	0	5	5	0	0	0	0	0	0
8.5054	0	0	0	4	3	3	4	0	0
8.5057	0	0	0	1	0	2	1	0	0
Outliers	0	0	0	0	0	0	0	0	0



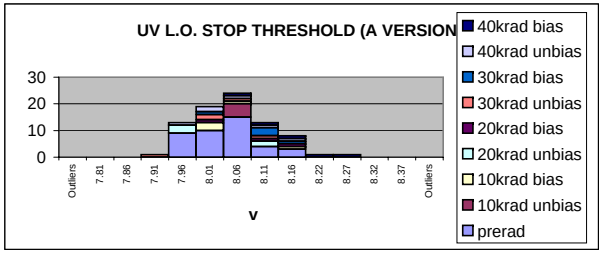
UV L.O. TURN ON THOLD (A VERSION)

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
8.40	0	0	0	0	0	0	0	0	0
8.45	0	0	0	0	0	0	0	0	0
8.50	0	0	0	0	0	1	0	0	0
8.56	3	0	0	2	0	0	0	1	0
8.61	9	0	1	1	0	1	0	2	0
8.66	9	1	3	0	1	2	1	0	1
8.71	15	4	0	2	0	1	3	2	1
8.77	3	0	1	0	2	0	1	1	2
8.82	2	0	0	0	0	0	0	0	0
8.87	0	0	0	0	0	0	0	0	1
8.92	0	0	0	0	0	0	0	0	0
8.98	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



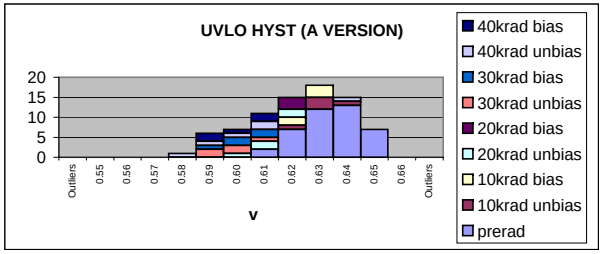
UV L.O. STOP THRESHOLD (A VERSION)

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
7.81	0	0	0	0	0	0	0	0	0
7.86	0	0	0	0	0	0	0	0	0
7.91	0	0	0	0	0	1	0	0	0
7.96	9	0	0	3	0	0	0	1	0
8.01	10	0	3	0	1	2	1	2	0
8.06	15	5	1	0	0	1	0	1	1
8.11	4	0	0	2	1	1	3	1	1
8.16	3	0	1	0	1	0	1	1	1
8.22	0	0	0	0	0	0	0	0	1
8.27	0	0	0	0	0	0	0	0	1
8.32	0	0	0	0	0	0	0	0	0
8.37	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



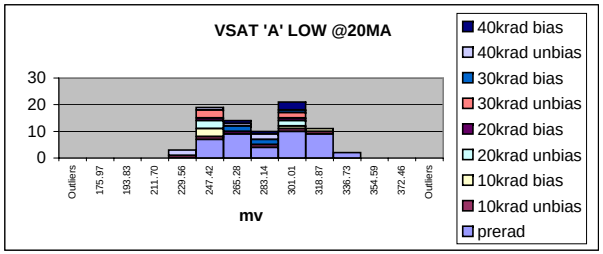
UVLO HYST (A VERSION)

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
0.55	0	0	0	0	0	0	0	0	0
0.56	0	0	0	0	0	0	0	0	0
0.57	0	0	0	0	0	0	0	0	0
0.58	0	0	0	0	0	0	0	1	0
0.59	0	0	0	0	0	2	1	1	2
0.60	0	0	0	1	0	2	2	1	1
0.61	2	0	0	2	0	1	2	2	2
0.62	7	1	2	2	3	0	0	0	0
0.63	12	3	3	0	0	0	0	0	0
0.64	13	1	0	0	0	0	0	1	0
0.65	7	0	0	0	0	0	0	0	0
0.66	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



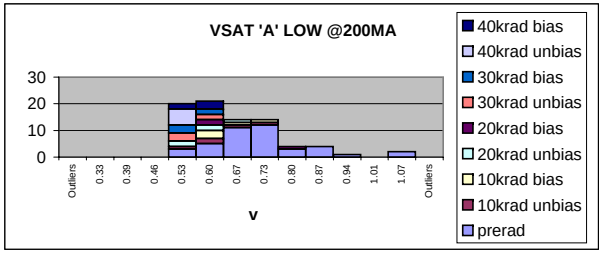
VSAT 'A' LOW @20MA

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
175.97	0	0	0	0	0	0	0	0	0
193.83	0	0	0	0	0	0	0	0	0
211.70	0	0	0	0	0	0	0	0	0
229.56	0	1	0	0	0	0	0	2	0
247.42	7	1	3	3	1	3	0	1	0
265.28	9	0	0	0	1	0	2	1	1
283.14	4	1	0	0	0	0	2	2	1
301.01	10	1	1	2	1	2	1	0	3
318.87	9	1	1	0	0	0	0	0	0
336.73	2	0	0	0	0	0	0	0	0
354.59	0	0	0	0	0	0	0	0	0
372.46	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



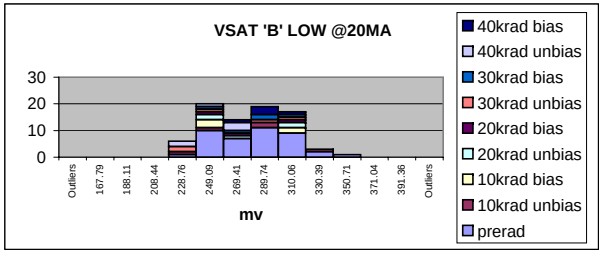
VSAT 'A' LOW @200MA

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
0.33	0	0	0	0	0	0	0	0	0
0.39	0	0	0	0	0	0	0	0	0
0.46	0	0	0	0	0	0	0	0	0
0.53	3	1	0	2	0	3	3	6	2
0.60	5	2	3	2	2	2	2	0	3
0.67	11	1	1	1	0	0	0	0	0
0.73	12	1	1	0	0	0	0	0	0
0.80	3	0	0	0	1	0	0	0	0
0.87	4	0	0	0	0	0	0	0	0
0.94	1	0	0	0	0	0	0	0	0
1.01	0	0	0	0	0	0	0	0	0
1.07	2	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



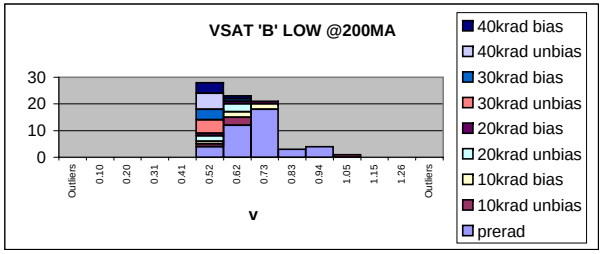
VSAT 'B' LOW @20MA

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
167.79	0	0	0	0	0	0	0	0	0
188.11	0	0	0	0	0	0	0	0	0
208.44	0	0	0	0	0	0	0	0	0
228.76	1	1	0	0	0	2	0	2	0
249.09	10	1	3	2	1	1	1	1	0
269.41	7	0	0	1	1	0	1	3	1
289.74	11	2	0	0	0	1	2	0	3
310.06	9	0	2	2	1	1	1	0	1
330.39	2	1	0	0	0	0	0	0	0
350.71	1	0	0	0	0	0	0	0	0
371.04	0	0	0	0	0	0	0	0	0
391.36	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



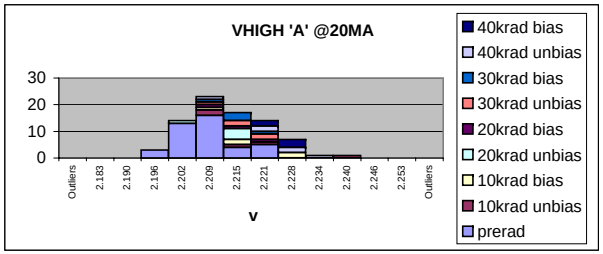
VSAT 'B' LOW @200MA

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
0.10	0	0	0	0	0	0	0	0	0
0.20	0	0	0	0	0	0	0	0	0
0.31	0	0	0	0	0	0	0	0	0
0.41	0	0	0	0	0	0	0	0	0
0.52	4	1	1	2	1	5	4	6	4
0.62	12	3	2	3	1	0	1	0	1
0.73	18	0	2	0	1	0	0	0	0
0.83	3	0	0	0	0	0	0	0	0
0.94	4	0	0	0	0	0	0	0	0
1.05	0	1	0	0	0	0	0	0	0
1.15	0	0	0	0	0	0	0	0	0
1.26	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



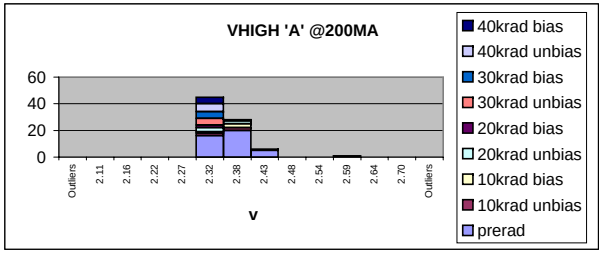
VHIGH 'A' @20MA

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
2.183	0	0	0	0	0	0	0	0	0
2.190	0	0	0	0	0	0	0	0	0
2.196	3	0	0	0	0	0	0	0	0
2.202	13	0	0	1	0	0	0	0	0
2.209	16	2	1	0	1	1	1	1	0
2.215	4	1	2	4	1	2	3	0	0
2.221	5	1	0	0	1	2	1	2	2
2.228	0	0	2	0	0	0	0	2	3
2.234	0	0	0	0	0	0	0	1	0
2.240	0	1	0	0	0	0	0	0	0
2.246	0	0	0	0	0	0	0	0	0
2.253	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



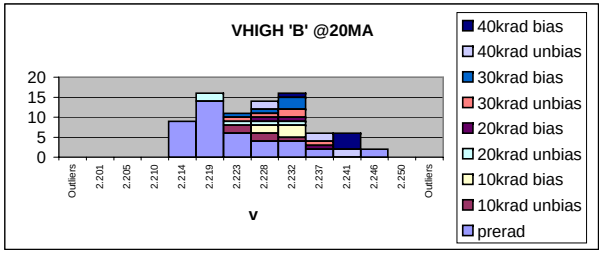
VHIGH 'A' @200MA

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
2.11	0	0	0	0	0	0	0	0	0
2.16	0	0	0	0	0	0	0	0	0
2.22	0	0	0	0	0	0	0	0	0
2.27	0	0	0	0	0	0	0	0	0
2.32	16	2	1	3	2	5	5	6	5
2.38	20	2	3	2	1	0	0	0	0
2.43	5	0	1	0	0	0	0	0	0
2.48	0	0	0	0	0	0	0	0	0
2.54	0	0	0	0	0	0	0	0	0
2.59	0	1	0	0	0	0	0	0	0
2.64	0	0	0	0	0	0	0	0	0
2.70	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



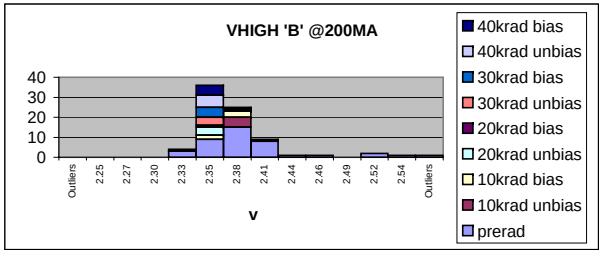
VHIGH 'B' @20MA

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
2.201	0	0	0	0	0	0	0	0	0
2.205	0	0	0	0	0	0	0	0	0
2.210	0	0	0	0	0	0	0	0	0
2.214	9	0	0	0	0	0	0	0	0
2.219	14	0	0	2	0	0	0	0	0
2.223	6	2	0	1	0	1	1	0	0
2.228	4	2	2	1	1	1	1	2	0
2.232	4	1	3	1	1	2	3	0	1
2.237	2	0	0	0	1	1	0	2	0
2.241	0	0	0	0	0	0	0	2	4
2.246	2	0	0	0	0	0	0	0	0
2.250	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0



VHIGH 'B' @200MA

Bin	prerad	10krad unbias	10krad bias	20krad unbias	20krad bias	30krad unbias	30krad bias	40krad unbias	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
2.25	0	0	0	0	0	0	0	0	0
2.27	0	0	0	0	0	0	0	0	0
2.30	0	0	0	0	0	0	0	0	0
2.33	3	0	0	0	0	1	0	0	0
2.35	9	0	2	4	1	4	5	6	5
2.38	15	5	3	1	1	0	0	0	0
2.41	8	0	0	0	1	0	0	0	0
2.44	1	0	0	0	0	0	0	0	0
2.46	1	0	0	0	0	0	0	0	0
2.49	0	0	0	0	0	0	0	0	0
2.52	2	0	0	0	0	0	0	0	0
2.54	1	0	0	0	0	0	0	0	0
Outliers	1	0	0	0	0	0	0	0	0



OUTPUT A VSAT @ VIN UVLO-300MV

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
0.64	0	0	0	0	0	0	0	0	0
0.67	0	0	0	0	0	0	0	0	0
0.70	0	0	0	0	0	0	0	0	0
0.74	0	0	0	0	0	0	0	0	0
0.77	5	2	0	1	0	0	2	1	0
0.80	34	3	5	3	3	5	3	4	5
0.83	0	0	0	0	0	0	0	0	0
0.86	0	0	0	0	0	0	0	0	0
0.89	1	0	0	0	0	0	0	0	0
0.92	1	0	0	1	0	0	0	1	0
0.95	0	0	0	0	0	0	0	0	0
0.98	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0

OUTPUT B VSAT @ VIN UVLO-300MV

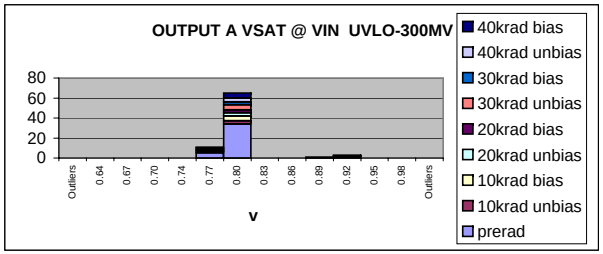
Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
0.757	0	0	0	0	0	0	0	0	0
0.762	0	0	0	0	0	0	0	0	0
0.767	1	0	0	0	0	0	0	0	0
0.772	1	0	0	0	0	0	0	1	0
0.777	5	2	1	0	0	0	1	0	0
0.782	17	2	1	2	1	3	3	0	1
0.787	11	0	3	3	2	2	1	5	3
0.792	5	0	0	0	0	0	0	0	1
0.796	1	0	0	0	0	0	0	0	0
0.801	0	1	0	0	0	0	0	0	0
0.806	0	0	0	0	0	0	0	0	0
0.811	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0

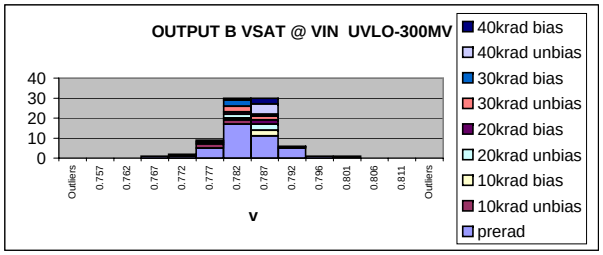
ICC START I @ VIN UVLO-300MV

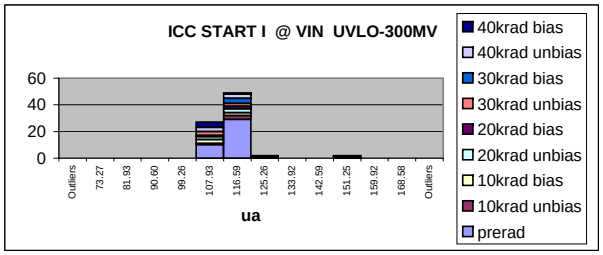
Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
73.27	0	0	0	0	0	0	0	0	0
81.93	0	0	0	0	0	0	0	0	0
90.60	0	0	0	0	0	0	0	0	0
99.26	0	0	0	0	0	0	0	0	0
107.93	10	1	3	2	1	3	0	3	4
116.59	29	3	2	3	2	2	4	3	1
125.26	1	0	0	0	0	0	1	0	0
133.92	0	0	0	0	0	0	0	0	0
142.59	0	0	0	0	0	0	0	0	0
151.25	1	1	0	0	0	0	0	0	0
159.92	0	0	0	0	0	0	0	0	0
168.58	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0

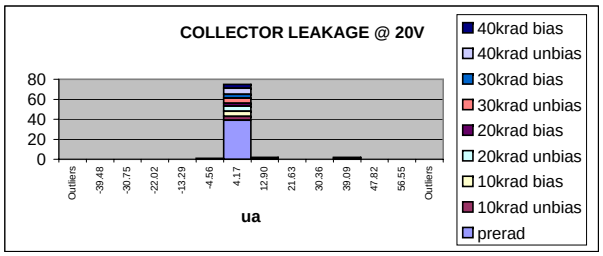
COLLECTOR LEAKAGE @ 20V

Bin	prerad	10krad unbia	10krad bias	20krad unbia	20krad bias	30krad unbia	30krad bias	40krad unbia	40krad bias
Outliers	0	0	0	0	0	0	0	0	0
-39.48	0	0	0	0	0	0	0	0	0
-30.75	0	0	0	0	0	0	0	0	0
-22.02	0	0	0	0	0	0	0	0	0
-13.29	0	0	0	0	0	0	0	0	0
-4.56	0	0	0	0	0	0	0	0	1
4.17	39	4	5	5	3	5	4	6	4
12.90	1	0	0	0	0	0	1	0	0
21.63	0	0	0	0	0	0	0	0	0
30.36	0	0	0	0	0	0	0	0	0
39.09	1	1	0	0	0	0	0	0	0
47.82	0	0	0	0	0	0	0	0	0
56.55	0	0	0	0	0	0	0	0	0
Outliers	0	0	0	0	0	0	0	0	0









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