

ADC128S102-SEP Radiation Tolerant, 8-Channel, CMOS, 12-Bit ADC TID Report



ABSTRACT

This report discusses the results of the total ionizing dose (TID) testing for the ADC128S102PW-SEP. This device is a 2.7-V to 5.25-V, low power, 8-channel, CMOS, 12-bit analog-to-digital converter (ADC) specified for conversion throughput rates of 50 kSPS to 1 MSPS. This study was done to determine the TID effects under a high dose rate (HDR) of 30krad (Si). The resulting data shows that no units failed any of the parametric tests.

Table of Contents

1 Device Information	2
1.1 Device Details.....	2
2 Total Dose Test Setup	3
2.1 Test Overview.....	3
2.2 Test Description and Facilities.....	3
2.3 Test Setup Details.....	3
2.4 Test Configuration and Condition.....	4
A Total Ionizing Dose HDR Report Appendix	5

List of Figures

Figure 2-1. ADC128S102PW-SEP Biased Diagram.....	3
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List of Tables

Table 1-1. Device and Exposure Details.....	2
Table 2-1. Pin Signal Connections.....	3
Table 2-2. Bill of Materials.....	4
Table 2-3. HDR = 51.61 rad(Si)/s Device Information.....	4

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1 Device Information

The ADC128S102PW-SEP is a low-power, 8-channel, CMOS, 12-bit ADC specified for conversion throughput rates of 50 kSPS to 1 MSPS. The converter is based on a successive approximation register (SAR) architecture with an internal track-and-hold circuit. The device can be configured to accept up to eight input signals at inputs IN0 through IN7.

The ADC128S102PW-SEP can be operated with independent analog and digital supplies. The analog supply (VA) can range from 2.7 V to 5.25 V, and the digital supply (VD) can range from +2.7 V to VA. The ADC128S102PW-SEP is packaged in a 16-lead TSSOP package.

1.1 Device Details

[Table 1-1](#) lists the device information used for TID HDR characterization and qualification.

Table 1-1. Device and Exposure Details

TID HDR Details: up to 30 krad(Si)	
TI device number	ADC128S102PWTSEP
Package	16-pin TSSOP (PW)
Technology	CMOS7
Die lot number	1012690CUA
Die name	GHX3UADC128SA0
Device / package lot number	1362809EM1
Lot trace code (LTC)	1BCGDCK
Quantity tested	22
Lot accept/reject	All 22 devices passed 30 krad (Si)
HDR radiation facility	Texas Instruments SVC Group, Santa Clara, CA
HDR dose level	30krad (Si)
HDR dose rate	51.61 rads (Si) / sec
HDR irradiation temperature	Ambient, room temperature
Radiation test date	24-Feb-2022

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2 Total Dose Test Setup

2.1 Test Overview

The ADC128S102PW-SEP was tested according to MIL-STD-883, test method 1019. For this test, the product was irradiated up to the target radiation level and then put through full electrical parametric testing on the production automated test equipment (ATE). The device was fully functional.

2.2 Test Description and Facilities

The ADC128S102PW-SEP HDR exposure was performed on biased devices in a Co-60 gamma cell at the TI SVC facility in Santa Clara, California. After exposure, the devices were tested post radiation for full electrical evaluation using Texas Instruments ATE. A minimum of 22 units are required to qualify a full wafer lot, a total sample size of 24 units (22 RLAT and two control units) were tested.

2.3 Test Setup Details

The devices under HDR exposure were tested in biased conditions as described in [Figure 2-1](#).

2.3.1 Biased

[Figure 2-1](#) shows the bias conditions for each pin during irradiation. [Table 2-1](#) describes the pin signal connections.

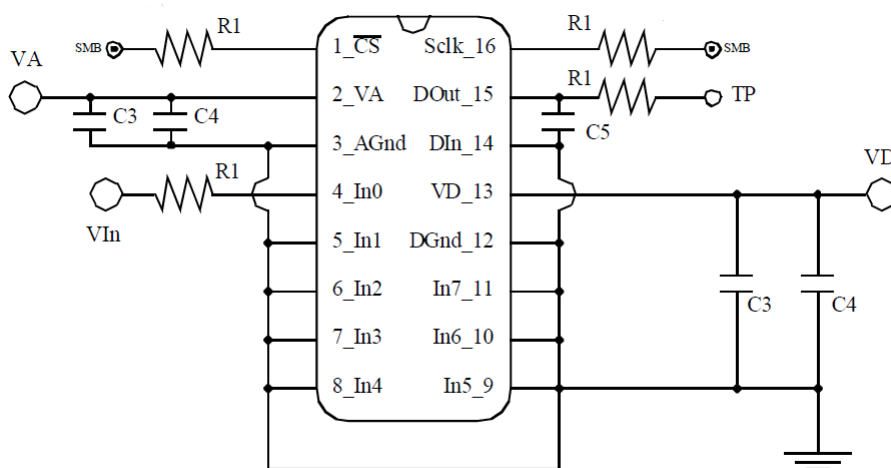


Figure 2-1. ADC128S102PW-SEP Biased Diagram

Table 2-1. Pin Signal Connections

Symbol	Limits	
	MIN	MAX
VA and VD	5.0 V	5.5 V
Vin	1.6 V	1.8 V
IDA and IDD	0.25 mA	0.33 mA
SCLK	50 kHz; 0 V–3.3 V	
\overline{CS}	2.5 kHz; 0 V–3.3 V	

Table 2-2 lists the bill of materials for the TID bias board used.

Table 2-2. Bill of Materials

Refs Designator	Qty	Description
R1	4	50 OHMS, 1/4 WATT, 1%
C3	2	10uF, 35V, 5%
C4	2	0.1uF, 35V, 5%
C5	1	1000pF, 35V, 5%
SMB	2	SMB JACK Right Angle

2.4 Test Configuration and Condition

Table 2-3 lists the serialized samples used for RHA characterization.

Table 2-3. HDR = 51.61 rad(Si)/s Device Information

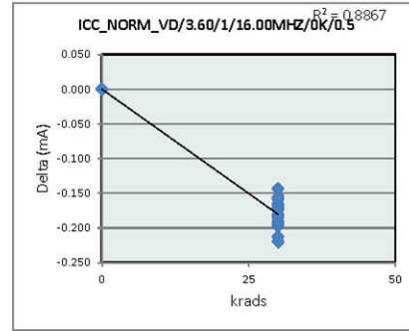
HDR = 51.61 rad(Si)/s	
Total Samples: 24	
30 krad(Si)	
Biased	Unbiased
3-24	1-2

A Total Ionizing Dose HDR Report Appendix

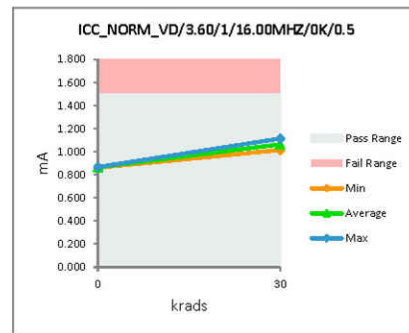
This appendix provides the ADC128S102PW-SEP TID HDR report. The report shows the variation for each parameter up to 30 krad(Si).

TID Report ADC128S102PWT-SEP

ICC_NORM_VD/3.60/1/16.00MH				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		1.5	1.5	
Min Limit		0	0	
krads	Serial #	Pre	Post	Delta
0	23	0.862	0.862	0.000
0	24	0.867	0.867	0.000
30	1	0.882	1.080	-0.197
30	2	0.873	1.039	-0.165
30	3	0.875	1.030	-0.155
30	4	0.913	1.086	-0.172
30	5	0.885	1.040	-0.156
30	6	0.869	1.013	-0.143
30	7	0.892	1.112	-0.220
30	8	0.894	1.078	-0.184
30	9	0.869	1.050	-0.181
30	10	0.901	1.098	-0.197
30	11	0.902	1.098	-0.196
30	12	0.871	1.038	-0.167
30	13	0.888	1.062	-0.174
30	14	0.897	1.110	-0.213
30	15	0.904	1.089	-0.185
30	16	0.860	1.031	-0.172
30	17	0.872	1.044	-0.173
30	18	0.873	1.031	-0.158
30	19	0.855	1.046	-0.191
30	20	0.866	1.057	-0.191
30	21	0.876	1.058	-0.182
30	22	0.899	1.093	-0.194
Max		0.913	1.112	0.000
Average		0.881	1.046	-0.165
Min		0.855	0.862	-0.220
Std Dev		0.016	0.063	0.054

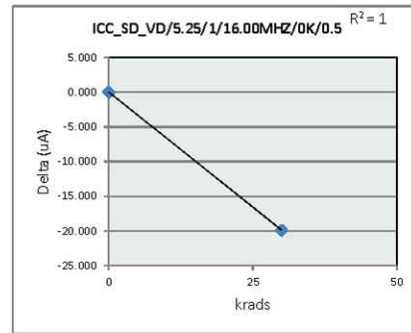


ICC_NORM_VD/3.60/1/16.00MHZ/0K/0.5		
Test Site		
Tester		
Test Number		
Max Limit	1.5	mA
Min Limit	0	mA
krads	0	30
LL	0.000	0.000
Min	0.862	1.013
Average	0.864	1.063
Max	0.867	1.112
UL	1.500	1.500

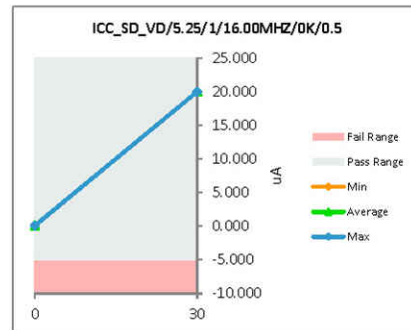


**TID Report
ADC128S102PWT-SEP**

ICC_SD_VD/5.25/1/16.00MHZ/0K/0.5				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit				
Min Limit		-5	-5	
krads	Serial #	Pre	Post	Delta
0	23	0.073	0.072	0.001
0	24	0.059	0.060	-0.002
30	1	0.062	19.999	-19.936
30	2	0.055	19.998	-19.944
30	3	0.053	19.999	-19.946
30	4	0.074	19.998	-19.924
30	5	0.064	19.999	-19.934
30	6	0.053	19.998	-19.944
30	7	0.065	19.998	-19.933
30	8	0.087	19.999	-19.912
30	9	0.053	19.999	-19.946
30	10	0.056	19.999	-19.943
30	11	0.061	19.999	-19.938
30	12	0.052	20.000	-19.947
30	13	0.051	19.999	-19.948
30	14	0.059	19.998	-19.939
30	15	0.050	19.998	-19.948
30	16	0.052	19.999	-19.947
30	17	0.053	19.999	-19.946
30	18	0.053	19.998	-19.945
30	19	0.050	19.998	-19.948
30	20	0.053	19.999	-19.945
30	21	0.052	19.997	-19.945
30	22	0.052	19.998	-19.945
Max		0.087	20.000	0.001
Average		0.058	18.337	-18.279
Min		0.050	0.060	-19.948
Std Dev		0.009	5.627	5.630

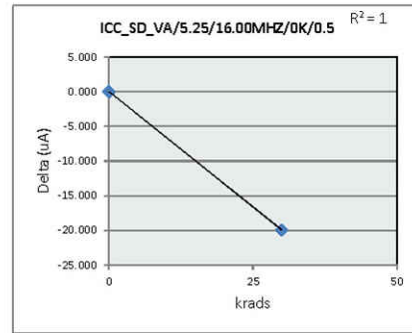


ICC_SD_VD/5.25/1/16.00MHZ		
Test Site		
Tester		
Test Number		
Max Limit		uA
Min Limit		-5
krads	0	30
LL	-5.000	-5.000
Min	0.060	19.997
Average	0.066	19.998
Max	0.072	20.000
UL		

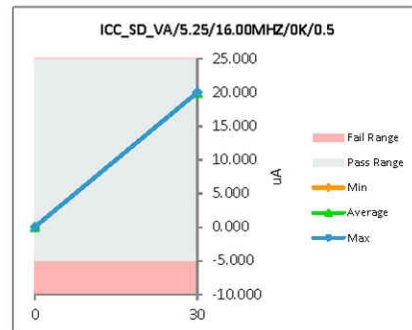


TID Report ADC128S102PWT-SEP

ICC_SD_VA/5.25/16.00MHZ/OK/				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit				
Min Limit		-5	-5	
krads	Serial #	Pre	Post	Delta
0	23	0.007	0.004	0.003
0	24	0.007	0.006	0.002
30	1	0.005	20.000	-19.995
30	2	0.007	19.999	-19.992
30	3	0.009	20.000	-19.991
30	4	0.007	19.998	-19.991
30	5	0.007	19.998	-19.991
30	6	0.007	19.999	-19.992
30	7	0.007	19.998	-19.991
30	8	0.005	19.999	-19.993
30	9	0.005	19.999	-19.994
30	10	0.005	19.999	-19.994
30	11	0.006	20.000	-19.994
30	12	0.007	20.000	-19.993
30	13	0.006	20.000	-19.994
30	14	0.007	19.999	-19.993
30	15	0.007	19.999	-19.992
30	16	0.007	19.998	-19.992
30	17	0.005	19.999	-19.994
30	18	0.007	19.999	-19.992
30	19	0.009	19.998	-19.989
30	20	0.008	19.999	-19.991
30	21	0.008	19.999	-19.990
30	22	0.005	19.998	-19.993
Max		0.009	20.000	0.003
Average		0.007	18.333	-18.326
Min		0.005	0.004	-19.995
Std Dev		0.001	5.645	5.645

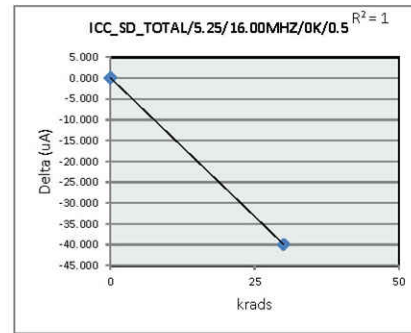


ICC_SD_VA/5.25/16.00MHZ/0		
Test Site		
Tester		
Test Number		
Max Limit		uA
Min Limit		uA
krads	0	30
LL	-5.000	-5.000
Min	0.004	19.998
Average	0.005	19.999
Max	0.006	20.000
UL		

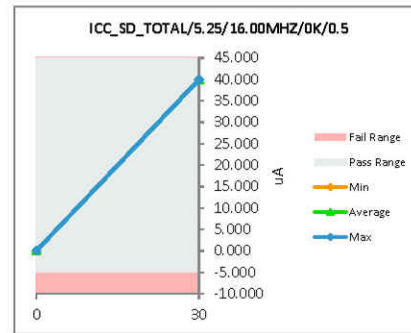


TID Report
ADC128S102PWT-SEP

ICC_SD_TOTAL/5.25/16.00MHZ/				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit				
Min Limit		-5	-5	
krads	Serial #	Pre	Post	Delta
0	23	0.080	0.076	0.004
0	24	0.066	0.066	0.000
30	1	0.068	39.999	-39.931
30	2	0.062	39.997	-39.935
30	3	0.062	39.999	-39.937
30	4	0.081	39.996	-39.915
30	5	0.071	39.996	-39.925
30	6	0.060	39.997	-39.936
30	7	0.071	39.996	-39.924
30	8	0.092	39.998	-39.906
30	9	0.058	39.998	-39.940
30	10	0.061	39.998	-39.937
30	11	0.067	39.999	-39.932
30	12	0.060	40.000	-39.940
30	13	0.057	39.999	-39.942
30	14	0.066	39.998	-39.932
30	15	0.057	39.997	-39.941
30	16	0.059	39.997	-39.938
30	17	0.058	39.998	-39.940
30	18	0.060	39.997	-39.937
30	19	0.059	39.996	-39.937
30	20	0.061	39.998	-39.936
30	21	0.061	39.996	-39.935
30	22	0.057	39.996	-39.939
	Max	0.092	40.000	0.004
	Average	0.065	36.670	-36.605
	Min	0.057	0.066	-39.942
	Std Dev	0.009	11.272	11.275

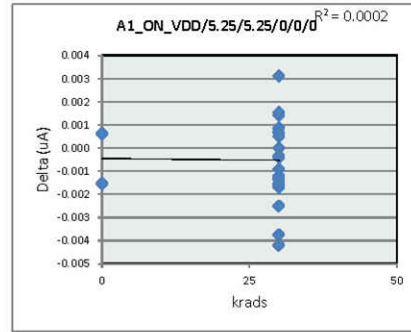


ICC_SD_TOTAL/5.25/16.00MHZ/		
Test Site		
Tester		
Test Number		
Max Limit		uA
Min Limit	-5	uA
krads	0	30
LL	-5.000	-5.000
Min	0.066	39.996
Average	0.071	39.997
Max	0.076	40.000
UL		

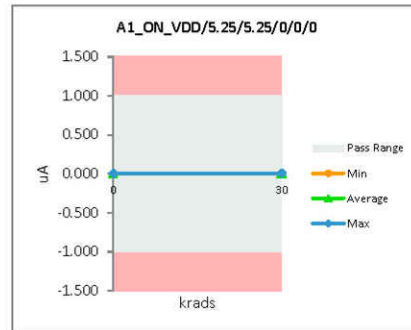


TID Report
ADC128S102PWT-SEP

A1_ON_VDD/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit		1	1	
Min Limit		-1	-1	
krads	Serial #	Pre	Post	Delta
0	23	0.004	0.004	0.001
0	24	0.004	0.006	-0.002
30	1	0.006	0.007	-0.001
30	2	0.007	0.006	0.001
30	3	0.006	0.004	0.001
30	4	0.004	0.007	-0.004
30	5	0.004	0.005	-0.001
30	6	0.003	0.007	-0.004
30	7	0.006	0.005	0.001
30	8	0.007	0.004	0.003
30	9	0.006	0.008	-0.002
30	10	0.005	0.005	0.000
30	11	0.006	0.006	0.000
30	12	0.007	0.007	0.001
30	13	0.003	0.006	-0.003
30	14	0.006	0.007	-0.002
30	15	0.005	0.005	0.001
30	16	0.005	0.007	-0.001
30	17	0.005	0.005	0.000
30	18	0.006	0.005	0.002
30	19	0.005	0.005	0.000
30	20	0.007	0.007	0.001
30	21	0.006	0.007	-0.001
30	22	0.005	0.007	-0.002
Max		0.007	0.008	0.003
Average		0.005	0.006	-0.001
Min		0.003	0.004	-0.004
Std Dev		0.001	0.001	0.002

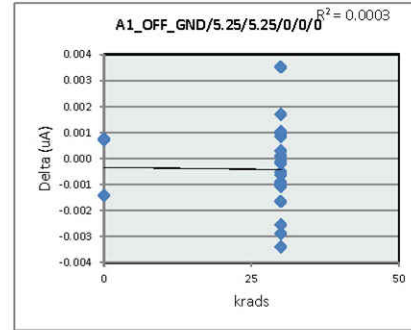


A1_ON_VDD/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit	1	uA
Min Limit	-1	uA
krads	0	30
LL	-1.000	-1.000
Min	0.004	0.004
Average	0.005	0.006
Max	0.006	0.008
UL	1.000	1.000



TID Report ADC128S102PWT-SEP

A1_OFF_GND/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit		1	1	
Min Limit		-1	-1	
kkrads	Serial #	Pre	Post	Delta
0	23	0.022	0.021	0.001
0	24	0.020	0.022	-0.001
30	1	0.022	0.022	0.000
30	2	0.019	0.019	0.000
30	3	0.021	0.023	-0.001
30	4	0.019	0.020	-0.001
30	5	0.020	0.021	-0.001
30	6	0.019	0.018	0.001
30	7	0.022	0.021	0.001
30	8	0.021	0.021	0.001
30	9	0.019	0.020	-0.001
30	10	0.021	0.022	-0.001
30	11	0.023	0.023	0.000
30	12	0.021	0.021	0.000
30	13	0.019	0.022	-0.003
30	14	0.022	0.021	0.001
30	15	0.019	0.022	-0.003
30	16	0.021	0.021	-0.001
30	17	0.022	0.020	0.002
30	18	0.020	0.020	0.000
30	19	0.018	0.021	-0.003
30	20	0.020	0.022	-0.002
30	21	0.019	0.022	-0.003
30	22	0.022	0.019	0.004
Max		0.023	0.023	0.004
Average		0.021	0.021	0.000
Min		0.018	0.018	-0.003
Std Dev		0.001	0.001	0.002

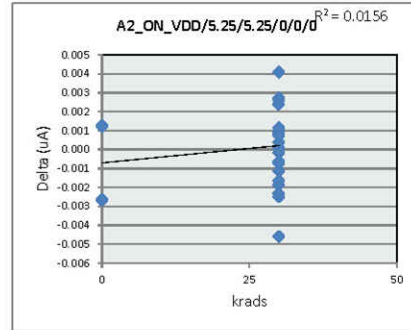


A1_OFF_GND/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit	1	uA
Min Limit	-1	uA
kkrads	0	30
LL	-1.000	-1.000
Min	0.021	0.018
Average	0.022	0.021
Max	0.022	0.023
UL	1.000	1.000



TID Report ADC128S102PWT-SEP

A2_ON_VDD/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit	uA		uA	
Max Limit	1		1	
Min Limit	-1		-1	
krams	Serial #	Pre	Post	Delta
0	23	-0.012	-0.009	-0.003
0	24	-0.009	-0.010	0.001
30	1	-0.012	-0.011	-0.001
30	2	-0.011	-0.009	-0.003
30	3	-0.010	-0.011	0.000
30	4	-0.012	-0.007	-0.005
30	5	-0.011	-0.009	-0.002
30	6	-0.010	-0.010	0.000
30	7	-0.009	-0.012	0.003
30	8	-0.008	-0.012	0.004
30	9	-0.009	-0.012	0.003
30	10	-0.012	-0.009	-0.002
30	11	-0.011	-0.012	0.001
30	12	-0.012	-0.010	-0.002
30	13	-0.009	-0.010	0.001
30	14	-0.010	-0.009	-0.001
30	15	-0.009	-0.010	0.001
30	16	-0.011	-0.011	0.001
30	17	-0.009	-0.010	0.001
30	18	-0.009	-0.011	0.003
30	19	-0.009	-0.008	-0.001
30	20	-0.008	-0.011	0.002
30	21	-0.009	-0.009	0.000
30	22	-0.011	-0.012	0.001
Max		-0.008	-0.007	0.004
Average		-0.010	-0.010	0.000
Min		-0.012	-0.012	-0.005
Std Dev		0.001	0.001	0.002

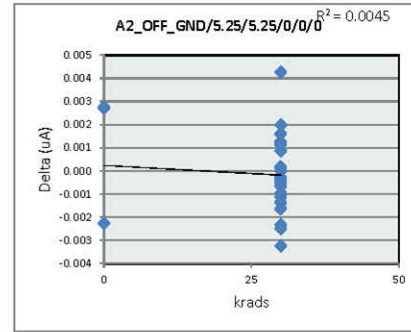


A2_ON_VDD/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit	1	uA
Min Limit	-1	uA
krams	0	30
LL	-1.000	-1.000
Min	-0.010	-0.012
Average	-0.010	-0.010
Max	-0.009	-0.007
UL	1.000	1.000

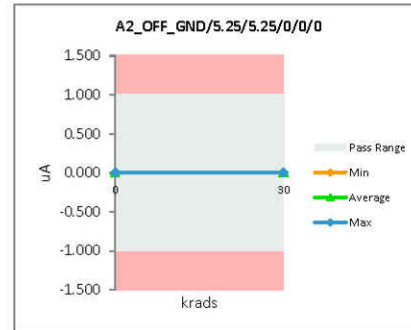


TID Report ADC128S102PWT-SEP

A2_OFF_GND/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit		1	1	
Min Limit		-1	-1	
kkrads	Serial #	Pre	Post	Delta
0	23	0.005	0.007	-0.002
0	24	0.007	0.004	0.003
30	1	0.006	0.007	-0.001
30	2	0.004	0.005	-0.002
30	3	0.004	0.006	-0.002
30	4	0.006	0.006	-0.001
30	5	0.003	0.004	-0.001
30	6	0.005	0.004	0.001
30	7	0.006	0.004	0.002
30	8	0.004	0.005	-0.001
30	9	0.005	0.005	0.000
30	10	0.008	0.006	0.002
30	11	0.004	0.005	-0.001
30	12	0.007	0.007	0.000
30	13	0.007	0.007	0.000
30	14	0.006	0.005	0.001
30	15	0.006	0.006	0.000
30	16	0.004	0.008	-0.003
30	17	0.007	0.002	0.004
30	18	0.004	0.004	0.000
30	19	0.005	0.006	-0.001
30	20	0.005	0.004	0.001
30	21	0.006	0.006	0.000
30	22	0.004	0.007	-0.003
Max		0.008	0.008	0.004
Average		0.005	0.005	0.000
Min		0.003	0.002	-0.003
Std Dev		0.001	0.001	0.002

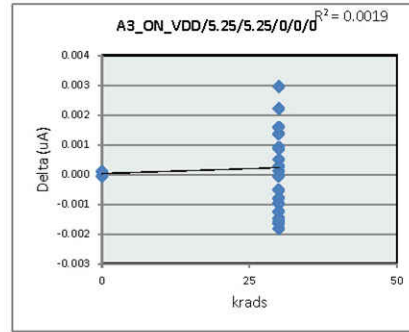


A2_OFF_GND/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit	1	uA
Min Limit	-1	uA
kkrads	0	30
LL	-1.000	-1.000
Min	0.004	0.002
Average	0.005	0.005
Max	0.007	0.008
UL	1.000	1.000

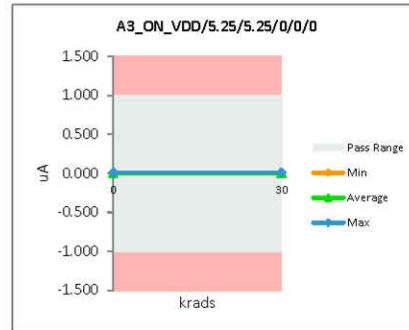


TID Report
ADC128S102PWT-SEP

A3_ON_VDD/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit		1	1	
Min Limit		-1	-1	
krads	Serial #	Pre	Post	Delta
0	23	0.005	0.005	0.000
0	24	0.007	0.007	0.000
30	1	0.006	0.005	0.001
30	2	0.007	0.005	0.002
30	3	0.006	0.006	0.000
30	4	0.005	0.006	-0.001
30	5	0.003	0.004	-0.001
30	6	0.007	0.005	0.002
30	7	0.005	0.004	0.001
30	8	0.004	0.005	0.000
30	9	0.006	0.005	0.001
30	10	0.004	0.005	-0.001
30	11	0.003	0.005	-0.002
30	12	0.006	0.004	0.003
30	13	0.006	0.005	0.000
30	14	0.005	0.007	-0.002
30	15	0.004	0.006	-0.002
30	16	0.006	0.005	0.001
30	17	0.007	0.006	0.001
30	18	0.006	0.004	0.001
30	19	0.005	0.005	0.001
30	20	0.007	0.005	0.002
30	21	0.005	0.007	-0.001
30	22	0.004	0.005	-0.001
Max		0.007	0.007	0.003
Average		0.005	0.005	0.000
Min		0.003	0.004	-0.002
Std Dev		0.001	0.001	0.001

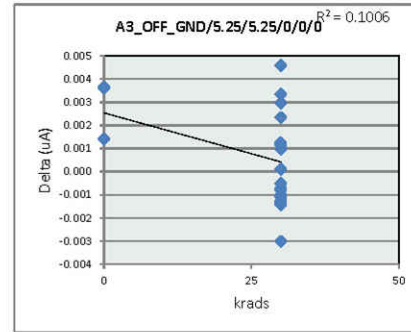


A3_ON_VDD/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit		1 uA
Min Limit		-1 uA
krads	0	30
LL	-1.000	-1.000
Min	0.005	0.004
Average	0.006	0.005
Max	0.007	0.007
UL	1.000	1.000



TID Report ADC128S102PWT-SEP

A3_OFF_GND/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit		1	1	
Min Limit		-1	-1	
krads	Serial #	Pre	Post	Delta
0	23	0.023	0.021	0.001
0	24	0.022	0.018	0.004
30	1	0.018	0.021	-0.003
30	2	0.021	0.020	0.001
30	3	0.019	0.021	-0.001
30	4	0.020	0.021	-0.001
30	5	0.019	0.019	0.000
30	6	0.021	0.022	-0.001
30	7	0.019	0.018	0.001
30	8	0.022	0.021	0.001
30	9	0.022	0.023	-0.001
30	10	0.021	0.019	0.002
30	11	0.021	0.021	-0.001
30	12	0.021	0.022	-0.001
30	13	0.022	0.019	0.003
30	14	0.021	0.022	-0.001
30	15	0.020	0.019	0.001
30	16	0.020	0.019	0.001
30	17	0.021	0.022	-0.001
30	18	0.023	0.019	0.003
30	19	0.022	0.020	0.001
30	20	0.021	0.022	-0.001
30	21	0.023	0.019	0.005
30	22	0.021	0.020	0.001
Max		0.023	0.023	0.005
Average		0.021	0.020	0.001
Min		0.018	0.018	-0.003
Std Dev		0.001	0.001	0.002

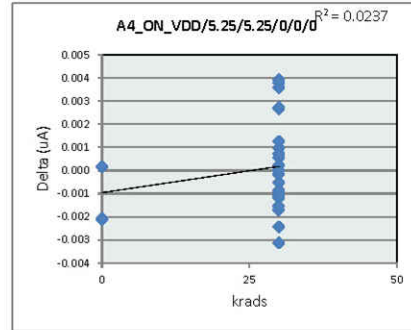


A3_OFF_GND/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit	1	uA
Min Limit	-1	uA
krads	0	30
LL	-1.000	-1.000
Min	0.018	0.018
Average	0.020	0.020
Max	0.021	0.023
UL	1.000	1.000



TID Report ADC128S102PWT-SEP

A4_ON_VDD/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit		1	1	
Min Limit		-1	-1	
krads	Serial #	Pre	Post	Delta
0	23	-0.008	-0.008	0.000
0	24	-0.012	-0.010	-0.002
30	1	-0.010	-0.009	-0.001
30	2	-0.008	-0.011	0.004
30	3	-0.012	-0.012	0.000
30	4	-0.011	-0.007	-0.003
30	5	-0.012	-0.013	0.001
30	6	-0.011	-0.010	-0.002
30	7	-0.009	-0.012	0.003
30	8	-0.012	-0.009	-0.002
30	9	-0.011	-0.010	-0.001
30	10	-0.013	-0.012	-0.001
30	11	-0.008	-0.009	0.001
30	12	-0.010	-0.010	0.001
30	13	-0.010	-0.009	-0.001
30	14	-0.012	-0.010	-0.002
30	15	-0.011	-0.012	0.001
30	16	-0.012	-0.011	-0.001
30	17	-0.010	-0.010	0.000
30	18	-0.008	-0.010	0.003
30	19	-0.008	-0.012	0.004
30	20	-0.012	-0.009	-0.003
30	21	-0.009	-0.012	0.004
30	22	-0.009	-0.009	0.000
Max		-0.008	-0.007	0.004
Average		-0.010	-0.010	0.000
Min		-0.013	-0.013	-0.003
Std Dev		0.002	0.001	0.002

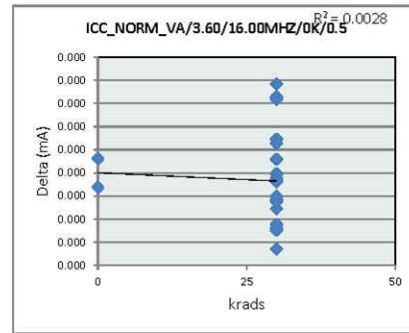


A4_ON_VDD/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit	1	uA
Min Limit	-1	uA
krads	0	30
LL	-1.000	-1.000
Min	-0.010	-0.013
Average	-0.009	-0.010
Max	-0.008	-0.007
UL	1.000	1.000

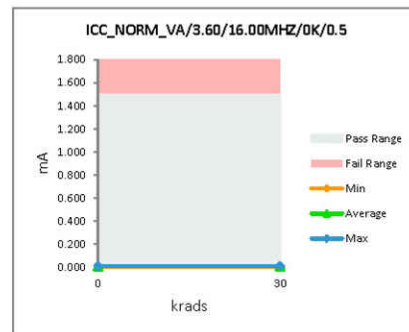


TID Report ADC128S102PWT-SEP

ICC_NORM_VA/3.60/16.00MHZ/				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		1.5	1.5	
Min Limit		0	0	
krads	Serial #	Pre	Post	Delta
0	23	0.008	0.008	0.000
0	24	0.008	0.008	0.000
30	1	0.008	0.008	0.000
30	2	0.008	0.008	0.000
30	3	0.008	0.008	0.000
30	4	0.008	0.008	0.000
30	5	0.008	0.008	0.000
30	6	0.008	0.008	0.000
30	7	0.008	0.008	0.000
30	8	0.008	0.008	0.000
30	9	0.008	0.008	0.000
30	10	0.008	0.008	0.000
30	11	0.008	0.008	0.000
30	12	0.008	0.008	0.000
30	13	0.008	0.008	0.000
30	14	0.008	0.008	0.000
30	15	0.008	0.008	0.000
30	16	0.007	0.008	0.000
30	17	0.008	0.008	0.000
30	18	0.008	0.008	0.000
30	19	0.007	0.007	0.000
30	20	0.008	0.008	0.000
30	21	0.008	0.008	0.000
30	22	0.008	0.008	0.000
Max		0.008	0.008	0.000
Average		0.008	0.008	0.000
Min		0.007	0.007	0.000
Std Dev		0.000	0.000	0.000

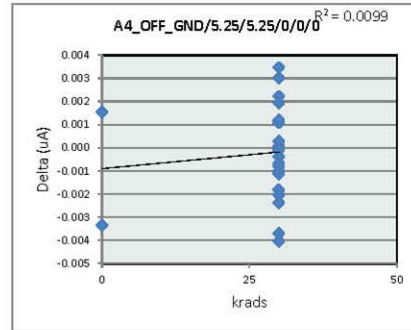


ICC_NORM_VA/3.60/16.00MHZ/		
Test Site		
Tester		
Test Number		
Max Limit		1.5 mA
Min Limit		0 mA
krads	0	30
LL	0.000	0.000
Min	0.008	0.007
Average	0.008	0.008
Max	0.008	0.008
UL	1.500	1.500

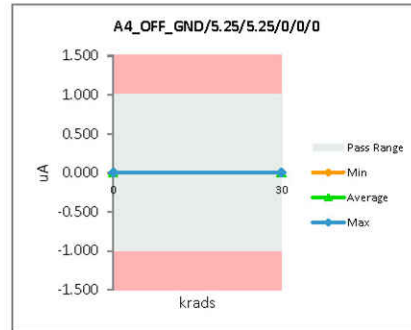


TID Report ADC128S102PWT-SEP

A4_OFF_GND/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit				
Max Limit	1	1	1	1
Min Limit	-1	-1	-1	-1
krads	Serial #	Pre	Post	Delta
0	23	0.008	0.006	0.002
0	24	0.005	0.008	-0.003
30	1	0.005	0.003	0.002
30	2	0.006	0.003	0.003
30	3	0.004	0.008	-0.004
30	4	0.003	0.004	-0.001
30	5	0.004	0.004	0.000
30	6	0.007	0.004	0.003
30	7	0.005	0.005	0.000
30	8	0.004	0.005	-0.001
30	9	0.003	0.005	-0.002
30	10	0.003	0.007	-0.004
30	11	0.003	0.005	-0.002
30	12	0.006	0.004	0.002
30	13	0.007	0.006	0.001
30	14	0.004	0.004	0.000
30	15	0.003	0.004	-0.001
30	16	0.006	0.005	0.001
30	17	0.006	0.006	0.000
30	18	0.005	0.006	0.000
30	19	0.004	0.006	-0.002
30	20	0.004	0.005	-0.001
30	21	0.005	0.006	-0.001
30	22	0.006	0.004	0.002
Max		0.008	0.008	0.003
Average		0.005	0.005	0.000
Min		0.003	0.003	-0.004
Std Dev		0.001	0.001	0.002

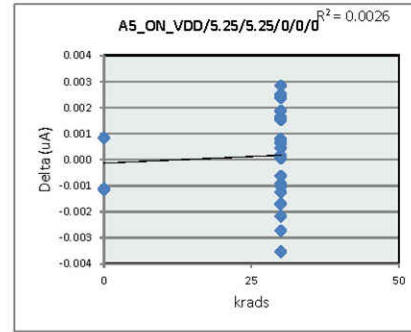


A4_OFF_GND/5.25/5.25/0/0		
Test Site		
Tester		
Test Number		
Max Limit	1	uA
Min Limit	-1	uA
krads	0	30
LL	-1.000	-1.000
Min	0.006	0.003
Average	0.007	0.005
Max	0.008	0.008
UL	1.000	1.000

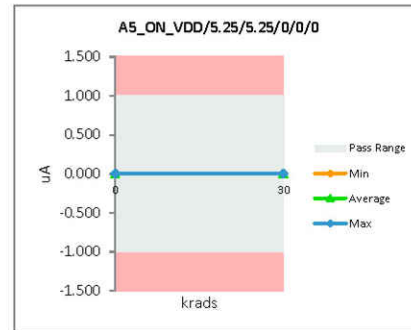


TID Report ADC128S102PWT-SEP

A5_ON_VDD/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit		1	1	
Min Limit		-1	-1	
kkrads	Serial #	Pre	Post	Delta
0	23	0.004	0.005	-0.001
0	24	0.006	0.005	0.001
30	1	0.005	0.005	0.000
30	2	0.005	0.004	0.001
30	3	0.006	0.004	0.002
30	4	0.006	0.003	0.003
30	5	0.003	0.005	-0.003
30	6	0.006	0.004	0.002
30	7	0.005	0.007	-0.002
30	8	0.005	0.005	0.001
30	9	0.004	0.006	-0.002
30	10	0.006	0.004	0.002
30	11	0.005	0.006	-0.001
30	12	0.007	0.006	0.000
30	13	0.004	0.008	-0.004
30	14	0.007	0.006	0.001
30	15	0.006	0.004	0.002
30	16	0.007	0.007	0.000
30	17	0.007	0.007	0.000
30	18	0.006	0.004	0.002
30	19	0.006	0.007	-0.001
30	20	0.003	0.004	-0.001
30	21	0.006	0.003	0.003
30	22	0.004	0.005	-0.001
Max		0.007	0.008	0.003
Average		0.005	0.005	0.000
Min		0.003	0.003	-0.004
Std Dev		0.001	0.001	0.002

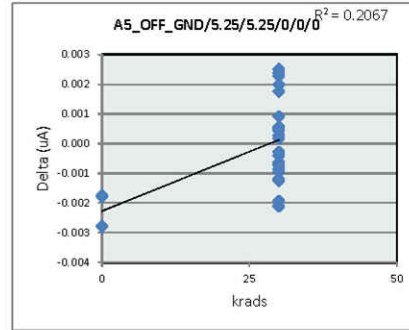


A5_ON_VDD/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit	1	uA
Min Limit	-1	uA
kkrads	0	30
LL	-1.000	-1.000
Min	0.005	0.003
Average	0.005	0.005
Max	0.005	0.008
UL	1.000	1.000

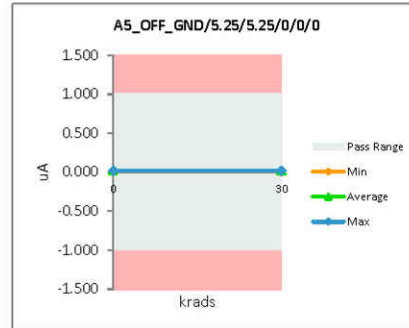


TID Report
ADC128S102PWT-SEP

A5_OFF_GND/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit		1	1	
Min Limit		-1	-1	
krads	Serial #	Pre	Post	Delta
0	23	0.020	0.022	-0.003
0	24	0.020	0.021	-0.002
30	1	0.021	0.021	-0.001
30	2	0.019	0.019	-0.001
30	3	0.021	0.022	-0.001
30	4	0.019	0.021	-0.002
30	5	0.021	0.021	0.000
30	6	0.022	0.019	0.003
30	7	0.022	0.022	0.000
30	8	0.020	0.020	0.000
30	9	0.020	0.019	0.002
30	10	0.018	0.018	0.000
30	11	0.019	0.020	-0.001
30	12	0.022	0.021	0.001
30	13	0.021	0.022	-0.001
30	14	0.021	0.019	0.002
30	15	0.020	0.020	0.000
30	16	0.021	0.019	0.002
30	17	0.020	0.021	-0.001
30	18	0.023	0.020	0.002
30	19	0.022	0.021	0.001
30	20	0.021	0.022	-0.001
30	21	0.019	0.021	-0.002
30	22	0.019	0.019	0.001
Max		0.023	0.022	0.003
Average		0.020	0.020	0.000
Min		0.018	0.018	-0.003
Std Dev		0.001	0.001	0.001

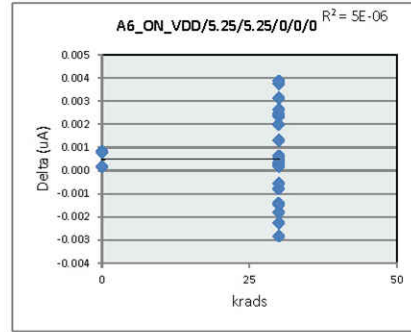


A5_OFF_GND/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit	1	uA
Min Limit	-1	uA
krads	0	30
LL	-1.000	-1.000
Min	0.021	0.018
Average	0.022	0.020
Max	0.022	0.022
UL	1.000	1.000



TID Report ADC128S102PWT-SEP

A6_ON_VDD/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit		1	1	
Min Limit		-1	-1	
krads	Serial #	Pre	Post	Delta
0	23	-0.010	-0.011	0.001
0	24	-0.010	-0.010	0.000
30	1	-0.011	-0.010	-0.001
30	2	-0.011	-0.011	0.000
30	3	-0.011	-0.009	-0.001
30	4	-0.010	-0.010	-0.001
30	5	-0.009	-0.009	0.000
30	6	-0.010	-0.012	0.002
30	7	-0.009	-0.013	0.004
30	8	-0.009	-0.008	-0.001
30	9	-0.011	-0.010	-0.001
30	10	-0.009	-0.012	0.004
30	11	-0.011	-0.013	0.001
30	12	-0.012	-0.012	0.000
30	13	-0.011	-0.008	-0.002
30	14	-0.011	-0.011	0.001
30	15	-0.009	-0.011	0.002
30	16	-0.007	-0.010	0.002
30	17	-0.012	-0.009	-0.003
30	18	-0.008	-0.011	0.003
30	19	-0.009	-0.011	0.003
30	20	-0.010	-0.011	0.000
30	21	-0.010	-0.009	-0.002
30	22	-0.012	-0.012	0.001
Max		-0.007	-0.008	0.004
Average		-0.010	-0.011	0.000
Min		-0.012	-0.013	-0.003
Std Dev		0.001	0.001	0.002

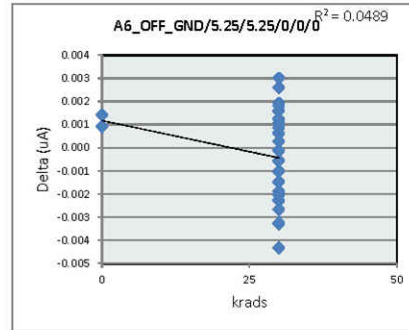


A6_ON_VDD/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit	1	uA
Min Limit	-1	uA
krads	0	30
LL	-1.000	-1.000
Min	-0.011	-0.013
Average	-0.010	-0.011
Max	-0.010	-0.008
UL	1.000	1.000

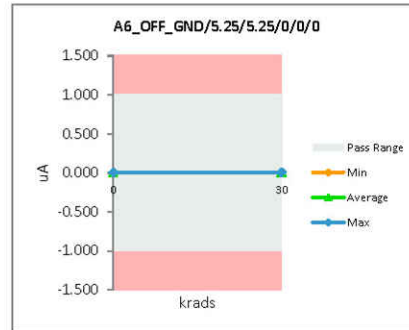


TID Report ADC128S102PWT-SEP

A6_OFF_GND/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit		1	1	
Min Limit		-1	-1	
krads	Serial #	Pre	Post	Delta
0	23	0.007	0.006	0.001
0	24	0.004	0.003	0.001
30	1	0.006	0.005	0.001
30	2	0.005	0.002	0.003
30	3	0.004	0.007	-0.002
30	4	0.006	0.007	-0.001
30	5	0.002	0.005	-0.003
30	6	0.003	0.005	-0.002
30	7	0.006	0.004	0.002
30	8	0.005	0.003	0.003
30	9	0.005	0.007	-0.002
30	10	0.008	0.007	0.000
30	11	0.005	0.006	-0.001
30	12	0.004	0.007	-0.003
30	13	0.004	0.006	-0.002
30	14	0.005	0.004	0.001
30	15	0.003	0.007	-0.004
30	16	0.004	0.003	0.001
30	17	0.006	0.007	-0.001
30	18	0.003	0.007	-0.003
30	19	0.006	0.004	0.002
30	20	0.007	0.006	0.002
30	21	0.006	0.006	0.000
30	22	0.006	0.005	0.001
Max		0.008	0.007	0.003
Average		0.005	0.005	0.000
Min		0.002	0.002	-0.004
Std Dev		0.001	0.002	0.002

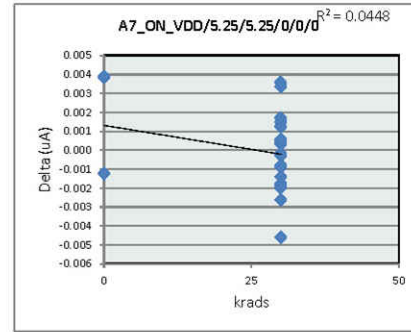


A6_OFF_GND/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit	1	uA
Min Limit	-1	uA
krads	0	30
LL	-1.000	-1.000
Min	0.003	0.002
Average	0.004	0.005
Max	0.006	0.007
UL	1.000	1.000

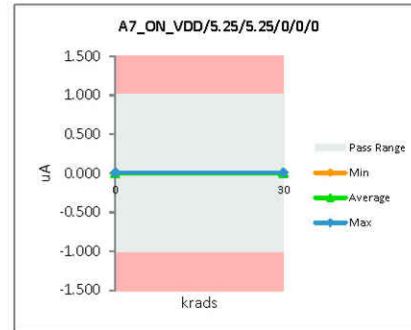


TID Report ADC128S102PWT-SEP

A7_ON_VDD/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit		1	1	
Min Limit		-1	-1	
krams	Serial #	Pre	Post	Delta
0	23	0.008	0.004	0.004
0	24	0.006	0.007	-0.001
30	1	0.006	0.004	0.002
30	2	0.006	0.006	0.000
30	3	0.007	0.006	0.000
30	4	0.006	0.006	-0.001
30	5	0.007	0.003	0.004
30	6	0.005	0.006	-0.001
30	7	0.004	0.006	-0.002
30	8	0.007	0.006	0.000
30	9	0.004	0.003	0.001
30	10	0.006	0.007	0.000
30	11	0.002	0.007	-0.005
30	12	0.005	0.007	-0.003
30	13	0.007	0.007	0.000
30	14	0.005	0.007	-0.002
30	15	0.004	0.005	-0.001
30	16	0.005	0.004	0.001
30	17	0.005	0.005	0.001
30	18	0.003	0.005	-0.002
30	19	0.005	0.007	-0.002
30	20	0.006	0.003	0.003
30	21	0.005	0.006	0.000
30	22	0.005	0.004	0.001
Max		0.008	0.007	0.004
Average		0.005	0.005	0.000
Min		0.002	0.003	-0.005
Std Dev		0.001	0.001	0.002

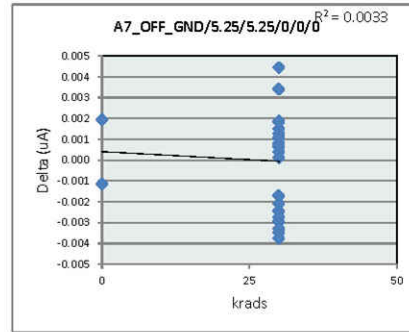


A7_ON_VDD/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit	1	uA
Min Limit	-1	uA
krams	0	30
LL	-1.000	-1.000
Min	0.004	0.003
Average	0.005	0.005
Max	0.007	0.007
UL	1.000	1.000

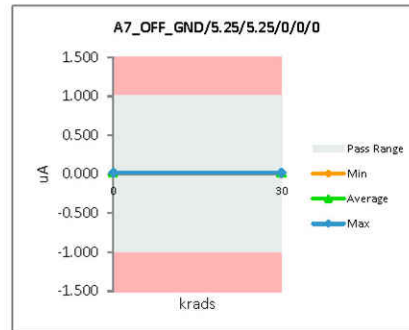


TID Report ADC128S102PWT-SEP

A7_OFF_GND/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit		1	1	
Min Limit		-1	-1	
krads	Serial #	Pre	Post	Delta
0	23	0.018	0.019	-0.001
0	24	0.021	0.019	0.002
30	1	0.022	0.020	0.002
30	2	0.019	0.020	-0.002
30	3	0.020	0.018	0.002
30	4	0.021	0.020	0.000
30	5	0.020	0.018	0.001
30	6	0.022	0.021	0.001
30	7	0.021	0.020	0.001
30	8	0.019	0.022	-0.002
30	9	0.022	0.018	0.004
30	10	0.021	0.020	0.001
30	11	0.020	0.018	0.002
30	12	0.023	0.023	0.000
30	13	0.018	0.021	-0.003
30	14	0.019	0.022	-0.003
30	15	0.019	0.018	0.001
30	16	0.019	0.021	-0.002
30	17	0.018	0.022	-0.003
30	18	0.022	0.019	0.003
30	19	0.019	0.022	-0.003
30	20	0.021	0.019	0.001
30	21	0.022	0.021	0.001
30	22	0.018	0.022	-0.004
Max		0.023	0.023	0.004
Average		0.020	0.020	0.000
Min		0.018	0.018	-0.004
Std Dev		0.002	0.001	0.002

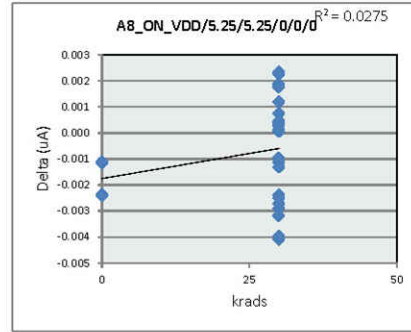


A7_OFF_GND/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit		1 uA
Min Limit		-1 uA
krads		0 30
LL	-1.000	-1.000
Min	0.019	0.018
Average	0.019	0.020
Max	0.019	0.023
UL	1.000	1.000



TID Report ADC128S102PWT-SEP

A8_ON_VDD/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit		1	1	
Min Limit		-1	-1	
krams	Serial #	Pre	Post	Delta
0	23	-0.011	-0.010	-0.001
0	24	-0.012	-0.010	-0.002
30	1	-0.011	-0.009	-0.002
30	2	-0.009	-0.011	0.002
30	3	-0.008	-0.009	0.000
30	4	-0.011	-0.008	-0.003
30	5	-0.011	-0.010	-0.001
30	6	-0.010	-0.009	-0.001
30	7	-0.008	-0.010	0.002
30	8	-0.012	-0.008	-0.004
30	9	-0.009	-0.009	0.000
30	10	-0.011	-0.010	-0.001
30	11	-0.009	-0.011	0.001
30	12	-0.010	-0.008	-0.003
30	13	-0.009	-0.010	0.001
30	14	-0.009	-0.010	0.000
30	15	-0.010	-0.011	0.000
30	16	-0.012	-0.009	-0.003
30	17	-0.009	-0.010	0.000
30	18	-0.011	-0.011	0.000
30	19	-0.008	-0.010	0.002
30	20	-0.009	-0.011	0.002
30	21	-0.013	-0.010	-0.003
30	22	-0.012	-0.008	-0.004
Max		-0.008	-0.008	0.002
Average		-0.010	-0.010	-0.001
Min		-0.013	-0.011	-0.004
Std Dev		0.001	0.001	0.002

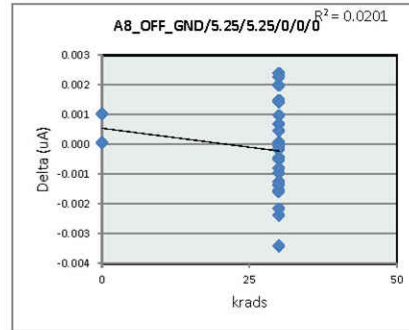


A8_ON_VDD/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit	1	uA
Min Limit	-1	uA
krams	0	30
LL	-1.000	-1.000
Min	-0.010	-0.011
Average	-0.010	-0.010
Max	-0.010	-0.008
UL	1.000	1.000



TID Report ADC128S102PWT-SEP

A8_OFF_GND/5.25/5.25/0/0/0				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit		1	1	
Min Limit		-1	-1	
krads	Serial #	Pre	Post	Delta
0	23	0.007	0.006	0.001
0	24	0.004	0.003	0.000
30	1	0.003	0.007	-0.003
30	2	0.003	0.003	0.000
30	3	0.007	0.006	0.001
30	4	0.003	0.006	-0.002
30	5	0.006	0.004	0.002
30	6	0.006	0.006	0.000
30	7	0.006	0.005	0.001
30	8	0.006	0.003	0.002
30	9	0.003	0.004	-0.001
30	10	0.004	0.006	-0.002
30	11	0.005	0.007	-0.002
30	12	0.005	0.003	0.001
30	13	0.004	0.006	-0.002
30	14	0.004	0.005	0.000
30	15	0.005	0.002	0.002
30	16	0.006	0.007	-0.001
30	17	0.007	0.006	0.001
30	18	0.004	0.005	-0.001
30	19	0.006	0.007	-0.001
30	20	0.006	0.006	0.000
30	21	0.005	0.005	0.000
30	22	0.005	0.006	-0.001
Max		0.007	0.007	0.002
Average		0.005	0.005	0.000
Min		0.003	0.002	-0.003
Std Dev		0.001	0.001	0.002

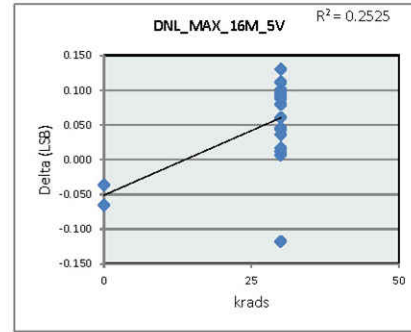


A8_OFF_GND/5.25/5.25/0/0/0		
Test Site		
Tester		
Test Number		
Max Limit	1	uA
Min Limit	-1	uA
krads	0	30
LL	-1.000	-1.000
Min	0.003	0.002
Average	0.005	0.005
Max	0.006	0.007
UL	1.000	1.000

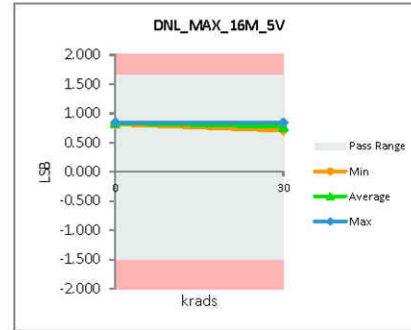


TID Report ADC128S102PWT-SEP

DNL_MAX_16M_5V				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		1.65	1.65	
Min Limit		-1.5	-1.5	
krams	Serial #	Pre	Post	Delta
0	23	0.806	0.843	-0.037
0	24	0.748	0.814	-0.066
30	1	0.815	0.702	0.112
30	2	0.873	0.829	0.043
30	3	0.741	0.735	0.006
30	4	0.896	0.800	0.096
30	5	0.830	0.750	0.080
30	6	0.814	0.807	0.007
30	7	0.808	0.763	0.045
30	8	0.846	0.829	0.017
30	9	0.836	0.756	0.080
30	10	0.845	0.754	0.091
30	11	0.852	0.764	0.089
30	12	0.770	0.758	0.012
30	13	0.881	0.845	0.036
30	14	0.881	0.750	0.130
30	15	0.815	0.723	0.093
30	16	0.719	0.837	-0.118
30	17	0.865	0.764	0.101
30	18	0.822	0.734	0.087
30	19	0.749	0.732	0.017
30	20	0.916	0.786	0.130
30	21	0.843	0.732	0.112
30	22	0.811	0.750	0.061
Max		0.916	0.845	0.130
Average		0.824	0.773	0.051
Min		0.719	0.702	-0.118
Std Dev		0.051	0.042	0.063

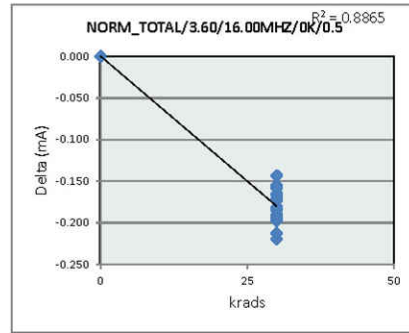


DNL_MAX_16M_5V		
Test Site		
Tester		
Test Number		
Max Limit	1.65	LSB
Min Limit	-1.5	LSB
krams	0	30
LL	-1.500	-1.500
Min	0.814	0.703
Average	0.829	0.768
Max	0.843	0.845
UL	1.650	1.650

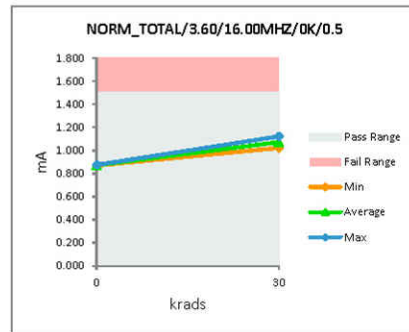


TID Report
ADC128S102PWT-SEP

NORM_TOTAL/3.60/16.00MHZ/0				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		1.5	1.5	
Min Limit		0	0	
krads	Serial #	Pre	Post	Delta
0	23	0.869	0.869	0.000
0	24	0.875	0.875	0.000
30	1	0.890	1.087	-0.198
30	2	0.881	1.046	-0.165
30	3	0.883	1.038	-0.155
30	4	0.921	1.094	-0.173
30	5	0.892	1.048	-0.156
30	6	0.877	1.020	-0.143
30	7	0.900	1.120	-0.220
30	8	0.902	1.086	-0.184
30	9	0.877	1.058	-0.182
30	10	0.909	1.106	-0.197
30	11	0.910	1.107	-0.196
30	12	0.879	1.046	-0.167
30	13	0.896	1.070	-0.174
30	14	0.905	1.118	-0.213
30	15	0.912	1.097	-0.185
30	16	0.867	1.039	-0.172
30	17	0.879	1.052	-0.173
30	18	0.880	1.039	-0.158
30	19	0.862	1.053	-0.191
30	20	0.874	1.065	-0.191
30	21	0.884	1.066	-0.182
30	22	0.907	1.101	-0.194
Max		0.921	1.120	0.000
Average		0.889	1.054	-0.165
Min		0.862	0.869	-0.220
Std Dev		0.016	0.063	0.054

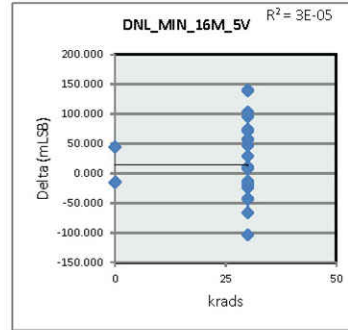


NORM_TOTAL/3.60/16.00MHZ		
Test Site		
Tester		
Test Number		
Max Limit	1.5	mA
Min Limit	0	mA
krads	0	30
LL	0.000	0.000
Min	0.869	1.020
Average	0.872	1.071
Max	0.875	1.120
UL	1.500	1.500

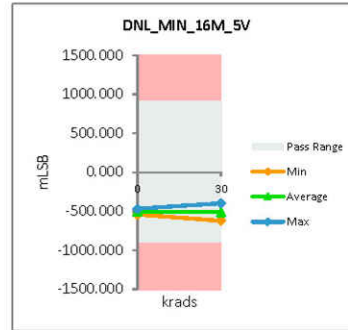


TID Report ADC128S102PWT-SEP

DNL_MIN_16M_5V				
Test Site				
Tester				
Test Number				
Unit		mLSB	mLSB	
Max Limit		900	900	
Min Limit		-900	-900	
krads	Serial #	Pre	Post	Delta
0	23	-500.743	-544.774	44.031
0	24	-485.949	-471.203	-14.746
30	1	-507.828	-485.677	-22.151
30	2	-478.497	-488.949	10.452
30	3	-485.859	-471.028	-14.831
30	4	-617.773	-575.102	-42.671
30	5	-434.197	-573.777	139.580
30	6	-478.634	-574.038	95.404
30	7	-559.218	-514.943	-44.275
30	8	-482.509	-529.174	46.665
30	9	-471.144	-427.004	-44.140
30	10	-463.648	-536.902	73.254
30	11	-500.147	-507.627	7.480
30	12	-522.668	-625.439	102.771
30	13	-573.916	-549.736	-24.180
30	14	-526.308	-508.616	-17.692
30	15	-529.680	-515.018	-14.662
30	16	-465.119	-522.462	57.343
30	17	-478.530	-507.741	29.211
30	18	-464.980	-537.213	72.233
30	19	-412.263	-398.305	-13.958
30	20	-559.362	-456.212	-103.150
30	21	-559.378	-493.162	-66.216
30	22	-451.051	-522.272	71.221
Max		-412.263	-398.305	139.580
Average		-500.392	-514.016	13.624
Min		-617.773	-625.439	-103.150
Std Dev		48.067	49.753	59.622

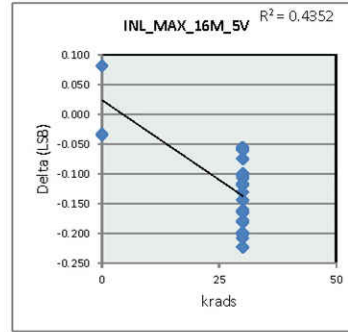


DNL_MIN_16M_5V		
Test Site		
Tester		
Test Number		
Max Limit	900	mLSB
Min Limit	-900	mLSB
krads	0	30
LL	-900.000	-900.000
Min	-544.774	-625.439
Average	-507.989	-514.564
Max	-471.203	-398.305
UL	900.000	900.000

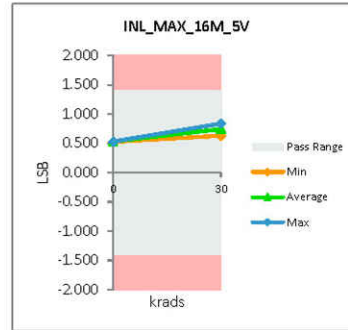


**TID Report
ADC128S102PWT-SEP**

INL_MAX_16M_5V				
Test Site				
Tester				
Test Number				
Unit				
LSB				
Max Limit				
Min Limit				
krads	Serial #	Pre	Post	Delta
0	23	0.490	0.524	-0.034
0	24	0.610	0.529	0.081
30	1	0.611	0.731	-0.120
30	2	0.620	0.728	-0.108
30	3	0.573	0.775	-0.202
30	4	0.643	0.747	-0.104
30	5	0.589	0.645	-0.056
30	6	0.626	0.825	-0.199
30	7	0.672	0.747	-0.075
30	8	0.575	0.741	-0.165
30	9	0.494	0.625	-0.131
30	10	0.527	0.707	-0.180
30	11	0.597	0.806	-0.208
30	12	0.638	0.757	-0.119
30	13	0.617	0.798	-0.182
30	14	0.713	0.769	-0.055
30	15	0.630	0.746	-0.116
30	16	0.606	0.830	-0.223
30	17	0.583	0.683	-0.100
30	18	0.702	0.833	-0.131
30	19	0.500	0.678	-0.178
30	20	0.608	0.668	-0.060
30	21	0.551	0.712	-0.161
30	22	0.620	0.764	-0.144
Max		0.713	0.833	0.081
Average		0.600	0.724	-0.124
Min		0.490	0.524	-0.223
Std Dev		0.058	0.082	0.069

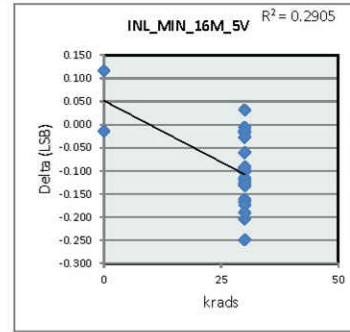


INL_MAX_16M_5V		
Test Site		
Tester		
Test Number		
Unit		
LSB		
Max Limit		
Min Limit		
krads	0	30
LL	-1.400	-1.400
Min	0.524	0.625
Average	0.526	0.742
Max	0.529	0.833
UL	1.400	1.400

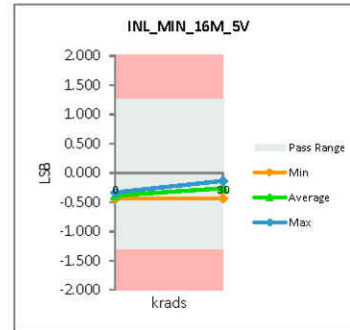


TID Report ADC128S102PWT-SEP

INL_MIN_16M_5V				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		1.25	1.25	
Min Limit		-1.3	-1.3	
krams	Serial #	Pre	Post	Delta
0	23	-0.349	-0.335	-0.014
0	24	-0.323	-0.440	0.117
30	1	-0.288	-0.282	-0.006
30	2	-0.479	-0.383	-0.096
30	3	-0.502	-0.340	-0.162
30	4	-0.451	-0.438	-0.014
30	5	-0.291	-0.230	-0.060
30	6	-0.407	-0.203	-0.204
30	7	-0.331	-0.362	0.032
30	8	-0.350	-0.249	-0.102
30	9	-0.383	-0.254	-0.130
30	10	-0.392	-0.296	-0.096
30	11	-0.410	-0.219	-0.190
30	12	-0.402	-0.235	-0.166
30	13	-0.376	-0.285	-0.091
30	14	-0.365	-0.348	-0.017
30	15	-0.305	-0.189	-0.116
30	16	-0.388	-0.139	-0.249
30	17	-0.303	-0.185	-0.117
30	18	-0.318	-0.196	-0.122
30	19	-0.328	-0.196	-0.132
30	20	-0.354	-0.326	-0.028
30	21	-0.390	-0.216	-0.174
30	22	-0.350	-0.222	-0.128
Max		-0.288	-0.139	0.117
Average		-0.368	-0.274	-0.094
Min		-0.502	-0.440	-0.249
Std Dev		0.056	0.082	0.083

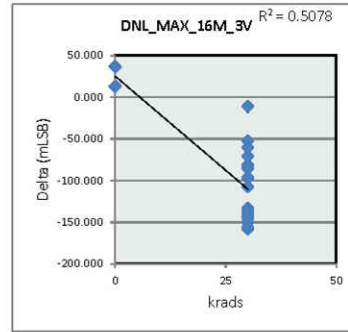


INL_MIN_16M_5V		
Test Site		
Tester		
Test Number		
Max Limit		1.25 LSB
Min Limit		-1.3 LSB
krams		0 30
LL	-1.300	-1.300
Min	-0.440	-0.438
Average	-0.387	-0.263
Max	-0.335	-0.139
UL	1.250	1.250

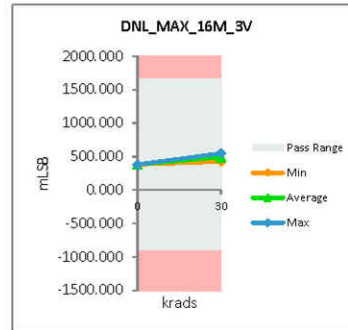


TID Report
ADC128S102PWT-SEP

DNL_MAX_16M_3V				
Test Site				
Tester				
Test Number				
Unit		mLSB	mLSB	
Max Limit		1650	1650	
Min Limit		-900	-900	
krads	Serial #	Pre	Post	Delta
0	23	390.004	376.827	13.177
0	24	415.413	378.641	36.772
30	1	366.135	503.130	-136.995
30	2	374.438	516.629	-142.191
30	3	382.669	515.367	-132.698
30	4	406.244	514.022	-107.778
30	5	335.092	442.788	-107.696
30	6	348.526	434.530	-86.004
30	7	350.198	431.546	-81.348
30	8	415.359	467.602	-52.243
30	9	378.846	449.571	-70.725
30	10	390.606	529.781	-139.175
30	11	370.344	515.318	-144.974
30	12	419.980	480.764	-60.784
30	13	357.030	507.248	-150.218
30	14	410.738	421.970	-11.232
30	15	352.147	486.662	-134.515
30	16	389.998	532.291	-142.293
30	17	367.130	451.807	-84.677
30	18	364.040	522.488	-158.448
30	19	390.033	546.118	-156.085
30	20	384.430	522.610	-138.180
30	21	372.580	468.630	-96.050
30	22	359.436	456.541	-97.105
Max		419.980	546.118	36.772
Average		378.809	478.037	-99.228
Min		335.092	376.827	-158.448
Std Dev		23.366	47.582	53.680

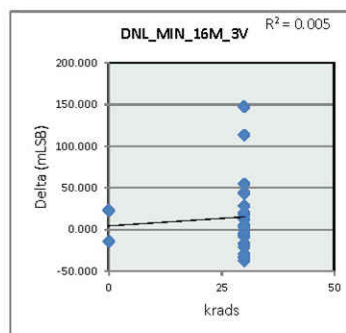


DNL_MAX_16M_3V		
Test Site		
Tester		
Test Number		
Max Limit	1650	mLSB
Min Limit	-900	mLSB
krads	0	30
LL	-900.000	-900.000
Min	376.827	421.970
Average	377.734	487.155
Max	378.641	546.118
UL	1650.000	1650.000

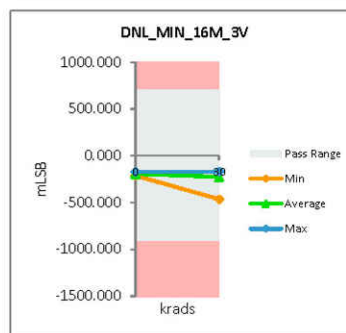


TID Report ADC128S102PWT-SEP

DNL_MIN_16M_3V				
Test Site				
Tester				
Test Number				
Unit		mLSB	mLSB	
Max Limit		700	700	
Min Limit		-900	-900	
krad	Serial #	Pre	Post	Delta
0	23	-189.947	-175.146	-14.801
0	24	-189.952	-213.003	23.051
30	1	-190.238	-244.485	54.247
30	2	-186.417	-299.786	113.369
30	3	-318.284	-465.628	147.344
30	4	-186.635	-230.531	43.896
30	5	-217.666	-201.216	-16.450
30	6	-186.577	-182.816	-3.761
30	7	-209.667	-222.160	12.493
30	8	-207.702	-251.062	43.360
30	9	-191.535	-209.719	18.184
30	10	-208.273	-200.225	-8.048
30	11	-222.655	-193.636	-29.019
30	12	-281.122	-262.973	-18.149
30	13	-198.318	-194.352	-3.966
30	14	-188.158	-191.401	3.243
30	15	-195.566	-214.740	19.174
30	16	-186.952	-207.253	20.301
30	17	-206.420	-168.691	-37.729
30	18	-185.604	-213.363	27.760
30	19	-231.633	-209.883	-21.750
30	20	-219.478	-214.492	-4.986
30	21	-214.800	-220.053	5.253
30	22	-224.826	-191.272	-33.554
Max		-185.604	-168.691	147.344
Average		-209.934	-224.079	14.144
Min		-318.284	-465.628	-37.729
Std Dev		31.595	59.019	43.699

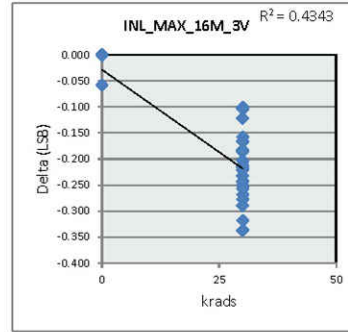


DNL_MIN_16M_3V		
Test Site		
Tester		
Test Number		
Max Limit	700	mLSB
Min Limit	-900	mLSB
krad	0	30
LL	-900.000	-900.000
Min	-213.003	-465.628
Average	-194.075	-226.806
Max	-175.146	-168.691
UL	700.000	700.000

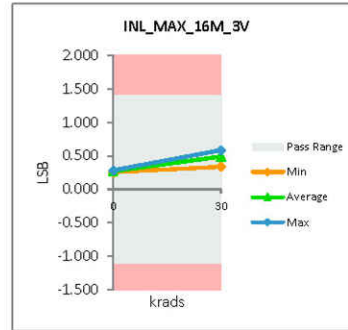


TID Report ADC128S102PWT-SEP

INL_MAX_16M_3V				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		1.4	1.4	
Min Limit		-1.1	-1.1	
krads	Serial #	Pre	Post	Delta
0	23	0.197	0.255	-0.058
0	24	0.281	0.281	0.000
30	1	0.375	0.562	-0.186
30	2	0.300	0.552	-0.252
30	3	0.284	0.553	-0.269
30	4	0.294	0.536	-0.243
30	5	0.237	0.395	-0.158
30	6	0.256	0.357	-0.101
30	7	0.346	0.579	-0.233
30	8	0.211	0.415	-0.204
30	9	0.229	0.333	-0.105
30	10	0.242	0.457	-0.215
30	11	0.300	0.521	-0.221
30	12	0.318	0.440	-0.122
30	13	0.241	0.579	-0.337
30	14	0.295	0.508	-0.214
30	15	0.281	0.497	-0.216
30	16	0.264	0.583	-0.318
30	17	0.201	0.478	-0.278
30	18	0.257	0.500	-0.243
30	19	0.349	0.516	-0.167
30	20	0.216	0.506	-0.290
30	21	0.207	0.464	-0.258
30	22	0.239	0.422	-0.183
	Max	0.375	0.583	0.000
	Average	0.268	0.470	-0.203
	Min	0.197	0.255	-0.337
	Std Dev	0.049	0.093	0.081

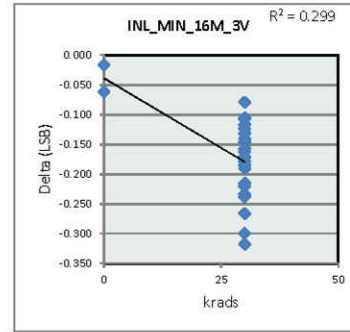


INL_MAX_16M_3V		
Test Site		
Tester		
Test Number		
Max Limit	1.4	LSB
Min Limit	-1.1	LSB
krads	0	30
LL	-1.100	-1.100
Min	0.255	0.333
Average	0.268	0.489
Max	0.281	0.583
UL	1.400	1.400

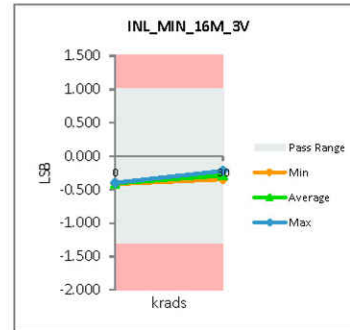


**TID Report
ADC128S102PWT-SEP**

INL_MIN_16M_3V				
Test Site				
Tester				
Test Number				
Unit				
Max Limit				
Min Limit				
krams	Serial #	Pre	Post	Delta
0	23	-0.482	-0.420	-0.062
0	24	-0.415	-0.399	-0.016
30	1	-0.399	-0.320	-0.079
30	2	-0.458	-0.296	-0.162
30	3	-0.579	-0.279	-0.299
30	4	-0.616	-0.298	-0.318
30	5	-0.369	-0.223	-0.146
30	6	-0.443	-0.338	-0.105
30	7	-0.549	-0.315	-0.234
30	8	-0.419	-0.295	-0.124
30	9	-0.412	-0.271	-0.140
30	10	-0.444	-0.287	-0.157
30	11	-0.433	-0.301	-0.131
30	12	-0.545	-0.329	-0.215
30	13	-0.505	-0.315	-0.191
30	14	-0.452	-0.344	-0.107
30	15	-0.445	-0.274	-0.170
30	16	-0.487	-0.249	-0.238
30	17	-0.471	-0.250	-0.221
30	18	-0.520	-0.254	-0.266
30	19	-0.360	-0.243	-0.117
30	20	-0.470	-0.291	-0.179
30	21	-0.505	-0.320	-0.185
30	22	-0.411	-0.239	-0.172
Max		-0.360	-0.223	-0.016
Average		-0.466	-0.298	-0.168
Min		-0.616	-0.420	-0.318
Std Dev		0.064	0.047	0.073

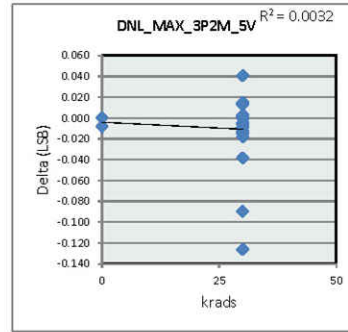


INL_MIN_16M_3V		
Test Site		
Tester		
Test Number		
Max Limit		
Min Limit		
krams	0	30
LL	-1.300	-1.300
Min	-0.420	-0.344
Average	-0.409	-0.288
Max	-0.399	-0.223
UL	1.000	1.000

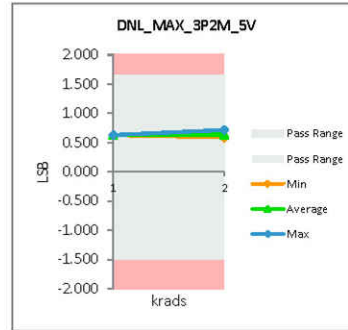


TID Report ADC128S102PWT-SEP

DNL_MAX_3P2M_5V				
Test Site				
Tester				
Test Number				
Unit				
Max Limit				
Min Limit				
krads	Serial #	Pre	Post	Delta
0	23	0.621	0.629	-0.008
0	24	0.629	0.629	0.000
30	1	0.629	0.635	-0.006
30	2	0.588	0.714	-0.126
30	3	0.615	0.629	-0.015
30	4	0.629	0.617	0.013
30	5	0.629	0.629	0.000
30	6	0.629	0.629	0.000
30	7	0.600	0.690	-0.090
30	8	0.625	0.629	-0.004
30	9	0.630	0.630	0.000
30	10	0.629	0.615	0.015
30	11	0.625	0.638	-0.013
30	12	0.629	0.667	-0.038
30	13	0.629	0.630	-0.001
30	14	0.629	0.615	0.014
30	15	0.629	0.589	0.041
30	16	0.629	0.629	-0.001
30	17	0.629	0.629	0.000
30	18	0.629	0.638	-0.009
30	19	0.625	0.630	-0.005
30	20	0.630	0.629	0.001
30	21	0.629	0.647	-0.018
30	22	0.609	0.606	0.003
Max		0.630	0.714	0.041
Average		0.624	0.634	-0.010
Min		0.588	0.589	-0.126
Std Dev		0.011	0.026	0.034

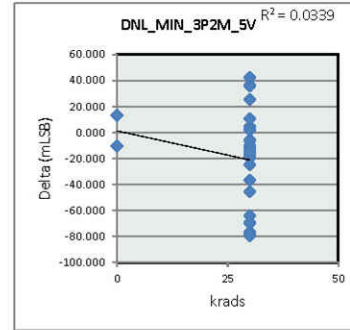


DNL_MAX_3P2M_5V		
Test Site		
Tester		
Test Number		
Max Limit		
Min Limit		
krads	0	30
LL	-1.500	-1.500
Min	0.629	0.589
Average	0.629	0.635
Max	0.629	0.714
UL	1.650	1.650

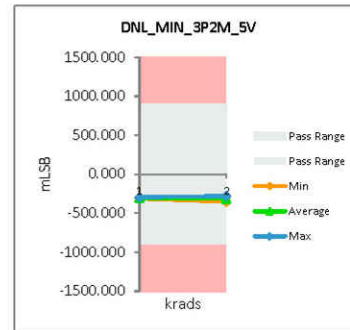


TID Report ADC128S102PWT-SEP

DNL_MIN_3P2M_5V				
Test Site				
Tester				
Test Number				
Unit		mLSB	mLSB	
Max Limit		900	900	
Min Limit		-900	-900	
krads	Serial #	Pre	Post	Delta
0	23	-299.264	-312.240	12.976
0	24	-309.141	-298.687	-10.454
30	1	-322.528	-311.938	-10.590
30	2	-307.440	-288.339	-19.101
30	3	-327.403	-315.444	-11.959
30	4	-343.859	-324.902	-18.957
30	5	-342.588	-306.018	-36.570
30	6	-313.610	-317.744	4.134
30	7	-350.513	-281.144	-69.369
30	8	-335.388	-317.742	-17.646
30	9	-316.565	-296.947	-19.618
30	10	-334.391	-288.755	-45.636
30	11	-405.309	-335.655	-69.654
30	12	-375.683	-298.854	-76.829
30	13	-430.679	-351.576	-79.103
30	14	-318.028	-354.001	35.973
30	15	-337.014	-330.876	-6.138
30	16	-281.338	-323.675	42.337
30	17	-306.304	-307.914	1.610
30	18	-333.975	-344.381	10.406
30	19	-292.275	-317.614	25.339
30	20	-371.267	-307.147	-64.120
30	21	-302.022	-287.043	-14.979
30	22	-341.839	-317.134	-24.705
	Max	-281.338	-281.144	42.337
	Average	-333.268	-313.990	-19.277
	Min	-430.679	-354.001	-79.103
	Std Dev	34.919	19.744	34.338

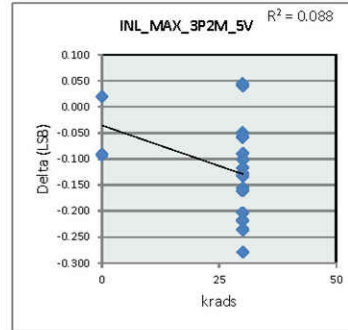


DNL_MIN_3P2M_5V		
Test Site		
Tester		
Test Number		
Max Limit	900	mLSB
Min Limit	-900	mLSB
krads	0	30
LL	-900.000	-900.000
Min	-312.240	-354.001
Average	-305.464	-314.766
Max	-298.687	-281.144
UL	900.000	900.000

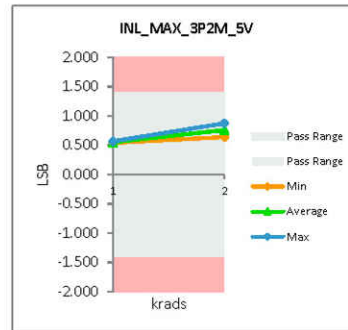


TID Report
ADC128S102PWT-SEP

INL_MAX_3P2M_5V				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		1.4	1.4	
Min Limit		-1.4	-1.4	
krads	Serial #	Pre	Post	Delta
0	23	0.474	0.566	-0.092
0	24	0.558	0.538	0.020
30	1	0.624	0.724	-0.101
30	2	0.640	0.857	-0.218
30	3	0.636	0.839	-0.203
30	4	0.829	0.788	0.041
30	5	0.573	0.729	-0.156
30	6	0.717	0.766	-0.049
30	7	0.519	0.754	-0.235
30	8	0.640	0.773	-0.133
30	9	0.590	0.649	-0.059
30	10	0.735	0.690	0.045
30	11	0.591	0.718	-0.127
30	12	0.781	0.831	-0.050
30	13	0.653	0.742	-0.090
30	14	0.606	0.738	-0.132
30	15	0.536	0.638	-0.102
30	16	0.535	0.771	-0.236
30	17	0.583	0.863	-0.279
30	18	0.647	0.764	-0.117
30	19	0.499	0.703	-0.204
30	20	0.654	0.873	-0.219
30	21	0.663	0.824	-0.161
30	22	0.595	0.652	-0.057
Max		0.829	0.873	0.045
Average		0.620	0.741	-0.121
Min		0.474	0.538	-0.279
Std Dev		0.086	0.088	0.089

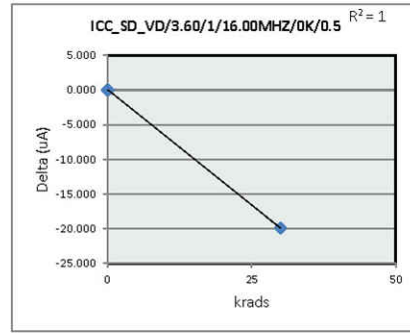


INL_MAX_3P2M_5V		
Test Site		
Tester		
Test Number		
Max Limit	1.4	LSB
Min Limit	-1.4	LSB
krads	0	30
LL	-1.400	-1.400
Min	0.538	0.638
Average	0.552	0.758
Max	0.566	0.873
UL	1.400	1.400

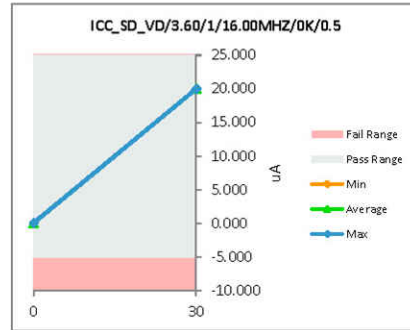


**TID Report
ADC128S102PWT-SEP**

ICC_SD_VD/3.60/1/16.00MHZ/0K				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit				
Min Limit		-5	-5	
krads	Serial #	Pre	Post	Delta
0	23	0.073	0.073	0.000
0	24	0.067	0.065	0.001
30	1	0.063	19.998	-19.935
30	2	0.061	19.998	-19.937
30	3	0.059	19.999	-19.940
30	4	0.071	19.999	-19.928
30	5	0.059	19.999	-19.940
30	6	0.061	19.998	-19.937
30	7	0.056	19.998	-19.941
30	8	0.075	19.999	-19.924
30	9	0.057	19.998	-19.941
30	10	0.057	20.000	-19.942
30	11	0.056	19.999	-19.942
30	12	0.056	19.998	-19.943
30	13	0.056	19.999	-19.944
30	14	0.060	19.998	-19.938
30	15	0.054	19.999	-19.945
30	16	0.053	19.998	-19.945
30	17	0.060	19.998	-19.938
30	18	0.063	19.998	-19.935
30	19	0.058	19.999	-19.941
30	20	0.057	19.998	-19.941
30	21	0.055	19.999	-19.944
30	22	0.051	19.999	-19.947
Max		0.075	20.000	0.001
Average		0.060	18.338	-18.278
Min		0.051	0.065	-19.947
Std Dev		0.006	5.627	5.630

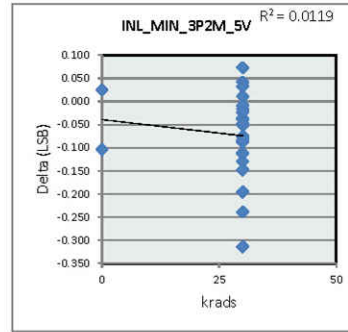


ICC_SD_VD/3.60/1/16.00MHZ		
Test Site		
Tester		
Test Number		
Max Limit		uA
Min Limit		uA
krads	0	30
LL	-5.000	-5.000
Min	0.065	19.998
Average	0.069	19.999
Max	0.073	20.000
UL		

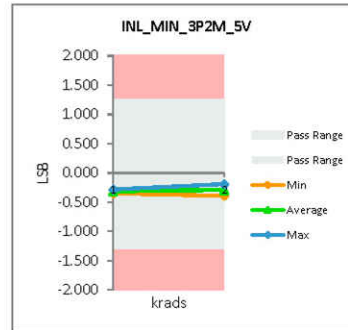


TID Report
ADC128S102PWT-SEP

INL_MIN_3P2M_5V				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		1.25	1.25	
Min Limit		-1.3	-1.3	
krams	Serial #	Pre	Post	Delta
0	23	-0.391	-0.287	-0.105
0	24	-0.325	-0.351	0.025
30	1	-0.389	-0.259	-0.130
30	2	-0.285	-0.327	0.042
30	3	-0.450	-0.255	-0.196
30	4	-0.263	-0.211	-0.052
30	5	-0.339	-0.300	-0.039
30	6	-0.320	-0.393	0.073
30	7	-0.508	-0.194	-0.314
30	8	-0.384	-0.308	-0.075
30	9	-0.328	-0.361	0.033
30	10	-0.326	-0.310	-0.016
30	11	-0.340	-0.303	-0.037
30	12	-0.361	-0.272	-0.088
30	13	-0.357	-0.245	-0.113
30	14	-0.563	-0.324	-0.239
30	15	-0.302	-0.226	-0.076
30	16	-0.286	-0.297	0.010
30	17	-0.344	-0.260	-0.084
30	18	-0.464	-0.316	-0.148
30	19	-0.308	-0.299	-0.009
30	20	-0.434	-0.323	-0.111
30	21	-0.321	-0.298	-0.023
30	22	-0.382	-0.334	-0.048
Max		-0.263	-0.194	0.073
Average		-0.365	-0.294	-0.072
Min		-0.563	-0.393	-0.314
Std Dev		0.073	0.047	0.091

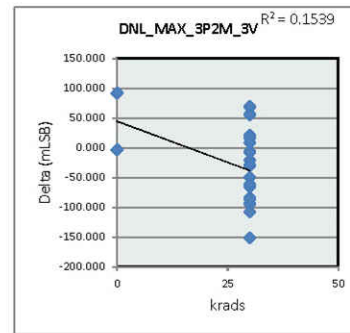


INL_MIN_3P2M_5V		
Test Site		
Tester		
Test Number		
Max Limit	1.25	LSB
Min Limit	-1.3	LSB
krams	0	30
LL	-1.300	-1.300
Min	-0.351	-0.393
Average	-0.319	-0.292
Max	-0.287	-0.194
UL	1.250	1.250

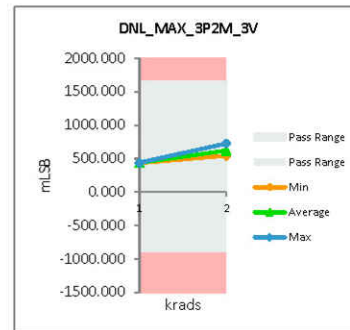


TID Report ADC128S102PWT-SEP

DNL_MAX_3P2M_3V				
Test Site				
Tester				
Test Number				
Unit		mLSB	mLSB	
Max Limit		1650	1650	
Min Limit		-900	-900	
krads	Serial #	Pre	Post	Delta
0	23	427.924	431.192	-3.268
0	24	528.992	436.768	92.224
30	1	603.764	668.396	-64.632
30	2	583.366	649.633	-66.267
30	3	518.122	612.180	-94.058
30	4	476.944	560.572	-83.628
30	5	568.703	589.446	-20.743
30	6	514.697	543.560	-28.863
30	7	666.254	597.393	68.861
30	8	582.482	561.681	20.801
30	9	577.447	626.614	-49.167
30	10	618.449	648.813	-30.364
30	11	541.325	602.225	-60.900
30	12	543.640	628.803	-85.163
30	13	637.070	643.949	-6.879
30	14	616.963	560.949	56.014
30	15	588.547	580.568	7.979
30	16	602.232	585.461	16.771
30	17	470.010	620.758	-150.748
30	18	568.679	653.750	-85.071
30	19	608.277	599.657	8.620
30	20	590.281	676.587	-86.306
30	21	616.934	725.041	-108.107
30	22	618.388	626.700	-8.312
Max		666.254	725.041	92.224
Average		569.562	601.279	-31.717
Min		427.924	431.192	-150.748
Std Dev		57.271	66.667	59.816

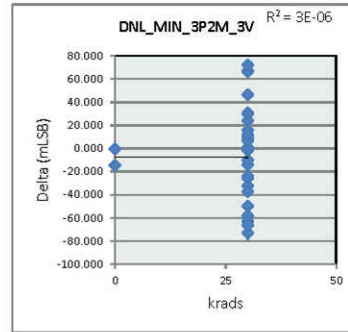


DNL_MAX_3P2M_3V		
Test Site		
Tester		
Test Number		
Max Limit	1650	mLSB
Min Limit	-900	mLSB
krads	0	30
LL	-900.000	-900.000
Min	431.192	543.560
Average	433.980	616.488
Max	436.768	725.041
UL	1650.000	1650.000

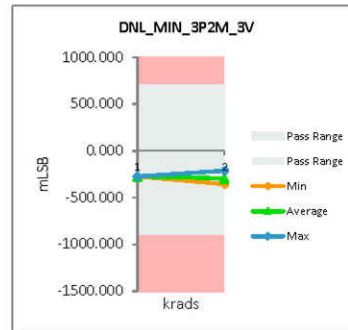


TID Report ADC128S102PWT-SEP

DNL_MIN_3P2M_3V				
Test Site				
Tester				
Test Number				
Unit				
mLSB				
mLSB				
Max Limit				
700				
Min Limit				
-900				
krads	Serial #	Pre	Post	Delta
0	23	-289.634	-275.037	-14.597
0	24	-277.560	-277.116	-0.444
30	1	-322.775	-297.404	-25.371
30	2	-332.702	-342.158	9.456
30	3	-287.923	-354.828	66.905
30	4	-297.279	-264.693	-32.586
30	5	-294.157	-269.809	-24.348
30	6	-313.826	-312.011	-1.815
30	7	-332.625	-259.442	-73.183
30	8	-285.138	-286.298	1.160
30	9	-337.773	-323.891	-13.882
30	10	-276.831	-213.858	-62.973
30	11	-245.235	-269.184	23.949
30	12	-330.515	-346.290	15.775
30	13	-308.335	-271.106	-37.229
30	14	-306.600	-240.209	-66.391
30	15	-289.928	-240.385	-49.543
30	16	-236.167	-308.413	72.246
30	17	-310.526	-357.356	46.830
30	18	-354.557	-344.483	-10.074
30	19	-254.575	-284.850	30.275
30	20	-344.958	-352.113	7.155
30	21	-333.044	-344.313	11.269
30	22	-292.862	-234.417	-58.445
Max		-236.167	-213.858	72.246
Average		-302.314	-294.569	-7.744
Min		-354.557	-357.356	-73.183
Std Dev		31.091	43.219	39.701

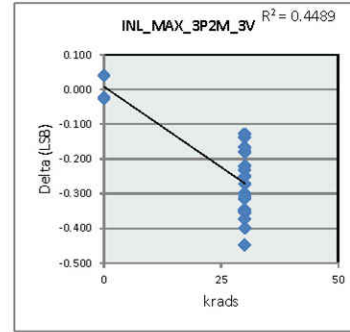


DNL_MIN_3P2M_3V		
Test Site		
Tester		
Test Number		
Max Limit		
700		
mLSB		
Min Limit		
-900		
mLSB		
krads	0	30
LL	-900.000	-900.000
Min	-277.116	-357.356
Average	-276.077	-296.251
Max	-275.037	-213.858
UL	700.000	700.000

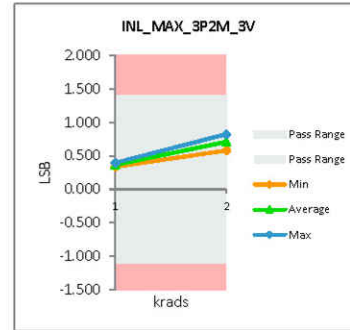


TID Report ADC128S102PWT-SEP

INL_MAX_3P2M_3V				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		1.4	1.4	
Min Limit		-1.1	-1.1	
krams	Serial #	Pre	Post	Delta
0	23	0.373	0.333	0.040
0	24	0.369	0.394	-0.025
30	1	0.477	0.750	-0.272
30	2	0.438	0.753	-0.315
30	3	0.593	0.731	-0.137
30	4	0.464	0.684	-0.220
30	5	0.472	0.638	-0.166
30	6	0.342	0.592	-0.251
30	7	0.522	0.701	-0.179
30	8	0.407	0.753	-0.346
30	9	0.358	0.578	-0.221
30	10	0.441	0.622	-0.181
30	11	0.424	0.771	-0.348
30	12	0.525	0.653	-0.128
30	13	0.508	0.739	-0.232
30	14	0.526	0.776	-0.250
30	15	0.392	0.690	-0.298
30	16	0.373	0.821	-0.448
30	17	0.326	0.725	-0.399
30	18	0.415	0.787	-0.373
30	19	0.344	0.652	-0.308
30	20	0.463	0.818	-0.355
30	21	0.356	0.709	-0.354
30	22	0.491	0.658	-0.167
Max		0.593	0.821	0.040
Average		0.433	0.680	-0.247
Min		0.326	0.333	-0.448
Std Dev		0.072	0.118	0.117

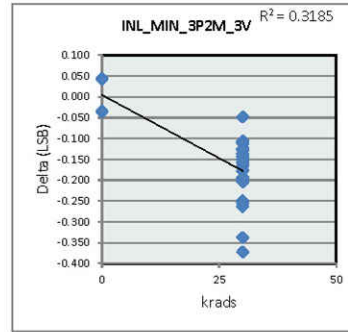


INL_MAX_3P2M_3V		
Test Site		
Tester		
Test Number		
Max Limit	1.4	LSB
Min Limit	-1.1	LSB
krams	0	30
LL	-1.100	-1.100
Min	0.333	0.578
Average	0.363	0.709
Max	0.394	0.821
UL	1.400	1.400

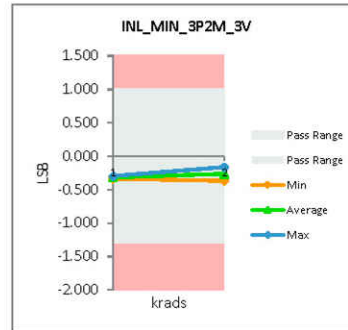


TID Report
ADC128S102PWT-SEP

INL_MIN_3P2M_3V				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		1	1	
Min Limit		-1.3	-1.3	
krams	Serial #	Pre	Post	Delta
0	23	-0.255	-0.298	0.043
0	24	-0.371	-0.336	-0.035
30	1	-0.509	-0.259	-0.250
30	2	-0.499	-0.371	-0.128
30	3	-0.458	-0.254	-0.204
30	4	-0.465	-0.300	-0.165
30	5	-0.339	-0.290	-0.048
30	6	-0.365	-0.208	-0.158
30	7	-0.603	-0.265	-0.337
30	8	-0.353	-0.228	-0.125
30	9	-0.414	-0.304	-0.110
30	10	-0.363	-0.257	-0.106
30	11	-0.366	-0.168	-0.198
30	12	-0.461	-0.349	-0.112
30	13	-0.555	-0.291	-0.264
30	14	-0.456	-0.312	-0.143
30	15	-0.539	-0.166	-0.373
30	16	-0.355	-0.177	-0.177
30	17	-0.414	-0.264	-0.150
30	18	-0.504	-0.250	-0.255
30	19	-0.430	-0.236	-0.194
30	20	-0.405	-0.269	-0.137
30	21	-0.515	-0.358	-0.157
30	22	-0.351	-0.225	-0.126
Max		-0.255	-0.166	0.043
Average		-0.431	-0.268	-0.163
Min		-0.603	-0.371	-0.373
Std Dev		0.083	0.057	0.091

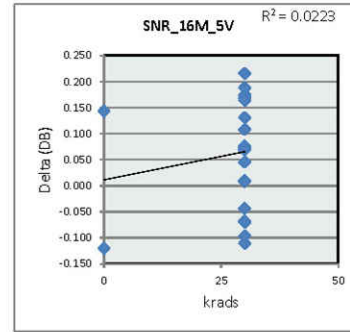


INL_MIN_3P2M_3V		
Test Site		
Tester		
Test Number		
Max Limit	1	LSB
Min Limit	-1.3	LSB
krams	0	30
LL	-1.300	-1.300
Min	-0.336	-0.371
Average	-0.317	-0.264
Max	-0.298	-0.166
UL	1.000	1.000

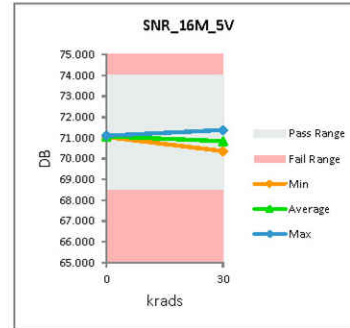


TID Report ADC128S102PWT-SEP

SNR_16M_5V				
Test Site				
Tester				
Test Number				
Unit	DB	DB		
Max Limit	74	74		
Min Limit	68.5	68.5		
krams	Serial #	Pre	Post	Delta
0	23	71.181	71.038	0.143
0	24	70.976	71.097	-0.121
30	1	70.948	70.817	0.131
30	2	70.810	70.803	0.008
30	3	70.789	70.780	0.010
30	4	70.530	70.355	0.174
30	5	71.030	71.141	-0.111
30	6	71.060	70.988	0.072
30	7	70.880	70.803	0.076
30	8	70.974	70.758	0.216
30	9	71.010	70.902	0.108
30	10	70.777	70.602	0.175
30	11	70.794	70.722	0.071
30	12	70.892	70.988	-0.096
30	13	70.903	70.829	0.074
30	14	70.746	70.558	0.188
30	15	70.705	70.536	0.169
30	16	71.140	70.976	0.164
30	17	71.055	71.098	-0.043
30	18	70.868	70.799	0.069
30	19	71.298	71.368	-0.070
30	20	71.055	71.009	0.046
30	21	71.061	70.990	0.071
30	22	70.601	70.668	-0.068
Max	71.298	71.368	0.216	
Average	70.920	70.859	0.061	
Min	70.530	70.355	-0.121	
Std Dev	0.182	0.226	0.102	

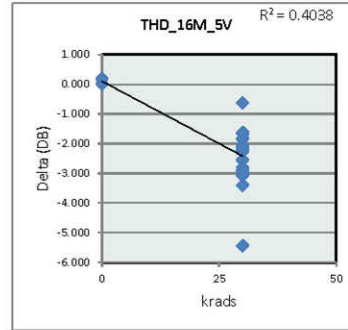


SNR_16M_5V		
Test Site		
Tester		
Test Number		
Max Limit	74	DB
Min Limit	68.5	DB
krams	0	30
LL	68.500	68.500
Min	71.038	70.355
Average	71.068	70.840
Max	71.097	71.368
UL	74.000	74.000

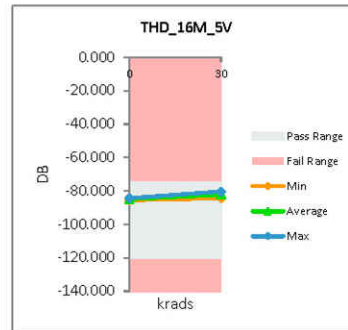


TID Report ADC128S102PWT-SEP

THD_16M_5V				
Test Site				
Tester				
Test Number				
Unit		DB	DB	
Max Limit		-74	-74	
Min Limit		-120	-120	
krads	Serial #	Pre	Post	Delta
0	23	-84.515	-84.534	0.019
0	24	-85.479	-85.658	0.178
30	1	-83.850	-80.451	-3.398
30	2	-83.247	-81.436	-1.811
30	3	-83.439	-81.324	-2.115
30	4	-84.172	-82.083	-2.090
30	5	-85.471	-83.386	-2.085
30	6	-85.976	-84.365	-1.611
30	7	-85.023	-82.455	-2.567
30	8	-84.285	-82.108	-2.176
30	9	-85.647	-83.581	-2.067
30	10	-83.870	-83.245	-0.625
30	11	-85.093	-82.028	-3.065
30	12	-84.229	-81.232	-2.997
30	13	-84.766	-82.221	-2.545
30	14	-83.456	-81.267	-2.189
30	15	-85.629	-83.781	-1.848
30	16	-86.991	-81.557	-5.434
30	17	-84.002	-81.751	-2.251
30	18	-85.677	-82.803	-2.874
30	19	-86.032	-83.238	-2.794
30	20	-84.080	-81.909	-2.171
30	21	-84.833	-81.925	-2.908
30	22	-85.228	-83.559	-1.669
Max		-83.247	-80.451	0.178
Average		-84.791	-82.579	-2.212
Min		-86.991	-85.658	-5.434
Std Dev		0.959	1.250	1.120

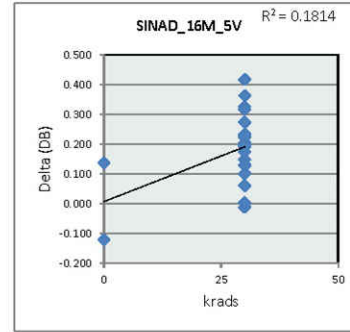


THD_16M_5V		
Test Site		
Tester		
Test Number		
Max Limit		-74 DB
Min Limit		-120 DB
krads	0	30
LL	-120.000	-120.000
Min	-85.658	-84.365
Average	-85.096	-82.350
Max	-84.534	-80.451
UL	-74.000	-74.000

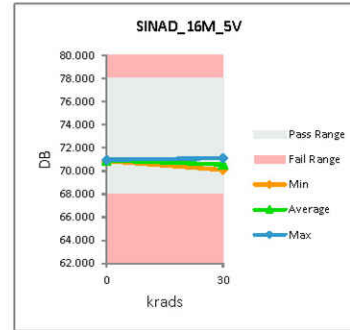


TID Report ADC128S102PWT-SEP

SINAD_16M_5V				
Test Site				
Tester				
Test Number				
Unit		DB	DB	
Max Limit		78	78	
Min Limit		68	68	
krams	Serial #	Pre	Post	Delta
0	23	70.984	70.848	0.136
0	24	70.825	70.948	-0.123
30	1	70.731	70.369	0.362
30	2	70.569	70.443	0.127
30	3	70.560	70.412	0.147
30	4	70.346	70.073	0.273
30	5	70.877	70.889	-0.012
30	6	70.922	70.793	0.130
30	7	70.715	70.516	0.199
30	8	70.776	70.451	0.325
30	9	70.863	70.673	0.190
30	10	70.569	70.372	0.197
30	11	70.635	70.412	0.223
30	12	70.695	70.595	0.099
30	13	70.728	70.524	0.203
30	14	70.519	70.203	0.315
30	15	70.568	70.335	0.232
30	16	71.028	70.612	0.417
30	17	70.840	70.740	0.100
30	18	70.727	70.533	0.194
30	19	71.154	71.095	0.060
30	20	70.844	70.670	0.174
30	21	70.882	70.654	0.229
30	22	70.454	70.451	0.003
Max		71.154	71.095	0.417
Average		70.742	70.567	0.175
Min		70.346	70.073	-0.123
Std Dev		0.192	0.240	0.122

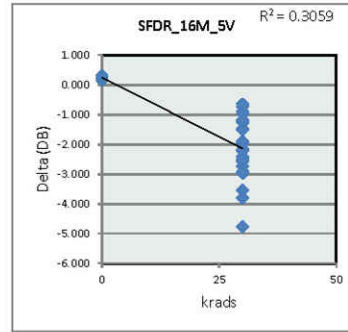


SINAD_16M_5V		
Test Site		
Tester		
Test Number		
Max Limit		78 DB
Min Limit		68 DB
krams		0 30
LL		68.000 68.000
Min		70.848 70.073
Average		70.898 70.537
Max		70.948 71.095
UL		78.000 78.000

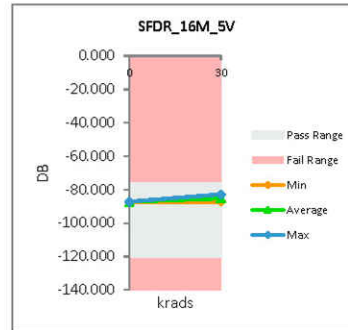


**TID Report
ADC128S102PWT-SEP**

SFDR_16M_5V				
krads	Serial #	Pre	Post	Delta
0	23	-87.153	-87.327	0.174
0	24	-87.016	-87.319	0.303
30	1	-87.261	-84.300	-2.961
30	2	-86.930	-84.513	-2.417
30	3	-86.086	-84.610	-1.476
30	4	-87.980	-86.058	-1.923
30	5	-87.340	-86.612	-0.728
30	6	-86.938	-85.967	-0.971
30	7	-87.412	-86.520	-0.891
30	8	-86.860	-84.694	-2.166
30	9	-87.920	-86.419	-1.501
30	10	-87.015	-85.762	-1.253
30	11	-88.620	-85.082	-3.539
30	12	-87.347	-84.617	-2.730
30	13	-87.345	-85.448	-1.897
30	14	-86.727	-82.932	-3.795
30	15	-87.869	-87.229	-0.640
30	16	-88.055	-83.293	-4.762
30	17	-87.112	-84.902	-2.210
30	18	-87.593	-84.677	-2.916
30	19	-88.115	-85.617	-2.498
30	20	-87.582	-85.010	-2.572
30	21	-87.542	-85.570	-1.972
30	22	-88.054	-86.869	-1.185
Max		-86.086	-82.932	0.303
Average		-87.411	-85.473	-1.939
Min		-88.620	-87.327	-4.762
Std Dev		0.557	1.194	1.212

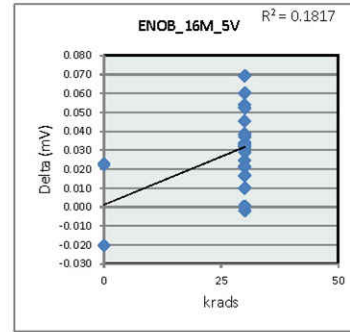


SFDR_16M_5V		
Test Site		
Tester		
Test Number		
Max Limit	-75	DB
Min Limit	-120	DB
krads	0	30
LL	-120.000	-120.000
Min	-87.327	-87.229
Average	-87.323	-85.305
Max	-87.319	-82.932
UL	-75.000	-75.000

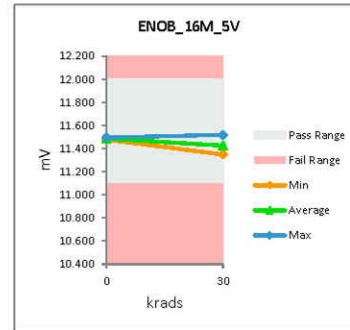


TID Report ADC128S102PWT-SEP

ENOB_16M_5V				
Test Site				
Tester				
Test Number				
Unit		mV	mV	
Max Limit		12	12	
Min Limit		11.1	11.1	
krams	Serial #	Pre	Post	Delta
0	23	11.499	11.476	0.023
0	24	11.473	11.493	-0.020
30	1	11.457	11.397	0.060
30	2	11.430	11.409	0.021
30	3	11.428	11.404	0.024
30	4	11.393	11.348	0.045
30	5	11.481	11.483	-0.002
30	6	11.489	11.467	0.022
30	7	11.454	11.421	0.033
30	8	11.464	11.410	0.054
30	9	11.479	11.447	0.032
30	10	11.430	11.397	0.033
30	11	11.441	11.404	0.037
30	12	11.451	11.434	0.016
30	13	11.456	11.423	0.034
30	14	11.422	11.369	0.052
30	15	11.430	11.391	0.039
30	16	11.506	11.437	0.069
30	17	11.475	11.458	0.017
30	18	11.456	11.424	0.032
30	19	11.527	11.517	0.010
30	20	11.476	11.447	0.029
30	21	11.482	11.444	0.038
30	22	11.411	11.410	0.001
Max		11.527	11.517	0.069
Average		11.459	11.430	0.029
Min		11.393	11.348	-0.020
Std Dev		0.032	0.040	0.020

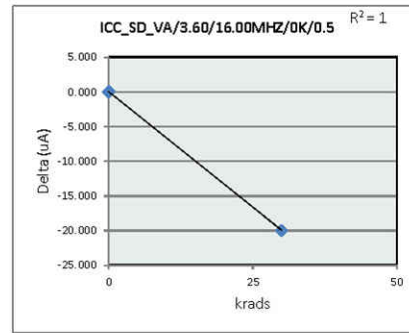


ENOB_16M_5V		
Test Site		
Tester		
Test Number		
Max Limit		12 mV
Min Limit		11.1 mV
krams		0 30
LL	11.100	11.100
Min	11.477	11.348
Average	11.485	11.425
Max	11.493	11.517
UL	12.000	12.000

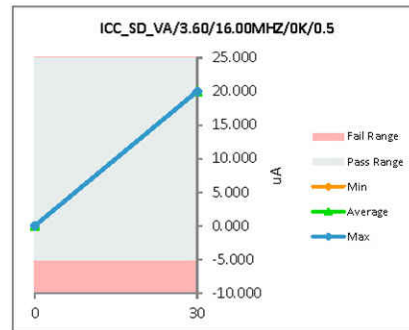


TID Report ADC128S102PWT-SEP

ICC_SD_VA/3.60/16.00MHZ/0K				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit				
Min Limit		-5	-5	
krads	Serial #	Pre	Post	Delta
0	23	0.006	0.007	-0.001
0	24	0.007	0.006	0.000
30	1	0.008	20.000	-19.992
30	2	0.007	20.000	-19.993
30	3	0.006	20.000	-19.994
30	4	0.007	19.998	-19.991
30	5	0.009	19.998	-19.989
30	6	0.007	19.998	-19.991
30	7	0.006	19.999	-19.992
30	8	0.008	19.999	-19.991
30	9	0.008	19.999	-19.991
30	10	0.007	19.999	-19.992
30	11	0.008	19.999	-19.990
30	12	0.007	20.000	-19.993
30	13	0.008	19.999	-19.991
30	14	0.006	19.998	-19.993
30	15	0.006	19.999	-19.993
30	16	0.008	20.000	-19.992
30	17	0.008	20.000	-19.992
30	18	0.007	19.999	-19.992
30	19	0.007	19.999	-19.993
30	20	0.008	20.000	-19.992
30	21	0.008	19.999	-19.991
30	22	0.005	20.000	-19.994
Max		0.009	20.000	0.000
Average		0.007	18.333	-18.326
Min		0.005	0.006	-19.994
Std Dev		0.001	5.645	5.644

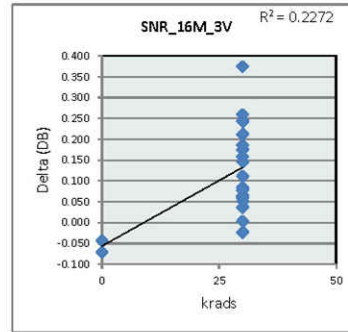


ICC_SD_VA/3.60/16.00MHZ/0		
Test Site		
Tester		
Test Number		
Max Limit		uA
Min Limit		uA
krads	0	30
LL	-5.000	-5.000
Min	0.006	19.998
Average	0.007	19.999
Max	0.007	20.001
UL		

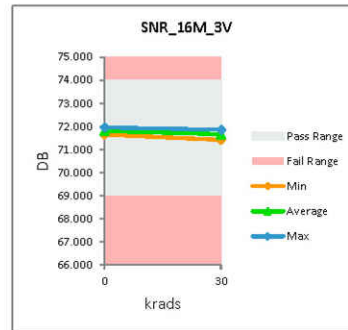


TID Report ADC128S102PWT-SEP

SNR_16M_3V				
Test Site				
Tester				
Test Number				
Unit		DB	DB	
Max Limit		74	74	
Min Limit		69	69	
krams	Serial #	Pre	Post	Delta
0	23	71.897	71.940	-0.043
0	24	71.575	71.646	-0.071
30	1	71.845	71.670	0.175
30	2	71.747	71.636	0.111
30	3	71.656	71.579	0.077
30	4	71.600	71.597	0.003
30	5	71.662	71.602	0.060
30	6	71.812	71.775	0.036
30	7	71.869	71.610	0.259
30	8	71.594	71.408	0.185
30	9	71.829	71.852	-0.024
30	10	71.811	71.437	0.374
30	11	71.765	71.607	0.158
30	12	71.726	71.749	-0.023
30	13	71.815	71.573	0.243
30	14	71.701	71.456	0.244
30	15	71.833	71.591	0.242
30	16	71.873	71.823	0.051
30	17	71.843	71.778	0.064
30	18	71.869	71.658	0.212
30	19	71.909	71.824	0.085
30	20	71.845	71.767	0.078
30	21	71.891	71.733	0.158
30	22	71.840	71.694	0.146
Max		71.909	71.940	0.374
Average		71.784	71.667	0.117
Min		71.575	71.408	-0.071
Std Dev		0.102	0.133	0.112

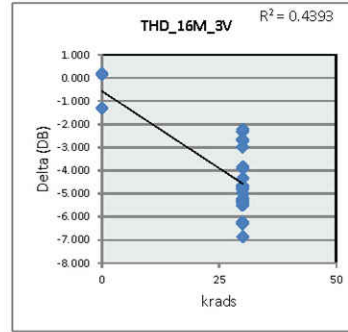


SNR_16M_3V		
Test Site		
Tester		
Test Number		
Max Limit		74 DB
Min Limit		69 DB
krams		0 30
LL	69.000	69.000
Min	71.646	71.408
Average	71.793	71.656
Max	71.940	71.852
UL	74.000	74.000

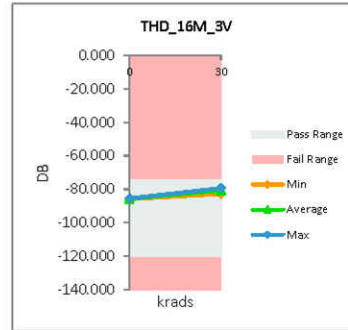


TID Report
ADC128S102PWT-SEP

THD_16M_3V				
Test Site				
Tester				
Test Number				
Unit		DB	DB	
Max Limit		-74	-74	
Min Limit		-120	-120	
krads	Serial #	Pre	Post	Delta
0	23	-85.774	-85.934	0.160
0	24	-86.871	-85.559	-1.312
30	1	-84.938	-79.577	-5.361
30	2	-84.715	-80.374	-4.341
30	3	-82.627	-79.910	-2.717
30	4	-83.974	-80.053	-3.921
30	5	-86.399	-81.653	-4.746
30	6	-85.190	-82.860	-2.329
30	7	-84.952	-79.978	-4.974
30	8	-86.521	-81.178	-5.343
30	9	-86.806	-82.965	-3.842
30	10	-86.910	-81.911	-4.999
30	11	-86.824	-79.957	-6.867
30	12	-83.226	-81.009	-2.217
30	13	-85.467	-79.963	-5.504
30	14	-85.429	-80.127	-5.303
30	15	-84.410	-81.755	-2.656
30	16	-85.745	-79.506	-6.239
30	17	-85.715	-80.503	-5.212
30	18	-84.849	-81.860	-2.988
30	19	-85.901	-79.598	-6.303
30	20	-85.681	-80.277	-5.404
30	21	-85.482	-80.834	-4.648
30	22	-86.519	-81.718	-4.801
Max		-82.627	-79.506	0.160
Average		-85.455	-81.211	-4.244
Min		-86.910	-85.934	-6.867
Std Dev		1.128	1.719	1.705

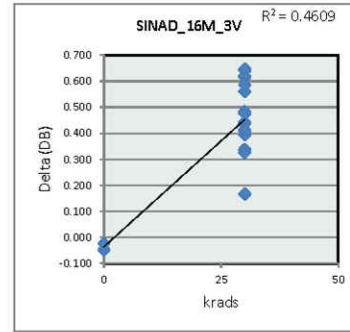


THD_16M_3V		
Test Site		
Tester		
Test Number		
Max Limit		-74 DB
Min Limit		-120 DB
krads	0	30
LL	-120.000	-120.000
Min	-85.934	-82.965
Average	-85.747	-80.799
Max	-85.559	-79.506
UL	-74.000	-74.000

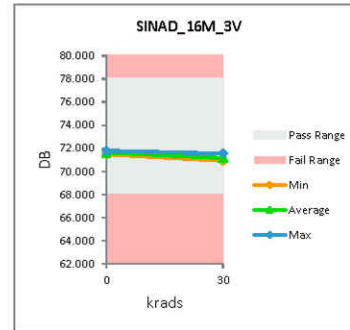


TID Report ADC128S102PWT-SEP

SINAD_16M_3V				
Test Site				
Tester				
Test Number				
Unit		DB	DB	
Max Limit		78	78	
Min Limit		68	68	
krams	Serial #	Pre	Post	Delta
0	23	71.723	71.771	-0.047
0	24	71.449	71.474	-0.025
30	1	71.637	71.019	0.619
30	2	71.533	71.091	0.442
30	3	71.322	70.984	0.338
30	4	71.355	71.018	0.337
30	5	71.518	71.192	0.326
30	6	71.617	71.450	0.167
30	7	71.661	71.020	0.641
30	8	71.456	70.973	0.483
30	9	71.693	71.529	0.164
30	10	71.679	71.064	0.615
30	11	71.631	71.015	0.617
30	12	71.429	71.262	0.166
30	13	71.632	70.985	0.647
30	14	71.521	70.903	0.617
30	15	71.600	71.192	0.408
30	16	71.699	71.139	0.560
30	17	71.668	71.232	0.437
30	18	71.656	71.262	0.394
30	19	71.739	71.154	0.585
30	20	71.669	71.194	0.475
30	21	71.705	71.230	0.475
30	22	71.695	71.283	0.412
	Max	71.739	71.771	0.647
	Average	71.595	71.185	0.411
	Min	71.322	70.903	-0.047
	Std Dev	0.119	0.207	0.203

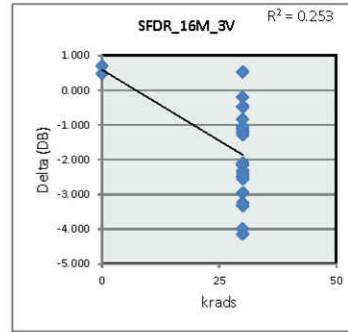


SINAD_16M_3V		
Test Site		
Tester		
Test Number		
Max Limit	78	DB
Min Limit	68	DB
krams	0	30
LL	68.000	68.000
Min	71.474	70.903
Average	71.622	71.145
Max	71.771	71.529
UL	78.000	78.000

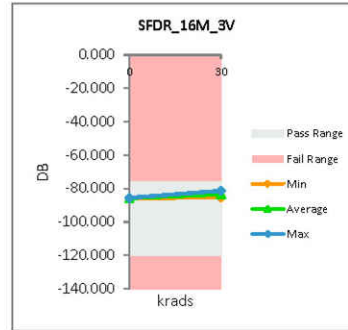


TID Report
ADC128S102PWT-SEP

SFDR_16M_3V				
Test Site				
Tester				
Test Number				
Unit	DB	DB		
Max Limit	-75	-75		
Min Limit	-120	-120		
krads	Serial #	Pre	Post	Delta
0	23	-85.396	-85.868	0.472
0	24	-84.929	-85.635	0.706
30	1	-85.637	-82.682	-2.955
30	2	-85.705	-83.385	-2.320
30	3	-84.945	-83.851	-1.094
30	4	-84.442	-83.226	-1.216
30	5	-85.655	-84.606	-1.049
30	6	-85.847	-85.370	-0.477
30	7	-85.272	-82.877	-2.395
30	8	-84.604	-83.478	-1.126
30	9	-85.637	-85.175	-0.461
30	10	-85.234	-84.398	-0.836
30	11	-85.307	-81.981	-3.326
30	12	-84.950	-85.480	0.530
30	13	-84.989	-82.428	-2.562
30	14	-85.512	-83.026	-2.486
30	15	-84.898	-84.701	-0.197
30	16	-85.495	-81.353	-4.143
30	17	-86.474	-83.224	-3.250
30	18	-85.671	-84.386	-1.285
30	19	-85.759	-81.772	-3.986
30	20	-85.317	-83.155	-2.162
30	21	-85.469	-83.373	-2.096
30	22	-85.679	-83.585	-2.094
Max		-84.442	-81.353	0.706
Average		-85.368	-83.709	-1.659
Min		-86.474	-85.868	-4.143
Std Dev		0.446	1.257	1.376

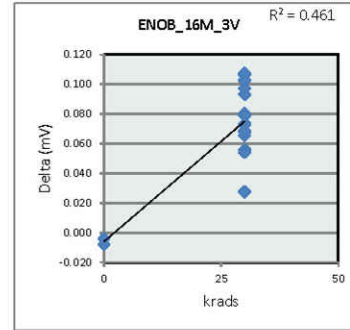


SFDR_16M_3V		
Test Site		
Tester		
Test Number		
Max Limit	-75	DB
Min Limit	-120	DB
krads	0	30
LL	-120.000	-120.000
Min	-85.868	-85.480
Average	-85.752	-83.523
Max	-85.635	-81.353
UL	-75.000	-75.000

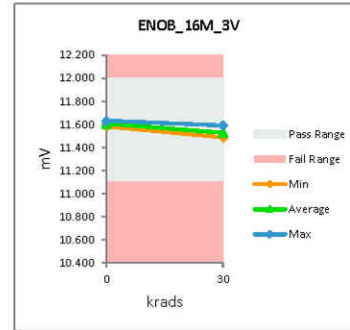


TID Report ADC128S102PWT-SEP

ENOB_16M_3V				
Test Site				
Tester				
Test Number				
Unit		mV	mV	
Max Limit		12	12	
Min Limit		11.1	11.1	
krams	Serial #	Pre	Post	Delta
0	23	11.622	11.630	-0.008
0	24	11.576	11.580	-0.004
30	1	11.608	11.505	0.103
30	2	11.590	11.517	0.073
30	3	11.555	11.499	0.056
30	4	11.561	11.505	0.056
30	5	11.588	11.534	0.054
30	6	11.604	11.576	0.028
30	7	11.611	11.505	0.106
30	8	11.577	11.497	0.080
30	9	11.617	11.590	0.027
30	10	11.614	11.512	0.102
30	11	11.607	11.504	0.102
30	12	11.573	11.545	0.028
30	13	11.607	11.499	0.107
30	14	11.588	11.486	0.103
30	15	11.601	11.533	0.068
30	16	11.618	11.525	0.093
30	17	11.613	11.540	0.073
30	18	11.611	11.545	0.065
30	19	11.624	11.527	0.097
30	20	11.613	11.534	0.079
30	21	11.619	11.540	0.079
30	22	11.617	11.549	0.068
Max		11.624	11.630	0.107
Average		11.601	11.532	0.068
Min		11.555	11.486	-0.008
Std Dev		0.020	0.034	0.034

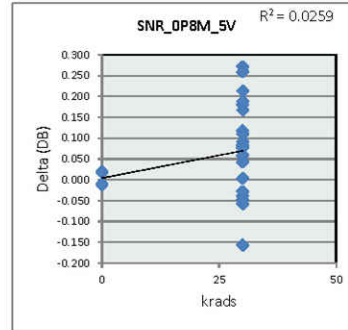


ENOB_16M_3V		
Test Site		
Tester		
Test Number		
Max Limit		12 mV
Min Limit		11.1 mV
krams	0	30
LL	11.100	11.100
Min	11.580	11.486
Average	11.605	11.526
Max	11.630	11.590
UL	12.000	12.000

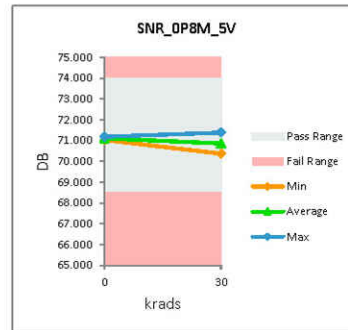


TID Report
ADC128S102PWT-SEP

SNR_OP8M_5V				
Test Site				
Tester				
Test Number				
Unit				
DB				
DB				
Max Limit				
74				
74				
Min Limit				
68.5				
68.5				
krads	Serial #	Pre	Post	Delta
0	23	71.202	71.183	0.019
0	24	71.014	71.025	-0.011
30	1	71.025	70.811	0.214
30	2	70.919	70.648	0.272
30	3	70.794	70.833	-0.039
30	4	70.529	70.362	0.167
30	5	71.193	71.102	0.091
30	6	71.069	70.991	0.078
30	7	70.781	70.704	0.077
30	8	71.009	70.926	0.083
30	9	71.038	70.962	0.076
30	10	70.784	70.604	0.180
30	11	70.867	70.679	0.188
30	12	70.894	70.922	-0.028
30	13	71.006	70.888	0.118
30	14	70.678	70.635	0.042
30	15	70.757	70.498	0.259
30	16	71.127	71.068	0.059
30	17	71.031	71.090	-0.058
30	18	71.005	71.002	0.003
30	19	71.227	71.384	-0.157
30	20	70.949	71.105	-0.157
30	21	71.039	71.087	-0.048
30	22	70.728	70.619	0.110
	Max	71.227	71.384	0.272
	Average	70.944	70.880	0.064
	Min	70.529	70.362	-0.157
	Std Dev	0.176	0.245	0.115

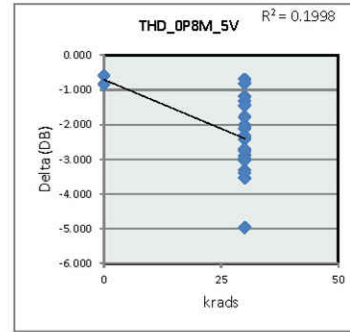


SNR_OP8M_5V		
Test Site		
Tester		
Test Number		
Max Limit		
74		
DB		
Min Limit		
68.5		
DB		
krads		
0		
30		
LL	68.500	68.500
Min	71.025	70.362
Average	71.104	70.860
Max	71.183	71.385
UL	74.000	74.000

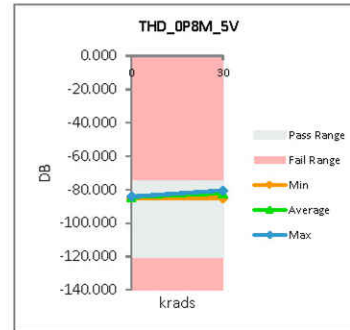


TID Report ADC128S102PWT-SEP

THD_OP8M_5V				
Test Site				
Tester				
Test Number				
Unit		DB	DB	
Max Limit		-74	-74	
Min Limit		-120	-120	
krads	Serial #	Pre	Post	Delta
0	23	-85.014	-84.171	-0.843
0	24	-85.445	-84.868	-0.577
30	1	-83.891	-80.577	-3.314
30	2	-83.579	-80.687	-2.892
30	3	-83.109	-81.335	-1.774
30	4	-84.619	-81.641	-2.978
30	5	-85.383	-83.358	-2.025
30	6	-85.751	-85.062	-0.689
30	7	-85.118	-82.092	-3.026
30	8	-84.827	-82.104	-2.723
30	9	-85.462	-84.648	-0.814
30	10	-84.300	-83.110	-1.190
30	11	-85.385	-82.623	-2.761
30	12	-83.358	-82.041	-1.318
30	13	-85.060	-81.664	-3.396
30	14	-83.275	-81.162	-2.113
30	15	-84.610	-83.164	-1.446
30	16	-87.561	-82.598	-4.964
30	17	-84.362	-82.040	-2.322
30	18	-85.436	-82.728	-2.707
30	19	-86.198	-82.662	-3.536
30	20	-84.656	-81.920	-2.736
30	21	-84.286	-81.866	-2.420
30	22	-84.666	-82.874	-1.792
Max		-83.109	-80.577	-0.577
Average		-84.806	-82.541	-2.265
Min		-87.561	-85.062	-4.964
Std Dev		1.001	1.223	1.071

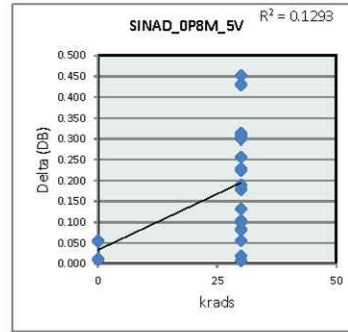


THD_OP8M_5V		
Test Site		
Tester		
Test Number		
Max Limit		-74 DB
Min Limit		-120 DB
krads	0	30
LL	-120.000	-120.000
Min	-84.868	-85.062
Average	-84.519	-82.362
Max	-84.171	-80.577
UL	-74.000	-74.000

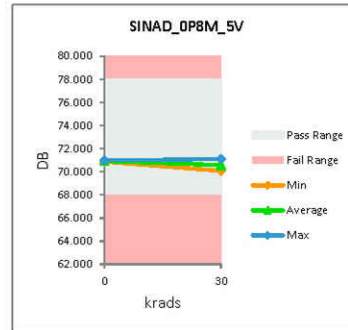


TID Report ADC128S102PWT-SEP

SINAD_0P8M_5V				
Test Site				
Tester				
Test Number				
Unit		DB	DB	
Max Limit		78	78	
Min Limit		68	68	
krams	Serial #	Pre	Post	Delta
0	23	71.025	70.970	0.055
0	24	70.860	70.849	0.011
30	1	70.806	70.375	0.431
30	2	70.690	70.237	0.453
30	3	70.547	70.463	0.084
30	4	70.363	70.050	0.313
30	5	71.030	70.851	0.180
30	6	70.923	70.824	0.100
30	7	70.624	70.399	0.224
30	8	70.832	70.607	0.225
30	9	70.884	70.780	0.104
30	10	70.595	70.366	0.229
30	11	70.716	70.410	0.306
30	12	70.654	70.599	0.056
30	13	70.839	70.539	0.299
30	14	70.445	70.267	0.178
30	15	70.582	70.269	0.313
30	16	71.029	70.773	0.256
30	17	70.834	70.754	0.080
30	18	70.851	70.719	0.132
30	19	71.091	71.072	0.019
30	20	70.768	70.760	0.008
30	21	70.838	70.738	0.100
30	22	70.557	70.368	0.189
Max		71.091	71.072	0.453
Average		70.766	70.585	0.181
Min		70.363	70.050	0.008
Std Dev		0.192	0.263	0.127

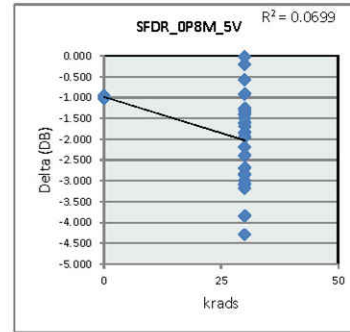


SINAD_0P8M_5V		
Test Site		
Tester		
Test Number		
Max Limit		78 DB
Min Limit		68 DB
krams		0 30
LL	68.000	68.000
Min	70.849	70.050
Average	70.910	70.555
Max	70.970	71.072
UL	78.000	78.000

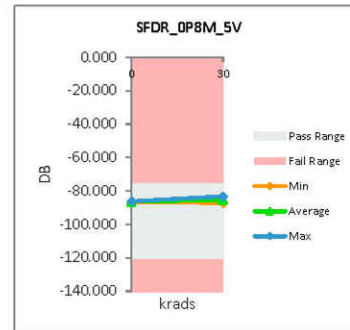


TID Report ADC128S102PWT-SEP

SFDR_OP8M_5V				
Test Site				
Tester				
Test Number				
Unit		DB	DB	
Max Limit		-75	-75	
Min Limit		-120	-120	
krams	Serial #	Pre	Post	Delta
0	23	-87.545	-86.581	-0.963
0	24	-87.447	-86.434	-1.013
30	1	-88.008	-84.168	-3.840
30	2	-87.268	-84.268	-3.000
30	3	-85.487	-84.568	-0.919
30	4	-87.686	-85.496	-2.189
30	5	-88.197	-86.504	-1.693
30	6	-86.877	-86.853	-0.025
30	7	-87.512	-86.117	-1.395
30	8	-87.244	-84.550	-2.693
30	9	-88.209	-86.949	-1.260
30	10	-87.620	-86.297	-1.323
30	11	-87.361	-85.752	-1.608
30	12	-85.872	-85.296	-0.575
30	13	-87.122	-85.287	-1.835
30	14	-86.193	-83.341	-2.852
30	15	-87.498	-87.286	-0.212
30	16	-88.795	-84.501	-4.293
30	17	-87.074	-85.126	-1.948
30	18	-87.093	-84.706	-2.387
30	19	-87.932	-84.850	-3.082
30	20	-87.704	-84.867	-2.837
30	21	-88.010	-84.825	-3.185
30	22	-87.414	-85.911	-1.503
Max		-85.487	-83.341	-0.025
Average		-87.382	-85.439	-1.943
Min		-88.795	-87.286	-4.293
Std Dev		0.739	1.025	1.113

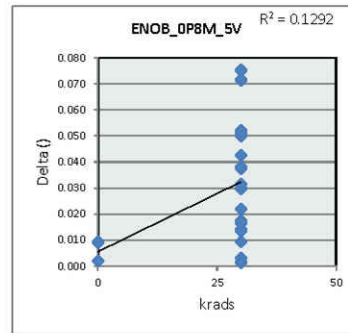


SFDR_OP8M_5V		
Test Site		
Tester		
Test Number		
Max Limit		-75 DB
Min Limit		-120 DB
krams	0	30
LL	-120.000	-120.000
Min	-86.581	-87.286
Average	-86.508	-85.342
Max	-86.434	-83.341
UL	-75.000	-75.000

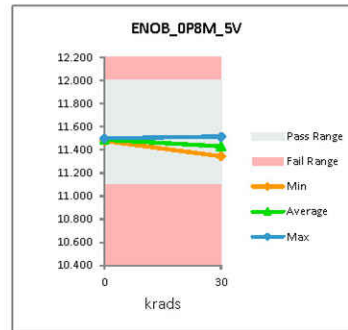


TID Report ADC128S102PWT-SEP

ENOB_0P8M_5V				
Test Site				
Tester				
Test Number				
Unit				
Max Limit		12	12	
Min Limit		11.1	11.1	
krams	Serial #	Pre	Post	Delta
0	23	11.506	11.497	0.009
0	24	11.478	11.477	0.002
30	1	11.469	11.398	0.071
30	2	11.450	11.375	0.075
30	3	11.426	11.412	0.014
30	4	11.396	11.344	0.052
30	5	11.507	11.477	0.030
30	6	11.489	11.472	0.017
30	7	11.439	11.402	0.037
30	8	11.474	11.436	0.037
30	9	11.482	11.465	0.017
30	10	11.434	11.396	0.038
30	11	11.455	11.404	0.051
30	12	11.444	11.435	0.009
30	13	11.475	11.425	0.050
30	14	11.409	11.380	0.030
30	15	11.432	11.380	0.052
30	16	11.507	11.464	0.043
30	17	11.474	11.461	0.013
30	18	11.477	11.455	0.022
30	19	11.517	11.514	0.003
30	20	11.463	11.462	0.001
30	21	11.475	11.458	0.017
30	22	11.428	11.397	0.031
Max		11.517	11.514	0.075
Average		11.463	11.433	0.030
Min		11.396	11.344	0.001
Std Dev		0.032	0.044	0.021

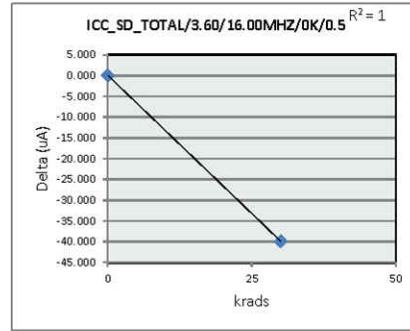


ENOB_0P8M_5V		
Test Site		
Tester		
Test Number		
Max Limit		12
Min Limit		11.1
krams	0	30
LL	11.100	11.100
Min	11.477	11.344
Average	11.487	11.428
Max	11.497	11.514
UL	12.000	12.000

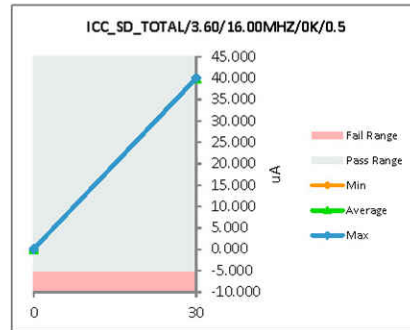


**TID Report
ADC128S102PWT-SEP**

ICC_SD_TOTAL/3.60/16.00MHZ/				
Test Site				
Tester				
Test Number				
Unit		uA	uA	
Max Limit				
Min Limit		-5	-5	
krads	Serial #	Pre	Post	Delta
0	23	0.079	0.080	-0.001
0	24	0.074	0.072	0.002
30	1	0.071	39.998	-39.927
30	2	0.067	39.998	-39.930
30	3	0.065	40.000	-39.934
30	4	0.077	39.997	-39.919
30	5	0.068	39.998	-39.929
30	6	0.068	39.996	-39.928
30	7	0.063	39.997	-39.934
30	8	0.083	39.998	-39.915
30	9	0.064	39.997	-39.933
30	10	0.064	39.998	-39.934
30	11	0.065	39.997	-39.933
30	12	0.062	39.998	-39.936
30	13	0.064	39.998	-39.934
30	14	0.066	39.996	-39.931
30	15	0.060	39.998	-39.938
30	16	0.061	39.998	-39.937
30	17	0.068	39.998	-39.930
30	18	0.070	39.997	-39.927
30	19	0.065	39.998	-39.933
30	20	0.065	39.998	-39.933
30	21	0.063	39.998	-39.935
30	22	0.057	39.998	-39.941
Max		0.083	40.000	0.002
Average		0.067	36.671	-36.604
Min		0.057	0.072	-39.941
Std Dev		0.006	11.271	11.274

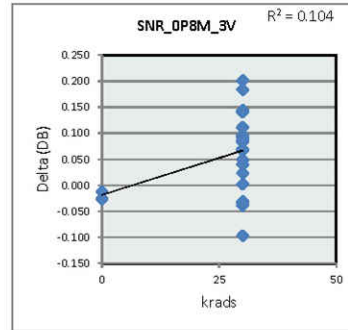


ICC_SD_TOTAL/3.60/16.00MHZ		
Test Site		
Tester		
Test Number		
Max Limit		uA
Min Limit		uA
krads	0	30
LL	-5.000	-5.000
Min	0.072	39.996
Average	0.076	39.998
Max	0.080	40.000
UL		

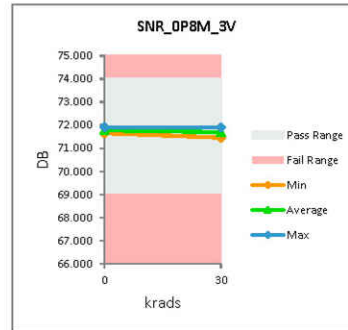


TID Report ADC128S102PWT-SEP

SNR_OP8M_3V				
Test Site				
Tester				
Test Number				
Unit	DB	DB		
Max Limit	74	74		
Min Limit	69	69		
krams	Serial #	Pre	Post	Delta
0	23	71.891	71.902	-0.011
0	24	71.613	71.639	-0.026
30	1	71.719	71.679	0.040
30	2	71.689	71.720	-0.031
30	3	71.618	71.615	0.002
30	4	71.590	71.494	0.095
30	5	71.644	71.621	0.023
30	6	71.925	71.856	0.070
30	7	71.766	71.654	0.112
30	8	71.609	71.468	0.141
30	9	71.855	71.894	-0.039
30	10	71.531	71.447	0.084
30	11	71.755	71.554	0.201
30	12	71.688	71.785	-0.097
30	13	71.738	71.594	0.144
30	14	71.698	71.605	0.092
30	15	71.765	71.655	0.111
30	16	71.903	71.811	0.093
30	17	71.881	71.833	0.049
30	18	71.819	71.732	0.087
30	19	71.909	71.843	0.067
30	20	71.875	71.805	0.069
30	21	71.797	71.830	-0.034
30	22	71.782	71.598	0.184
Max		71.925	71.902	0.201
Average		71.753	71.693	0.059
Min		71.531	71.447	-0.097
Std Dev		0.115	0.135	0.075

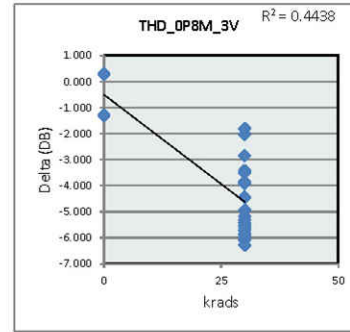


SNR_OP8M_3V		
Test Site		
Tester		
Test Number		
Max Limit	74	DB
Min Limit	69	DB
krams	0	30
LL	69.000	69.000
Min	71.639	71.447
Average	71.771	71.686
Max	71.902	71.894
UL	74.000	74.000

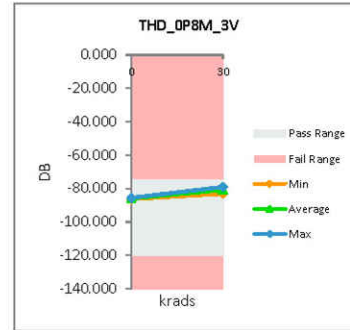


TID Report ADC128S102PWT-SEP

THD_OP8M_3V				
Test Site				
Tester				
Test Number				
Unit		DB	DB	
Max Limit		-74	-74	
Min Limit		-120	-120	
krads	Serial #	Pre	Post	Delta
0	23	-86.929	-85.631	-1.298
0	24	-86.060	-86.347	0.287
30	1	-84.926	-79.735	-5.190
30	2	-85.202	-80.259	-4.943
30	3	-83.281	-79.866	-3.414
30	4	-83.743	-79.897	-3.847
30	5	-86.252	-81.817	-4.434
30	6	-85.208	-83.176	-2.032
30	7	-84.617	-80.709	-3.908
30	8	-86.717	-81.000	-5.717
30	9	-85.280	-82.430	-2.851
30	10	-86.544	-82.096	-4.448
30	11	-86.019	-80.022	-5.997
30	12	-83.014	-81.202	-1.812
30	13	-85.619	-80.095	-5.524
30	14	-85.152	-79.832	-5.320
30	15	-84.760	-81.265	-3.494
30	16	-85.240	-79.145	-6.095
30	17	-86.290	-80.325	-5.965
30	18	-84.965	-81.468	-3.497
30	19	-86.136	-79.848	-6.288
30	20	-85.490	-80.058	-5.432
30	21	-86.451	-80.821	-5.630
30	22	-87.278	-81.417	-5.861
Max		-83.014	-79.145	0.287
Average		-85.466	-81.186	-4.280
Min		-87.278	-86.347	-6.288
Std Dev		1.091	1.774	1.745

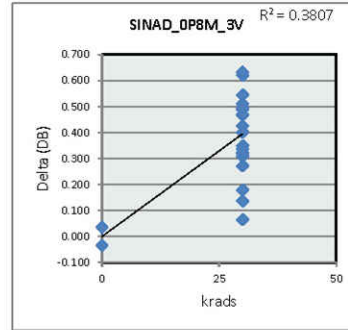


THD_OP8M_3V		
Test Site		
Tester		
Test Number		
Max Limit		-74 DB
Min Limit		-120 DB
krads	0	30
LL	-120.000	-120.000
Min	-86.347	-83.176
Average	-85.989	-80.749
Max	-85.631	-79.145
UL	-74.000	-74.000

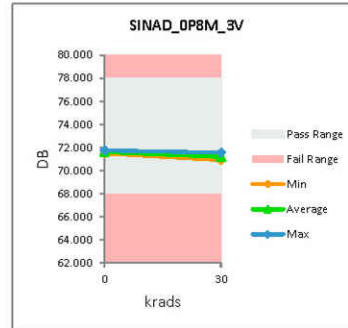


**TID Report
ADC128S102PWT-SEP**

SINAD_0P8M_3V				
Test Site				
Tester				
Test Number				
Unit	DB	DB		
Max Limit	78	78		
Min Limit	68	68		
krads	Serial #	Pre	Post	Delta
0	23	71.757	71.722	0.035
0	24	71.460	71.495	-0.035
30	1	71.516	71.048	0.468
30	2	71.500	71.151	0.349
30	3	71.331	71.010	0.321
30	4	71.333	70.908	0.425
30	5	71.496	71.225	0.272
30	6	71.726	71.546	0.179
30	7	71.546	71.145	0.401
30	8	71.477	71.010	0.467
30	9	71.662	71.526	0.136
30	10	71.396	71.088	0.308
30	11	71.596	70.976	0.619
30	12	71.380	71.315	0.065
30	13	71.564	71.020	0.544
30	14	71.506	70.997	0.509
30	15	71.553	71.204	0.349
30	16	71.706	71.074	0.632
30	17	71.727	71.258	0.469
30	18	71.613	71.293	0.320
30	19	71.748	71.204	0.544
30	20	71.690	71.200	0.489
30	21	71.650	71.314	0.336
30	22	71.661	71.167	0.494
Max	71.757	71.722	0.032	
Average	71.566	71.204	0.362	
Min	71.331	70.908	-0.035	
Std Dev	0.131	0.205	0.181	

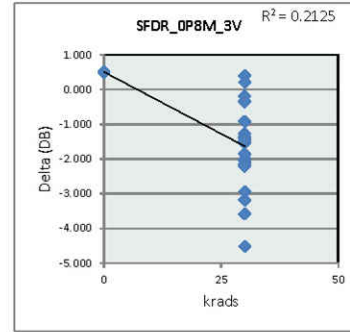


SINAD_0P8M_3V		
Test Site		
Tester		
Test Number		
Max Limit	78	DB
Min Limit	68	DB
krads	0	30
LL	68.000	68.000
Min	71.495	70.908
Average	71.608	71.167
Max	71.722	71.546
UL	78.000	78.000

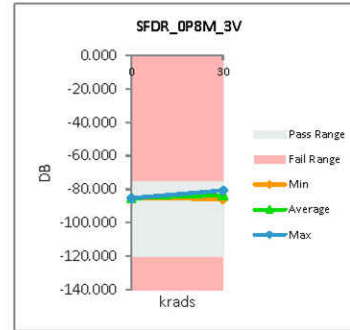


TID Report ADC128S102PWT-SEP

SFDR_OP8M_3V				
Test Site				
Tester				
Test Number				
Unit				
		DB	DB	
Max Limit		-75	-75	
Min Limit		-120	-120	
krads	Serial #	Pre	Post	Delta
0	23	-84.951	-85.438	0.487
0	24	-84.920	-85.442	0.522
30	1	-84.979	-82.923	-2.056
30	2	-85.334	-83.474	-1.859
30	3	-85.033	-83.759	-1.274
30	4	-84.692	-83.328	-1.363
30	5	-85.295	-84.935	-0.361
30	6	-85.527	-85.924	0.397
30	7	-84.976	-83.626	-1.350
30	8	-85.348	-83.345	-2.003
30	9	-85.538	-85.343	-0.194
30	10	-84.588	-84.263	-0.326
30	11	-85.351	-82.162	-3.189
30	12	-85.382	-85.595	0.212
30	13	-84.753	-82.899	-1.854
30	14	-85.055	-82.908	-2.147
30	15	-85.418	-84.498	-0.920
30	16	-85.246	-80.726	-4.520
30	17	-85.557	-83.350	-2.208
30	18	-85.403	-83.851	-1.552
30	19	-85.737	-82.150	-3.586
30	20	-85.802	-82.858	-2.944
30	21	-85.038	-83.636	-1.402
30	22	-85.072	-83.589	-1.483
Max		-84.588	-80.726	0.522
Average		-85.208	-83.751	-1.457
Min		-85.802	-85.924	-4.520
Std Dev		0.320	1.258	1.310

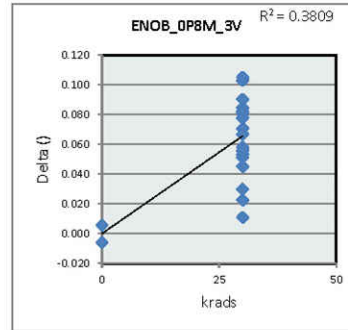


SFDR_OP8M_3V		
Test Site		
Tester		
Test Number		
Max Limit		-75 DB
Min Limit		-120 DB
krads	0	30
LL	-120.000	-120.000
Min	-85.442	-85.924
Average	-85.440	-83.597
Max	-85.438	-80.726
UL	-75.000	-75.000

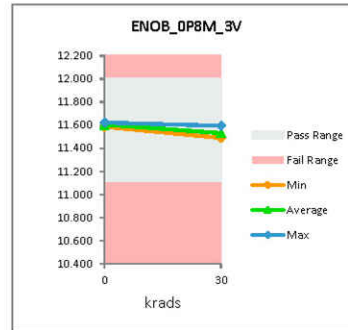


TID Report ADC128S102PWT-SEP

ENOB_0P8M_3V				
Test Site				
Tester				
Test Number				
Unit				
Max Limit		12	12	
Min Limit		11.1	11.1	
krams	Serial #	Pre	Post	Delta
0	23	11.627	11.622	0.006
0	24	11.578	11.584	-0.006
30	1	11.587	11.510	0.078
30	2	11.585	11.527	0.058
30	3	11.557	11.503	0.053
30	4	11.557	11.486	0.071
30	5	11.584	11.539	0.045
30	6	11.622	11.592	0.030
30	7	11.592	11.526	0.067
30	8	11.581	11.503	0.078
30	9	11.612	11.589	0.023
30	10	11.567	11.516	0.051
30	11	11.601	11.498	0.103
30	12	11.565	11.554	0.011
30	13	11.595	11.505	0.090
30	14	11.586	11.501	0.085
30	15	11.593	11.535	0.058
30	16	11.619	11.514	0.105
30	17	11.622	11.545	0.078
30	18	11.604	11.550	0.053
30	19	11.626	11.536	0.090
30	20	11.616	11.535	0.081
30	21	11.610	11.554	0.056
30	22	11.611	11.529	0.082
Max		11.627	11.622	0.105
Average		11.596	11.536	0.060
Min		11.557	11.486	-0.006
Std Dev		0.022	0.034	0.030

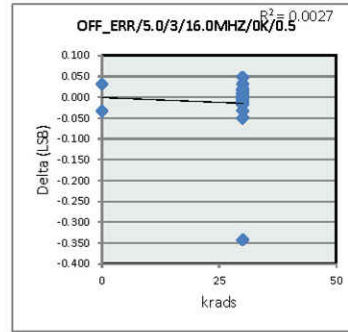


ENOB_0P8M_3V		
Test Site		
Tester		
Test Number		
Max Limit		12
Min Limit		11.1
krams	0	30
LL	11.100	11.100
Min	11.584	11.486
Average	11.603	11.529
Max	11.622	11.592
UL	12.000	12.000

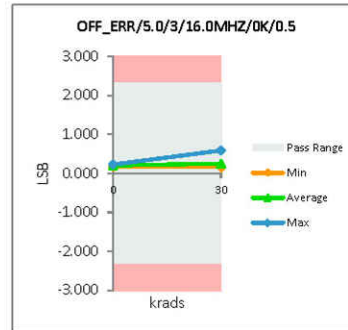


TID Report ADC128S102PWT-SEP

OFF_ERR/5.0/3/16.0MHZ/0K/0.5				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		2.3	2.3	
Min Limit		-2.3	-2.3	
krams	Serial #	Pre	Post	Delta
0	23	0.200	0.168	0.031
0	24	0.190	0.224	-0.034
30	1	0.229	0.220	0.008
30	2	0.207	0.187	0.020
30	3	0.180	0.177	0.003
30	4	0.323	0.356	-0.033
30	5	0.295	0.301	-0.006
30	6	0.162	0.169	-0.007
30	7	0.219	0.230	-0.012
30	8	0.282	0.281	0.001
30	9	0.187	0.196	-0.009
30	10	0.280	0.279	0.002
30	11	0.325	0.277	0.048
30	12	0.176	0.176	0.000
30	13	0.197	0.247	-0.050
30	14	0.272	0.284	-0.012
30	15	0.284	0.275	0.009
30	16	0.182	0.185	-0.003
30	17	0.185	0.169	0.016
30	18	0.176	0.194	-0.018
30	19	0.243	0.585	-0.342
30	20	0.184	0.153	0.031
30	21	0.174	0.165	0.009
30	22	0.292	0.272	0.020
Max		0.325	0.585	0.048
Average		0.227	0.240	-0.014
Min		0.162	0.153	-0.342
Std Dev		0.053	0.091	0.073

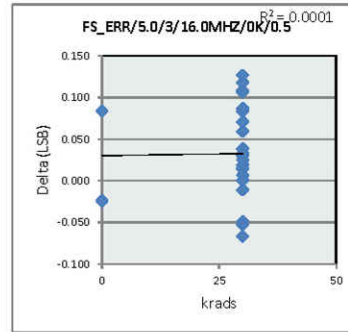


OFF_ERR/5.0/3/16.0MHZ/0K/0.5		
Test Site		
Tester		
Test Number		
Max Limit	2.3	LSB
Min Limit	-2.3	LSB
krams	0	30
LL	-2.300	-2.300
Min	0.168	0.153
Average	0.196	0.245
Max	0.224	0.585
UL	2.300	2.300

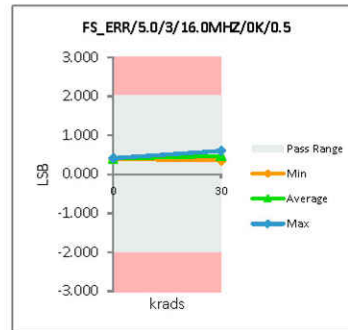


TID Report ADC128S102PWT-SEP

FS_ERR/5.0/3/16.0MHZ/0K/0.5				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		2	2	
Min Limit		-2	-2	
krams	Serial #	Pre	Post	Delta
0	23	0.382	0.406	-0.024
0	24	0.475	0.391	0.084
30	1	0.566	0.458	0.108
30	2	0.494	0.376	0.118
30	3	0.505	0.422	0.084
30	4	0.519	0.568	-0.049
30	5	0.598	0.492	0.106
30	6	0.344	0.393	-0.049
30	7	0.514	0.483	0.030
30	8	0.609	0.482	0.127
30	9	0.426	0.411	0.015
30	10	0.522	0.508	0.014
30	11	0.508	0.575	-0.067
30	12	0.530	0.471	0.059
30	13	0.499	0.412	0.087
30	14	0.605	0.598	0.007
30	15	0.547	0.528	0.019
30	16	0.418	0.347	0.071
30	17	0.432	0.484	-0.053
30	18	0.434	0.395	0.039
30	19	0.471	0.446	0.025
30	20	0.398	0.398	0.000
30	21	0.405	0.416	-0.011
30	22	0.541	0.508	0.033
Max		0.609	0.598	0.127
Average		0.489	0.457	0.032
Min		0.344	0.347	-0.067
Std Dev		0.072	0.067	0.057

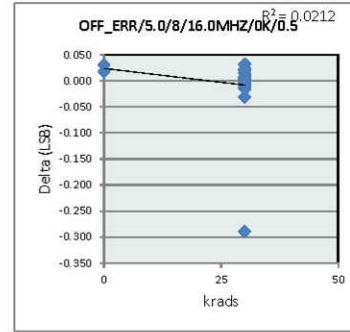


FS_ERR/5.0/3/16.0MHZ/0K/0.5		
Test Site		
Tester		
Test Number		
Max Limit	2	LSB
Min Limit	-2	LSB
krams	0	30
LL	-2.000	-2.000
Min	0.391	0.347
Average	0.399	0.462
Max	0.406	0.598
UL	2.000	2.000

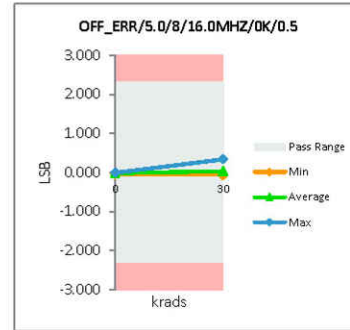


TID Report ADC128S102PWT-SEP

OFF_ERR/5.0/8/16.0MHZ/0K/0.5				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		2.3	2.3	
Min Limit		-2.3	-2.3	
krams	Serial #	Pre	Post	Delta
0	23	-0.008	-0.038	0.030
0	24	0.010	-0.008	0.017
30	1	0.013	0.028	-0.015
30	2	-0.002	-0.023	0.021
30	3	-0.012	-0.015	0.003
30	4	0.078	0.089	-0.011
30	5	0.086	0.092	-0.006
30	6	-0.059	-0.065	0.006
30	7	0.000	-0.003	0.003
30	8	0.091	0.069	0.023
30	9	0.003	0.008	-0.004
30	10	0.041	0.056	-0.015
30	11	0.080	0.065	0.015
30	12	-0.030	0.002	-0.031
30	13	0.031	-0.001	0.032
30	14	0.062	0.040	0.022
30	15	0.020	0.036	-0.015
30	16	0.002	-0.012	0.014
30	17	0.005	-0.003	0.009
30	18	-0.010	-0.018	0.008
30	19	0.047	0.336	-0.289
30	20	-0.026	-0.030	0.004
30	21	-0.004	-0.025	0.021
30	22	0.062	0.039	0.023
Max		0.091	0.336	0.032
Average		0.020	0.026	-0.006
Min		-0.059	-0.065	-0.289
Std Dev		0.040	0.078	0.063

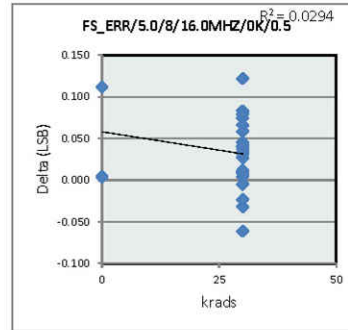


OFF_ERR/5.0/8/16.0MHZ/0K/0.5		
Test Site		
Tester		
Test Number		
Max Limit	2.3	LSB
Min Limit	-2.3	LSB
krams	0	30
LL	-2.300	-2.300
Min	-0.038	-0.065
Average	-0.023	0.030
Max	-0.008	0.336
UL	2.300	2.300

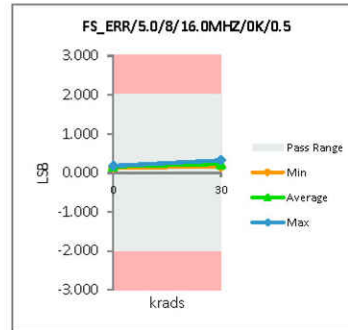


TID Report ADC128S102PWT-SEP

FS_ERR/5.0/8/16.0MHZ/0K/0.5				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		2	2	
Min Limit		-2	-2	
krads	Serial #	Pre	Post	Delta
0	23	0.186	0.182	0.004
0	24	0.254	0.142	0.112
30	1	0.304	0.182	0.122
30	2	0.245	0.162	0.083
30	3	0.254	0.249	0.004
30	4	0.261	0.323	-0.061
30	5	0.313	0.282	0.031
30	6	0.165	0.197	-0.032
30	7	0.281	0.273	0.008
30	8	0.311	0.252	0.058
30	9	0.214	0.168	0.045
30	10	0.307	0.266	0.041
30	11	0.291	0.296	-0.005
30	12	0.270	0.260	0.010
30	13	0.242	0.168	0.074
30	14	0.339	0.312	0.027
30	15	0.322	0.243	0.079
30	16	0.220	0.154	0.066
30	17	0.220	0.244	-0.023
30	18	0.203	0.163	0.040
30	19	0.241	0.204	0.037
30	20	0.211	0.169	0.041
30	21	0.212	0.178	0.033
30	22	0.290	0.279	0.012
Max		0.339	0.323	0.122
Average		0.256	0.223	0.034
Min		0.165	0.142	-0.061
Std Dev		0.047	0.056	0.044

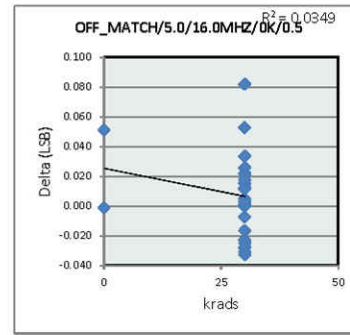


FS_ERR/5.0/8/16.0MHZ/0K/0.5		
Test Site		
Tester		
Test Number		
Max Limit	2	LSB
Min Limit	-2	LSB
krads	0	30
LL	-2.000	-2.000
Min	0.142	0.154
Average	0.162	0.228
Max	0.182	0.323
UL	2.000	2.000

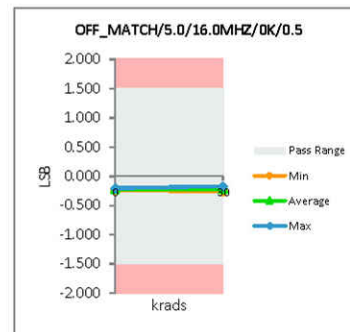


TID Report
ADC128S102PWT-SEP

OFF_MATCH/5.0/16.0MHZ/OK/0				
krads	Serial #	Pre	Post	Delta
0	23	-0.207	-0.206	-0.001
0	24	-0.180	-0.231	0.051
30	1	-0.216	-0.193	-0.023
30	2	-0.209	-0.210	0.001
30	3	-0.192	-0.192	0.000
30	4	-0.246	-0.267	0.021
30	5	-0.208	-0.208	0.000
30	6	-0.221	-0.233	0.012
30	7	-0.218	-0.233	0.015
30	8	-0.191	-0.213	0.022
30	9	-0.184	-0.188	0.005
30	10	-0.240	-0.223	-0.017
30	11	-0.245	-0.212	-0.033
30	12	-0.206	-0.175	-0.031
30	13	-0.166	-0.248	0.082
30	14	-0.210	-0.244	0.034
30	15	-0.264	-0.239	-0.025
30	16	-0.180	-0.198	0.018
30	17	-0.180	-0.173	-0.007
30	18	-0.187	-0.212	0.026
30	19	-0.196	-0.249	0.053
30	20	-0.210	-0.183	-0.028
30	21	-0.179	-0.190	0.012
30	22	-0.230	-0.233	0.003
Max		-0.166	-0.173	0.082
Average		-0.207	-0.215	0.008
Min		-0.264	-0.267	-0.033
Std Dev		0.025	0.025	0.028

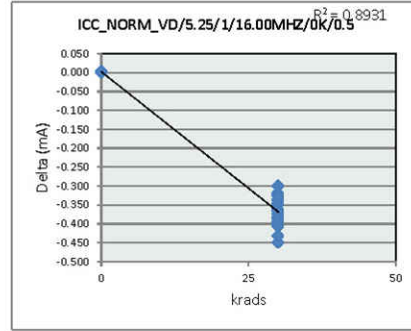


OFF_MATCH/5.0/16.0MHZ/OK		
krads	LL	UL
0	-1.500	-1.500
30	-0.231	-0.267
Average	-0.219	-0.214
Max	-0.206	-0.173
UL	1.500	1.500

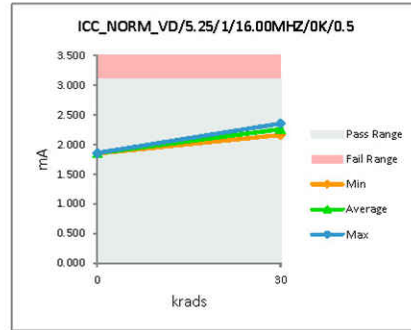


TID Report
ADC128S102PWT-SEP

ICC_NORM_VD/5.25/1/16.00MH				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		3.1	3.1	
Min Limit		0	0	
krads	Serial #	Pre	Post	Delta
0	23	1.848	1.846	0.003
0	24	1.856	1.855	0.001
30	1	1.887	2.284	-0.397
30	2	1.864	2.201	-0.336
30	3	1.871	2.190	-0.319
30	4	1.946	2.308	-0.362
30	5	1.891	2.214	-0.324
30	6	1.858	2.158	-0.299
30	7	1.901	2.350	-0.449
30	8	1.908	2.293	-0.386
30	9	1.863	2.237	-0.374
30	10	1.924	2.327	-0.402
30	11	1.927	2.334	-0.407
30	12	1.862	2.200	-0.338
30	13	1.890	2.239	-0.349
30	14	1.917	2.348	-0.431
30	15	1.932	2.318	-0.387
30	16	1.839	2.185	-0.347
30	17	1.861	2.207	-0.346
30	18	1.862	2.188	-0.326
30	19	1.830	2.212	-0.382
30	20	1.855	2.240	-0.385
30	21	1.868	2.234	-0.366
30	22	1.920	2.313	-0.393
Max		1.946	2.350	0.003
Average		1.882	2.220	-0.338
Min		1.830	1.846	-0.449
Std Dev		0.032	0.128	0.111

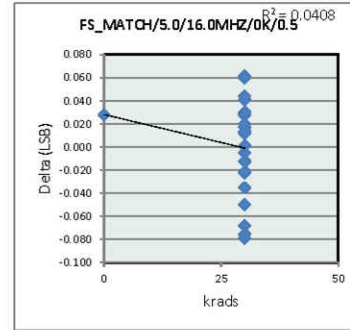


ICC_NORM_VD/5.25/1/16.00MH		
Test Site		
Tester		
Test Number		
Max Limit	3.1	mA
Min Limit	0	mA
krads	0	30
LL	0.000	0.000
Min	1.846	2.158
Average	1.850	2.254
Max	1.855	2.350
UL	3.100	3.100

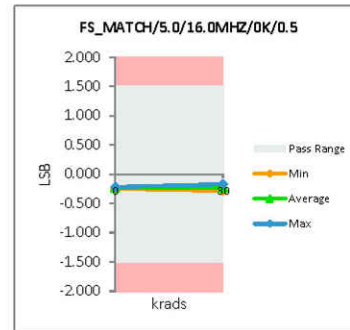


TID Report ADC128S102PWT-SEP

FS_MATCH/5.0/16.0MHZ/0K/0.5				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		1.5	1.5	
Min Limit		-1.5	-1.5	
krams	Serial #	Pre	Post	Delta
0	23	-0.196	-0.224	0.028
0	24	-0.221	-0.249	0.028
30	1	-0.262	-0.275	0.014
30	2	-0.249	-0.214	-0.035
30	3	-0.251	-0.172	-0.079
30	4	-0.257	-0.245	-0.012
30	5	-0.286	-0.210	-0.075
30	6	-0.180	-0.197	0.017
30	7	-0.233	-0.210	-0.022
30	8	-0.298	-0.229	-0.068
30	9	-0.212	-0.243	0.030
30	10	-0.215	-0.242	0.027
30	11	-0.217	-0.279	0.062
30	12	-0.260	-0.210	-0.050
30	13	-0.257	-0.244	-0.013
30	14	-0.266	-0.286	0.020
30	15	-0.225	-0.285	0.060
30	16	-0.198	-0.193	-0.005
30	17	-0.211	-0.241	0.029
30	18	-0.231	-0.232	0.001
30	19	-0.230	-0.242	0.012
30	20	-0.188	-0.229	0.041
30	21	-0.193	-0.237	0.044
30	22	-0.251	-0.229	-0.021
Max		-0.180	-0.172	0.062
Average		-0.233	-0.234	0.001
Min		-0.298	-0.286	-0.079
Std Dev		0.031	0.029	0.040

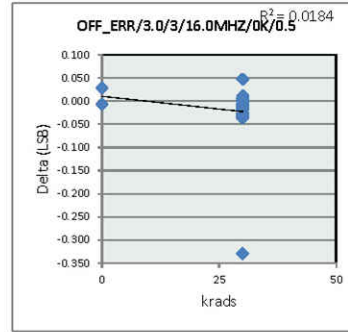


FS_MATCH/5.0/16.0MHZ/0K/0.5		
Test Site		
Tester		
Test Number		
Max Limit	1.5	LSB
Min Limit	-1.5	LSB
krams	0	30
LL	-1.500	-1.500
Min	-0.249	-0.286
Average	-0.236	-0.234
Max	-0.224	-0.172
UL	1.500	1.500

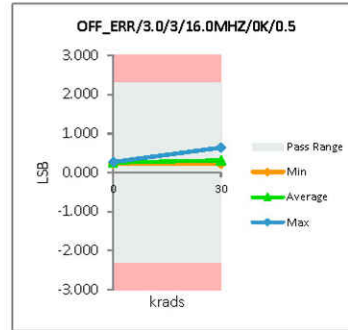


**TID Report
ADC128S102PWT-SEP**

OFF_ERR/3.0/3/16.0MHZ/0K/0.5				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		2.3	2.3	
Min Limit		-2.3	-2.3	
krads	Serial #	Pre	Post	Delta
0	23	0.266	0.238	0.028
0	24	0.264	0.271	-0.008
30	1	0.289	0.282	0.007
30	2	0.254	0.263	-0.009
30	3	0.268	0.262	0.006
30	4	0.379	0.390	-0.010
30	5	0.356	0.353	0.003
30	6	0.221	0.217	0.003
30	7	0.260	0.291	-0.031
30	8	0.339	0.354	-0.015
30	9	0.254	0.280	-0.026
30	10	0.306	0.324	-0.018
30	11	0.389	0.342	0.047
30	12	0.245	0.273	-0.028
30	13	0.264	0.293	-0.029
30	14	0.320	0.327	-0.006
30	15	0.318	0.355	-0.037
30	16	0.259	0.256	0.003
30	17	0.251	0.239	0.011
30	18	0.242	0.258	-0.016
30	19	0.309	0.639	-0.330
30	20	0.224	0.224	0.000
30	21	0.218	0.251	-0.032
30	22	0.344	0.338	0.006
Max		0.389	0.639	0.047
Average		0.285	0.305	-0.020
Min		0.218	0.217	-0.330
Std Dev		0.050	0.085	0.069

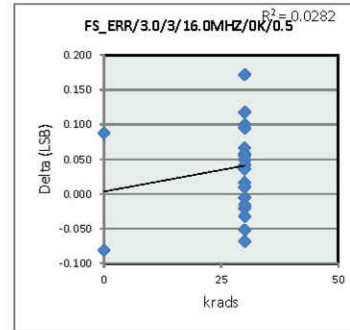


OFF_ERR/3.0/3/16.0MHZ/0K/0.5		
Test Site		
Tester		
Test Number		
Max Limit	2.3	LSB
Min Limit	-2.3	LSB
krads	0	30
LL	-2.300	-2.300
Min	0.238	0.217
Average	0.255	0.310
Max	0.271	0.639
UL	2.300	2.300

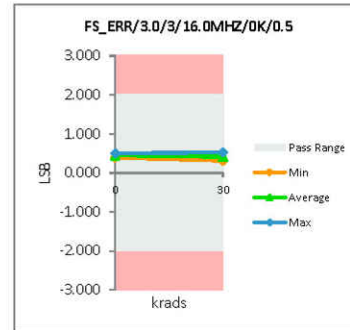


TID Report ADC128S102PWT-SEP

FS_ERR/3.0/3/16.0MHZ/0K/0.5				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		2	2	
Min Limit		-2	-2	
krams	Serial #	Pre	Post	Delta
0	23	0.477	0.390	0.087
0	24	0.423	0.503	-0.081
30	1	0.461	0.361	0.100
30	2	0.474	0.379	0.095
30	3	0.497	0.379	0.118
30	4	0.513	0.471	0.042
30	5	0.544	0.426	0.118
30	6	0.350	0.309	0.041
30	7	0.424	0.492	-0.068
30	8	0.537	0.521	0.017
30	9	0.424	0.444	-0.020
30	10	0.424	0.357	0.066
30	11	0.410	0.400	0.010
30	12	0.443	0.448	-0.005
30	13	0.422	0.367	0.056
30	14	0.483	0.428	0.056
30	15	0.455	0.405	0.049
30	16	0.407	0.351	0.056
30	17	0.441	0.456	-0.016
30	18	0.494	0.458	0.036
30	19	0.412	0.463	-0.051
30	20	0.349	0.382	-0.032
30	21	0.498	0.326	0.172
30	22	0.449	0.391	0.058
	Max	0.544	0.521	0.172
	Average	0.450	0.413	0.038
	Min	0.349	0.309	-0.081
	Std Dev	0.050	0.056	0.063

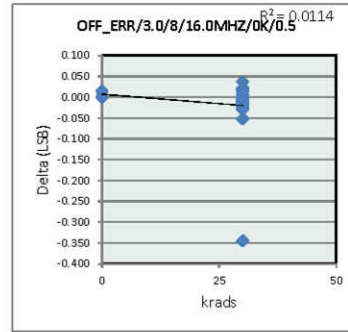


FS_ERR/3.0/3/16.0MHZ/0K/0.5		
Test Site		
Tester		
Test Number		
Max Limit		2
Min Limit		-2
krams		0
LL		-2.000
Min		0.390
Average		0.447
Max		0.503
UL		2.000

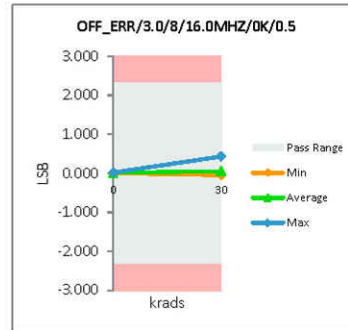


TID Report ADC128S102PWT-SEP

OFF_ERR/3.0/8/16.0MHZ/0K/0.5				
Test Site				
Tester				
Test Number				
Unit	LSB	LSB		
Max Limit	2.3	2.3		
Min Limit	-2.3	-2.3		
krams	Serial #	Pre	Post	Delta
0	23	-0.004	-0.004	0.000
0	24	0.029	0.015	0.014
30	1	0.009	0.018	-0.008
30	2	0.001	-0.017	0.018
30	3	0.003	0.002	0.000
30	4	0.100	0.125	-0.025
30	5	0.106	0.107	-0.001
30	6	-0.060	-0.047	-0.013
30	7	-0.010	0.015	-0.025
30	8	0.097	0.102	-0.006
30	9	0.006	0.025	-0.019
30	10	0.055	0.085	-0.030
30	11	0.112	0.092	0.019
30	12	0.008	0.001	0.007
30	13	0.011	0.033	-0.022
30	14	0.059	0.070	-0.011
30	15	0.037	0.089	-0.052
30	16	0.006	-0.011	0.018
30	17	0.036	-0.001	0.037
30	18	0.023	0.001	0.022
30	19	0.080	0.424	-0.345
30	20	-0.022	-0.023	0.001
30	21	-0.036	-0.007	-0.029
30	22	0.083	0.069	0.014
Max	0.112	0.424	0.037	
Average	0.030	0.048	-0.018	
Min	-0.060	-0.047	-0.345	
Std Dev	0.047	0.093	0.073	

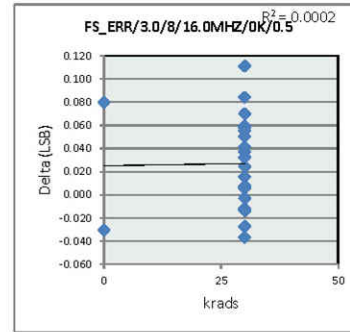


OFF_ERR/3.0/8/16.0MHZ/0K/0.5		
Test Site		
Tester		
Test Number		
Max Limit	2.3	LSB
Min Limit	-2.3	LSB
krams	0	30
LL	-2.300	-2.300
Min	-0.004	-0.047
Average	0.005	0.052
Max	0.015	0.424
UL	2.300	2.300

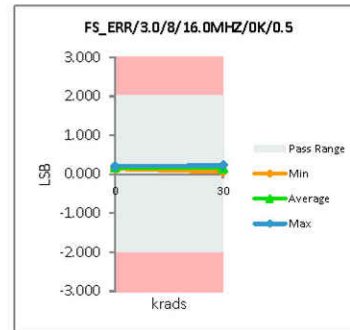


TID Report ADC128S102PWT-SEP

FS_ERR/3.0/8/16.0MHZ/0K/0.5				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		2	2	
Min Limit		-2	-2	
krams	Serial #	Pre	Post	Delta
0	23	0.215	0.135	0.080
0	24	0.170	0.200	-0.030
30	1	0.193	0.082	0.111
30	2	0.184	0.178	0.006
30	3	0.222	0.152	0.070
30	4	0.201	0.228	-0.027
30	5	0.253	0.203	0.050
30	6	0.115	0.057	0.058
30	7	0.165	0.201	-0.037
30	8	0.216	0.229	-0.012
30	9	0.166	0.178	-0.012
30	10	0.170	0.146	0.024
30	11	0.172	0.185	-0.013
30	12	0.205	0.173	0.032
30	13	0.161	0.102	0.059
30	14	0.224	0.208	0.015
30	15	0.181	0.122	0.059
30	16	0.162	0.107	0.055
30	17	0.184	0.187	-0.003
30	18	0.220	0.183	0.037
30	19	0.193	0.207	-0.014
30	20	0.139	0.132	0.008
30	21	0.204	0.120	0.084
30	22	0.173	0.132	0.041
Max		0.253	0.229	0.111
Average		0.187	0.160	0.027
Min		0.115	0.057	-0.037
Std Dev		0.031	0.047	0.041

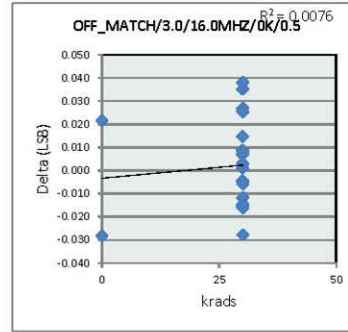


FS_ERR/3.0/8/16.0MHZ/0K/0.5		
Test Site		
Tester		
Test Number		
Max Limit	2	LSB
Min Limit	-2	LSB
krams	0	30
LL	-2.000	-2.000
Min	0.135	0.057
Average	0.167	0.160
Max	0.200	0.229
UL	2.000	2.000

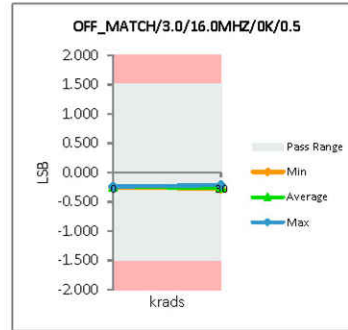


TID Report ADC128S102PWT-SEP

OFF_MATCH/3.0/16.0MHZ/0K/0				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		1.5	1.5	
Min Limit		-1.5	-1.5	
krams	Serial #	Pre	Post	Delta
0	23	-0.271	-0.242	-0.028
0	24	-0.235	-0.256	0.021
30	1	-0.279	-0.264	-0.015
30	2	-0.253	-0.280	0.027
30	3	-0.266	-0.260	-0.006
30	4	-0.280	-0.265	-0.015
30	5	-0.250	-0.246	-0.004
30	6	-0.280	-0.264	-0.016
30	7	-0.270	-0.276	0.007
30	8	-0.243	-0.251	0.009
30	9	-0.249	-0.255	0.007
30	10	-0.251	-0.239	-0.012
30	11	-0.278	-0.250	-0.028
30	12	-0.237	-0.272	0.035
30	13	-0.253	-0.260	0.007
30	14	-0.262	-0.257	-0.005
30	15	-0.281	-0.265	-0.016
30	16	-0.252	-0.267	0.015
30	17	-0.214	-0.240	0.025
30	18	-0.219	-0.257	0.038
30	19	-0.229	-0.214	-0.015
30	20	-0.246	-0.247	0.001
30	21	-0.255	-0.258	0.003
30	22	-0.261	-0.270	0.008
Max		-0.214	-0.214	0.038
Average		-0.255	-0.257	0.002
Min		-0.281	-0.280	-0.028
Std Dev		0.019	0.014	0.019

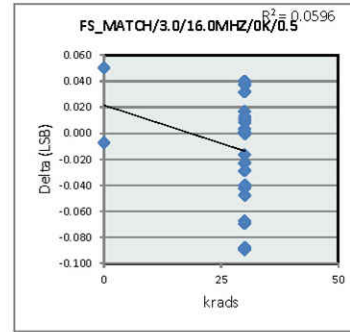


OFF_MATCH/3.0/16.0MHZ/0K		
Test Site		
Tester		
Test Number		
Max Limit	1.5	LSB
Min Limit	-1.5	LSB
krams	0	30
LL	-1.500	-1.500
Min	-0.256	-0.280
Average	-0.249	-0.257
Max	-0.242	-0.214
UL	1.500	1.500

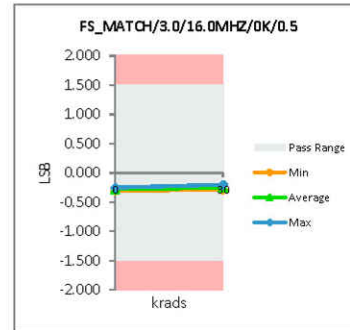


TID Report ADC128S102PWT-SEP

FS_MATCH/3.0/16.0MHZ/0K/0.5				
Test Site				
Tester				
Test Number				
Unit		LSB	LSB	
Max Limit		1.5	1.5	
Min Limit		-1.5	-1.5	
krams	Serial #	Pre	Post	Delta
0	23	-0.262	-0.255	-0.007
0	24	-0.253	-0.303	0.050
30	1	-0.269	-0.280	0.011
30	2	-0.290	-0.201	-0.089
30	3	-0.274	-0.227	-0.048
30	4	-0.313	-0.243	-0.069
30	5	-0.291	-0.223	-0.067
30	6	-0.235	-0.252	0.017
30	7	-0.259	-0.291	0.032
30	8	-0.321	-0.292	-0.029
30	9	-0.258	-0.266	0.008
30	10	-0.253	-0.211	-0.042
30	11	-0.238	-0.215	-0.023
30	12	-0.238	-0.275	0.037
30	13	-0.261	-0.265	0.003
30	14	-0.259	-0.219	-0.040
30	15	-0.273	-0.283	0.010
30	16	-0.245	-0.244	0.000
30	17	-0.257	-0.270	0.013
30	18	-0.273	-0.274	0.001
30	19	-0.219	-0.256	0.038
30	20	-0.210	-0.250	0.040
30	21	-0.294	-0.206	-0.088
30	22	-0.276	-0.260	-0.017
Max		-0.210	-0.201	0.050
Average		-0.263	-0.253	-0.011
Min		-0.321	-0.303	-0.089
Std Dev		0.027	0.029	0.041

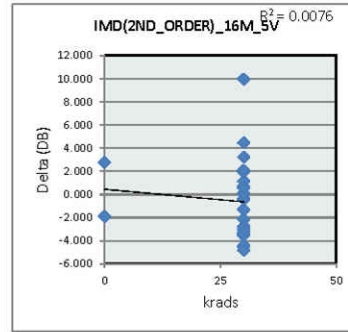


FS_MATCH/3.0/16.0MHZ/0K/0.5		
Test Site		
Tester		
Test Number		
Max Limit	1.5	LSB
Min Limit	-1.5	LSB
krams	0	30
LL	-1.500	-1.500
Min	-0.303	-0.292
Average	-0.279	-0.250
Max	-0.255	-0.201
UL	1.500	1.500

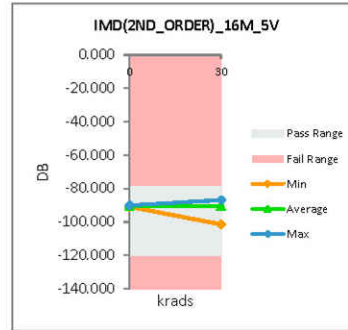


TID Report ADC128S102PWT-SEP

IMD(2ND_ORDER)_16M_5V				
Test Site				
Tester				
Test Number				
Unit	DB	DB		
Max Limit	-78	-78		
Min Limit	-120	-120		
krads	Serial #	Pre	Post	Delta
0	23	-88.214	-90.983	2.769
0	24	-92.021	-90.119	-1.902
30	1	-92.902	-88.388	-4.514
30	2	-88.603	-88.163	-0.439
30	3	-90.263	-86.909	-3.354
30	4	-90.620	-91.761	1.142
30	5	-95.159	-90.748	-4.411
30	6	-89.449	-92.676	3.227
30	7	-90.051	-91.996	1.945
30	8	-88.438	-88.044	-0.394
30	9	-92.671	-89.126	-3.545
30	10	-89.510	-90.184	0.674
30	11	-90.452	-90.540	0.088
30	12	-90.840	-92.920	2.080
30	13	-86.988	-91.444	4.457
30	14	-91.547	-101.512	9.965
30	15	-97.519	-93.085	-4.434
30	16	-90.598	-87.589	-3.009
30	17	-92.377	-87.537	-4.839
30	18	-90.157	-88.010	-2.147
30	19	-92.133	-89.334	-2.799
30	20	-89.989	-90.544	0.555
30	21	-94.821	-91.583	-3.238
30	22	-91.553	-90.230	-1.322
	Max	-86.988	-86.909	9.965
	Average	-91.120	-90.559	-0.560
	Min	-97.519	-101.512	-4.839
	Std Dev	2.376	2.953	3.516

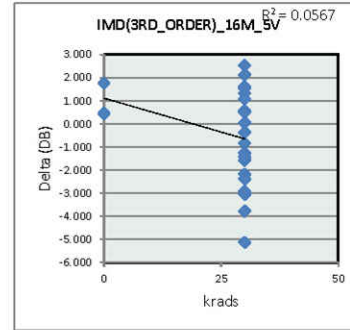


IMD(2ND_ORDER)_16M_5V		
Test Site		
Tester		
Test Number		
Max Limit	-78	DB
Min Limit	-120	DB
krads	0	30
LL	-120.000	-120.000
Min	-90.984	-101.512
Average	-90.551	-90.560
Max	-90.119	-86.909
UL	-78.000	-78.000

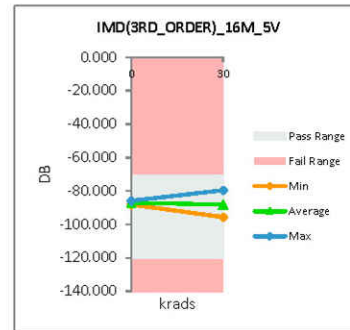


TID Report ADC128S102PWT-SEP

IMD(3RD_ORDER)_16M_5V				
Test Site				
Tester				
Test Number				
Unit		DB	DB	
Max Limit		-70	-70	
Min Limit		-120	-120	
krams	Serial #	Pre	Post	Delta
0	23	-84.247	-86.011	1.765
0	24	-87.880	-88.325	0.444
30	1	-88.414	-88.989	0.575
30	2	-87.660	-86.815	-0.846
30	3	-87.993	-89.551	1.558
30	4	-91.373	-93.504	2.131
30	5	-88.499	-86.106	-2.393
30	6	-97.642	-92.511	-5.131
30	7	-95.727	-95.774	0.047
30	8	-85.337	-81.562	-3.775
30	9	-87.980	-87.606	-0.374
30	10	-82.673	-84.001	1.328
30	11	-86.817	-84.640	-2.178
30	12	-90.651	-93.171	2.521
30	13	-87.223	-88.836	1.613
30	14	-82.800	-79.745	-3.055
30	15	-93.351	-93.847	0.496
30	16	-97.802	-94.876	-2.926
30	17	-85.356	-84.087	-1.269
30	18	-92.616	-93.696	1.079
30	19	-94.452	-92.875	-1.577
30	20	-85.871	-84.434	-1.437
30	21	-82.255	-83.782	1.526
30	22	-85.629	-83.253	-2.376
Max		-82.255	-79.745	2.521
Average		-88.760	-88.250	-0.511
Min		-97.802	-95.774	-5.131
Std Dev		4.540	4.628	2.090

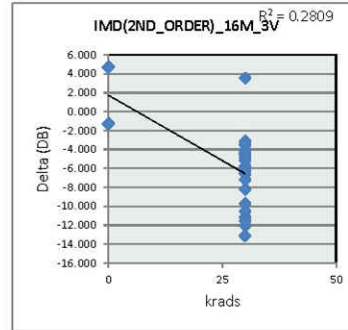


IMD(3RD_ORDER)_16M_5V		
Test Site		
Tester		
Test Number		
Max Limit		-70 DB
Min Limit		-120 DB
krams	0	30
LL	-120.000	-120.000
Min	-88.325	-95.774
Average	-87.168	-88.348
Max	-86.011	-79.745
UL	-70.000	-70.000

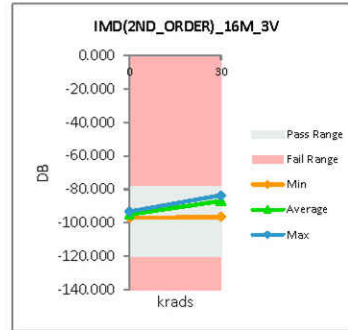


TID Report ADC128S102PWT-SEP

IMD(2ND_ORDER)_16M_3V				
Test Site				
Tester				
Test Number				
Unit		DB	DB	
Max Limit		-78	-78	
Min Limit		-120	-120	
krads	Serial #	Pre	Post	Delta
0	23	-94.633	-93.333	-1.301
0	24	-92.309	-97.004	4.695
30	1	-95.268	-84.141	-11.127
30	2	-95.945	-85.421	-10.524
30	3	-95.215	-89.461	-5.754
30	4	-91.780	-86.627	-5.153
30	5	-94.345	-87.132	-7.214
30	6	-96.540	-90.190	-6.350
30	7	-99.815	-86.689	-13.126
30	8	-92.450	-85.970	-6.480
30	9	-94.267	-90.163	-4.104
30	10	-90.652	-86.095	-4.557
30	11	-96.831	-84.772	-12.060
30	12	-91.950	-88.853	-3.096
30	13	-95.615	-84.035	-11.580
30	14	-93.096	-86.551	-6.545
30	15	-93.092	-96.636	3.544
30	16	-89.028	-85.527	-3.500
30	17	-91.975	-83.740	-8.235
30	18	-91.787	-88.326	-3.462
30	19	-95.369	-85.629	-9.740
30	20	-89.012	-84.119	-4.893
30	21	-93.071	-88.783	-4.288
30	22	-94.884	-88.700	-6.184
Max		-89.012	-83.740	4.695
Average		-93.705	-87.829	-5.876
Min		-99.815	-97.004	-13.126
Std Dev		2.516	3.634	4.401

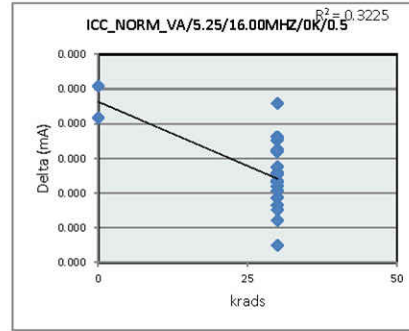


IMD(2ND_ORDER)_16M_3V		
Test Site		
Tester		
Test Number		
Max Limit	-78	DB
Min Limit	-120	DB
krads	0	30
LL	-120.000	-120.000
Min	-97.004	-96.636
Average	-95.169	-87.162
Max	-93.333	-83.740
UL	-78.000	-78.000

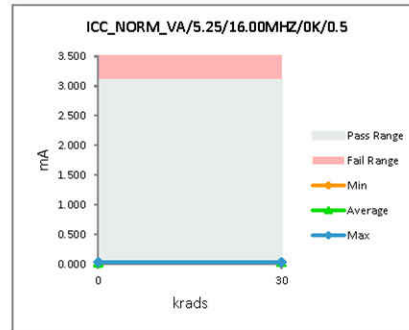


TID Report
ADC128S102PWT-SEP

ICC_NORM_VA/5.25/16.00MHZ/				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	3.1	3.1		
Min Limit	0	0		
krams	Serial #	Pre	Post	Delta
0	23	0.031	0.031	0.000
0	24	0.030	0.030	0.000
30	1	0.031	0.031	0.000
30	2	0.030	0.031	0.000
30	3	0.031	0.032	0.000
30	4	0.033	0.033	0.000
30	5	0.032	0.032	0.000
30	6	0.031	0.031	0.000
30	7	0.032	0.032	0.000
30	8	0.032	0.033	0.000
30	9	0.031	0.031	0.000
30	10	0.032	0.033	0.000
30	11	0.032	0.032	0.000
30	12	0.030	0.030	0.000
30	13	0.031	0.031	0.000
30	14	0.032	0.032	0.000
30	15	0.033	0.033	0.000
30	16	0.030	0.030	0.000
30	17	0.030	0.031	0.000
30	18	0.031	0.031	0.000
30	19	0.030	0.030	0.000
30	20	0.031	0.031	0.000
30	21	0.031	0.031	0.000
30	22	0.033	0.033	0.000
Max		0.033	0.033	0.000
Average		0.031	0.031	0.000
Min		0.030	0.030	0.000
Std Dev		0.001	0.001	0.000

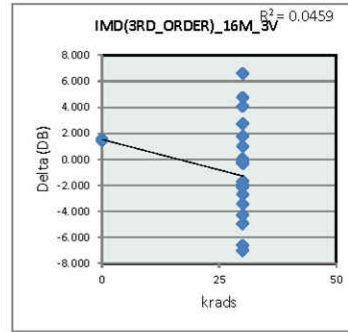


ICC_NORM_VA/5.25/16.00MHZ/		
Test Site		
Tester		
Test Number		
Max Limit	3.1	mA
Min Limit	0	mA
krams	0	30
LL	0.000	0.000
Min	0.030	0.030
Average	0.030	0.031
Max	0.031	0.033
UL	3.100	3.100

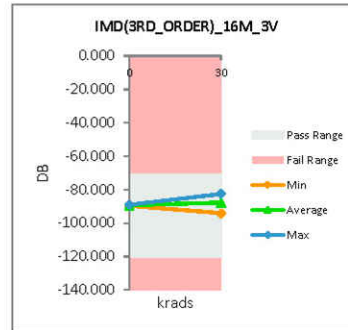


TID Report
ADC128S102PWT-SEP

IMD(3RD_ORDER)_16M_3V				
Test Site				
Tester				
Test Number				
Unit	DB	DB		
Max Limit	-70	-70		
Min Limit	-120	-120		
krads	Serial #	Pre	Post	Delta
0	23	-88.193	-89.763	1.571
0	24	-87.613	-89.068	1.455
30	1	-91.152	-88.990	-2.161
30	2	-95.036	-88.440	-6.596
30	3	-83.334	-88.080	4.746
30	4	-87.921	-89.686	1.765
30	5	-91.403	-89.359	-2.044
30	6	-86.684	-89.428	2.744
30	7	-86.587	-90.651	4.064
30	8	-85.837	-85.683	-0.154
30	9	-92.749	-91.062	-1.687
30	10	-87.820	-85.117	-2.703
30	11	-89.619	-82.611	-7.008
30	12	-85.795	-86.783	0.988
30	13	-92.111	-87.174	-4.938
30	14	-83.417	-83.449	0.032
30	15	-87.584	-94.184	6.601
30	16	-95.433	-90.477	-4.956
30	17	-90.825	-83.813	-7.013
30	18	-89.153	-88.827	-0.327
30	19	-93.576	-89.292	-4.284
30	20	-85.527	-83.578	-1.949
30	21	-88.535	-85.090	-3.445
30	22	-90.523	-90.289	-0.234
Max		-83.334	-82.611	6.601
Average		-89.018	-87.954	-1.064
Min		-95.433	-94.184	-7.013
Std Dev		3.317	2.887	3.704

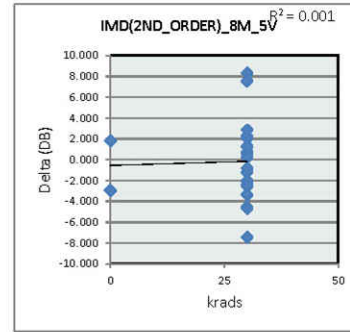


IMD(3RD_ORDER)_16M_3V		
Test Site		
Tester		
Test Number		
Max Limit	-70	DB
Min Limit	-120	DB
krads	0	30
LL	-120.000	-120.000
Min	-89.763	-94.184
Average	-89.416	-87.821
Max	-89.068	-82.611
UL	-70.000	-70.000

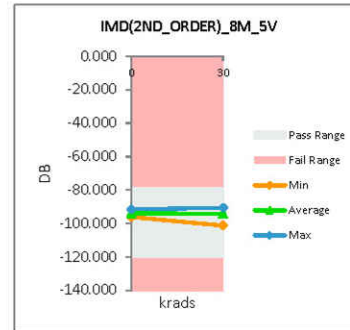


TID Report
ADC128S102PWT-SEP

IMD(2ND_ORDER)_8M_5V				
Test Site				
Tester				
Test Number				
Unit		DB	DB	
Max Limit		-78	-78	
Min Limit		-120	-120	
krads	Serial #	Pre	Post	Delta
0	23	-94.505	-91.552	-2.953
0	24	-94.374	-96.207	1.833
30	1	-91.076	-91.391	0.315
30	2	-97.551	-95.478	-2.073
30	3	-93.071	-101.383	8.312
30	4	-94.635	-95.208	0.573
30	5	-91.419	-93.488	2.068
30	6	-100.023	-95.481	-4.542
30	7	-92.441	-94.713	2.272
30	8	-92.593	-91.379	-1.214
30	9	-96.007	-96.186	0.179
30	10	-95.405	-92.047	-3.358
30	11	-91.585	-99.128	7.543
30	12	-93.550	-91.008	-2.542
30	13	-93.389	-94.167	0.777
30	14	-96.004	-96.212	0.208
30	15	-100.339	-92.885	-7.455
30	16	-96.603	-94.572	-2.030
30	17	-90.396	-91.700	1.304
30	18	-98.164	-99.372	1.208
30	19	-94.165	-91.735	-2.430
30	20	-93.305	-96.175	2.870
30	21	-96.785	-92.090	-4.695
30	22	-91.392	-90.608	-0.784
Max		-90.396	-90.608	8.312
Average		-94.532	-94.340	-0.192
Min		-100.339	-101.383	-7.455
Std Dev		2.736	2.892	3.560

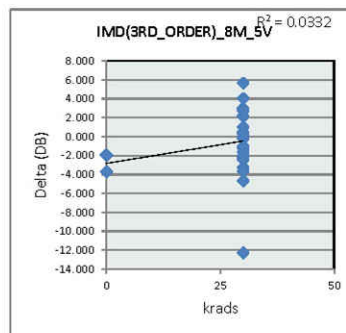


IMD(2ND_ORDER)_8M_5V		
Test Site		
Tester		
Test Number		
Max Limit		-78 DB
Min Limit		-120 DB
krads		0 30
LL		-120.000 -120.000
Min		-96.207 -101.383
Average		-93.879 -94.382
Max		-91.552 -90.608
UL		-78.000 -78.000

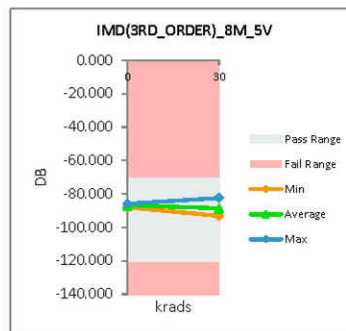


TID Report ADC128S102PWT-SEP

IMD(3RD_ORDER)_8M_5V				
krads	Serial #	Pre	Post	Delta
0	23	-87.894	-85.940	-1.953
0	24	-91.831	-88.120	-3.712
30	1	-90.897	-88.485	-2.412
30	2	-89.499	-87.429	-2.071
30	3	-91.014	-88.825	-2.189
30	4	-89.537	-92.506	2.969
30	5	-86.451	-92.094	5.643
30	6	-94.426	-93.174	-1.252
30	7	-91.271	-91.229	-0.042
30	8	-84.394	-82.757	-1.637
30	9	-93.739	-92.726	-1.013
30	10	-85.426	-85.868	0.442
30	11	-87.362	-84.076	-3.287
30	12	-88.934	-92.950	4.017
30	13	-89.207	-91.967	2.760
30	14	-82.698	-82.450	-0.248
30	15	-95.085	-91.395	-3.691
30	16	-100.623	-88.364	-12.259
30	17	-85.742	-86.024	0.282
30	18	-90.118	-92.960	2.842
30	19	-89.575	-91.743	2.167
30	20	-83.213	-85.335	2.122
30	21	-83.256	-84.229	0.974
30	22	-89.398	-84.737	-4.661
Max		-82.698	-82.450	5.643
Average		-89.233	-88.558	-0.675
Min		-100.623	-93.174	-12.259
Std Dev		4.207	3.607	3.649

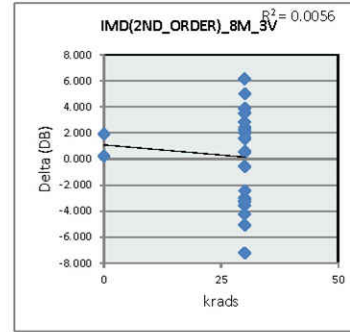


IMD(3RD_ORDER)_8M_5V		
Test Site		
Tester		
Test Number		
Max Limit	-70	DB
Min Limit	-120	DB
krads	0	30
LL	-120.000	-120.000
Min	-88.120	-93.174
Average	-87.030	-88.696
Max	-85.941	-82.450
UL	-70.000	-70.000

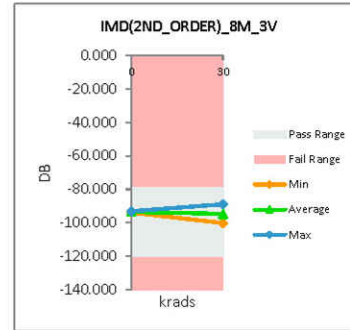


TID Report ADC128S102PWT-SEP

IMD(2ND_ORDER)_8M_3V				
Test Site				
Tester				
Test Number				
Unit		DB	DB	
Max Limit		-78	-78	
Min Limit		-120	-120	
krads	Serial #	Pre	Post	Delta
0	23	-91.301	-93.209	1.908
0	24	-93.739	-93.949	0.210
30	1	-99.078	-96.083	-2.995
30	2	-93.316	-96.808	3.492
30	3	-96.707	-94.264	-2.444
30	4	-94.730	-91.129	-3.601
30	5	-97.886	-100.306	2.420
30	6	-93.175	-98.180	5.005
30	7	-93.829	-96.039	2.210
30	8	-93.326	-94.875	1.549
30	9	-92.041	-94.039	1.998
30	10	-90.973	-94.856	3.884
30	11	-96.524	-89.296	-7.228
30	12	-91.348	-91.842	0.494
30	13	-92.209	-89.001	-3.207
30	14	-103.677	-100.401	-3.276
30	15	-93.206	-99.351	6.145
30	16	-102.063	-96.968	-5.095
30	17	-96.177	-91.917	-4.260
30	18	-91.637	-92.205	0.568
30	19	-90.376	-93.198	2.822
30	20	-94.412	-96.027	1.614
30	21	-94.337	-93.740	-0.597
30	22	-91.794	-95.324	3.530
Max		-90.376	-89.001	6.145
Average		-94.494	-94.709	0.214
Min		-103.677	-100.401	-7.228
Std Dev		3.422	3.067	3.483

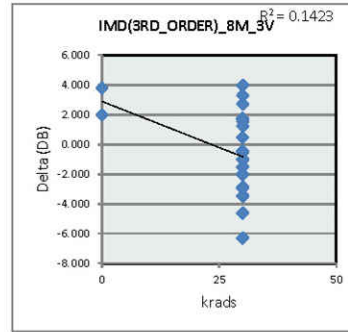


IMD(2ND_ORDER)_8M_3V		
Test Site		
Tester		
Test Number		
Max Limit		-78 DB
Min Limit		-120 DB
krads	0	30
LL	-120.000	-120.000
Min	-93.949	-100.401
Average	-93.579	-94.811
Max	-93.209	-89.001
UL	-78.000	-78.000

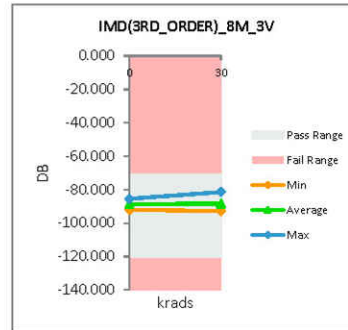


TID Report
ADC128S102PWT-SEP

IMD(3RD_ORDER)_8M_3V				
Test Site				
Tester				
Test Number				
Unit		DB	DB	
Max Limit		-70	-70	
Min Limit		-120	-120	
krads	Serial #	Pre	Post	Delta
0	23	-83.563	-85.565	2.002
0	24	-88.304	-92.080	3.776
30	1	-94.003	-91.987	-2.016
30	2	-89.298	-85.825	-3.473
30	3	-86.000	-88.708	2.708
30	4	-89.056	-93.020	3.964
30	5	-90.603	-87.167	-3.436
30	6	-92.251	-92.729	0.478
30	7	-95.379	-92.495	-2.884
30	8	-84.708	-84.277	-0.432
30	9	-88.095	-91.383	3.288
30	10	-84.376	-86.136	1.760
30	11	-85.794	-82.920	-2.874
30	12	-89.959	-88.922	-1.038
30	13	-96.041	-89.750	-6.290
30	14	-83.032	-81.526	-1.506
30	15	-94.691	-91.742	-2.949
30	16	-94.534	-89.918	-4.616
30	17	-87.062	-85.051	-2.011
30	18	-93.185	-92.668	-0.518
30	19	-89.432	-90.970	1.538
30	20	-84.613	-83.616	-0.997
30	21	-83.507	-84.731	1.224
30	22	-85.608	-87.336	1.728
Max		-83.032	-81.526	3.964
Average		-88.879	-88.355	-0.524
Min		-96.041	-93.020	-6.290
Std Dev		4.171	3.593	2.787

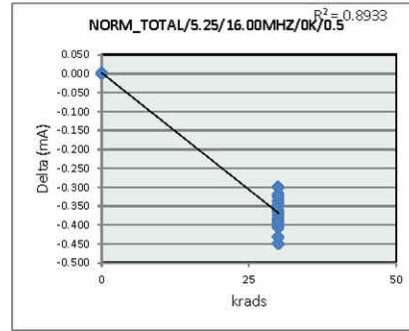


IMD(3RD_ORDER)_8M_3V		
Test Site		
Tester		
Test Number		
Max Limit	-70	DB
Min Limit	-120	DB
krads	0	30
LL	-120.000	-120.000
Min	-92.080	-93.020
Average	-88.822	-88.313
Max	-85.565	-81.526
UL	-70.000	-70.000

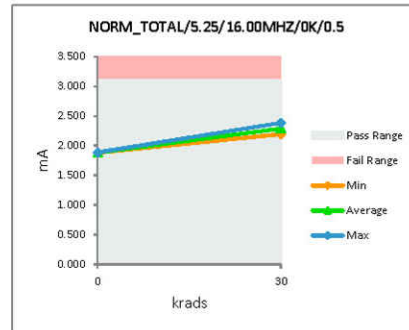


TID Report ADC128S102PWT-SEP

NORM_TOTAL/5.25/16.00MHZ/0				
Test Site				
Tester				
Test Number				
Unit		mA	mA	
Max Limit		3.1	3.1	
Min Limit		0	0	
krads	Serial #	Pre	Post	Delta
0	23	1.879	1.876	0.003
0	24	1.886	1.885	0.001
30	1	1.918	2.315	-0.398
30	2	1.895	2.231	-0.336
30	3	1.902	2.222	-0.319
30	4	1.979	2.341	-0.362
30	5	1.923	2.246	-0.324
30	6	1.890	2.189	-0.299
30	7	1.933	2.382	-0.449
30	8	1.940	2.326	-0.386
30	9	1.894	2.268	-0.374
30	10	1.957	2.359	-0.403
30	11	1.959	2.366	-0.407
30	12	1.892	2.231	-0.339
30	13	1.920	2.270	-0.350
30	14	1.949	2.381	-0.431
30	15	1.965	2.351	-0.387
30	16	1.869	2.215	-0.347
30	17	1.891	2.238	-0.346
30	18	1.893	2.219	-0.326
30	19	1.859	2.241	-0.382
30	20	1.886	2.271	-0.385
30	21	1.899	2.265	-0.366
30	22	1.952	2.346	-0.393
Max		1.979	2.382	0.003
Average		1.914	2.251	-0.338
Min		1.859	1.876	-0.449
Std Dev		0.033	0.128	0.111



NORM_TOTAL/5.25/16.00MHZ		
Test Site		
Tester		
Test Number		
Max Limit	3.1	mA
Min Limit	0	mA
krads	0	30
LL	0.000	0.000
Min	1.876	2.189
Average	1.881	2.285
Max	1.885	2.382
UL	3.100	3.100



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