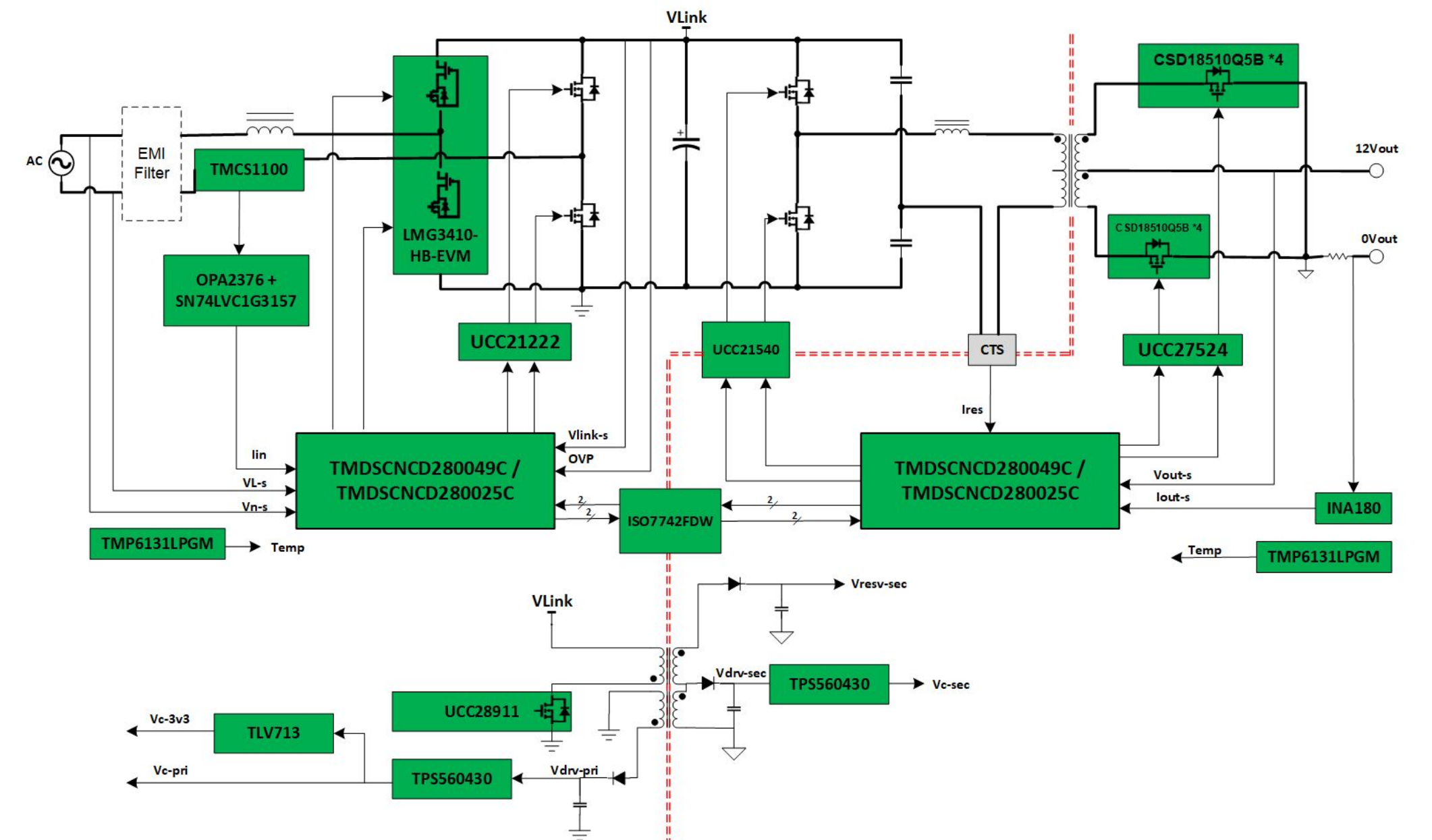



Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A



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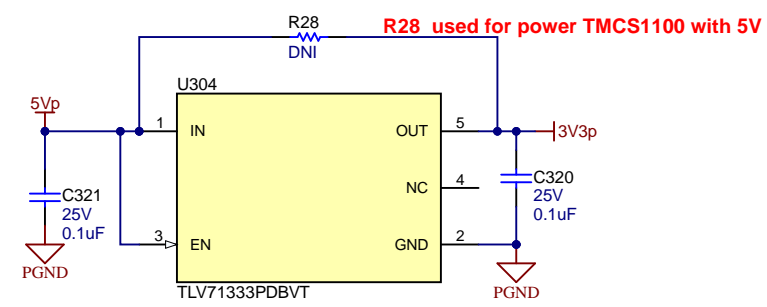
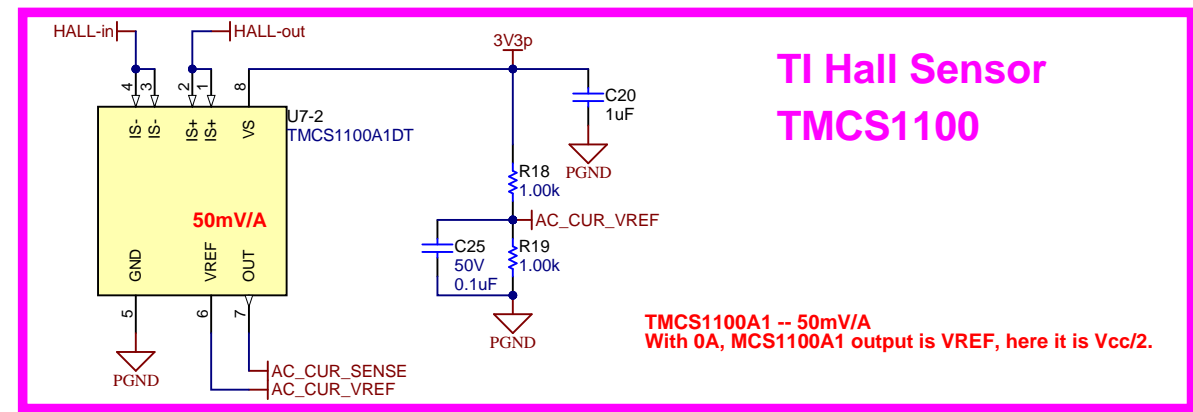
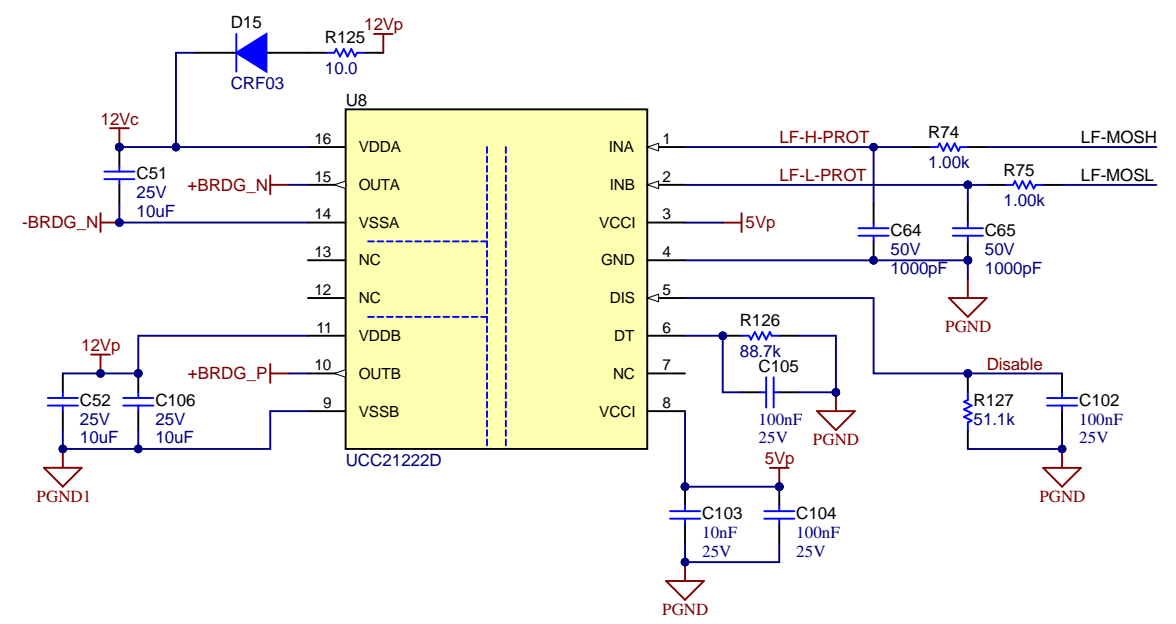
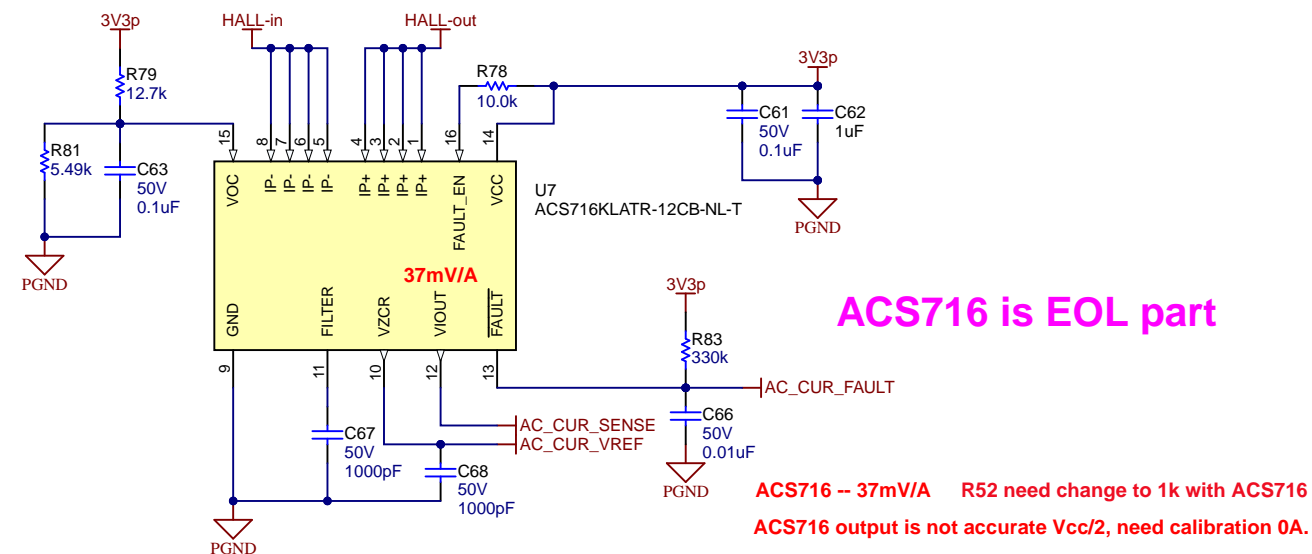
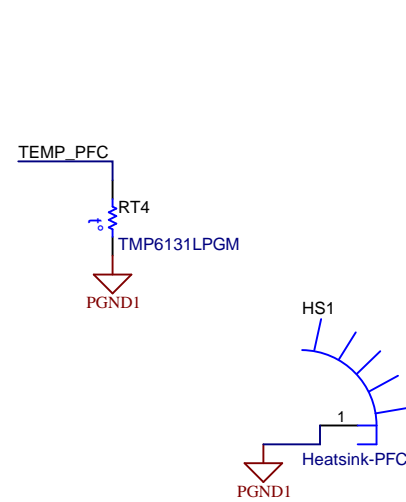
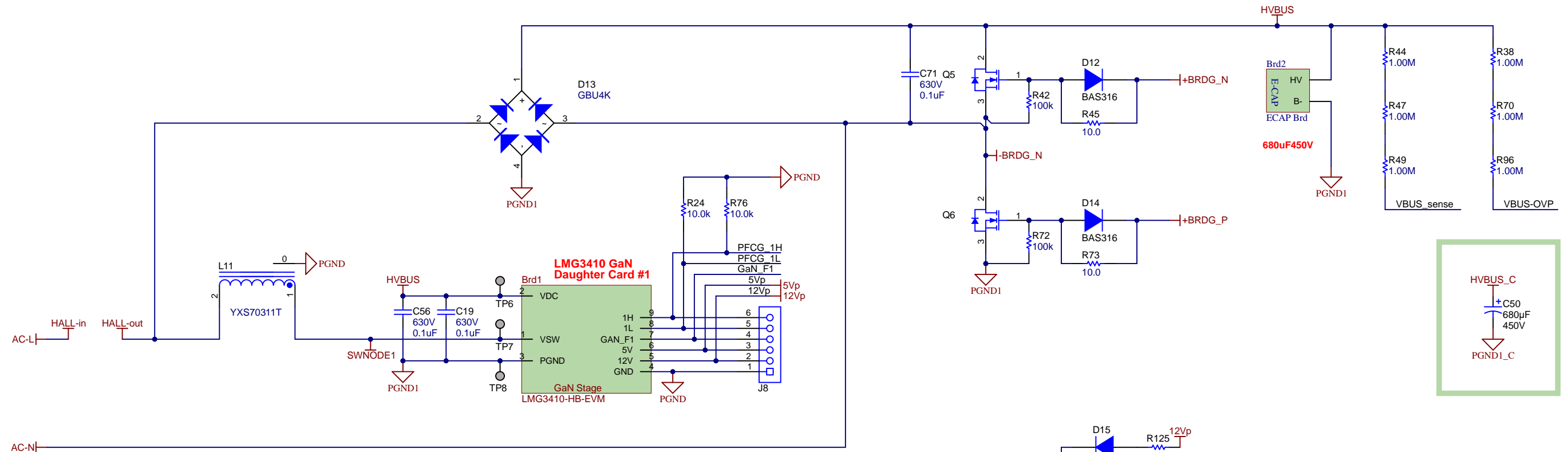
Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 11/30/2021
TID #: TIDA-010062	Project Title: 1kW Titanium Server PSU	
Number: PMP-41006	Rev: V4.1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 8
Drawn By: Desheng Guo	File: PMP41006_CoverSheet_SchDoc	Size: B
Engineer: Desheng Guo	Contact: http://www.ti.com/support	



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PFC Power Stages



PCB Number: PMP-41006
PCB Rev: V4.1

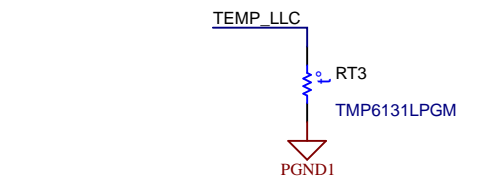
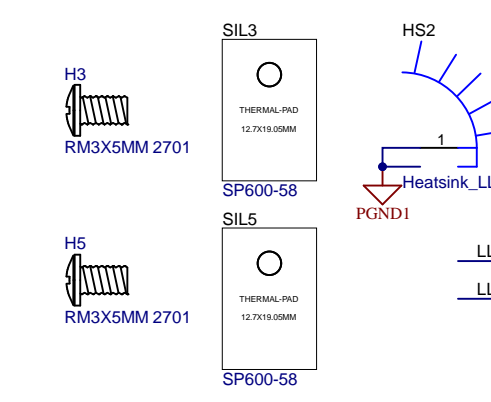
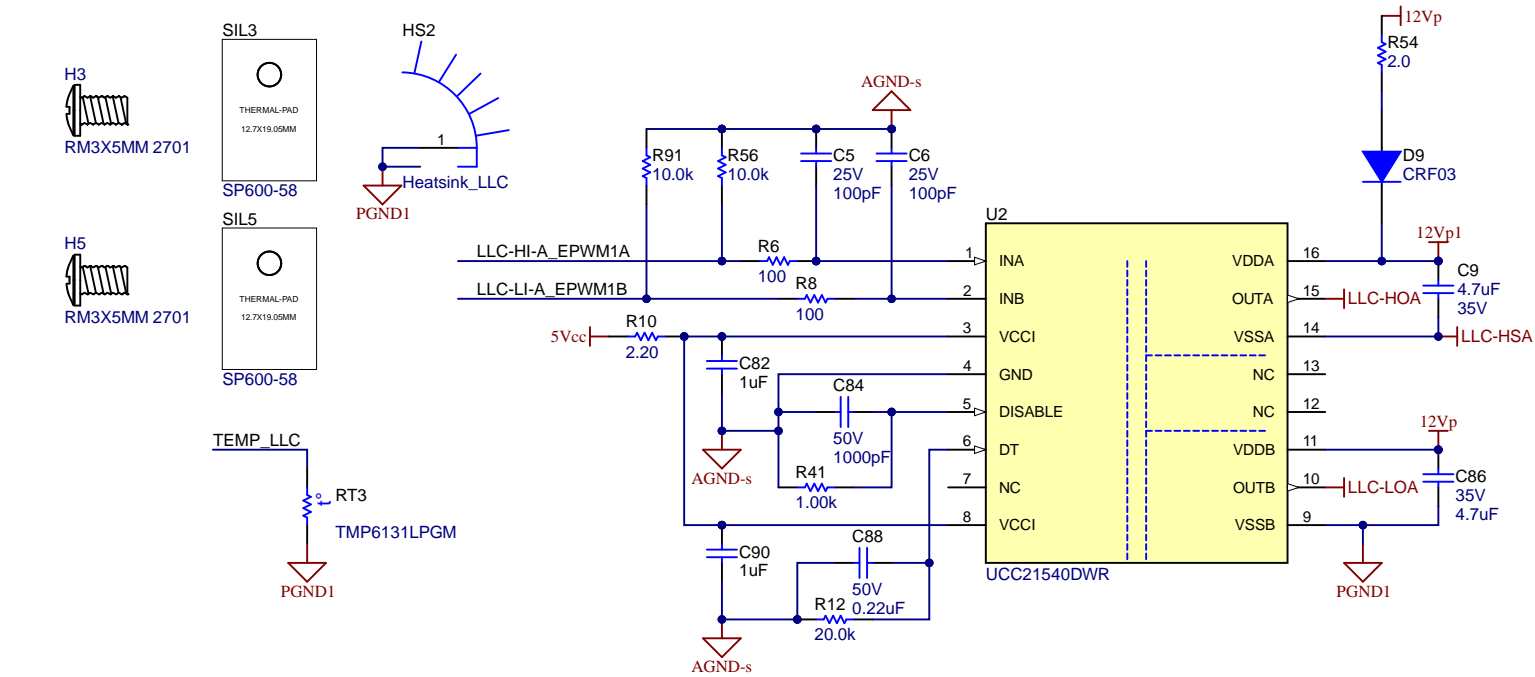
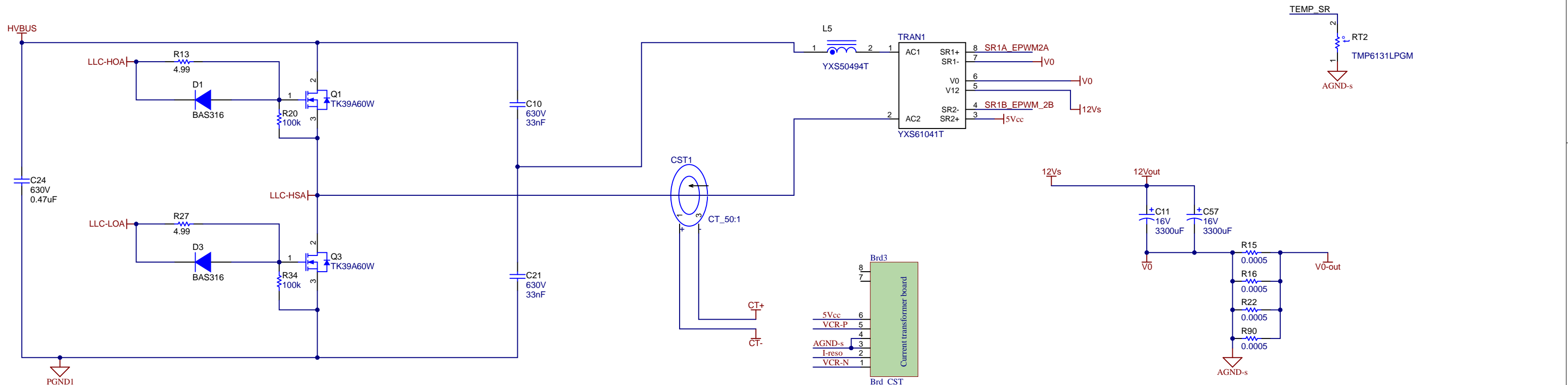
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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 11/30/2021
TID #: TIDA-010062	Project Title: 1kW Titanium Server PSU	
Number: PMP-41006	Rev: V4.1	Sheet Title: PFC Power Stage
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 10
Drawn By: Desheng Guo	File: PMP41006_Power-PFC.SchDoc	Size: B
Engineer: Desheng Guo	Contact: http://www.ti.com/support	

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LLC Power Stages

Lr=13uH Lm=200uH Cr=66nF



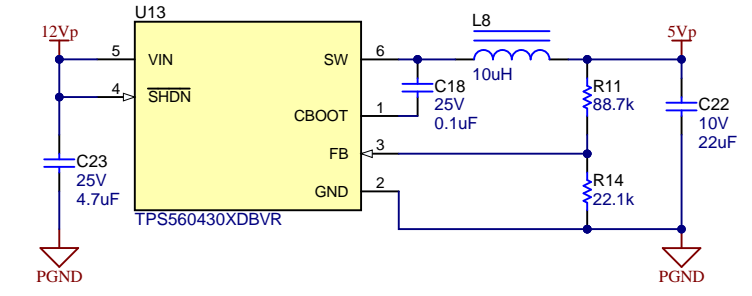
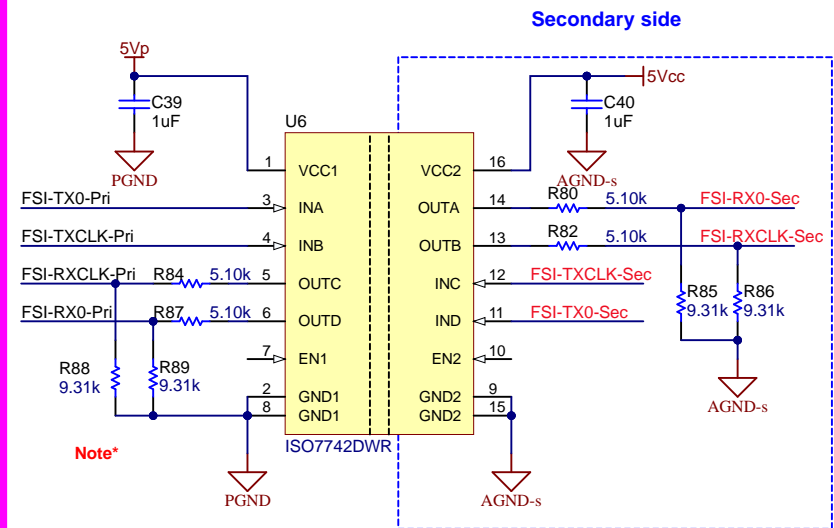
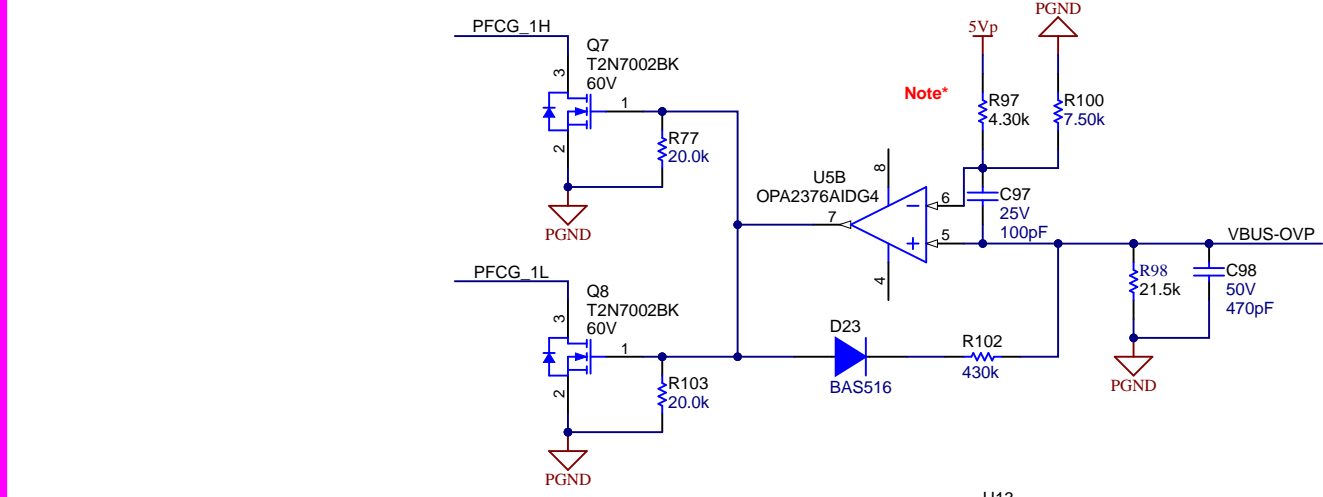
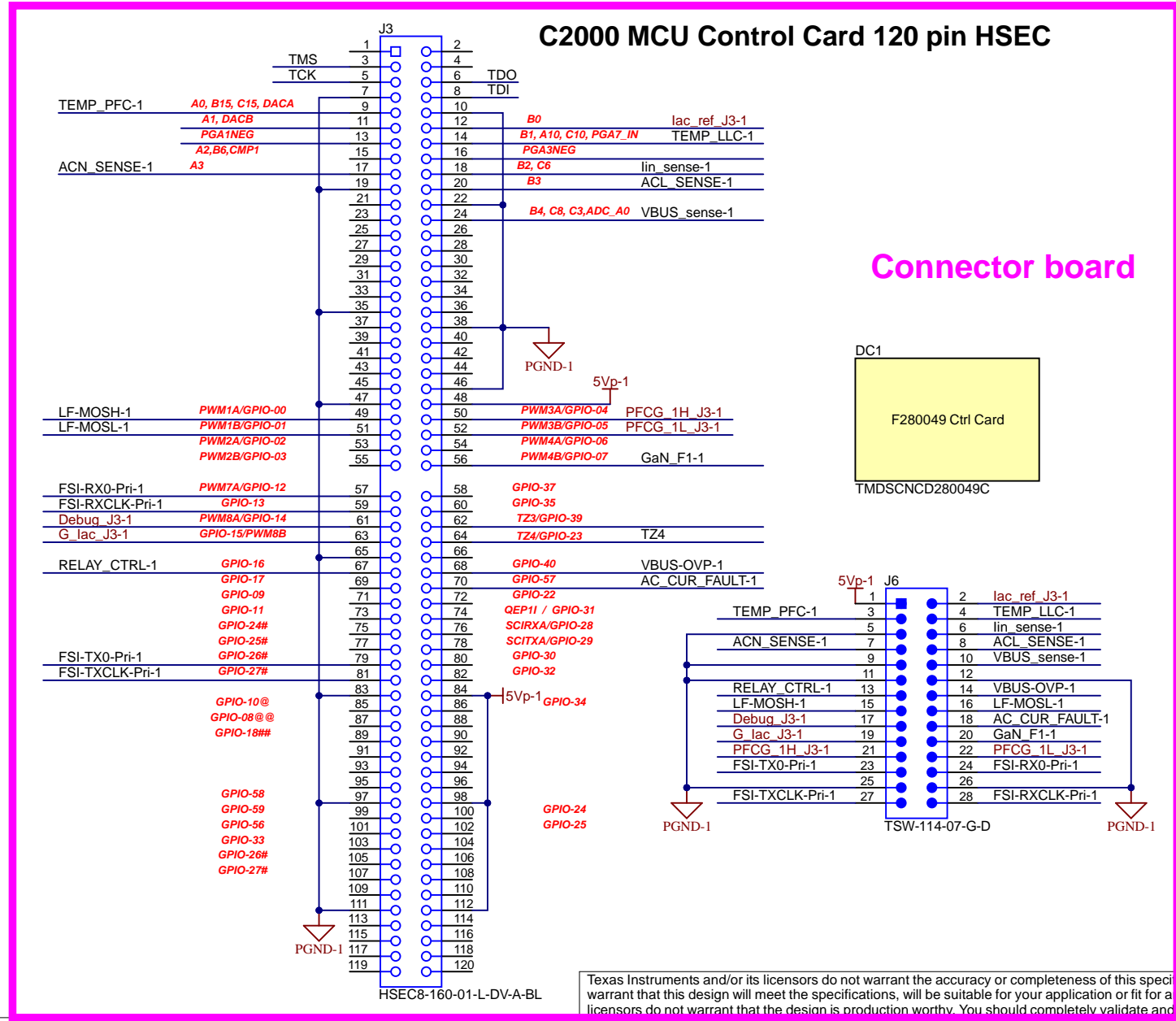
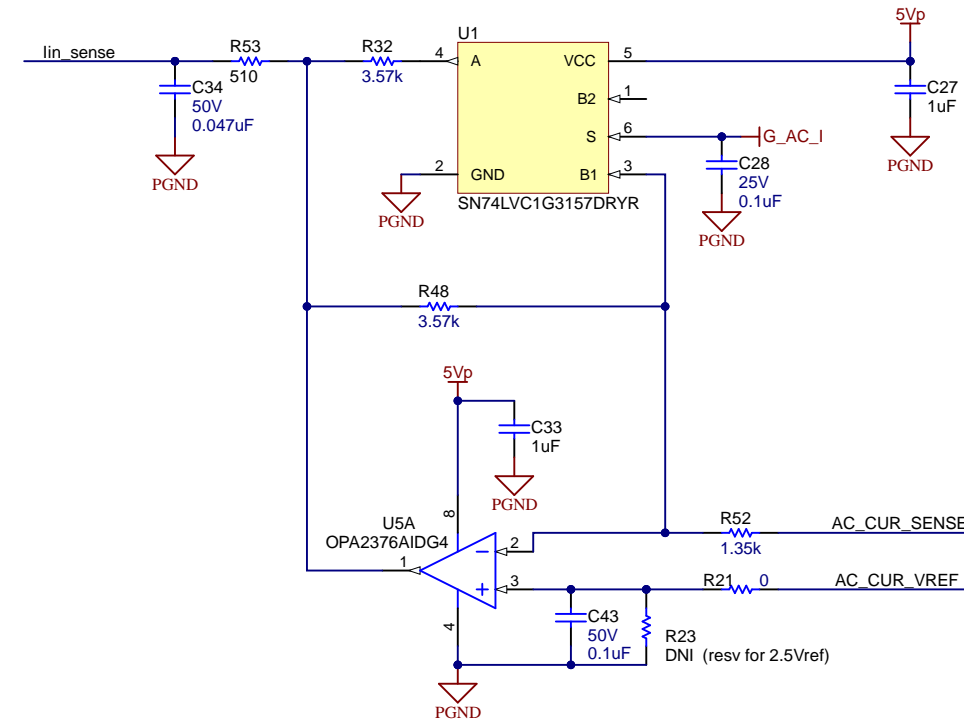
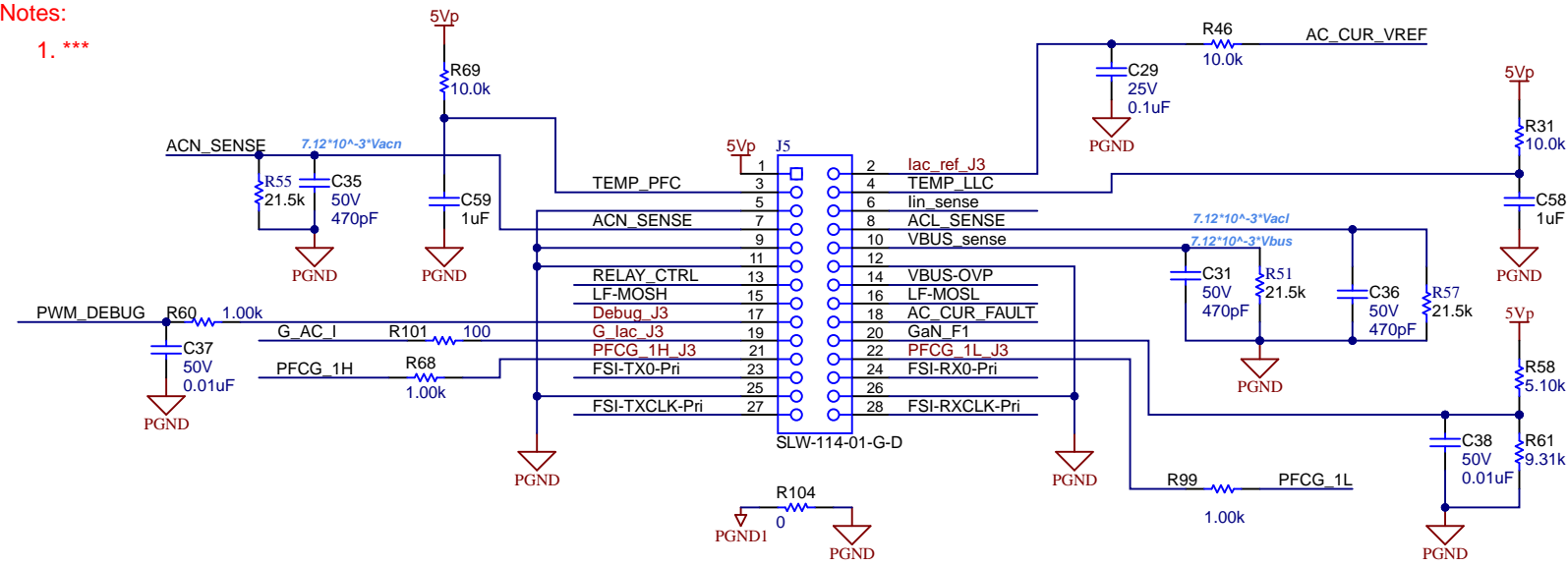
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Number: PMP-41006	Rev: V4.1	Sheet Title: LLC Power Stage
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 10
Drawn By: Desheng Guo	File: PMP41006_Power-LLC.SchDoc	Size: B
Engineer: Desheng Guo	Contact: http://www.ti.com/support	

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Control for PFC

Notes:
1. ***

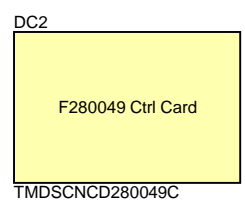
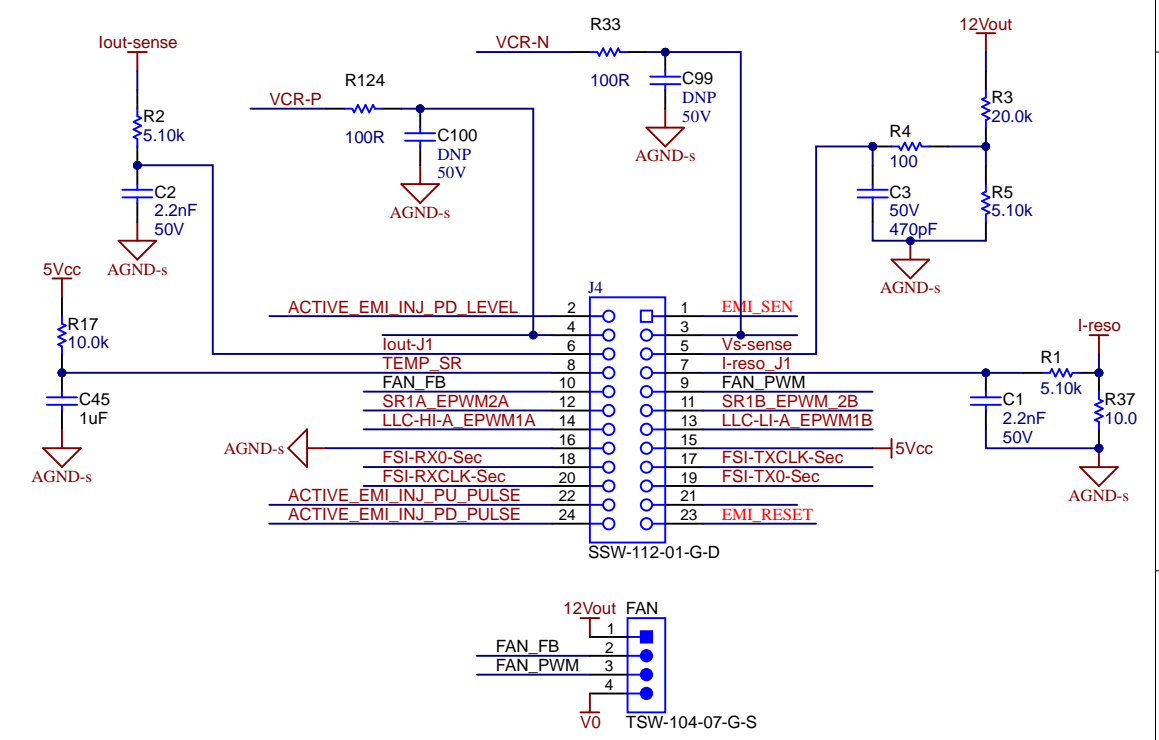
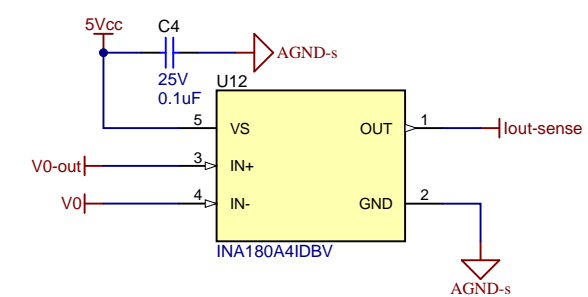
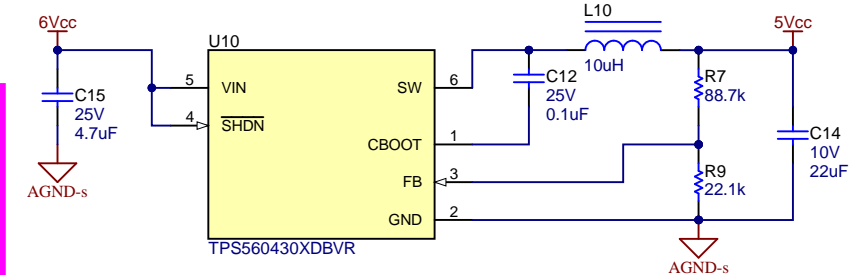
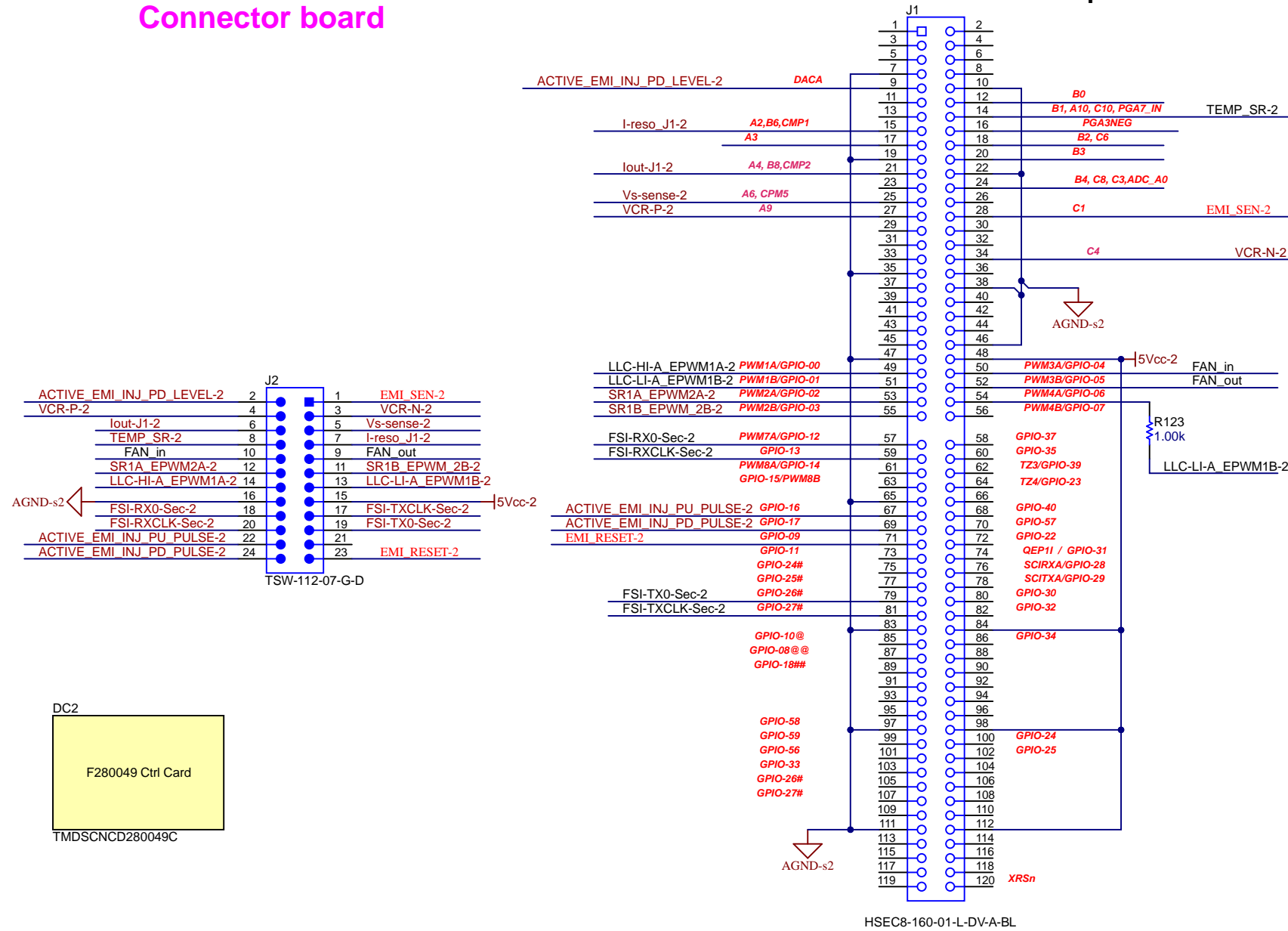


LLC Control Stage

Notes:
1. ***

Connector board

C2000 MCU Control Card 120 pin HSEC



A

B

C

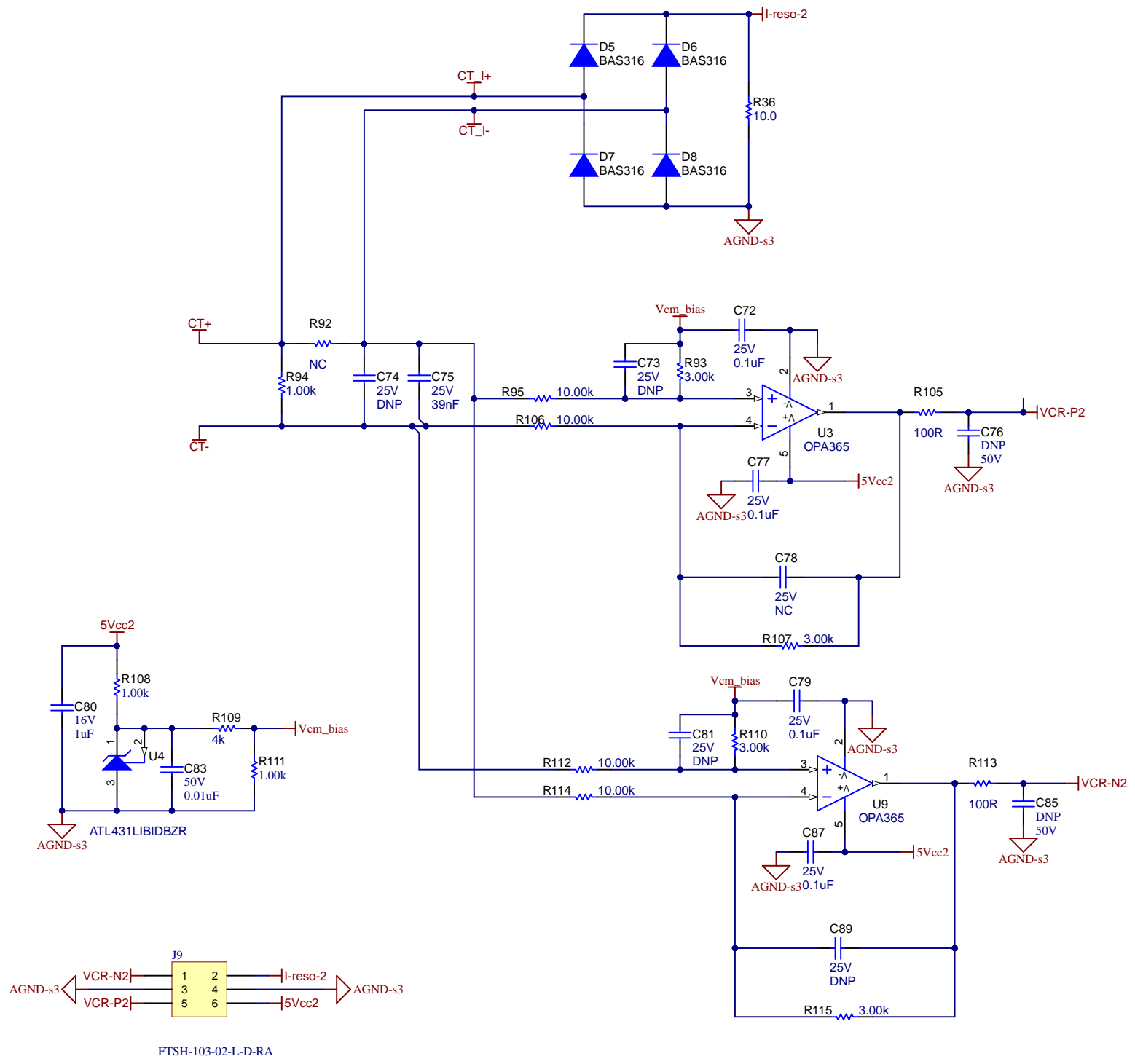
D

A

B

C

D



VCR sensing design process:

- 1, get the peak-peak voltage of the Cr (resonant CAP) @ min Vin and full load;
 - 2, select the CT and its turn ratio N:1;
 - 3, select Cs (sensing CAP) by keeping the CT within its volt-sec;

$$V_{s_pp} = V_{cr_pp} * Cr / (N * Cs)$$
 - 4, select Ks (ratio of OPA circuits) to adjust the sensing volt to 2.0V max;

$$VCR_sense_p = V_{cm} + 0.5 * V_{cr_pp} * Cr / (N * Cs) * Ks$$
- Min volt is clamped by Vcm, and Vcm is set at 0.5V by default.

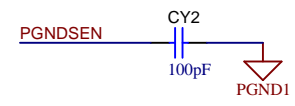
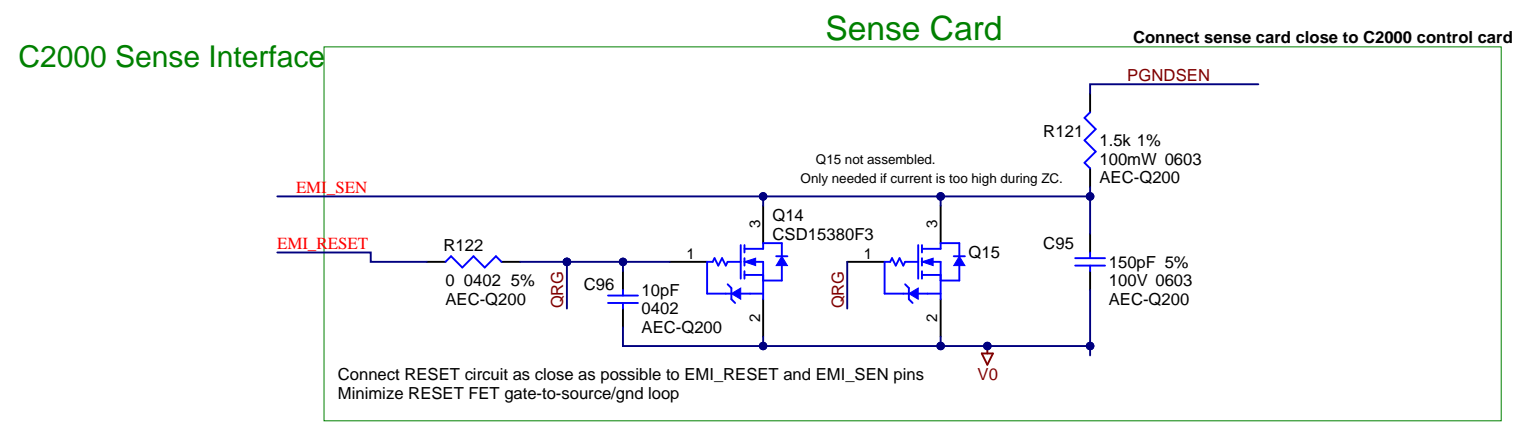
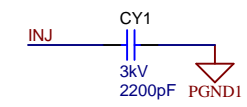
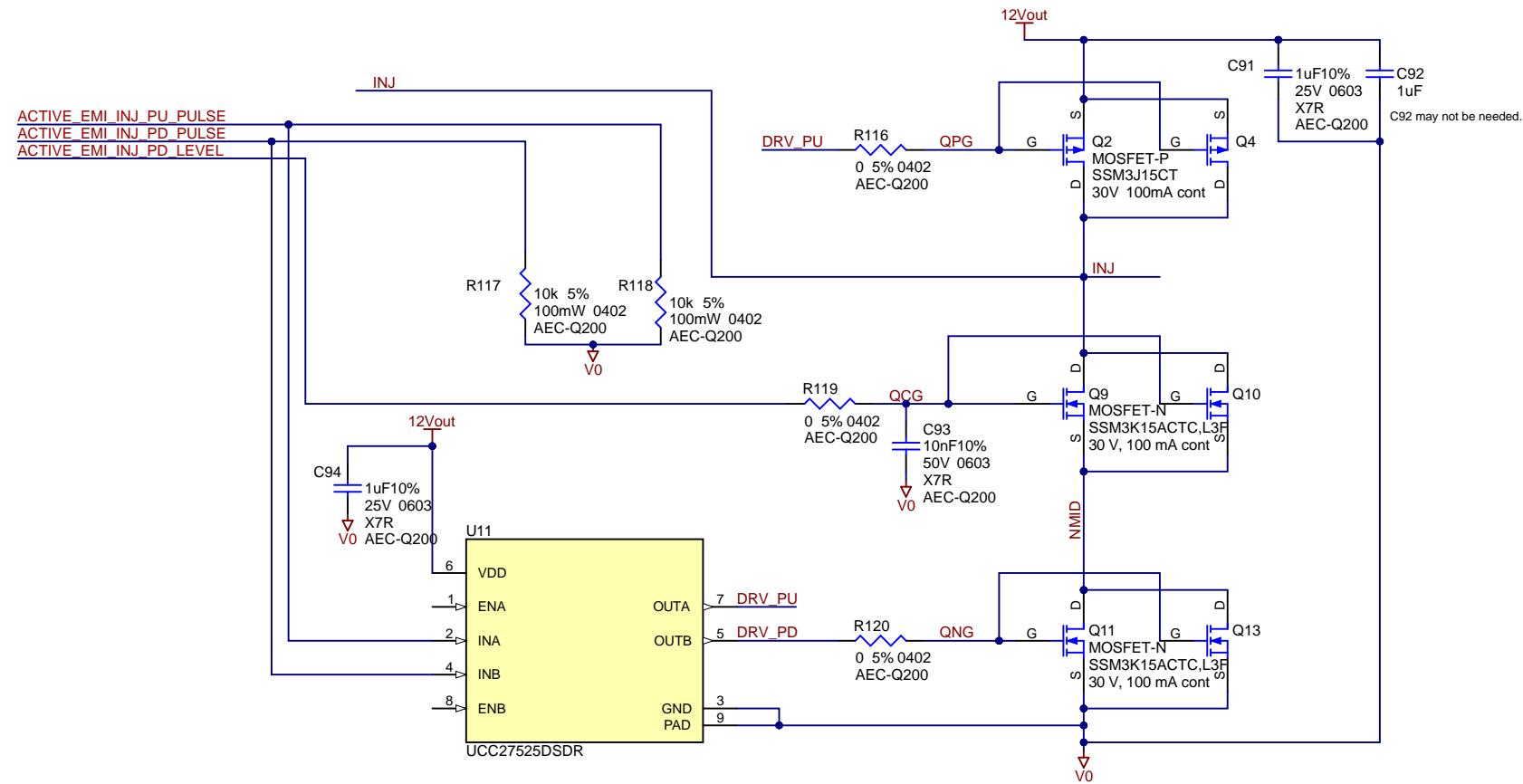
Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 11/30/2021
TID #: TIDA-010062	Project Title: 1kW Titanium Server PSU	
Number: PMP-41006	Rev: V4.1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 6 of 10
Drawn By:	File: PMP41006_CTS-VCR_SchDoc	Size: B
Engineer: Desheng Guo	Contact: http://www.ti.com/support	



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Discrete Canceller Inject Daughter Card with Non-Automotive Devices

Connect inject card close to high-frequency switching bridge

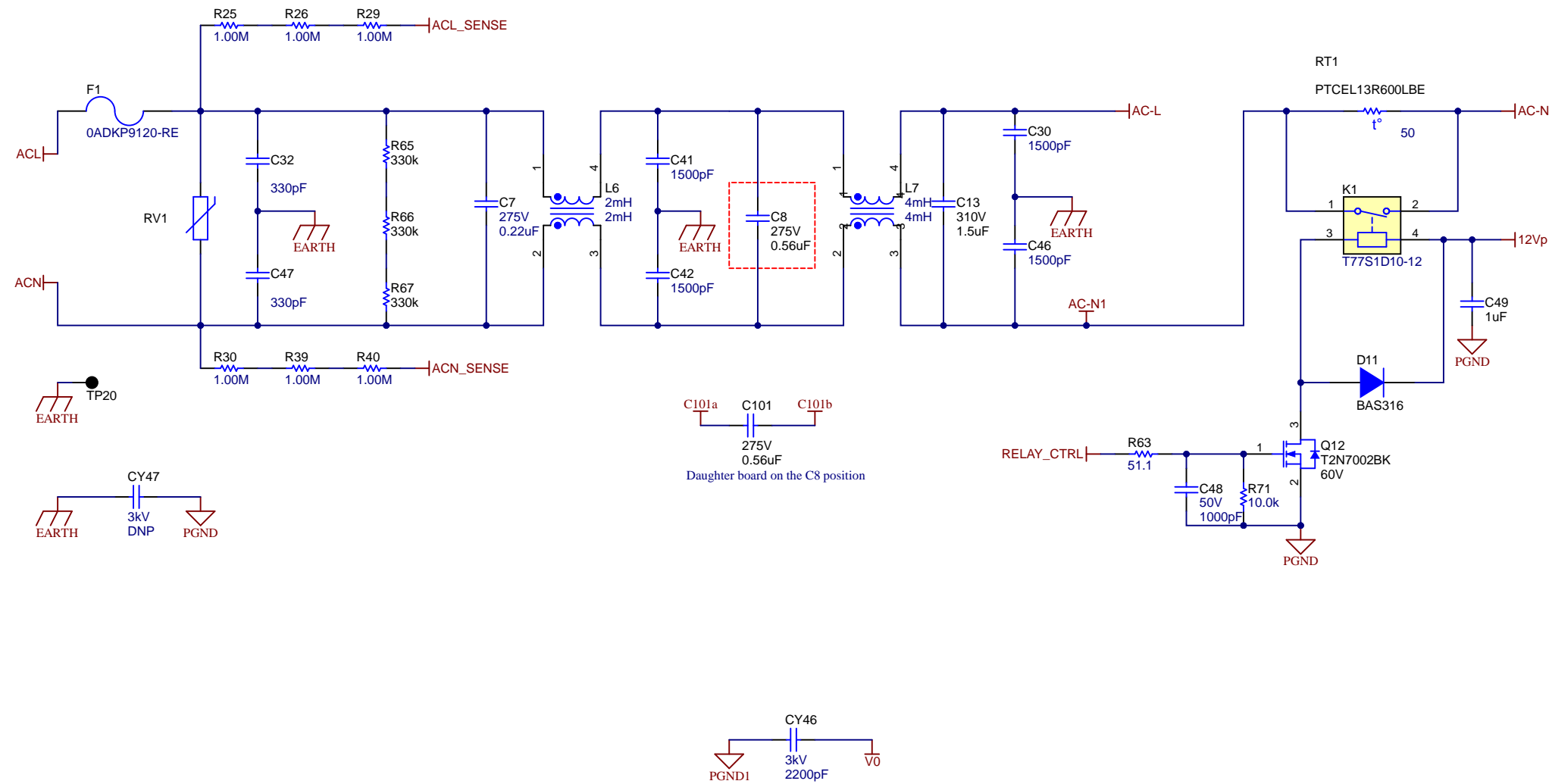


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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 11/30/2021
TID #: TIDA-010062	Project Title: 1kW Titanium Server PSU	
Number: PMP-41006	Rev: V4.1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 7 of 10
Drawn By:	File: PMP41006-DCAEF.SchDoc	Size: A3
Engineer: Desheng Guo	Contact: http://www.ti.com/support	



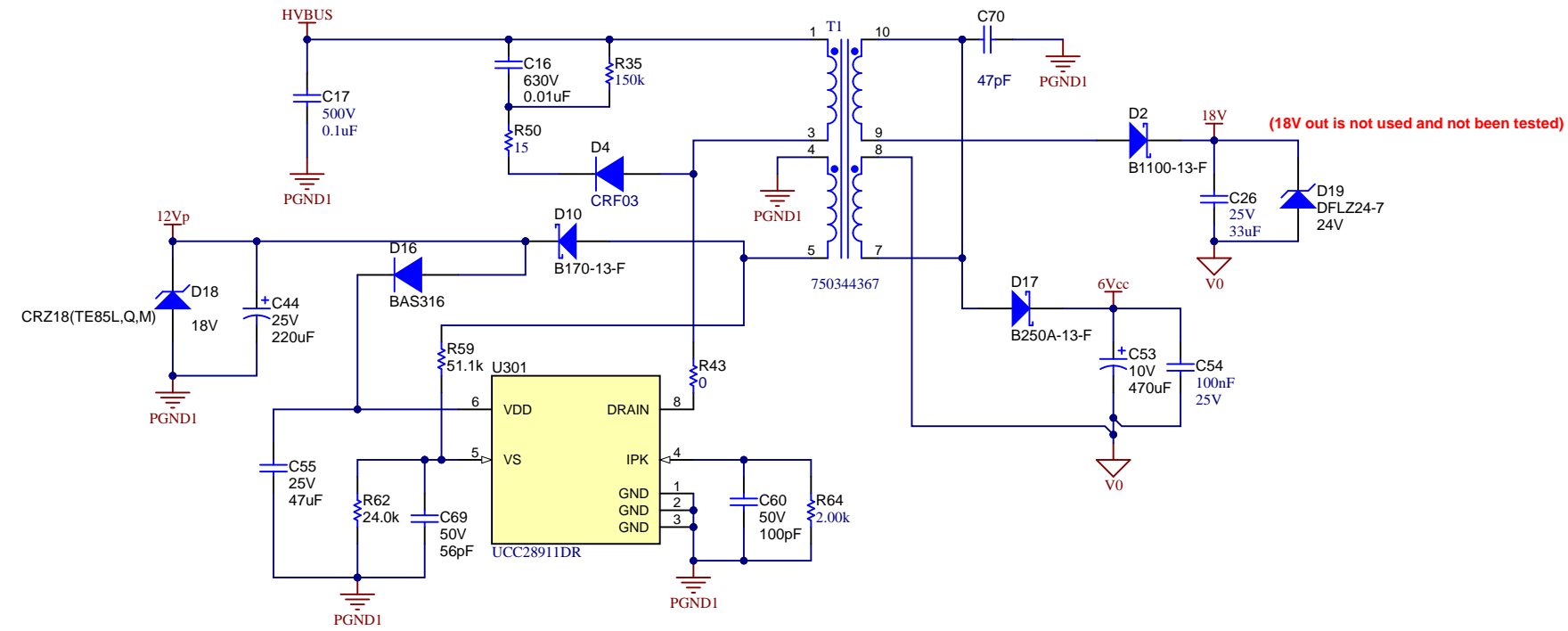
EMI Part



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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 11/30/2021
TID #: TIDA-010062	Project Title: 1kW Titanium Server PSU	
Number: PMP-41006	Rev: V4.1	Sheet Title: EMI Stage
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 8 of 10
Drawn By: Desheng Guo	File: PMP41006_EMI.SchDoc	Size: B
Engineer: Desheng Guo	Contact: http://www.ti.com/support	

Aux Power Stage



(18V out is not used and not been tested)

LOGO301



DANGER HIGH VOLTAGE

LOGO302



CAUTION HOT SURFACE

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Designed for: Public Release		Mod. Date: 11/30/2021	
Project Title: 1kW Titanium Server PSU		Sheet Title: Bias Power Stage	
Number: PMP-41006	Rev: V4.1	Assembly Variant: 001	Sheet: 9 of 10
SVN Rev: Not in version control		File: PMP41006_Bias.SchDoc	
Drawn By: Desheng Guo		Size: B	
Engineer: Desheng Guo		Contact: http://www.ti.com/support	
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PCB Number: PMP-41006
PCB Rev: V4.1

Variant/Label Table	
Variant	Label Text
001	TIDA-010062
002	PMP41006

LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 11/30/2021
TID #: TIDA-010062	Project Title: 1kW Titanium Server PSU	
Number: PMP-41006	Rev: V4.1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 10 of 10
Drawn By:	File: PMP41006_Hardware.SchDoc	Size: B
Engineer: Desheng Guo	Contact: http://www.ti.com/support	

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