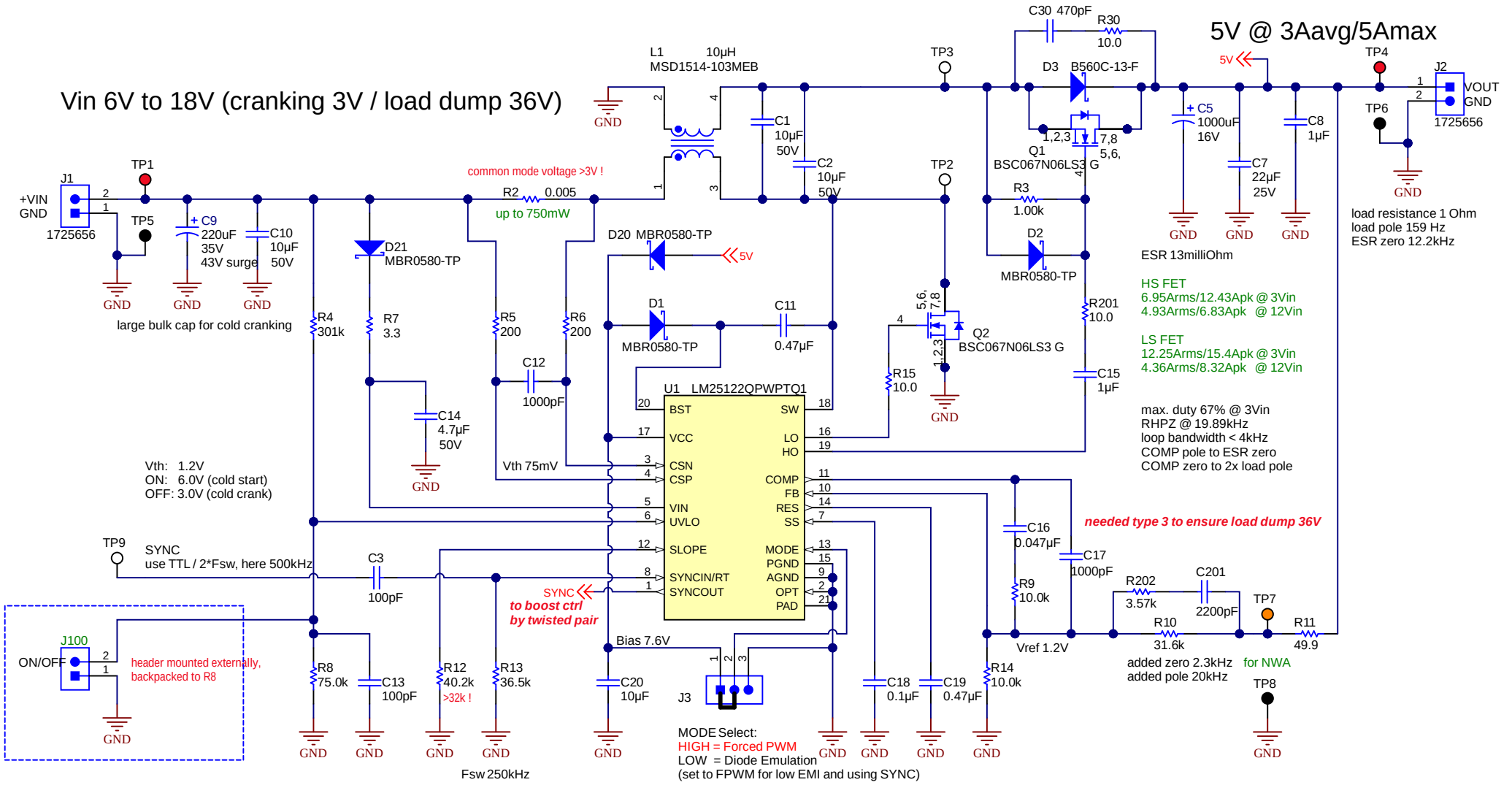

Revision History	
Revision	Notes
A	*** preliminary *** (cold start needs Vin > 6V)
B	used LL FETs to avoid charge pump of RevA
C	adjusted loop, optimized gate drive

1) automotive FETs IAUC41N06S5L100 for short term cranking and output current <3Aavg - upper BSCs for prototyping only

2) R201, R202, C201 mounted externally; C31 OPEN

built on PCB 30232 A

Orderable:		Designed for: Public Release	Mod. Date: 2/23/2022
TID #: N/A		Project Title: 25W sync SEPIC for 3V Cold Cranking	
Number: PMP31114	Rev: C	Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: [No Variations]	Sheet: 1 of 2
Drawn By:		File: PMP31114RevC_sh1_SEPIC.SchDoc	Size: A4
Engineer: B. Geck		Contact: http://www.ti.com/support	

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