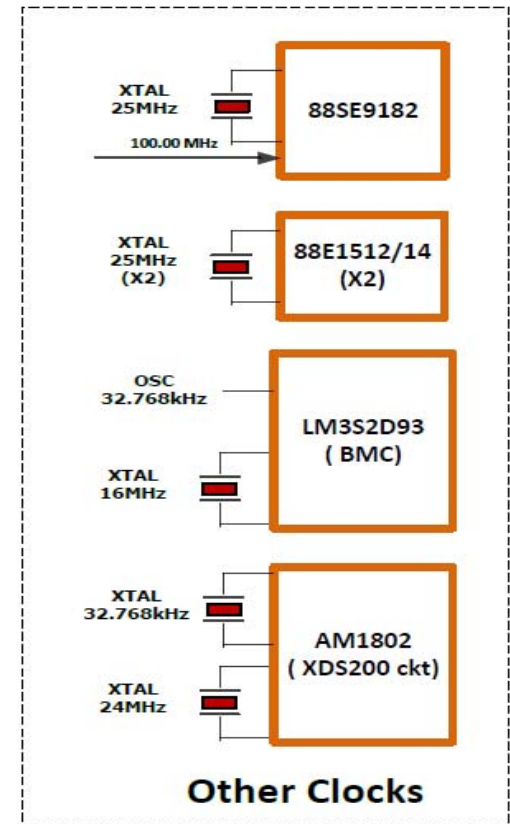
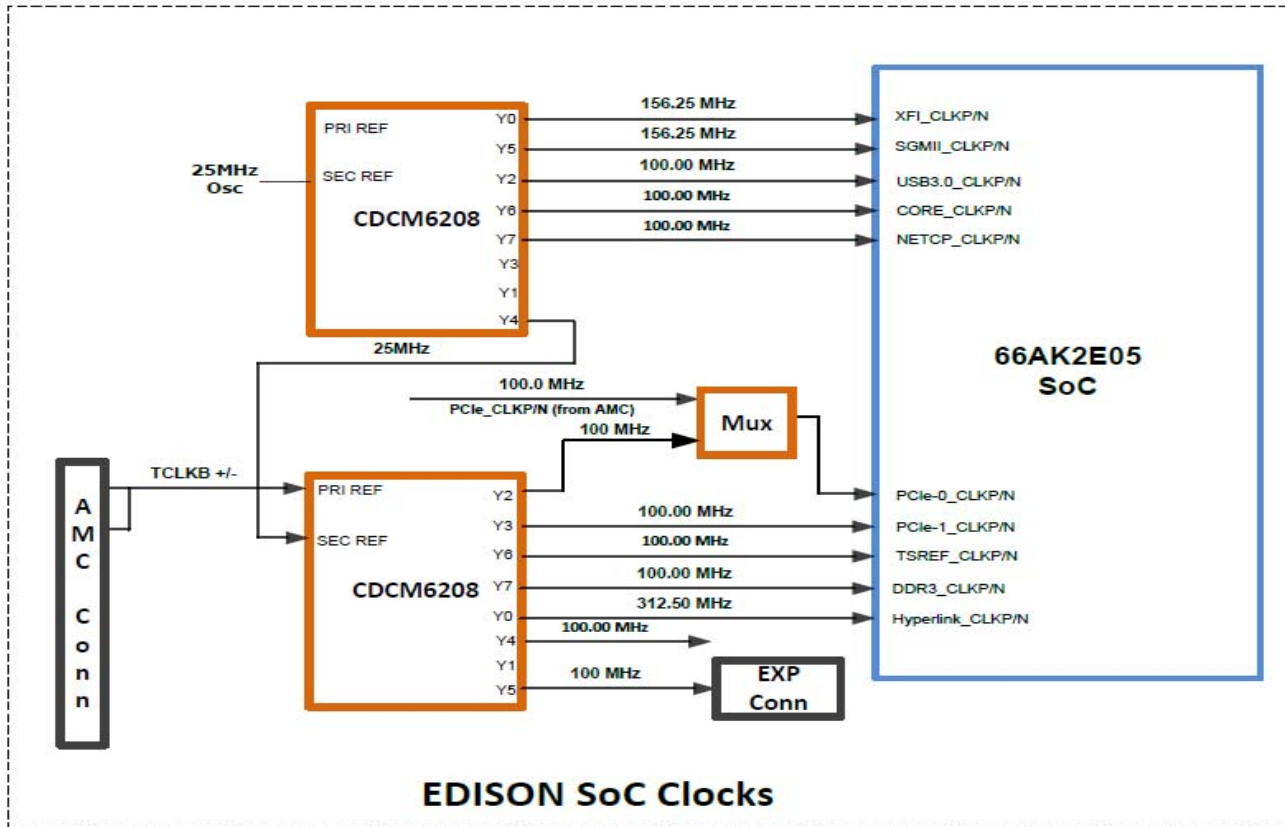


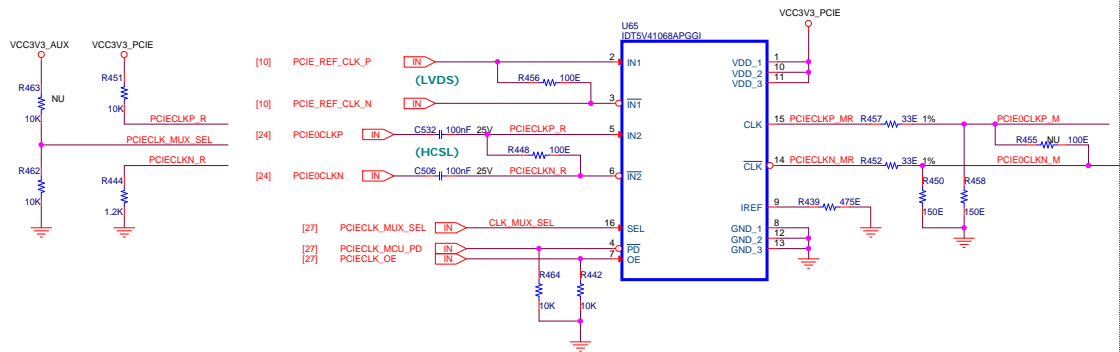
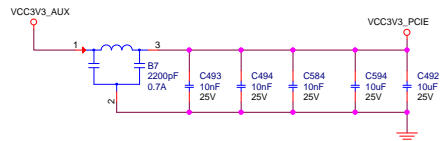
# EDISON CLOCK GENERATION



Project K2E EVM		Designed for TI by elfochips	
Title CLOCK DIAGRAM			
Size C	Document Number 16_00175_01	Rev 1.0	
Date: Thursday, September 25, 2014		Sheet 8 of 37	

## PCI CLOCK MUX

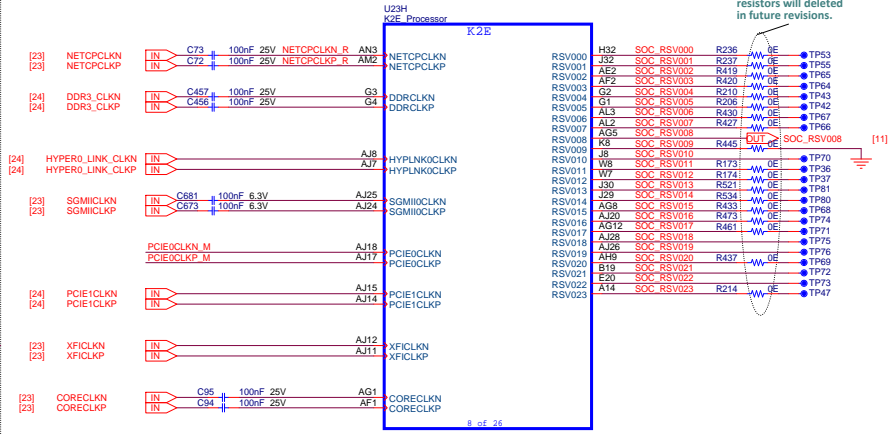
SEL	I/p PAIR SEL
LOW	IN2/IN2#
HIGH	IN1/IN1#



## SOC REFERENCE CLOCK

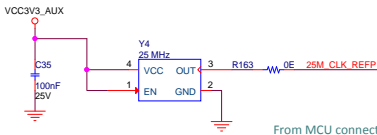
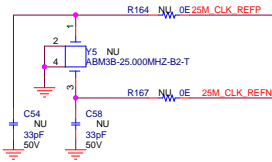
All blocking capacitors should be placed near SOC to keep connecting routes short and minimize vias

NOTE: These series resistors will be deleted in future revisions.

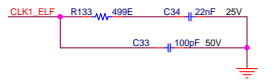


Project K2E EVM		Designed for TI by elfnfochips	
Title CLOCK and SMART REFLEX			
Size C	Document Number 16_00175_01	Rev 1.0	
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# CLOCK SOURCE-- 1



From MCU connect to this signal

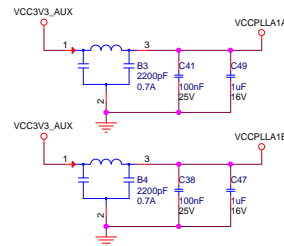
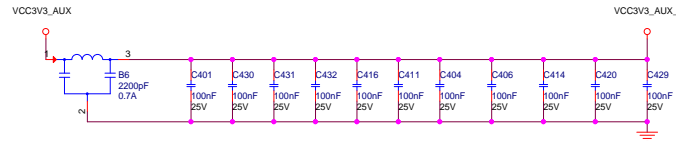
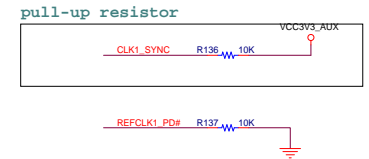
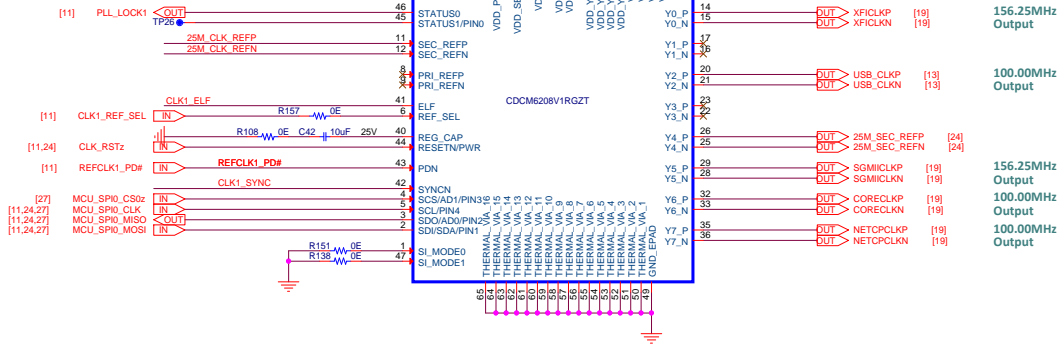


Synthesizer mode (high loop bandwidth)  
CDCM6208V1:  
With C1=100pF, R2=500, C2=22nF and  
Internal components R3=100, C3=242.5pF,  
fFDS=20MHz, and ICP=2.5mA  
Loop bandwidth = (337kHz)

## Serial Interface Mode or Pin Mode Selection

MCU_SI_MODE[1:0]	DESCRIPTION
00	SPI MODE (Default)
01	I2C MODE
10	PIN MODE (NO SERIAL PROGRAMMING)
11	RESEERVD

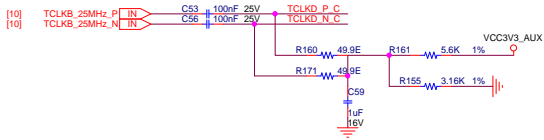
STATUS0 outputs the PLL\_LOCK signal  
STATUS1 the LOSS OF REFERENCE.



Project		K2E EVM		Designed for TI by elfnchips	
Title		CLOCK SOURCE-- 1			
Size	Document Number	Rev			
C	16_00175_01	1.0			
Date: Monday, March 10, 2014			Sheet 23 of 37		

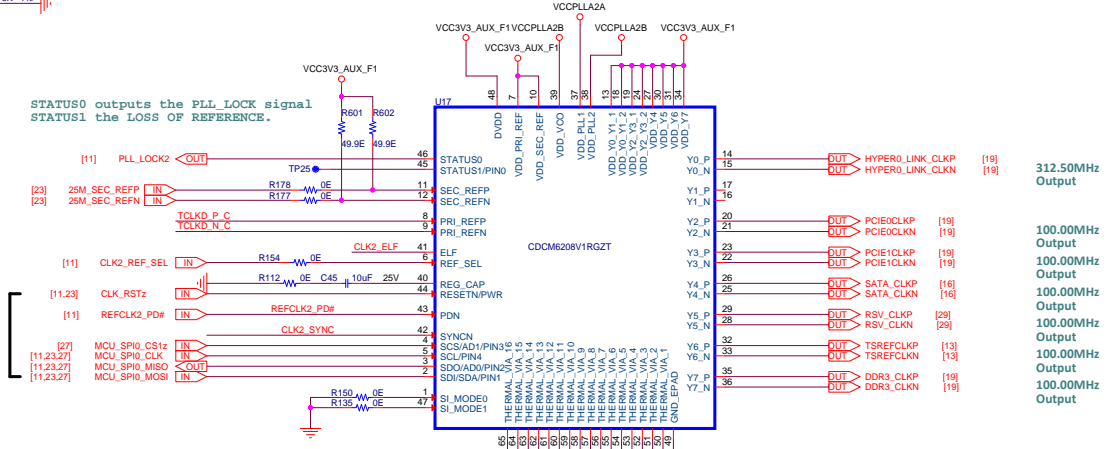
# CLOCK SOURCE --2

from AMC.0

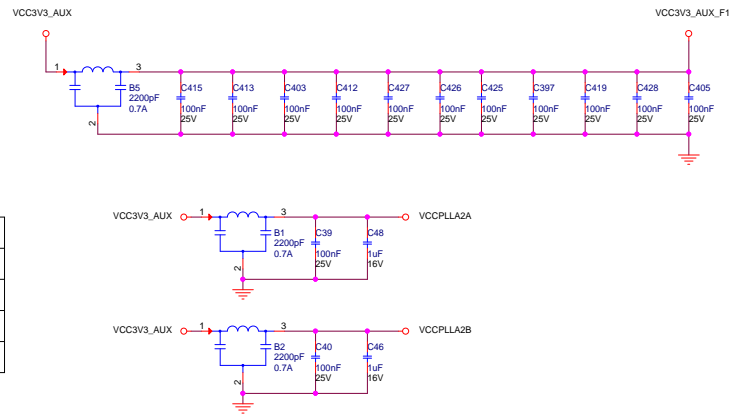
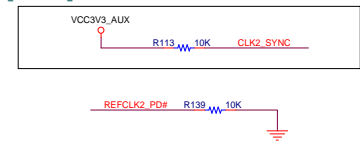


from VCTCXO  
25Mhz

From MCU connect to this  
signal

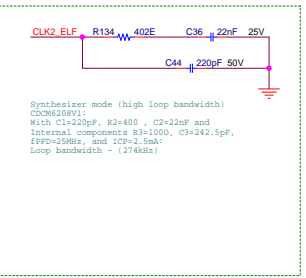


pull-up resistor



## Serial Interface Mode or Pin Mode Selection

MCU_SI_MODE[1:0]	DESCRIPTION
00	SPI MODE (Default)
01	I2C MODE
10	PIN MODE (NO SERIAL PROGRAMMING)
11	RESEERVED



Synthesiser mode (high loop bandwidth)  
CDCE6208V1  
With C3=220pF, R2=400, C2=22nF and  
Internal components R3=1000, C3=242.5pF,  
FFFD=25MHz, and ICP=2.5mA  
Loop bandwidth = 1.27MHz

Project		K2E EVM		Designed for TI by elfnchips	
Title		CLOCK SOURCE-- 2			
Size	Document Number	Rev			
C	16_00175_01	1.0			
Date: Monday, March 10, 2014			Sheet 24 of 37		

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