

# ***Tag-it HF-I Standard Transponder IC***

## *Reference Guide*

***October 2007***

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**SCBU047 (11-09-21-064)**

# ***Tag-it HF-I Standard Transponder IC***

## ***Reference Guide***



Literature Number: SCBU047 (11-09-21-064)  
October 2007



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**Edition Three – October 2007**

This is the third edition of this reference guide. It contains a description of the Tag-it HF-I Standard Transponder IC, the specifications, part numbers, dimensions and instructions for further handling.

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## About This Guide

This reference guide for the Tag-it HF-I Standard Transponder IC is designed for use by TI partners who are engineers experienced with Radio Frequency Identification Devices (RFID) and the processing of wafers.

Regulatory, safety and warranty notices that must be followed are given in [Chapter 4](#).

## Conventions

### **WARNING**

**A WARNING IS USED WHERE CARE MUST BE TAKEN OR A CERTAIN PROCEDURE MUST BE FOLLOWED, IN ORDER TO PREVENT INJURY OR HARM TO YOUR HEALTH.**

### **CAUTION**

This indicates information on conditions which must be met, or a procedure which must be followed, which if not heeded could cause permanent damage to the equipment or software.

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**Note:** Indicates conditions which must be met, or procedures which must be followed, to ensure proper functioning of the equipment or software.

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**Note:** Indicates information which makes usage of the equipment or software easier.

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### 2.2.1 If You Need Assistance

For more information, please contact the sales office or distributor nearest you. This contact information can be found on our web site at:

<http://www.ti-rfid.com>.

## Introduction

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This chapter introduces you to the Tag-it HF-I Standard Transponder IC.

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## 1.1 General

The **Tag-it HF-I Standard** Transponder IC is part of TI's 13.56 MHz product family which is based on the ISO/IEC 15693 standard for contactless integrated circuit cards (vicinity cards) and ISO/IEC 18000-3 standard for item management. The **Tag-it HF-I Standard** Transponder IC builds the basis for various available inlay shapes which are used as consumable smart labels in markets requiring quick and accurate identification of items, such as:

- Asset tagging
- Electronic ticketing
- Anti-counterfeit prevention
- Distribution logistics and supply chain management
- Building access badges
- Express parcel delivery
- Airline boarding pass and baggage handling

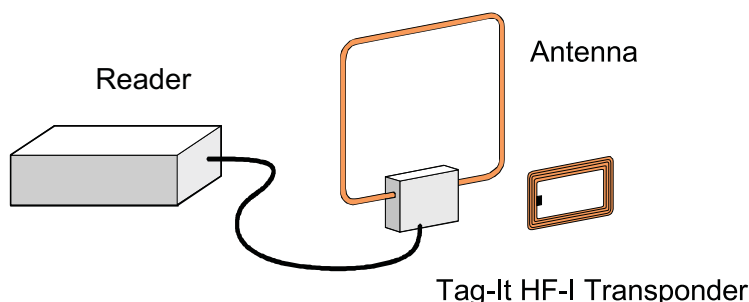
User data is written to and read from memory blocks using a non-volatile EEPROM silicon technology. Each block is separately programmable by the user and can be locked to protect data from modification. Once the data has been 'locked' then it cannot be changed.

To give some examples, information about delivery checkpoints and timing, place of origin/destination, pallet assignments, inventory numbers and even transportation routes can be coded into the transponder.

Multiple transponders, which appear in the Readers RF field, can be identified, read from and written to by using the **Unique Identifier (UID)**, which is programmed and locked at the factory.

## 1.2 System Description

For operation a reader with antenna is required to send a command to the transponder and to receive its response (see [Figure 1-1](#)). The command of the Reader can be either in addressed or non-addressed mode. The Transponder does not transmit data until the reader sends a request (Reader talks first principle).



**Figure 1-1. RFID System With Reader, Antenna and Tag-it HF-I Transponder**

## 1.3 Product Description

The Tag-it HF-I Standard Transponder IC is compliant to the ISO/IEC 15693 and ISO/IEC 18000-3 standard. To build a complete transponder, the Tag-it HF-I Standard Transponder IC has to build a resonance circuit with the antenna it is assembled on e.g. an etched aluminum antenna.



## 1.4 Functional Description

The Tag-it HF-I Standard Transponder IC is a low power, full duplex Transponder IC for use with passive contactless identification transponder systems.

The transponder IC is designed to operate with a 13.56 MHz carrier frequency. The ISO standard defines for some communication parameters several modes in order to meet different international radio regulations and different application requirements. Therefore communication between the reader and the transponder (Down-Link communication) takes place using ASK modulation index between 10% and 30% or 100% and datacoding (pulse position modulation) '1 out of 4' or '1 out of 256'.

According to ISO 15693 Up-Link communication (Transponder to Reader) can be accomplished with one subcarrier (ASK modulation) or with two subcarrier (FSK modulation). Both modes (ASK and FSK) can operate with either high or low data rate. **The transponder will answer in the mode it was interrogated from the reader and supports all communication parameter combinations.** Up- and Down-Link are frame synchronized and CRC check sum secured.

Each Tag-it HF-I Standard Transponder IC has a 'unique' address (UID) stored in two blocks which are factory-programmed and 64 bits long ( $=2^{64}$  different addresses). This can be used for addressing each transponder uniquely and individually for a one-to-one exchange between the reader and the transponder. A mechanism to resolve collisions of a multiplicity of transponders (Anticollision) is also implemented. This special feature allows multiple transponders to be read simultaneously and offers the capability to inventory in a very short time a large number of transponders by their unique address, provided they are within the reader operating range.

Also, the Application Family Identifier (AFI), which is optional in the ISO15693, is supported by the Tag-it HF-I Standard Transponder.

For more details about the communication between reader and transponder see ISO/IEC 15693 and the Tag-it HF-I Standard Extended Command Specification.

## 1.5 Memory Organization

User data is read and stored in a 256 bit non-volatile user memory that is organized in 8 blocks. Each block with 32 bit is user programmable and can be locked individually to protect data from modification. Once set, the lock bit cannot be reset. The user memory is field programmable per block. Two levels of block locking are supported: Individual block locking by the user (U) or individual block locking of factory programmed data (F) during manufacturing. Bit 2 of the "Block Security Status" Byte defined in ISO 15693-3 is used to store the Factory Lock Status of the Block. Block locking irreversibly protects the locked data from any further reprogramming.

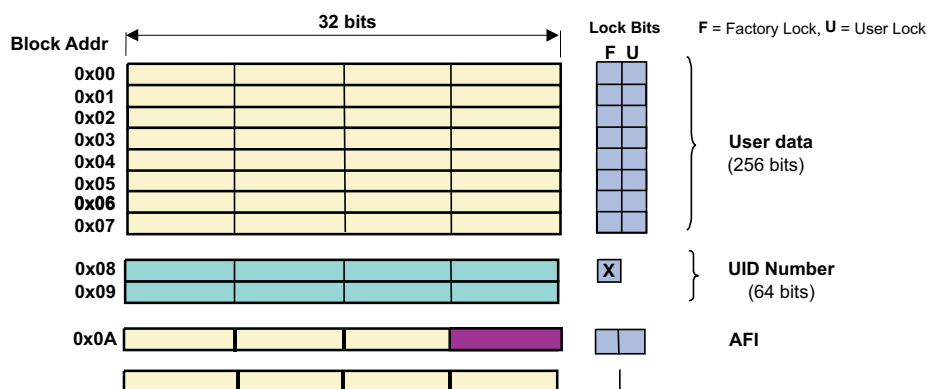


Figure 1-2. Memory Organization of the Tag-it HF-I Standard Transponder IC

## 1.6 Command Set

**Table 1-1. Command Set for Tag-it HF-I Standard Transponder IC**

Request	Request Code	Request Mode <sup>(1)</sup>				
		Inventory	Addressed	Non-Addressed	AFI	Opt. Flag
<b>ISO 15693 Mandatory and Optional Commands</b>						
Inventory	0x01	Ü	-	-	Ü	0/-
Stay Quiet	0x02	-	Ü	-	-	0/-
Read_Single_Block	0x20	-	Ü	Ü	-	-/1
Write_Single_Block	0x21	-	Ü	Ü	-	-/1
Lock_Block	0x22	-	Ü	Ü	-	-/1

(1) Ü : Implemented  
 - : Not applicable  
 0/1 : Option flag needed

**Note:** The Option Flag (Bit 7) of the ISO 15693 defined Request Flags must be set to 1 for all Write and Lock commands to respond properly.

For reliable programming, a programming time  $\geq 10$  ms is recommended before the reader sends the end of frame (EOF) to request the response from the transponder.

## 1.7 Ordering Information and Part Numbers

The Tag-it HF-I Standard Transponder IC is available with following finishing options:

**Table 1-2. Part Numbers**

Part-number	Bumping		Inking	Grinding	Sawing	Packing	
	NI/AU	AU				Multi Wafer	Single Wafer
RF-HDT-WNME-M0						Yes	
RF-HDT-SNLE-G1			Yes	Yes	Yes		Yes
RF-HDT-SJLE-G1	Yes		Yes	Yes	Yes		Yes
RF-HDT-AJLE-G1		Yes	Yes	Yes	Yes		Yes

**Note:** Other finishing options on request

## **Specification**

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This chapter provides the electrical and mechanical specifications of the Tag-it HF-I Standard Transponder IC.

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## 2.1 Electrical Specification

**Table 2-1. Absolute Maximum Ratings**

Parameter	Symbol	Note	Min	Nom	Max	Unit
Antenna Input Current	I <sub>ant_dc</sub>				10	mA
Antenna Input Voltage	V <sub>ant_dc</sub>				10	V
Storage Temperature	T <sub>s</sub>		–40		125	°C
Junction (Chip) Temp.	T <sub>j</sub>				150	°C
ESD Immunity	ANT1, ANT2 TDAT, GND	HBM	2.5 2.0			kV

**Note:** Stress beyond the limits of those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. Functional operation of the device under these or any other conditions beyond those indicated under 'Recommended Operating Conditions' is not implied. Exposure to absolute-maximum-rated conditions for extended time may affect device reliability.

**Table 2-2. Recommended Operating Conditions**

Parameter	Symbol	Note	Min	Nom	Max	Unit
Operating Temperature	T <sub>A</sub>		–40		+85	°C
Carrier Frequency	f <sub>TX</sub>			13.56		MHz
Antenna Input Voltage	V <sub>ANT</sub>	@ f <sub>TX</sub> unmodulated	2.5		V <sub>lim</sub>	V
Impedance of LC circuit	Z		6.5		15.5	kΩ

**Table 2-3. Electrical Characteristics**

Parameter	Symbol	Note	Min	Nom	Max	Unit
Input Capacitance	C <sub>IN</sub>	@ 2V <sub>RMS</sub>	–10%	23.5	+10%	pF
Operating Supply Current	I <sub>CC</sub>	V <sub>ANT</sub> =min			25	μA
Operating Supply Current	I <sub>CC</sub>	Programming			35	μA
Uplink Modulation Index	M <sub>PICC</sub>	V <sub>ANT</sub> <7V	0.1		0.3	
Limiter Clamping Volt.	V <sub>lim</sub>				10	V
Data Retention	t <sub>DRET</sub>	55°C	10			Years
Write & Erase Endurance	W&E	T <sub>a</sub> =25°C	100 000			Cycles

**Note:** For highest possible read-out coverage it is recommended to operate readers at a modulation depth of 20% or higher.

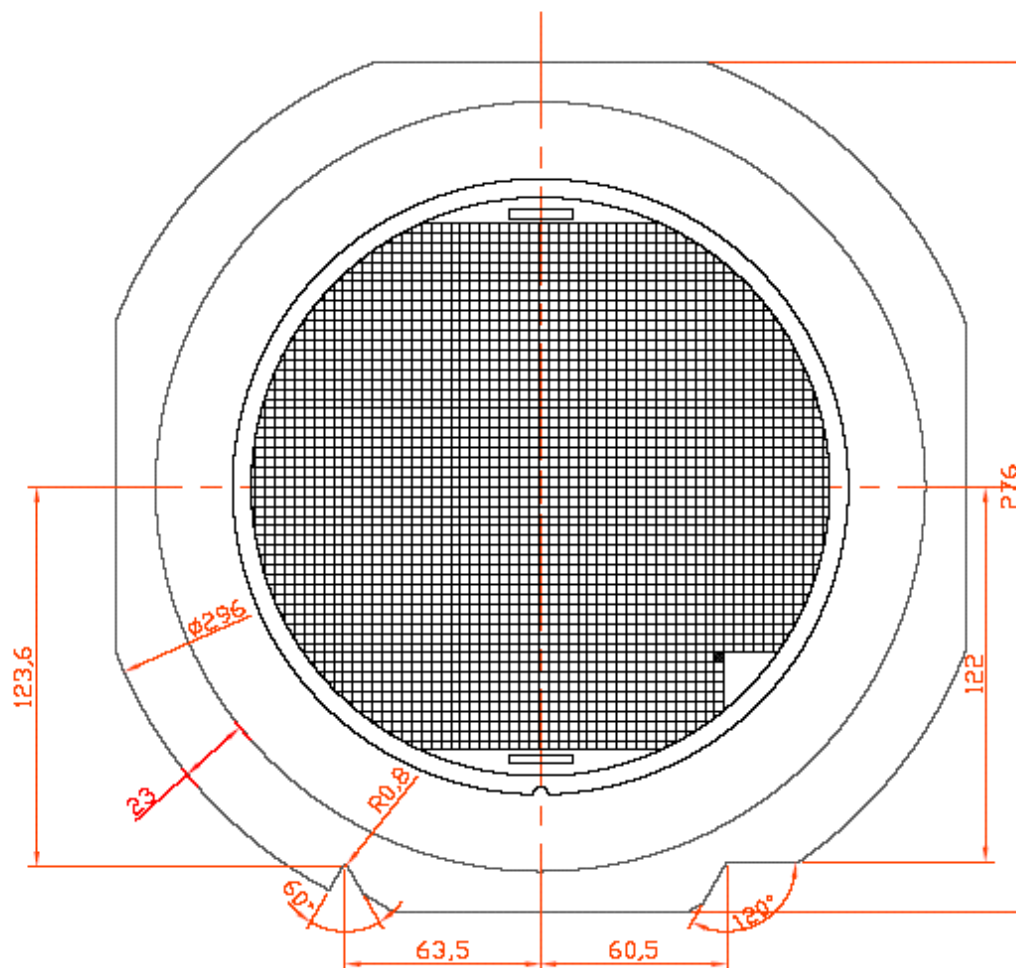
## 2.2 Mechanical Wafer Specification

**Table 2-4. General Mechanical Wafer Specification**

Parameter	Value
Wafer diameter	200 mm $\pm$ 0.3 mm (8 inch)
Thickness	711 $\mu$ m
Scribe line width	84 $\mu$ m
Electrical connection of substrate	VSS potential
Complete dies per wafer	24172

**Table 2-5. Mechanical Wafer Specification After Grinding, Sawing on FFC**

Parameter		Value
Backside Material		Si
Roughness:		
Ra		500 ngstrom
Rtm		2500 ngstrom
	Product	
	RF-HDT-WNME-M0	711 $\mu$ m
Thickness	RF-HDT-SNLE-G1	150 $\mu$ m $\pm$ 5 $\mu$ m
	RF-HDT-SJLE-G1	
	RF-HDT-AJLE-G1	



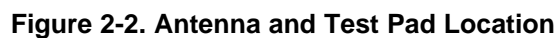
**Figure 2-1. Wafer on FFC**

## 2.3 Mechanical Die Specification

### Table 2-6. Mechanical Die Specification

Parameter	Value
Bond pad metallization material	ALCu0.5 %
Bond pad metallization thickness	0.95 μm
Bond & test pad location	<a href="#">Table 2-7</a>
Die dimension (including scribe line)	1080 * 1080 μm ±15 μm
Die dimension (excluding scribe line)	996 * 996 μm ±15 μm
Top side passivation material	SiNi
Passivation thickness	1.1 μm

Pad No	Name	LLCx[μm]	LLCy[μm]	URCx[μm]	URCy[μm]
1	ANT1	30	30	n.a.	n.a.
2	ANT2	n.a.	n.a.	966	966
Test pad					
3	TDAT	118	866	168	936
4	GND	836	60	886	130

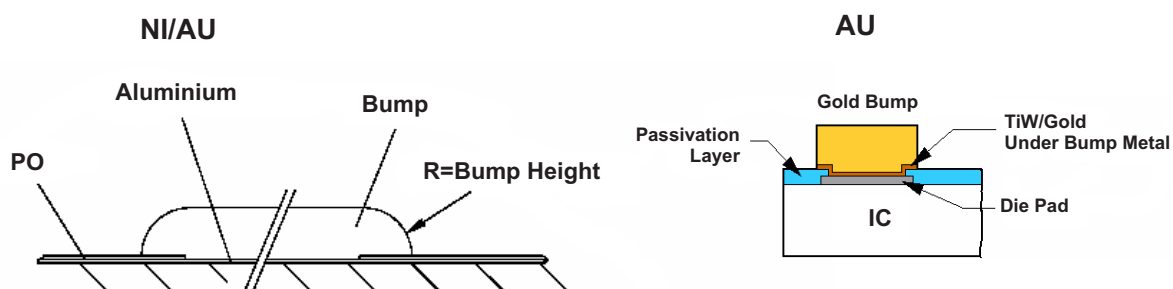


## 2.4 Bump Specification

**Table 2-8. Bump Specification**

Parameter	Value	
Bump material	Ni covered with AU, chemical process	AU
Bump height	25 $\mu\text{m} \pm 10\%$	20 $\mu\text{m} \pm 3 \mu\text{m}$
Bump hardness	>HV 450	HV 35–80
Surface roughness	<1 $\mu\text{m}$	<3 $\mu\text{m}$
Shear strength	>150 cN	>400 cN

**Note:** Contact between the test pads and the antenna is not allowed as it can have an impact on the electrical performance of the transponder.



**Figure 2-3. Cross Section of Bump**



## ***Shipping, Packing and Further Handling***

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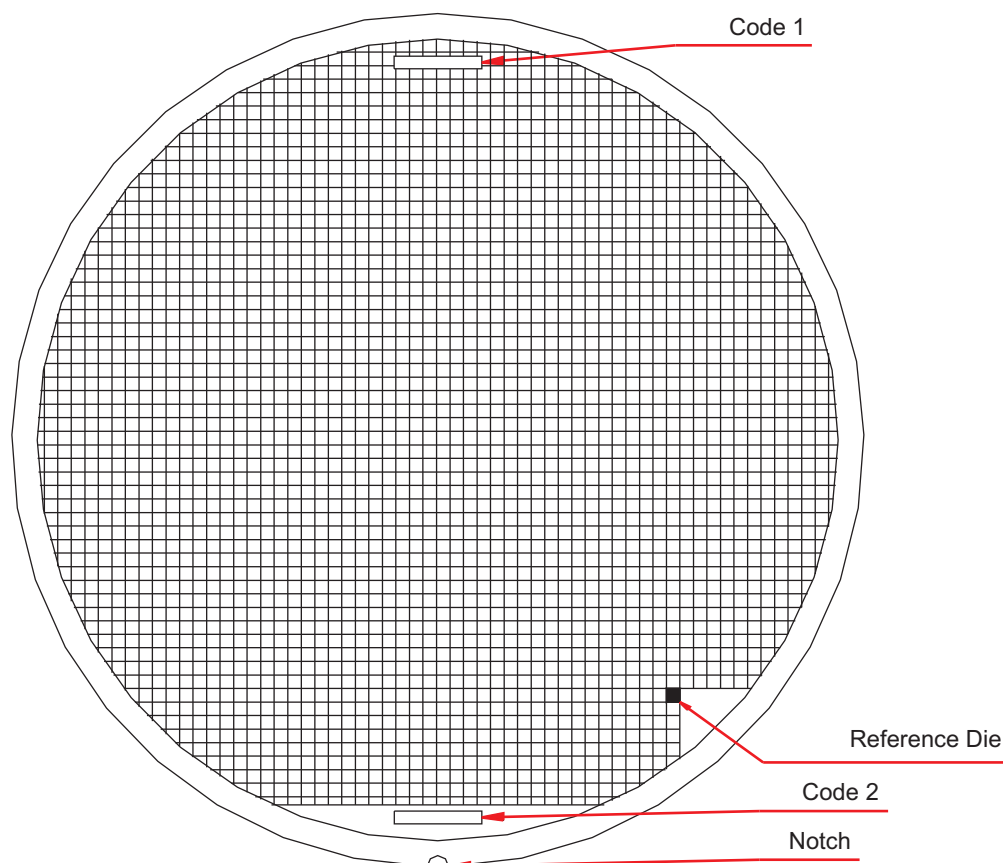
### 3.1 Low Definition

A definite quantity of wafers from the same diffusion batch produced under presumed uniform conditions. Occasionally a lot equals 25 wafers.

### 3.2 Wafer Identification

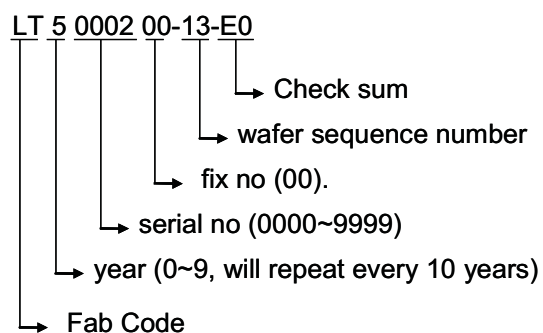
Each wafer is marked with laser marking to identify the wafer. The wafer map file is linked to the wafer id. There are 2 marks on the wafer.

The following figure shows the position of the wafer identification codes. The reference die is the black marked die in the corner at the right lower position of the wafer.



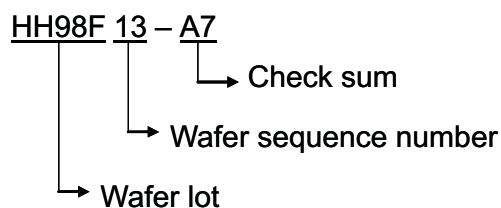
**Figure 3-1. Position of Wafer Identification Code**

**Code 1:** Wafer Lot number naming rule:



**Figure 3-2. Wafer Identification Code 1**

**Code 2:** Wafer Lot number naming rule:

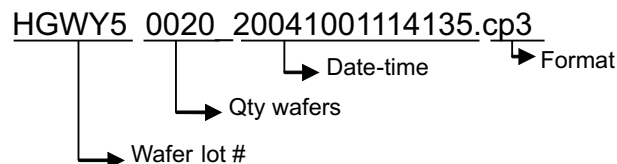


**Figure 3-3. Wafer Identification Code 2**

## 3.3 Wafer Map File

All lots are supplied with wafer mapping file. This mapping file is stored on a CD and enclosed in the pack box.

The mapping file is stored for 3 years, if any problem might occur. We handle our TI world wide wafer map standard. The wafer file name is explained as follows:



Wafer map files are provided in a TI Worldwide (WW) format which is in ASCII text form. An example of the TI WW wafer map file is shown below. You may contact your sales representative to obtain details on the TI WW format.

```

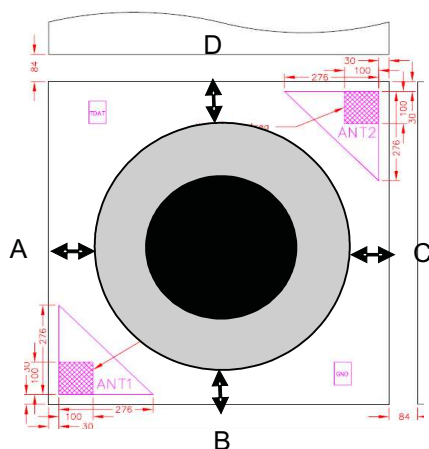
FACILITY=UMC-F8E
LOT=HGWY5
DEVICE=$8TMS37114A
WAFERS=20
X_SIZE=074.134
Y_SIZE=083.858
BIN_NAME.01="G,PASS"
BIN_NAME.09="FAIL"
STATUS="PROD"
SCRIBE="BOTTOM,15,NTRL,FAB"
WAFER_SIZE=200
WAFERID.01=LT5000200-13-E0
NUM_BINS.01=01
BIN_COUNT.01.01=02463
MAP_XY.01.01="Y-10 19 15 12 10/8
Y-9 20 18 15 13/12 9/7
Y-8 20/14 12/10 8/6 3
Y-7 15/13 10 8 4 2"
  
```

### 3.4 Ink Dot Specification

All Tag-it HF-I Standard Transponder ICs are electrically tested and dies that fail the probe test will be inked. Bump failures are not marked with an ink dot.

**Table 3-1. Ink Dot Specification**

Parameter	Value
Diameter	Min 250 $\mu\text{m}$
	Max 600 $\mu\text{m}$
Height	Max 25 $\mu\text{m}$
Colour	Black
Position	Central, not to touch bond pads



**Figure 3-4. Ink Dot Drawing**

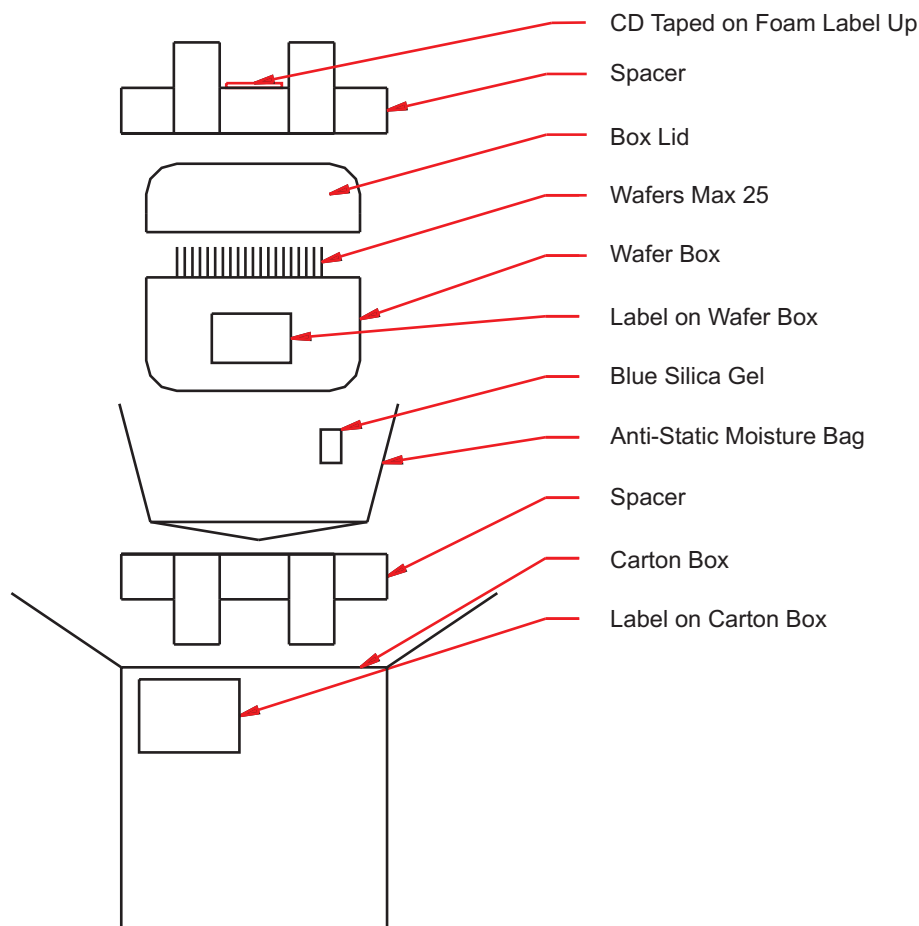
**Table 3-2. Ink Dot Placement**

No.	Max	Min	Remark
A	400	150	
B	400	150	
C	400	150	
D	400	150	
Ink	600	250	Size limit

### 3.5 Packing for Wafers

The wafers are packed for transportation to protect them against shock, static discharge and contamination in a wafer shipper box up to 25 wafers. This box is packed in an antistatic moisture bag with silica gel and in a double layered carton box.

**Note:** When the silica gel has changed the color to blue, it is an indication that moisture has entered the bag.



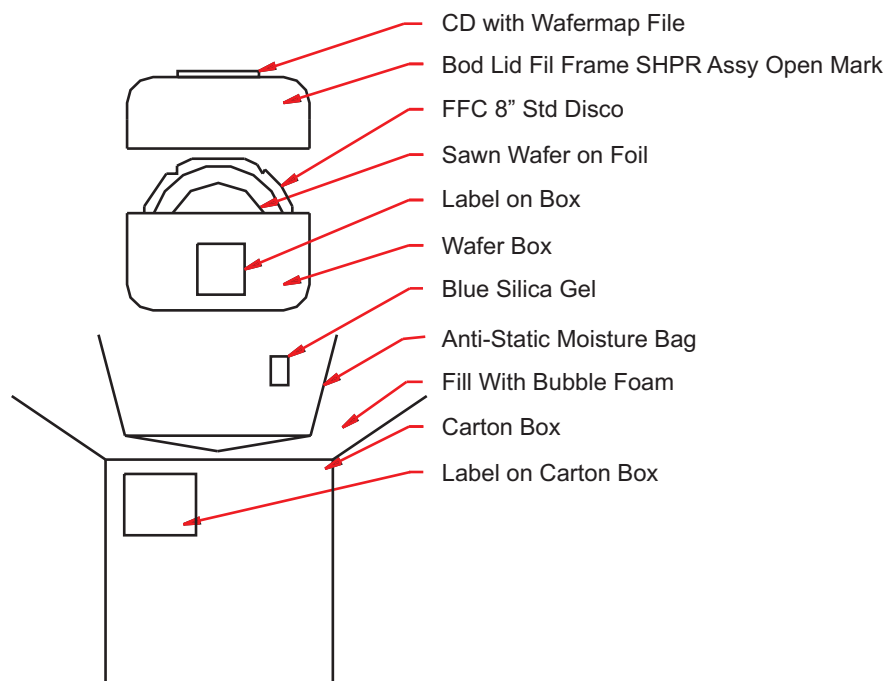
**Figure 3-5. Packing of Wafers**

## 3.6 Packing for Sawn Wafers

### Packing in Multi Wafer Box

Sawn wafers are mounted on foil and delivered on standard 8" disco wafer frame (see [Figure 2-1 Wafer on FCC](#)). A special plastic container is used to store up to 25 wafers in frames. This plastic container is packed in an antistatic moisture bag with silica gel and in a double layered carton box.

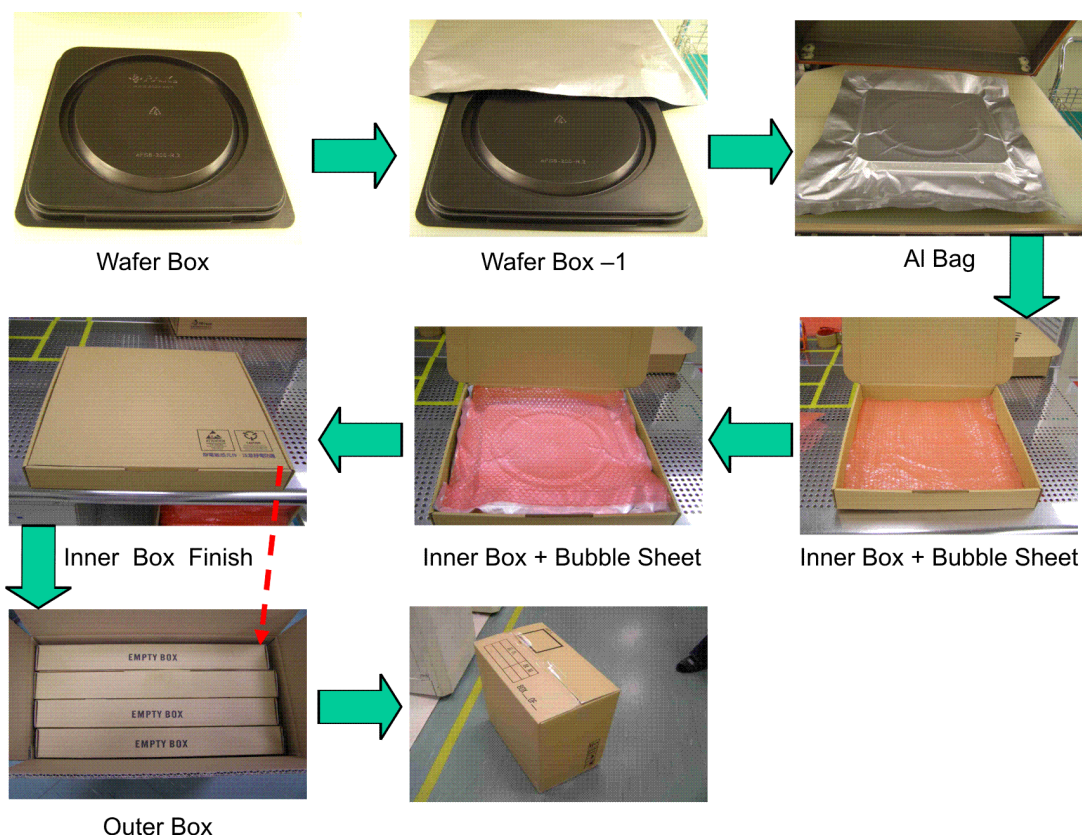
**Note:** When the silica gel has changed the color to blue, it is an indication that moisture has entered the bag.



**Figure 3-6. Packing of Sawn Wafers (Multi)**

## Packing in Single Wafer Box

Sawn wafers are mounted on foil and delivered on standard 8" disco wafer frame (see [Figure 2-1 Wafer on FCC](#)). A special plastic container is used to store the wafer in frames. This plastic container is packed in an antistatic moisture bag and in a carton box.



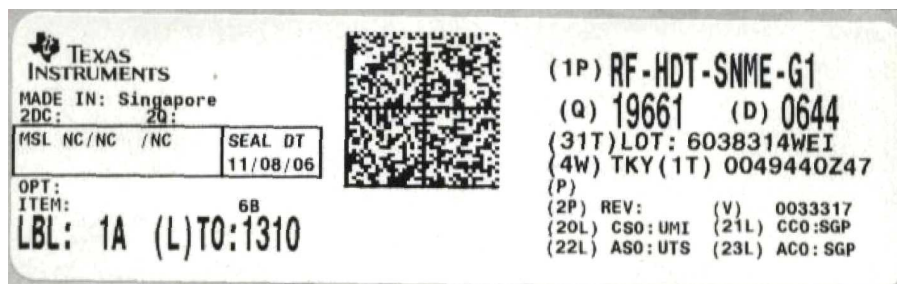
**Figure 3-7. Packing of Sawn Wafers (Single)**



### 3.7 Barcode Label

The following figure shows the barcode label that is placed on the packing box, the wafer container and the CD with the map file.

**Note:** The data provided below is an example and should only be viewed as guide values.



(1P) Part number

(Q) Quantity of functional chips

(D) Datecode; lot number

**Figure 3-8. Barcode Label**

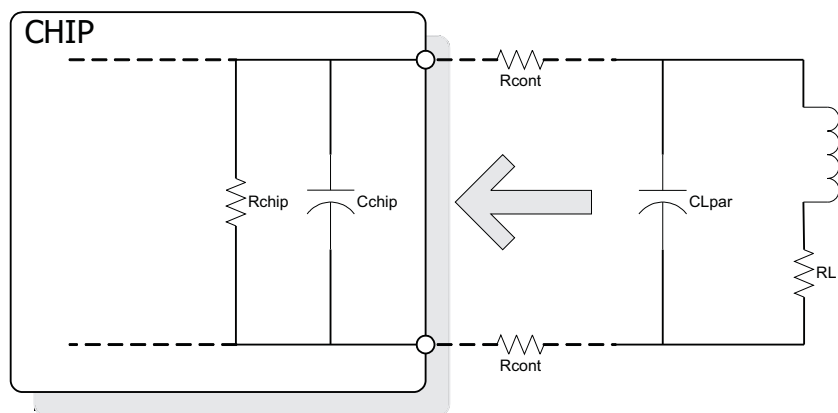
### 3.8 Storage Conditions

The wafers should be kept in the original packing during storage.

**Table 3-3. Storage Conditions**

Parameter	Value
Temperature	20°C ± 5°C
Atmosphere	dried N <sub>2</sub> or dried air with 40%–60% r.h.
Duration	Max. 6 months

### 3.9 Antenna Calculation



Rchip: IC input impedance  
Cchip: IC input capacitance  
Rcont: Pad/assembly contact resistance  
CLpar: Parasitic capacitance of antenna  
RL: Series resistance of antenna  
L: Antenna Inductance

Resonance frequency: 
$$f_{res} = \frac{1}{2 \cdot \pi \cdot \sqrt{L \cdot C_{chip}}}$$

Total Quality factor: 
$$Q_{res} = \frac{Q_c \cdot Q_l}{Q_c + Q_l}$$

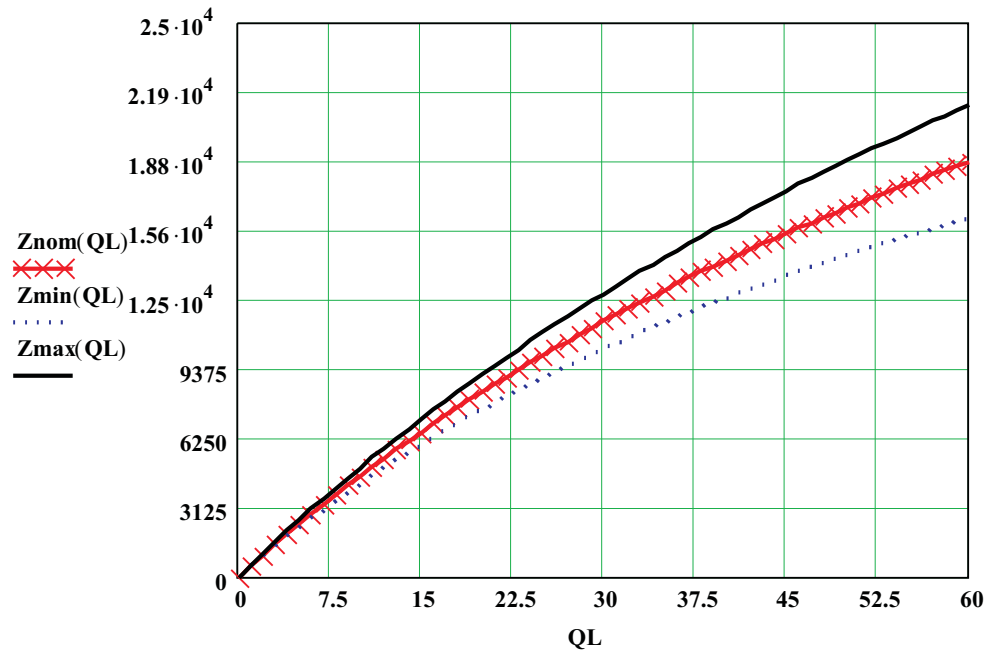
Input Impedance: 
$$Z = Q_{res} \cdot \sqrt{\frac{L}{C_{chip}}}$$

Based on an IC capacitance of 23.5 pF, the impedance shall be matched to be in the specified impedance range of 6.5 to 15.5 kΩ to fit the IC capabilities.

**Note:** If  $Z > 15.5 \text{ k}\Omega$ , reduced performance of read range must be considered.

**Table 3-4. Antenna System Parameters**

Parameter	Min	Nom	Max	Tolerance [%]	Test Conditions	Comment
Cchip [pf]	21.15	23.5	25.85	10	13.56 MHz @ 2 Vrms	
Qchip	80	100	120	20		
L [uH]	5.74	5.86	5.98	2	13.56 MHz @ 2 Vrms	
QL	15	40	44	10		
fres [MHz]	12.8	13.56	14.44			
Qres	12.63	28.57	32.2			
Z [kOhm]	6.58	14.27	15.48			



**Figure 3-9. Recommended Operating Range – Impedance vs Antenna Q**



## ***Regulatory, Safety and Warranty Notices***

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This chapter describes important safety precautions and safety regulations.

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## 4.1 Regulatory, Safety and Warranty Notices

An RFID system comprises an RF transmission device, and is therefore subject to national and international regulations.

A system reading from or writing to these transponders may be operated only under an experimental license or final approval issued by the relevant approval authority. Before any such device or system can be marketed, an equipment authorization must be obtained from the relevant approval authority.

The Tag-it HF-I Standard Transponder IC has been manufactured using state-of-the-art technology and in accordance with the recognized safety rules.

### Observe precautions in operating instructions

- Condition for the safe processing, handling and fault-free operation of the Tag-it HF-I Standard Transponder IC is the knowledge of the basic safety regulations.
- All persons who operate with the Tag-it HF-I Standard Transponder IC must observe the guidelines and particularly the safety precautions outlined in this document.
- In addition, basic rules and regulations for accident prevention applicable to the operating site must also be considered.

## 4.2 Warranty and Liability

The "General Conditions of Sale and Delivery" of Texas Instruments Incorporated or a TI subsidiary apply. Warranty and liability claims for defect products, injuries to persons and property damages are void if they are the result of one or more of the following causes:

- Improper use of the transponders IC
- Unauthorized assembly, operation and maintenance of the transponders IC
- Operation of the transponder IC with defective and/or non-functioning safety and protective equipment
- Failure to observe the instructions given in this document during transport, storage, assembly, operation, maintenance and setting up of the transponder IC
- Unauthorized changes to the transponder IC
- Insufficient monitoring of the transponder ICs' operation or environmental conditions
- Repairs
- Catastrophes caused by foreign bodies and acts of God.

### CAUTION

Tag-it HF-I Standard Transponder ICs are 100% thoroughly tested. It is the responsibility of TI's customer to evaluate their assembly process for compatibility with the Tag-it HF-I Standard Transponder IC properties and to ensure through appropriate process controls that determined machine and material parameter are met on an ongoing basis. TI does not accept warranty claims for material that has already undergone packaging or conversion process.

### 4.3 Hazards from Electrostatic Discharge ESD

**WARNING**

**ELECTRONIC DEVICES CAN ALSO BE DESTROYED BY  
ELECTROSTATIC ENERGY.**





## ***Terms and Abbreviations***

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A list of the abbreviations and terms used in various TI-RFID manuals can now be found in a separate manual:

**TI-RFID Product Manuals - Terms & Abbreviations**

Document number SCBU014 (11-03-21-002)

## PACKAGING INFORMATION

Orderable part number	Status (1)	Material type (2)	Package   Pins	Package qty   Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
RF-HDT-AJLE-G1	Active	Production	null (null)   0	1   NOT REQUIRED	Yes	AU	N/A for Pkg Type	-40 to 85	
RF-HDT-AJLE-G1.B	Active	Production	null (null)   0	1   NOT REQUIRED	Yes	AU	N/A for Pkg Type	-40 to 85	

<sup>(1)</sup> **Status:** For more details on status, see our [product life cycle](#).

<sup>(2)</sup> **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

<sup>(3)</sup> **RoHS values:** Yes, No, RoHS Exempt. See the [TI RoHS Statement](#) for additional information and value definition.

<sup>(4)</sup> **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

<sup>(5)</sup> **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

<sup>(6)</sup> **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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