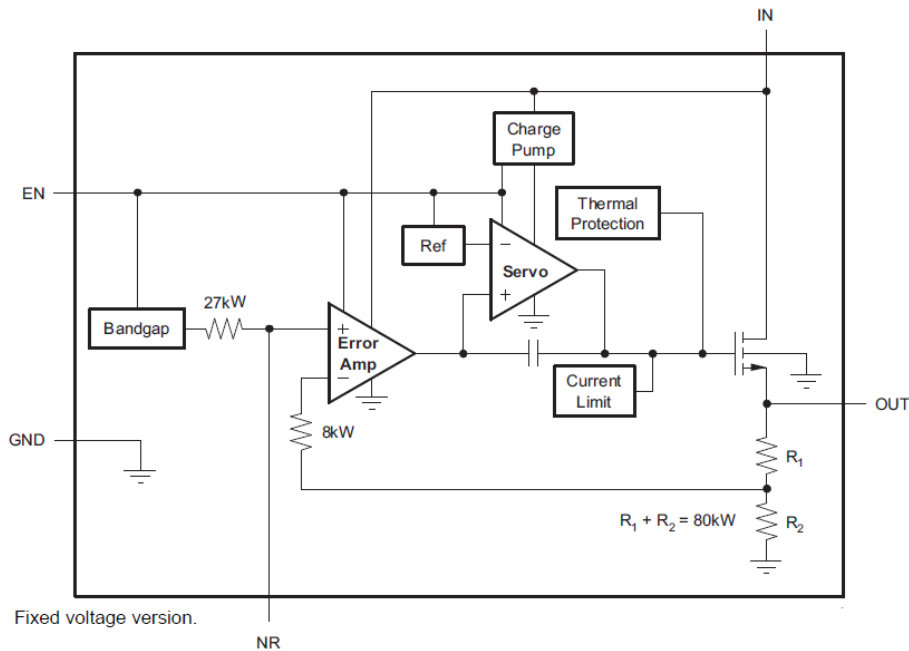


Functional Safety FIT Rate, Failure Mode Distribution TPS732-Q1

Cap-Free NMOS 250-mA Low-Dropout Regulator With Reverse-Current Protection

Functional Block Diagram



Failure Rate Mission Profile (1)	Per 10 ⁹ Hours (FIT)
Total FIT Rate	6
Die FIT Rate	2
Package FIT Rate	4

Failure Modes	Failure Mode Distribution (%)
No OUTPUT (Output low)	50%
OUTPUT High (Following Input)	10%
OUTPUT not in specification	35%
Short circuit any two pins	5%

(1) Failure Rate, Mission Profile and Failure Modes Distribution

The failure rate and mission profile information comes from the Reliability data handbook IEC TR 62380 using the reliability modeling for Integrated circuits with automotive motor control mission profiles

Power dissipation 100mW

Climate type: World-wide Table 8

Package factor lambda 3 Table 17b

Substrate Material: FR4

EOS FIT rate assumed = 0

The failure mode distribution estimation comes from the combination of common failure modes listed in standards such as IEC 61508 and ISO 26262, the ratio of sub-circuit function size and complexity and from best engineering judgment. The failure rates listed reflect random failure events and do not include failures due to misuse or over stress.

The TPS732-Q1 is a catalog product and not compliant to ISO-26262 standards.

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