



ABSTRACT

This document describes the known exceptions to the functional specifications (advisories). This document may also contain usage notes. Usage notes describe situations where the device's behavior may not match presumed or documented behavior. This may include behaviors that affect device performance or functional correctness.

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1 Usage Notes and Advisories Matrices

[Table 1-1](#) lists all advisories, modules affected, and the applicable silicon revisions.

Table 1-1. Advisories Matrix

MODULE	DESCRIPTION	SILICON REVISIONS AFFECTED
LFOSC	Synchronous wakeup sources for STANDBY0 mode are not functioning as expected. Additionally, STANDBY0/1 modes exhibit higher than specified current consumption due to the absence of LFOSC.	1.0

2 Nomenclature, Package Symbolization, and Revision Identification

2.1 Device and Development-Support Tool Nomenclature

To designate the stages in the product development cycle, TI assigns prefixes to the part numbers of all microprocessors (MPUs) and support tools. Each device has one of three prefixes: X, P, or null (no prefix) (for example, *your device*). Texas Instruments recommends two of three possible prefix designators for its support tools: TMDX and TMDS. These prefixes represent evolutionary stages of product development from engineering prototypes (TMDX) through fully qualified production devices and tools (TMDS).

Device development evolutionary flow:

- X** Experimental device that is not necessarily representative of the final device's electrical specifications and may not use production assembly flow.
- P** Prototype device that is not necessarily the final silicon die and may not necessarily meet final electrical specifications.
- null** Production version of the silicon die that is fully qualified.

Support tool development evolutionary flow:

- TMDX** Development-support product that has not yet completed Texas Instruments internal qualification testing.
- TMDS** Fully-qualified development-support product.

X and P devices and TMDX development-support tools are shipped against the following disclaimer:

To designate the stages in the product development cycle, TI assigns prefixes to the part numbers of all DSP devices and support tools. Each DSP commercial family member has one of three prefixes: TMX, TMP, or TMS (for example, *your device*). Texas Instruments recommends two of three possible prefix designators for its support tools: TMDX and TMDS. These prefixes represent evolutionary stages of product development from engineering prototypes (TMX and TMDX) through fully qualified production devices and tools (TMS and TMDS).

Device development evolutionary flow:

- TMX** Experimental device that is not necessarily representative of the final device's electrical specifications and may not use production assembly flow.
- TMP** Prototype device that is not necessarily the final silicon die and may not necessarily meet final electrical specifications.
- TMS** Production version of the silicon die that is fully qualified.

Support tool development evolutionary flow:

- TMDX** Development-support product that has not yet completed Texas Instruments internal qualification testing.
- TMDS** Fully-qualified development-support product.

TMX and TMP devices and TMDX development-support tools are shipped against the following disclaimer:

"Developmental product is intended for internal evaluation purposes."

Production devices and TMDS development-support tools have been characterized fully, and the quality and reliability of the device have been demonstrated fully. TI's standard warranty applies.

Predictions show that prototype devices (X or P) have a greater failure rate than the standard production devices. Texas Instruments recommends that these devices not be used in any production system because their expected end-use failure rate still is undefined. Only qualified production devices are to be used.

2.2 Devices Supported

This document supports the following devices:

- AM13E23019
- AM13E23018
- AM13E23017

3 Silicon Revision 1.0 Advisories

The following advisories are known design exceptions to functional specifications. Advisories are numbered in the order in which the items were added to this document. Some advisory numbers can be removed in future revisions of this document because the design exception was fixed or documented in the device-specific data manual or technical reference manual. When items are deleted, the remaining advisory numbers are not re-sequenced.

Advisory List

Advisory-001 — Synchronous wakeup sources for STANDBY0 mode are not functioning as expected.
STANDBY0/1 modes exhibit higher than specified current consumption due to the absence of LFOSC.5

Advisory-001 *Synchronous wakeup sources for STANDBY0 mode are not functioning as expected. STANDBY0/1 modes exhibit higher than specified current consumption due to the absence of LFOSC.*

Revisions Affected 1.0

Details

The LFOSC does not turn on when the device enters STANDBY mode (both STANDBY0 and STANDBY1). The LFCLK is not available in the system.

Effects of LFCLK absence in STANDBY0/1 Power Modes:

- Gates all PD0 peripherals in STANDBY0 modes.
- The Windowed Watchdog Timer (WWDT) halts and cannot provide an interrupt/NMI to WAKE the device from STANDBY0 modes.
- Debug WAKE is also non-functional in STANDBY0/1 modes. NRST cannot assert a POR or BOOTRST in STANDBY0/1 modes.
- PMU does not enter into sample and hold mode, contributing to higher currents in both STANDBY0/1 modes.

GPIO (asynchronous)/BOR threshold based WAKE is functional in STANDBY0/1 modes.

Workaround

Either use GPIO or BOR threshold-based WAKE sources in STANDBY0/1 modes or use STOP mode, which can enable all synchronous/asynchronous based wakeup sources.

4 Trademarks

All trademarks are the property of their respective owners.

5 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

DATE	REVISION	NOTES
March 2026	*	Initial Release

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