User's Guide

TPS56C231LEVM 12-A Synchronous Step-down Converter Evaluation Module

ABSTRACT

This user's guide contains information for the TPS56C231L as well as support documentation for the TPS56C231LEVM evaluation module. This document also includes the performance specifications, board layout, schematic, and the list of materials of the TPS56C231LEVM.

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Trademarks

D-CAP3™ is a trademark of Texas Instruments.
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1 Introduction

The TPS56C231L is a single, D-CAP3™ control mode, synchronous buck converter requiring a very low external component count. The TPS56C231L is a high-efficiency, cost effective, low quiescent current synchronous buck converter with integrated FETs. A mode pin is used to select output current limit, switching frequency, and forced continuous conduction mode (FCCM) and discontinuous conduction mode (DCM) operation. The device uses D-CAP3 control mode to provide a fast transient response, good line, load regulation, no requirement for external compensation, and supports low ESR output capacitors. Additionally, the TPS56C231L provides adjustable soft start, undervoltage lockout inputs, and a power-good output. Rated input voltage and output current ranges for the evaluation module are given in Table 1-1.

The TPS56C231LEVM evaluation module (EVM) is a single, synchronous buck converter providing 1.2 V at 12 A from 4.5-V to 17-V input. This user's guide describes the TPS56C231LEVM performance.

Table 1-1. Input Voltage and Output Current Summary

EVM	Input Voltage (V_{IN}) Range	Output Current (I_{OUT}) Range
TPS56C231LEVM	4.5 V to 17 V	0 A to 12 A

2 Performance Specification Summary

A summary of the TPS56C231LEVM performance specifications is provided in Table 2-1. Specifications are given for an input voltage of 12 V and an output voltage of 1.2 V, unless otherwise noted. The ambient temperature is 25°C for all measurement, unless otherwise noted.

Table 2-1. TPS56C231LEVM Performance Specifications Summary

Specifications		Test Conditions	MIN	TYP	MAX	Unit
V_{IN}	Input voltage		4.5	12	17	V
CH1	Output voltage			1.2		V
	Operating frequency	V_{IN} = 12 V, I_{OUT} = 6 A		800		kHz
	Output current range		0		12	A
	Overcurrent limit	V_{IN} = 12 V, L_{OUT} = 0.68 µH		13.8		A
	Output ripple voltage	V_{IN} = 12 V, I_{OUT} = 12 A		10		mV_{PP}

3 Modifications

This evaluation module is designed to provide access to the features of the TPS56C231L. Some modifications can be made to this module.

3.1 Output Voltage Setpoint

To change the output voltage of the EVM, change the value of resistor R7 (R_{UPPER}) and R9 (R_{LOWER}). The value of R7 and R9 for a specific output voltage can be calculated using Equation 1 and refer to Table 3-1 for some recommendation values. See the *TPS56C231 3.8-V to 17-V Input , 12-A Synchronous Step-Down Converter* data sheet. See Table 3-1 to set the switching frequency.

$$V_{OUT} = 0.6 \times (1 + \frac{R_{UPPER}}{R_{LOWER}}) \quad (1)$$

Table 3-1. Recommended Component Values

V_{OUT} (V)	R_{LOWER} (kΩ)	R_{UPPER} (kΩ)	f_{SW} (kHz)	L_{OUT} (µH)	$C_{OUT(min)}$ (µF)	$C_{OUT(max)}$ (µF)	C_{FF} (PF)
0.6	10	0	400	0.68	300	500	—
			800	0.47	100	500	—
			1200	0.33	88	500	—
1.2	10	10	400	1.2	100	500	—
			800	0.68	88	500	—
			1200	0.47	88	500	—
3.3	10	45.3	400	2.4	88	500	100-220
			800	1.5	88	500	100-220
			1200	1.2	88	500	100-220
5.5	10	82.5	400	3.3	88	500	100-220
			800	2.4	88	500	100-220
			1200	1.5	88	700	100-220

3.2 Adjustable UVLO

The undervoltage lockout (UVLO) can be adjusted externally using R1 ($R_{EN(TOP)}$) and R2 ($R_{EN(BOT)}$). See the *TPS56C231 3.8-V to 17-V Input , 12-A Synchronous Step-Down Converter* data sheet for detailed instructions for setting the external UVLO.

4 Test Setup and Results

This section describes how to properly connect, set up, and use the TPS56C231LEVM. The section also includes test results typical for the evaluation modules, includes power on, power off, and voltage ripple.

4.1 Input and Output Connections

The TPS56C231LEVM is provided with input and output connectors and test points as shown in Table 4-1. A power supply capable of supplying 6 A must be connected to J1 through a pair of 20-AWG wires. The load must be connected to J2 through a pair of 20-AWG wires. The maximum load current capability is 12 A. Wire lengths must be minimized to reduce losses in the wires. Test point TP2 provides a place to monitor the V_{IN} input voltages with TP4 providing a convenient ground reference. TP7 is used to monitor the output voltage with TP10 as the ground reference.

Table 4-1. Connection and Test Points

Reference Designator	Function
J1	V_{IN} (see Table 1-1 for V_{IN} range)
J2	V_{OUT}, 1.2 V at 12-A maximum
J3	En Control. Short pin1 and pin2 to make EN low. Short pin2 and pin3 to make EN high.
J4	V_{IN} positive monitor point
J5	GND monitor test point
J6	GND monitor test point
J7	PGOOD monitor test point
J8	VREG5 monitor test point
J9	MODE monitor test point
J10	Soft Start test point
J11	Switch node test point
J12	Loop test point
J13	GND monitor test point
J14	V_{OUT} positive monitor point
J15	GND monitor test point
J16	GND monitor test point
TP2	V_{IN} positive monitor point
TP4	GND monitor test point
TP7	V_{OUT} positive monitor point
TP10	GND monitor test point

4.2 Start-Up Procedure

1. Ensure that the J3 (Enable control) pins 1 and 2 are shorted to shunt EN to GND, disabling the output.
2. Apply appropriate input voltage to VIN (J1-2) or TP2 and GND (J1-1) or TP4. Note that the board cannot support hot plug-in. Connect the input lines between J1 and external power source first before turning on the power source.
3. Disconnect J3 (Enable control) pins 1 and 2 (EN and L). Ensure that pins 2 and 3 (EN and H) are shorted, then the output can be enabled.
4. Apply the loading to VOUT (J2-1) or TP7 and GND (J2-2) or TP10.

4.3 Start-Up

The TPS56C231LEVM start-up waveform relative to EN is shown in Figure 4-1.

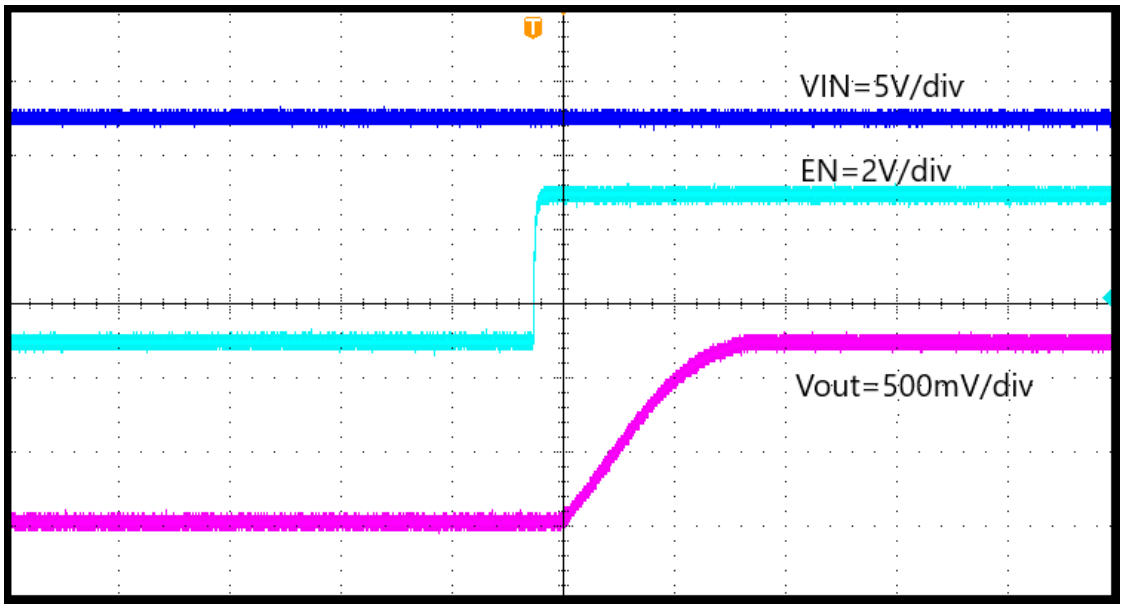

Figure 4-1. Start-Up Relative to EN, I_{OUT} = 6 A (4 ms/div)

4.4 Shutdown

The TPS56C231LEVM shutdown waveform relative to EN is shown in Figure 4-2.

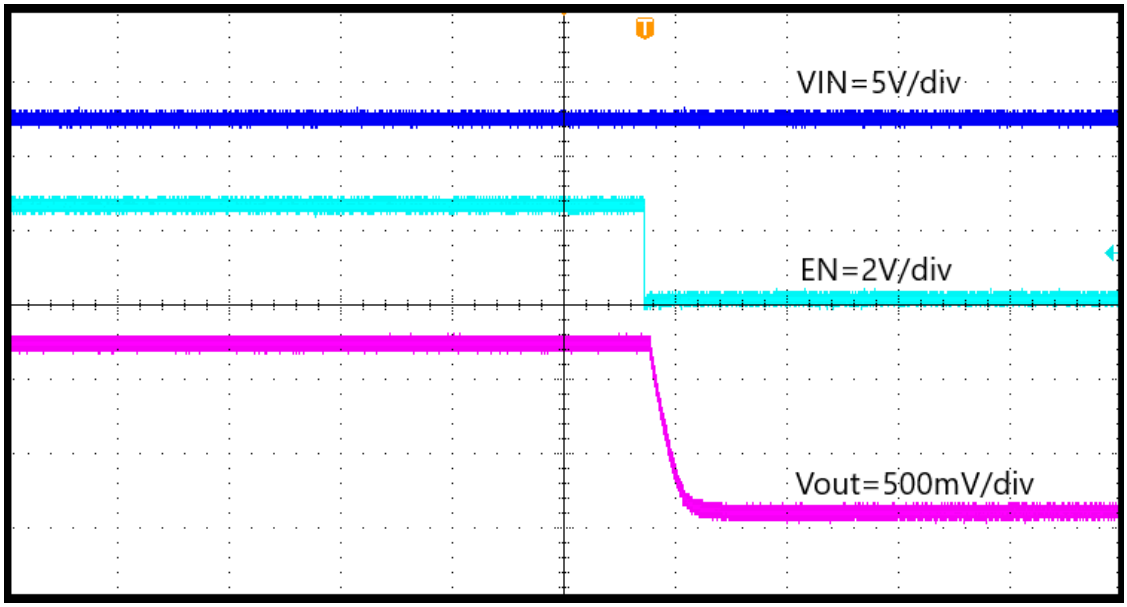

Figure 4-2. Shutdown Relative to EN, I_{OUT} = 6 A (200 µs/div)

4.5 Output Voltage Ripple

The TPS56C231LEVM output voltage ripple is shown in Figure 4-3 and Figure 4-4. The output currents are as indicated.

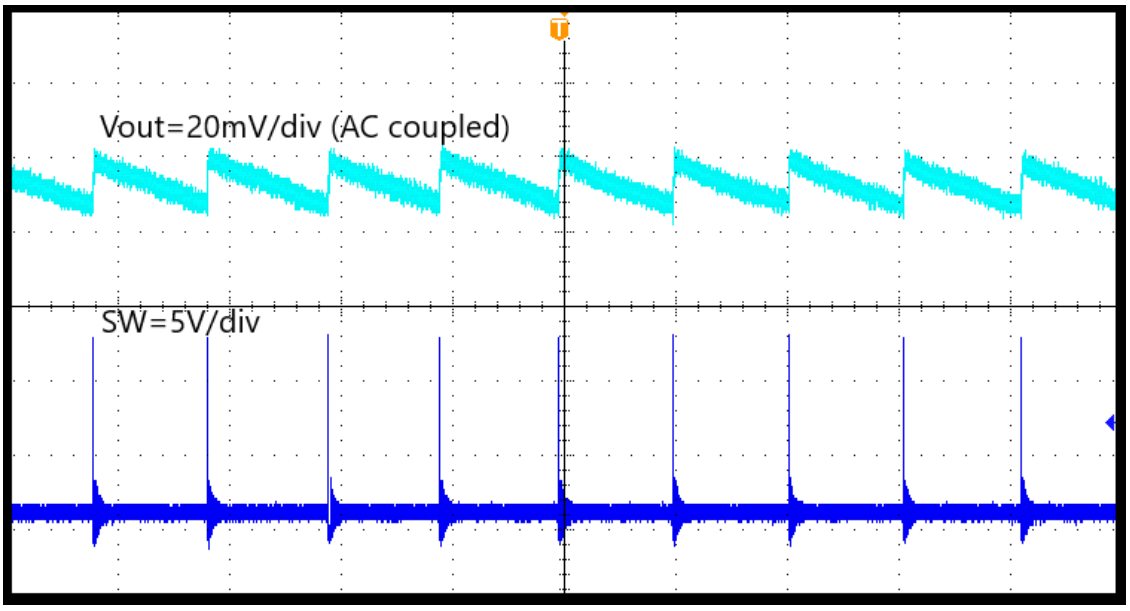

Figure 4-3. TPS56C231L Output Voltage Ripple, I_{OUT} = 0.01 A (80 µs/div)

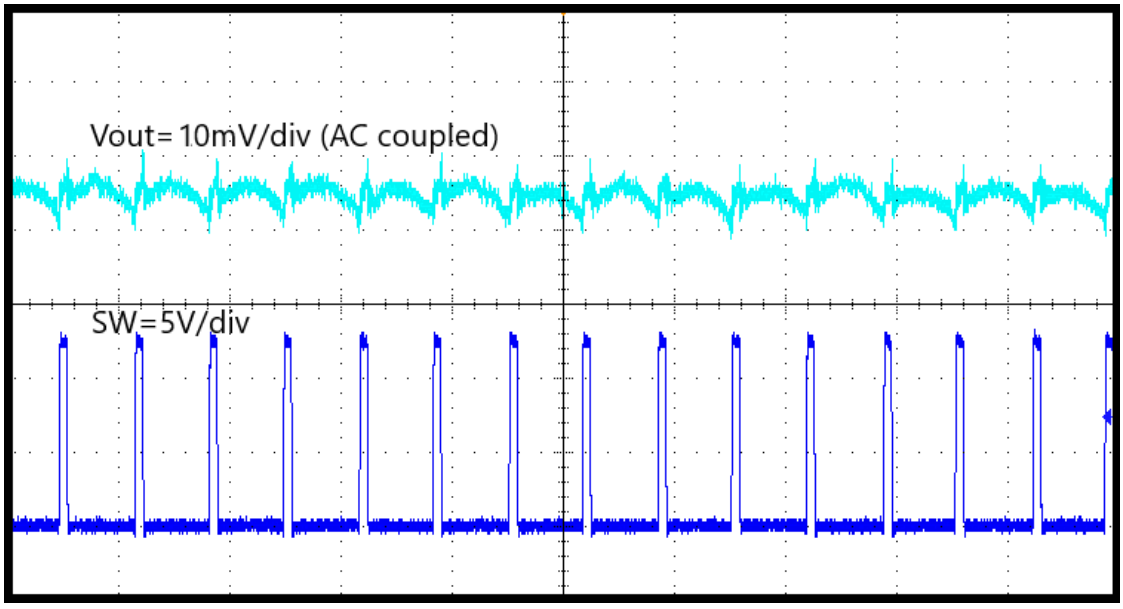

Figure 4-4. TPS56C231L Output Voltage Ripple, I_{OUT} = 12 A (2 µs/div)

5 Board Layout

This section provides a description of the TPS56C231LEVM, board layout, and layer illustrations.

5.1 Layout

The board layout for the TPS56C231LEVM is shown in Figure 5-1 to Figure 5-5. The TPS56C231LEVM is with four layers. The top layer contains the main power traces for VIN, VOUT, SW, and GND. Also, on the top layer are connections for the pins of the TPS56C231L and a large area filled with ground. Most of the signal traces are also located on the top side. The input decoupling capacitors are located as close to the VIN pins and PGND pins of the IC as possible. The internal layer-1 is dedicated ground plane. The internal layer-2 contains an additional large ground copper area as well as an additional VIN and VOUT copper fill. The bottom layer is a ground plane along with 4 traces for VIN, VOUT, EN, and BOOT connection.

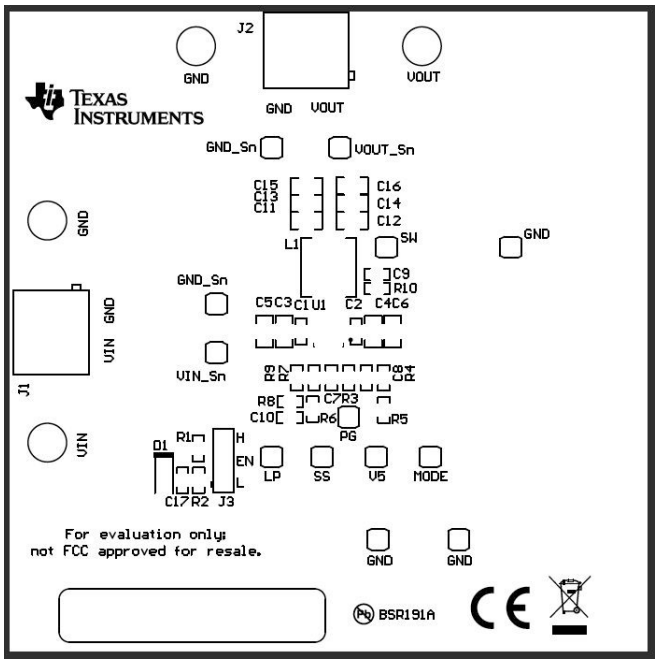

Figure 5-1. Top Assembly

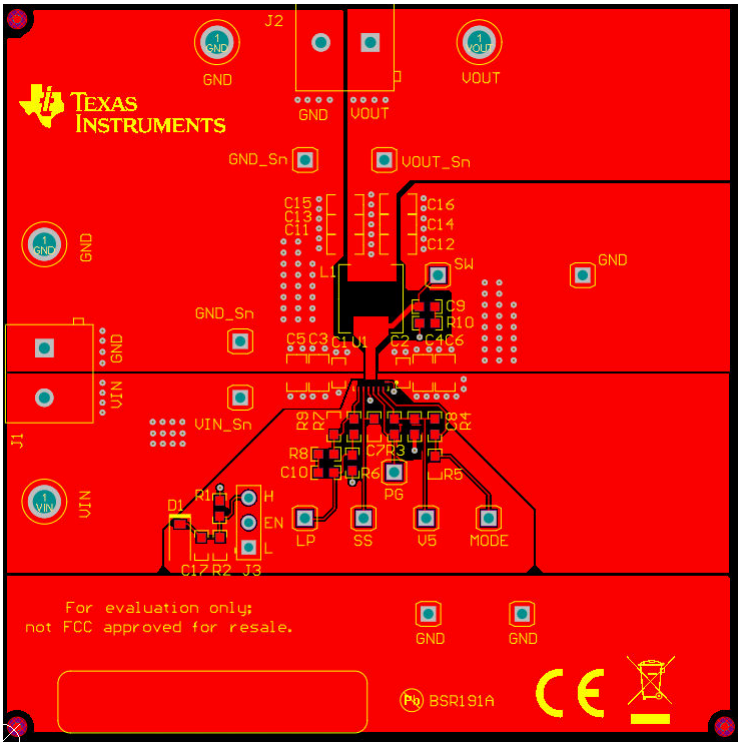

Figure 5-2. Top Layer

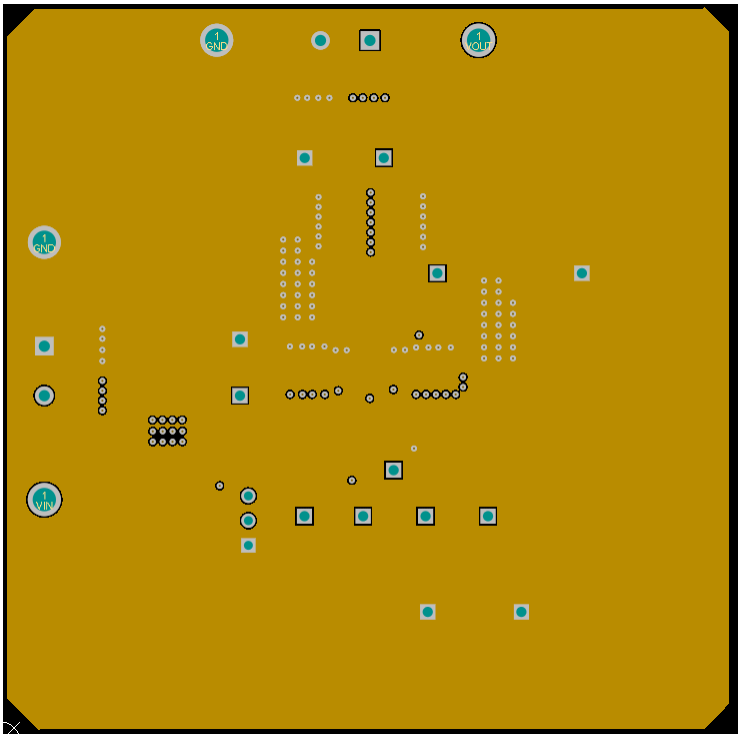

Figure 5-3. Inner1 Layer

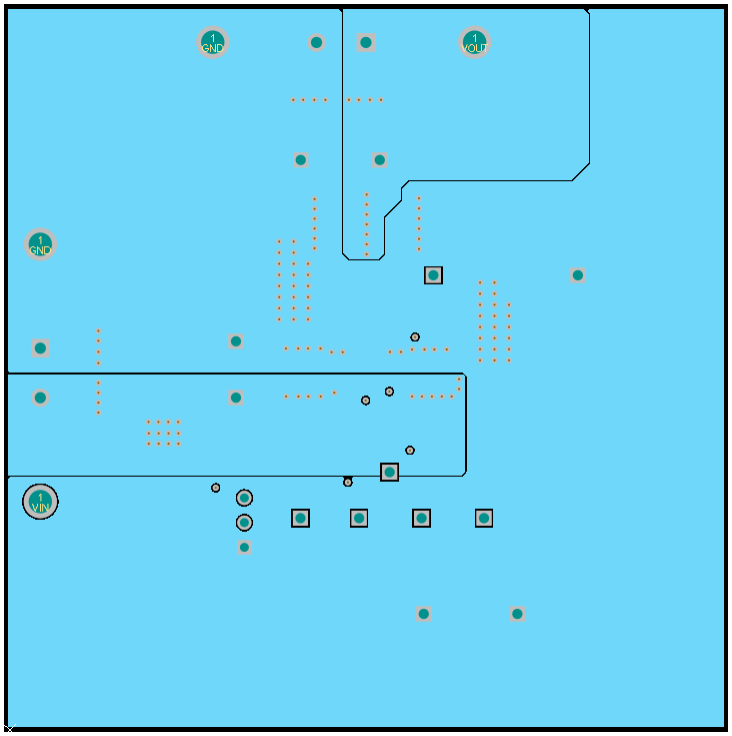

Figure 5-4. Inner2 Layer

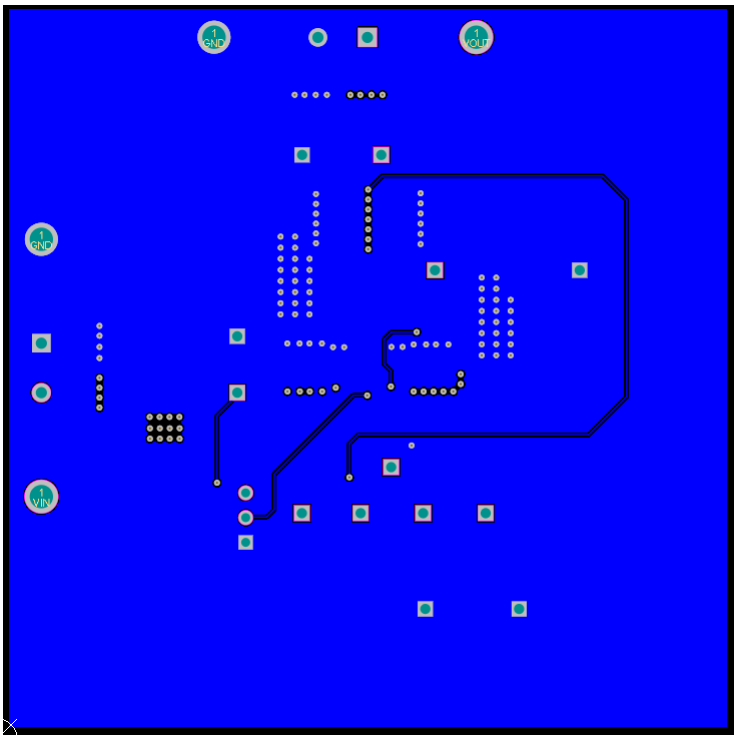

Figure 5-5. Bottom Layer

6 Board Profile, Schematic, and List of Materials

6.1 Board Profile

Figure 6-1 is the top view for the TPS56C231LEVM.

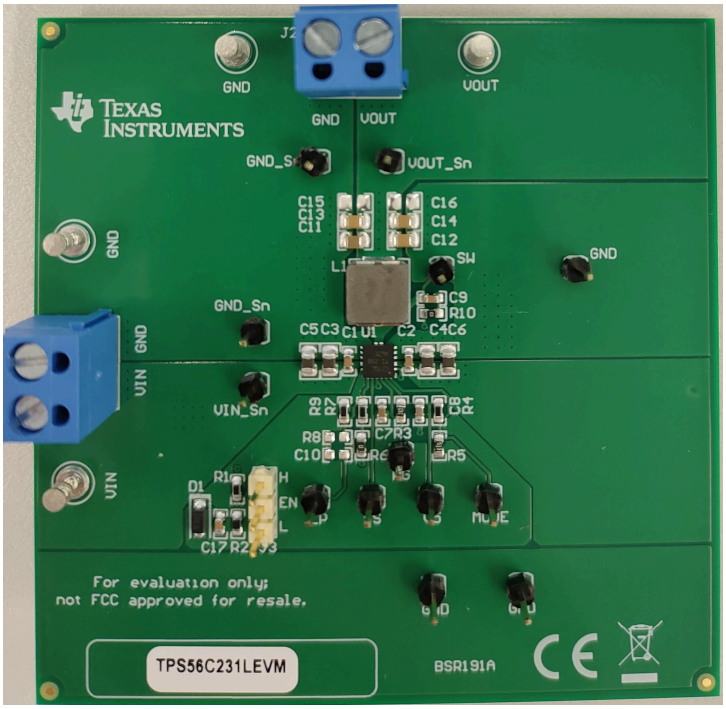

Figure 6-1. Top View of TPS56C231LEVM

Figure 6-2 is the bottom view for the TPS56C231LEVM.

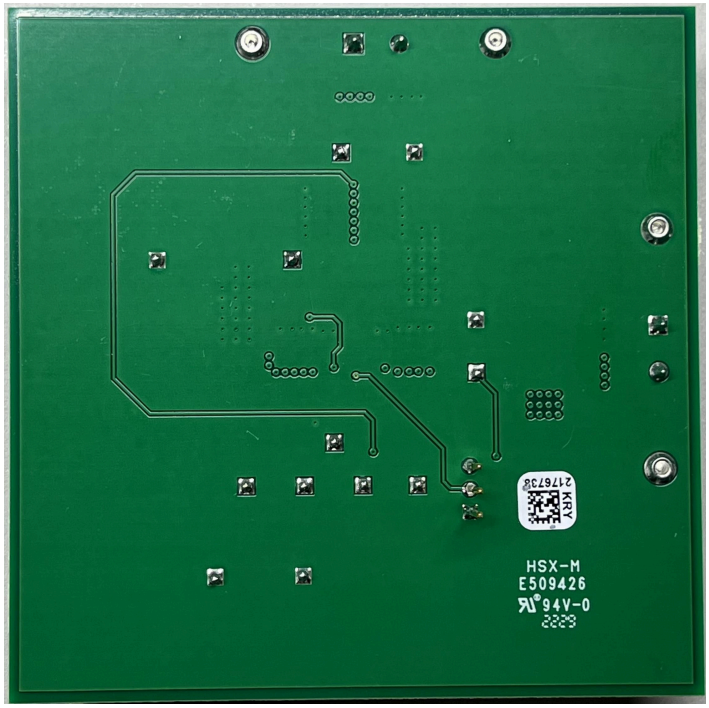

Figure 6-2. Bottom View of TPS56C231LEVM

6.2 Schematic

Figure 6-3 is the schematic for the TPS56C231LEVM.

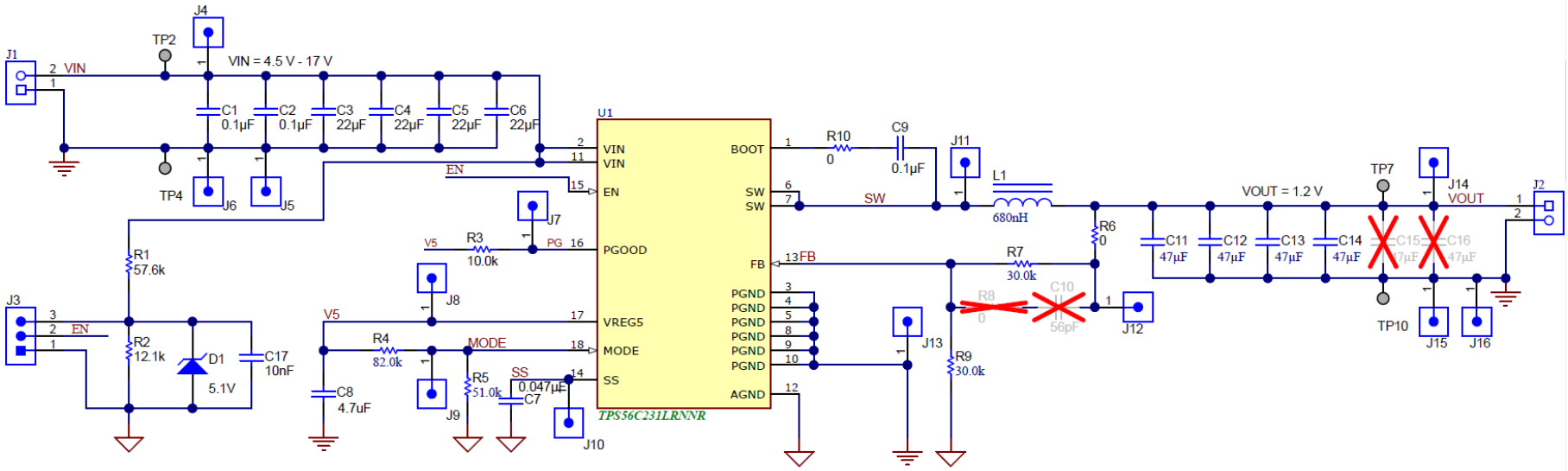

Figure 6-3. TPS56C231LEVM Schematic Diagram

6.3 List of Materials

Table 6-1 displays the TPS56C231LEVM list of materials.

Table 6-1. List of Materials

Designator	Qty	Description	Part Number(1)	Manufacturer
PCB1	1	Printed Circuit Board	BSR191	Any
C1, C2, C9	3	Capacitor, ceramic, 0.1 µF, 25 V, ±10%, X7R, 0603	GRM188R71E104KA01D	MuRata
C3, C4, C5, C6	4	Capacitor, ceramic, 22 µF, 35 V, ±20%, X5R, 0805	C2012X5R1V226M125AC	TDK
C7	1	Capacitor, ceramic, 0.047 µF, 50 V, ±10%, X7R, 0603	GRM188R71H473KA61D	MuRata
C8	1	Capacitor, ceramic, 4.7 µF, 10 V, ±10%, X5R, 0805	C0603C475K8PACTU	Kemet
C11, C12, C13, C14	4	Capacitor, ceramic, 47 µF, 10 V, ±20%, X5R, 0805	GRM21BR61A476ME15L	MuRata
C17	1	Capacitor, ceramic, 0.01 µF, 50 V, ±20%, X7R, 0603	C1608X7R1H103K080AA	TDK
D1	1	Diode, Zener, 5.1 V, 500 mW, SOD-123	MMSZ5231B-7-F	Diodes Inc.
J1, J2	2	Terminal Block, 5.08 mm, 2 × 1, Brass, TH	ED120/2DS	On-Shore Technology
J3	1	Header, 100mil, 3 × 1, Gold, TH	HTSW-103-09-G-S	Samtec
J4, J5, J6, J7, J8, J9, J10, J11, J12, J13, J14, J15, J16	13	Header, 2.54 mm, 1 × 1, Gold, TH	61300111121	Wurth Elektronik
L1	1	Inductor, Shielded Drum Core, Powdered Iron, 680 nH, 15.5 A, 0.005 Ω, SMD	IHLP2525CZERR68M01	Vishay-Dale
LBL1	1	Thermal Transfer Printable Labels, 1.250" W × 0.250" H - 10,000 per roll	THT-13-457-10	Brady
R1	1	Resistor, 57.6 k, 1%, 0.1 W, 0603	CRCW060357K6FKEA	Vishay-Dale
R2	1	Resistor, 12.1 k, 1%, 0.1 W, 0603	CRCW060312K1FKEA	Vishay-Dale
R3	1	Resistor, 10.0 k, 1%, 0.1 W, 0603	CRCW060310K0FKEA	Vishay-Dale
R4	1	Resistor, 82.0 k, 1%, 0.1 W, 0603	RC0603FR-0782KL	Yageo
R5	1	Resistor, 51.0 k, 1%, 0.1 W, 0603	RC0603FR-0751KL	Yageo
R6	1	Resistor, 0, 5%, 0.1 W, 0603	MCR03EZPJ000	Rohm
R7, R9	2	Resistor, 30 k, 1%, 0.1 W, 0603	RC0603FR-0730KL	Yageo
R10	1	Resistor, 0, 5%, 0.1 W, AEC-Q200 Grade 0, 0603	CRCW06030000Z0EA	Vishay-Dale
TP2, TP4, TP7, TP10	4	Terminal, Turret, TH, Double	1502-2	Keystone
U1	1	3.8-V to 17-V Input, 12-A Synchronous Step-Down Voltage Regulator	TPS56C231LRNNR	Texas Instruments
C15, C16	0	Capacitor, ceramic, 47 µF, 10 V, ±20%, X5R, 0805	GRM21BR61A476ME15L	MuRata
C10	0	Capacitor, ceramic, 56 pF, 50 V, ±5%, C0G/NP0, 0603	GRM1885C1H560JA01D	MuRata
R8	0	Resistor, 0, 5%, 0.1 W, 0603	MCR03EZPJ000	Rohm
FID1, FID2, FID3	0	Fiducial mark. There is nothing to buy or mount.	N/A	N/A

(1) Unless otherwise noted in the *Alternate Part Number* or *Alternate Manufacturer* columns, all parts can be substituted with equivalents.

7 References

Texas Instruments, *TPS56C231 3.8 V to 17 V Input, 12-A Synchronous Step-Down Converter* data sheet

STANDARD TERMS FOR EVALUATION MODULES

1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an “EVM” or “EVMs”) to the User (“User”) in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.

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   1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.

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   2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

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Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI’s recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI’s recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI’s instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGREDATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

3 *Regulatory Notices:*

3.1 *United States*

3.1.1 *Notice applicable to EVMs not FCC-Approved:*

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

3.2 *Canada*

3.2.1 *For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247*

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

3.3 *Japan*

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。
http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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1. 電波法施行規則第6条第1項第1号に基づく平成18年3月28日総務省告示第173号で定められた電波暗室等の試験設備でご使用いただく。
2. 実験局の免許を取得後ご使用いただく。
3. 技術基準適合証明を取得後ご使用いただく。

なお、本製品は、上記の「ご使用にあたっての注意」を譲渡先、移転先に通知しない限り、譲渡、移転できないものとします。
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3.3.3 *Notice for EVMs for Power Line Communication:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page
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3.4 *European Union*

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive)*:

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

5. *Accuracy of Information:* To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

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