

# DP83822 EVM Getting Started

## User's Guide



Literature Number: SNLU207  
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## Abstract

This Getting Started Guide details the operation and use of the DP83822 EVM. For more information regarding the schematic, layout and bill of materials, please reference the DP83822 EVM User's Guide.

**Table 0-1. Terminology**

ACRONYM	DEFINITION
PHY	Physical Layer Transceiver
MAC	Media Access Controller
EEE	Energy Efficient Ethernet
WoL	Wake-on-LAN
SMI	Serial Management Interface
MDIO	Management Data I/O
MDC	Management Data Clock
SFP	Small Form-Factor Pluggable (Fiber Transceiver)
MII	Media Independent Interface
RMII	Reduced Media Independent Interface
RGMII	Reduced Gigabit Media Independent Interface
SFD	Start-of-Frame Detection
CAT5	Category 5 (cable electrical characteristics)
AVD	Analog Supply Rail
VDDIO	Digital Supply Rail
CT	Center Tap Supply Rail

## ***Introduction***

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The DP83822 EVM supports 10/100 Mbps and is compliant to the IEEE 802.3u standard. This reference design supports MII, RMI and RGMII MAC interfaces.

The DP83822 EVM includes two onboard status LEDs, optional Fiber SFP connector and cage, and onboard supply through a 5-V micro USB connector. The DP83822 EVM is capable of providing a 125-MHz, 50-MHz or 25-MHz reference clock from an onboard 25-MHz crystal. The EVM includes the CDCE925 programmable 2-PLL VCXO clock synthesizer with 1.8-V to 3.3-V LVCMOS outputs. Serial management interface, MDIO/MDC, is supported and can be used to access PHY registers for additional features. There are 4-level straps, which allow for system configurations without the need to directly access PHY registers. External power supplies can be connected to each specified voltage rail for additional system evaluation. The DP83822 supports Wake-on-LAN, Energy Efficient Ethernet (IEEE802.3az), Start-of-Frame Detect IEEE 1588 Time Stamp, and configurable I/O voltages.

## **Key Features**

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- IEEE 802.3u Compliant: 100BASE-FX, 100BASE-TX and 10BASE-Te
- CDCE925 Programmable 2-PLL VCXO Clock Synthesizer with 1.8 V to 3.3 V
- MII, RMII, and RGMII MAC interfaces
- SFD IEEE 1588 Time Stamp
- Two status LEDs
  - LED LINK/ACTIVITY
  - LED SPEED
- Low Power Modes
  - Energy Efficient Ethernet (IEEE802.3az)
  - Wake-on-LAN
  - Active Sleep
  - Passive Sleep
  - IEEE Power Down
  - Deep Power Down
- Variable I/O voltage range: 1.8 V, 2.5 V and 3.3 V
- 100BASE-TX error free data transfer over 150 meters on CAT5 cable

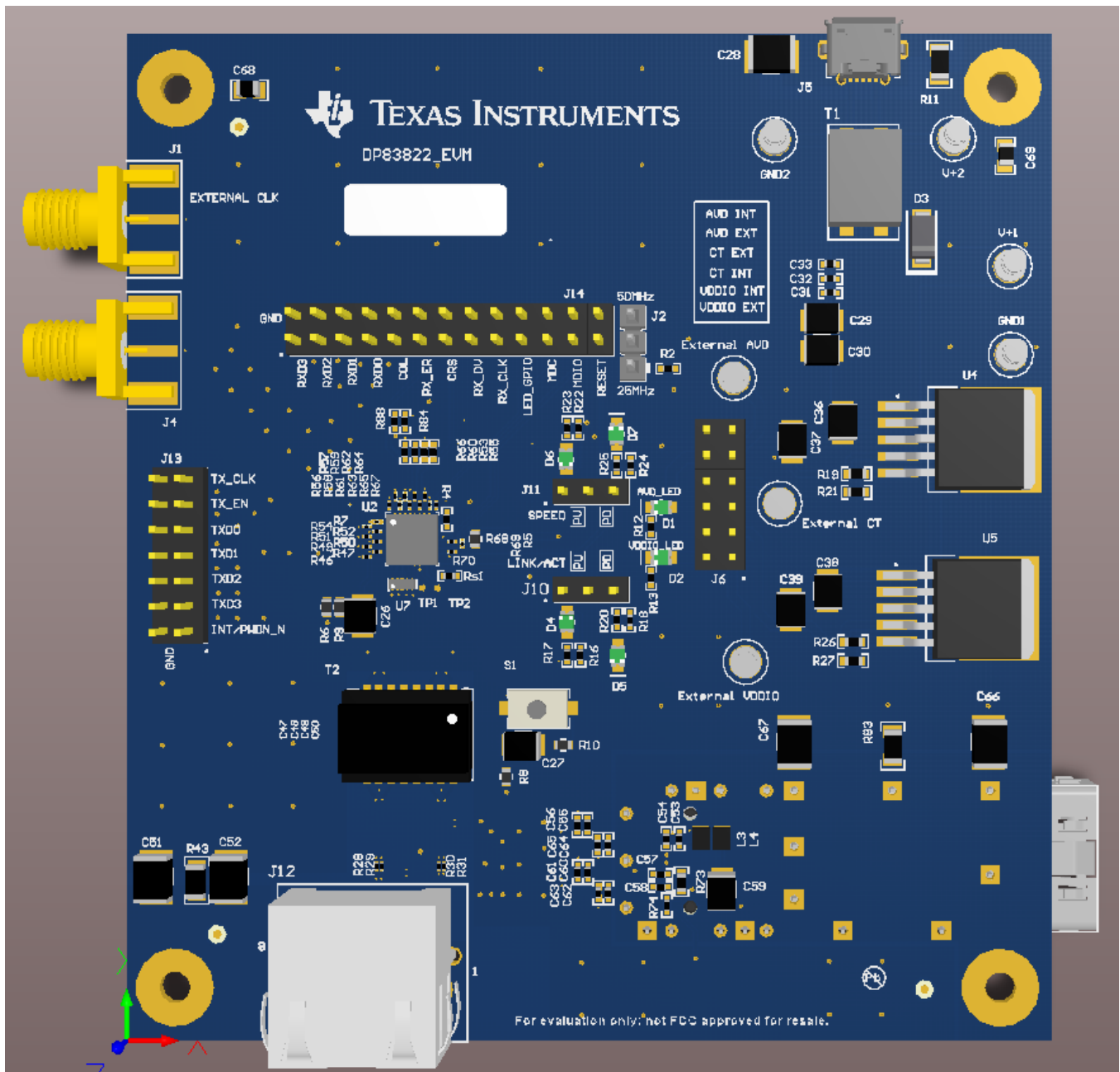
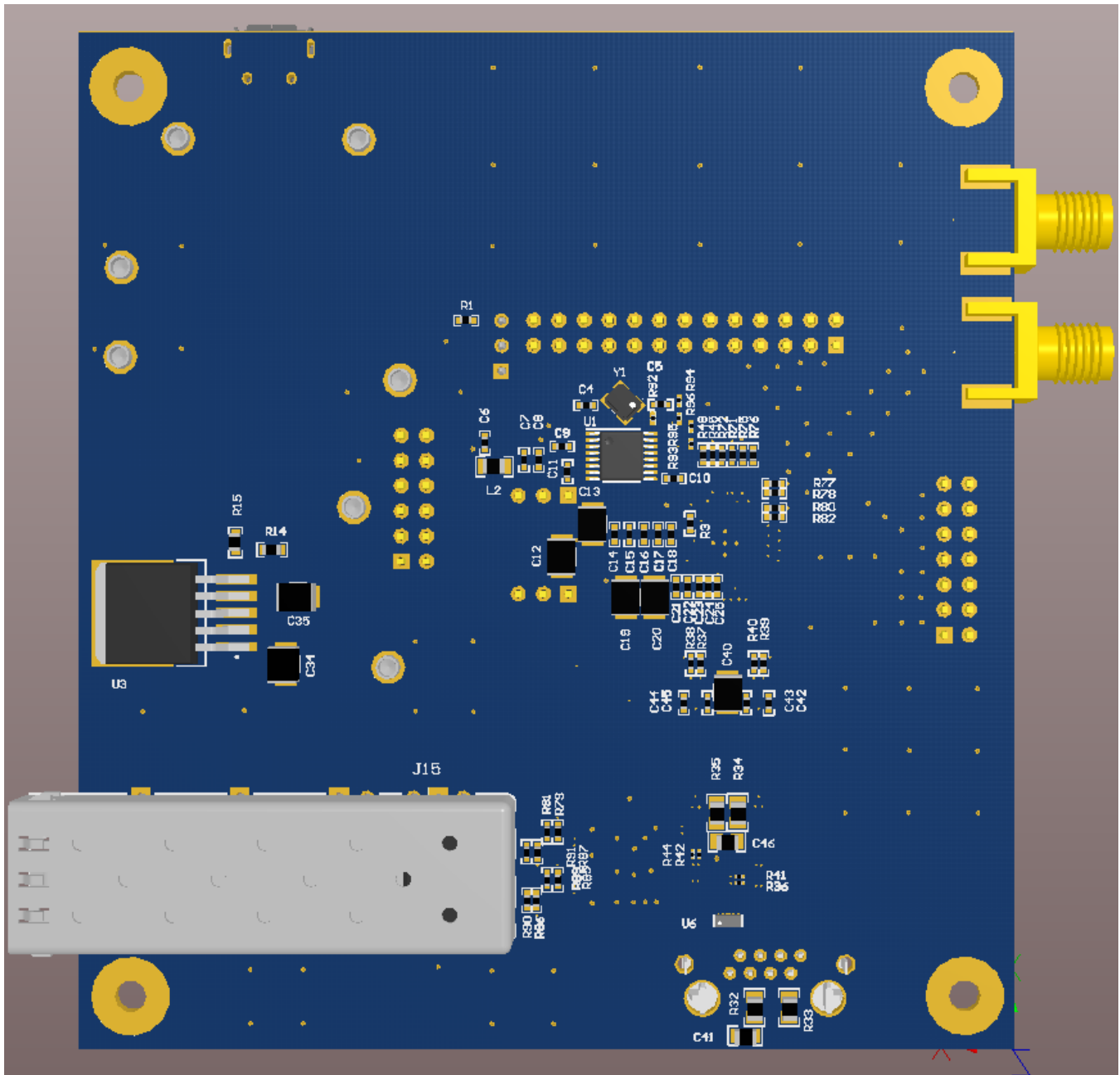


Figure 2-1. DP83822 EVM – Top Side

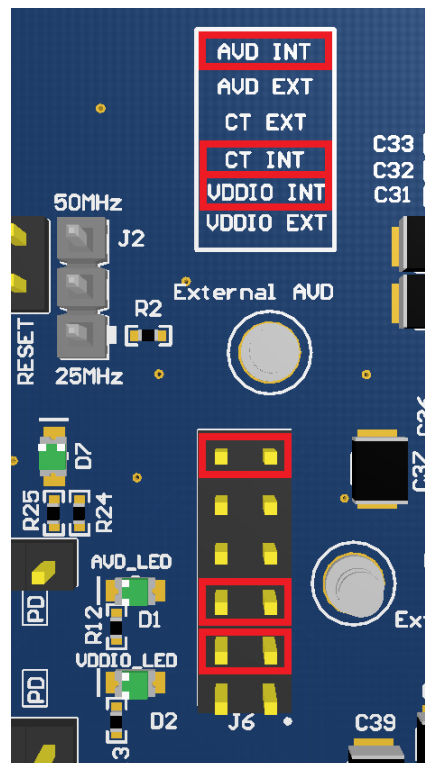
The DP83822 EVM has an RJ45 connector with discrete magnetics, stuffing resistor array for configurable straps and can be operated from a single supply (5-V, USB micro A/B). Customers are encouraged to use a design similar to the EVM circuit to expedite their product development. Serial management interface pins allow customers to also access additional features by directly controlling PHY registers.



**Figure 2-2. DP83822 EVM - Bottom Side**

## Operation – Quick Setup

1. Select internal supply by populating the following with jumpers (J6):
  - Place jumper #1 at 'AVD INT' position
  - Place jumper #2 at 'CT INT' position
  - Place jumper #3 at 'VDDIO INT' position

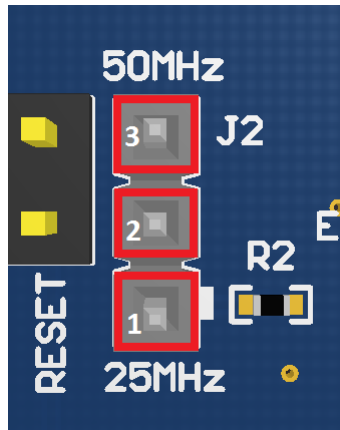


**Figure 3-1. On-board Power Supply Settings**

2. Select VDDIO voltage level (R26):
  - 4.22 k $\Omega$  - 3.3 V operation (Default)
  - 2.56 k $\Omega$  - 2.5 V operation
  - 1.20 k $\Omega$  - 1.8 V operation
3. Select AVD voltage level (R19):
  - 4.22 k $\Omega$  - 3.3 V operation (Default)
  - 1.20 k $\Omega$  - 1.8 V operation

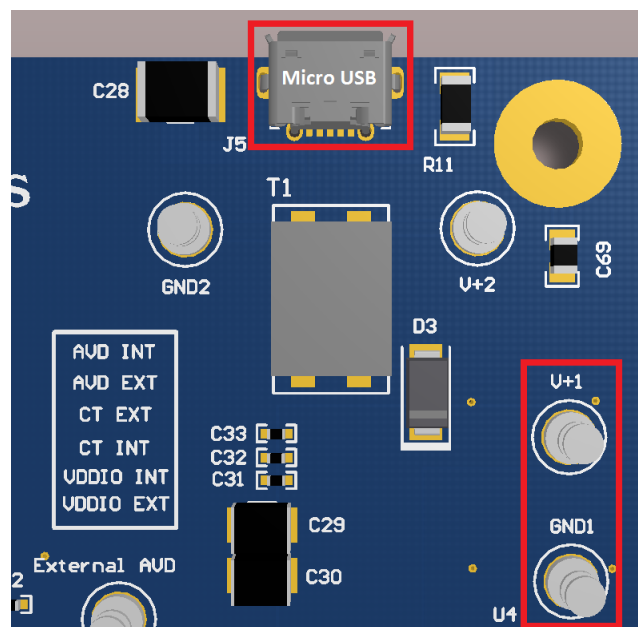


4. Select reference clock frequency (J2):
  - 25 MHz – jumper across position 1 and 2
  - 50 MHz – jumper across position 2 and 3



**Figure 3-2. Reference Clock Selection**

5. Place a jumper in the 'PU' position for LED LINK/ACT (J10)
6. Turn ON the PHY by connecting a 5-V USB micro power supply to J5
7. Plug a CAT5, CAT5E or CAT6 cable into the RJ45 connector (J12)
8. Connect the far-end of the Ethernet cable to a link partner
9. Connect a MAC interface to J13 and J14



**Figure 3-3. USB and Turrent Power Connections**

- LED Indication
  - The AVD LED (D1) and VDDIO LED (D2) will be illuminated if the 5-V supply is connected
  - Look for the LINK LED (D4) to light up on the DP83822 EVM after the PHY links with a connected partner

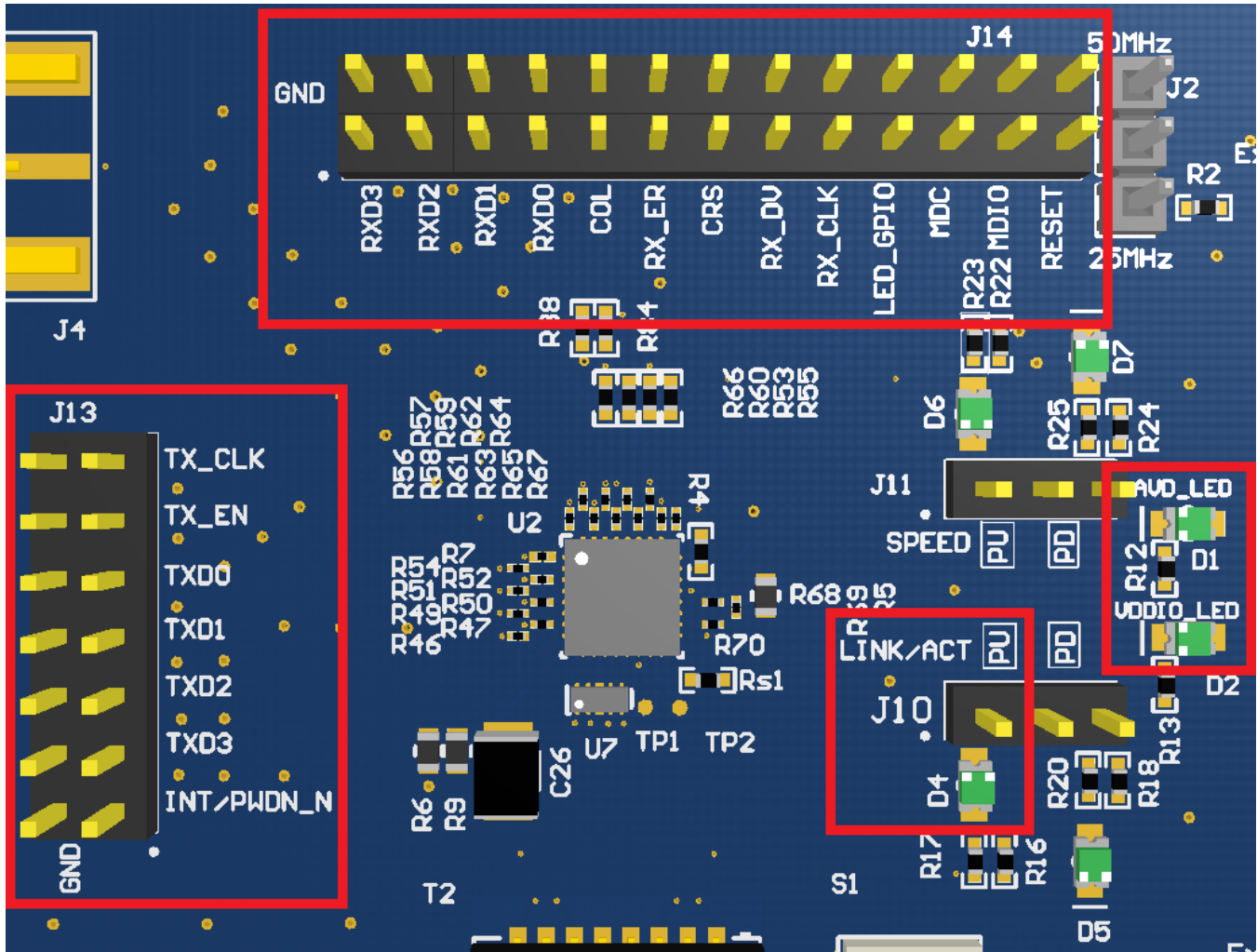


Figure 3-4. MAC IF Connection and LED Indication

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- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

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#### 3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210

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##### **Concernant les EVMs avec antennes détachables**

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

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[http://www.tij.co.jp/lstds/ti\\_ja/general/eStore/notice\\_01.page](http://www.tij.co.jp/lstds/ti_ja/general/eStore/notice_01.page)

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2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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Amplifiers	<a href="http://amplifier.ti.com">amplifier.ti.com</a>
Data Converters	<a href="http://dataconverter.ti.com">dataconverter.ti.com</a>
DLP® Products	<a href="http://www.dlp.com">www.dlp.com</a>
DSP	<a href="http://dsp.ti.com">dsp.ti.com</a>
Clocks and Timers	<a href="http://www.ti.com/clocks">www.ti.com/clocks</a>
Interface	<a href="http://interface.ti.com">interface.ti.com</a>
Logic	<a href="http://logic.ti.com">logic.ti.com</a>
Power Mgmt	<a href="http://power.ti.com">power.ti.com</a>
Microcontrollers	<a href="http://microcontroller.ti.com">microcontroller.ti.com</a>
RFID	<a href="http://www.ti-rfid.com">www.ti-rfid.com</a>
OMAP Applications Processors	<a href="http://www.ti.com/omap">www.ti.com/omap</a>
Wireless Connectivity	<a href="http://www.ti.com/wirelessconnectivity">www.ti.com/wirelessconnectivity</a>

### Applications

Automotive and Transportation	<a href="http://www.ti.com/automotive">www.ti.com/automotive</a>
Communications and Telecom	<a href="http://www.ti.com/communications">www.ti.com/communications</a>
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