

TEXAS INSTRUMENTS

1 Evaluation Module Overview

1.1 Introduction

The LM656x5 family of devices are easy-to-use synchronous step-down converters capable of supplying up to 2.5A, 3.5A, or 4.5A of load current from an input voltage as high as 65V. See [Section 1.4](#) for more details. Throughout this document, the terms evaluation board, evaluation module, and EVM are synonymous with the LM65635EVM.

This user's guide describes the operational use of the LM65635EVM evaluation module (EVM) as a reference for engineering demonstration and evaluation of the LM656x5 family of devices. Included in this user's guide are setup and operating instructions, thermal and layout guidelines, a printed-circuit board (PCB) layout, a schematic diagram, and a bill of materials (BOM).

1.2 Kit Contents

This kit includes one LM65635EVM.

1.3 Specification

Performance characteristics for the LM65635EVM, with LM65635, are found in [Table 1-1](#)

Unless otherwise stated: $V_{IN} = 24V$, $V_{OUT} = 5V$, $T_A = 25^{\circ}C$.

Table 1-1. LM65635EVM Electrical Performance Characteristics

Parameter	Test Conditions		MIN	TYP	MAX	UNITS
INPUT CHARACTERISTICS						
Input voltage range, V _{VIN}	EVM input voltage operating range		6	12	65	V
Input current, no load, I _{IN(NL)}	I _{OUT} = 0A	AUTO mode		5.5		μA
Input current, disabled, I _{IN(OFF)}	V _{EN/UVLO} = 0V, no EN divider	V _{IN} = 24V		1		μA
OUTPUT CHARACTERISTICS						
Output voltage, V _O	I _{OUT} = 0A, AUTO mode			5.01		V
	I _{OUT} = 3.5A			5.005		V
Output voltage regulation, ΔV _{OUT}	Load regulation, AUTO mode	I _{OUT} = 0A to 3.5A		4		mV
Output voltage regulation, ΔV _{OUT}	Load regulation, FPWM mode	I _{OUT} = 0A to 3.5A		0.5		
Output voltage regulation, ΔV _{OUT}	Line regulation, V _{IN} = 12V to 48V	I _{OUT} = 3.5A		2		
Maximum output current	V _{IN} = 24V			4.5		A
SYSTEM CHARACTERISTICS						
Switching frequency	I _{OUT} = 3.5A			2200		kHz
Peak efficiency	I _{OUT} = 1.5A	V _{IN} = 24V		87%		
Full load efficiency	I _{OUT} = 3.5A	V _{IN} = 24V		86%		

1.4 Device Information

The default EVM incorporates the LM65635. [Table 1-2](#) provides a list of additional devices that can be used with the LM65635EVM. Appropriate passive component changes must be made to use another device in the EVM.

Table 1-2. LM65635EVM Device Options

Device OPN	Output Current	Q-Grade ?
LM65645SRZTRQ1	4.5A	Y
LM65625SRZTRQ1	2.5A	Y
LM65645SRZTR	4.5A	N
LM65625SRZTR	2.5A	N

2 Hardware

2.1 Additional Images

Figure 2-1 and Figure 2-2 show the front and back of the LM65635EVM, respectively.

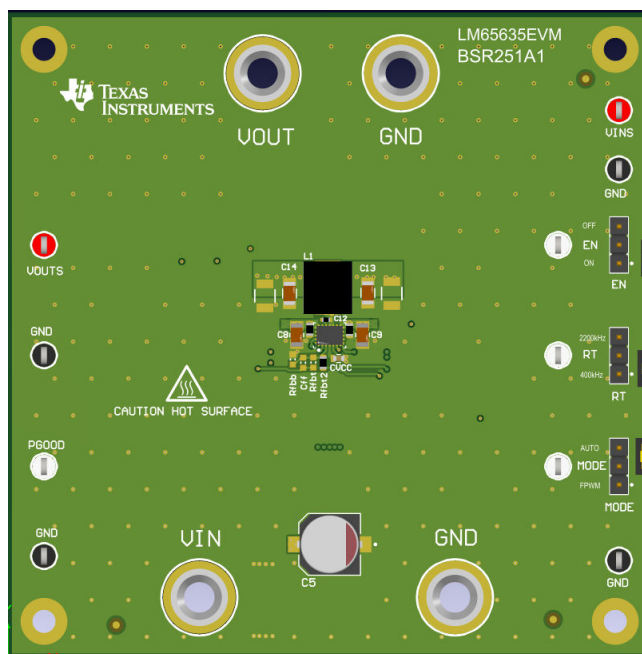


Figure 2-1. LM65635EVM (Top View)

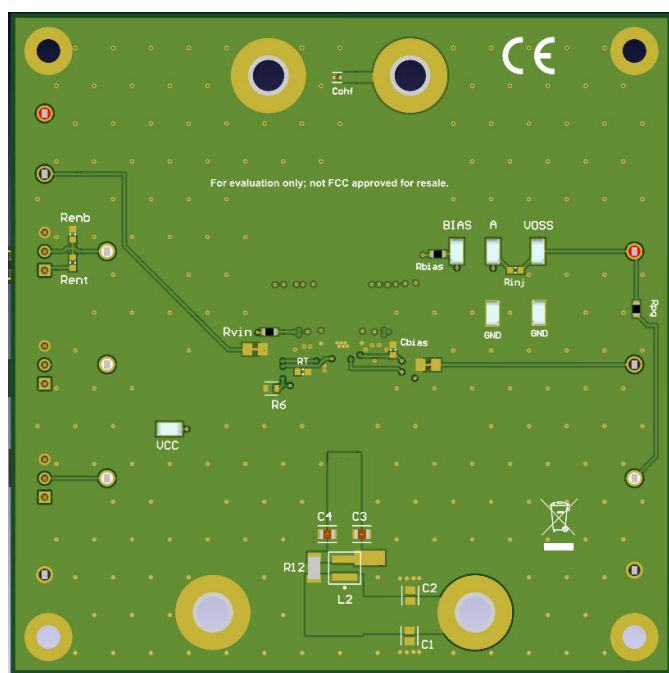


Figure 2-2. LM65635EVM (Bottom View)

2.2 Power Requirements

Any power source in the range of 6V to 65V, and capable of delivering 3.5A, can be used to evaluate the LM65635EVM.

2.3 Setup and Operation

This section describes the connectors, test points, and jumpers on the EVM and how to properly connect, set up, and use the LM65635EVM. See [Figure 2-3](#) for location of connectors and jumpers and typical setup.

VOUT	Output voltage of the converter. VOUT banana post. Apply load to this connector.
VOUTS	The VOUTS test point is used to monitor output voltage.
GNDS	Test point next to VOUTS test point. This GNDS test point is use as the negative DMM connection for VOUT sensing.
VIN	Input voltage to the converter. VIN banana post. Apply input voltage to this connector.
VINS	The VINS test point is used to monitor input voltage.
GNDS	Test point next to VINS test point. This GNDS test point is use as the negative DMM connection for VIN sensing.
GND	Ground of the converter GND banana posts. Apply load ground and input voltage ground to these connectors. The various GND test point are used as ground sense for input voltage and output voltage measurements.
EN	The use of the EN jumper is self-explanatory. To supply an external signal to the EN input of the device, remove the EN jumper shunt and apply the signal to the EN test point. To use the external UVLO feature, populate Rent (R3) and Renb (R4) as desired and remove the EN jumper shunt. Note that for accurate shutdown current measurement, these resistors must be removed (if used) and the EN jumper shunt moved to "OFF".
RT	The RT jumper is used to select the switching frequency and is self explanatory. The default inductor on the EVM is designed for 2.2MHz operation. Other frequencies require a different value of inductance. To adjust the switching frequency, remove the RT jumper shunt and populate RT (R5) with the desired value. See the LM656x5 data sheet for frequency vs. RT resistor value.
MODE	MODE jumper is used to select the operating mode of the device. With MODE in the AUTO position, the device operates in automatic PFM/FPWM mode depending on load current. With the MODE in the FPWM position, the device operates at fixed frequency for all load currents. The MODE pin is also the frequency synchronization input. To synchronize the device to an external clock, remove the MODE jumper shunt and apply the clock to the MODE test point.
Feed-back Connections	The EVM is set for a fixed 5V output, with Rfbt2 (R7), 0Ω, populated. To set the output voltage to 3.3V, remove Rfbt2 (R7) and populate Rfbb (R10) with a 0Ω resistor. To use the adjustable output voltage option, populate Rfbt (R9) and Rfbb (R10) with the appropriate value resistors. Rinj (R8) must also be populated with a 10Ω to 50Ω resistor. The reference voltage is 0.8V. See the LM656x5-Q1 High Performance Power Converter, 3V to 65V, Pin-Compatible, 2.5A/3.5A/4.5A, Automotive, Low EMI Synchronous Buck Converter data sheet for appropriate values of feed-back resistors. When using the adjustable output voltage mode, a Bode plot can be taken using the Rinj (R8) resistor. This resistor becomes the injection point for the frequency response analyzer, allowing the loop frequency response to be taken in the usual way. In the fixed output voltage mode, a Bode plot can not be taken.
PGOOD	The PGOOD test point is used to monitor the power-good indicator. This flag indicates whether the output voltage has reached the regulation level. PGOOD is an open-drain output that is tied to VOUT through a 100kΩ, resistor, Rpg (R1).
VCC	VCC Test point

The VCC pin is the output of the internal LDO. The VCC voltage is typically 3.3V. This point can be used for logic inputs and/or pull-up. Do not connect to external loads.

BIAS

Auxiliary input to LDO

Connected to VOUT through 0Ω Rbias (R11) on EVM. To change the input supply of the LDO, remove Rbias (R11) and connect external supply to BIAS pin, or ground BIAS pin as required. Populate Cbias (C11) when using an external bias supply. See the LM656x5 data sheet for more information.

Bode Plot

When using the adjustable output voltage mode, a bode plot can be taken using the connection shown in [Figure 2-4](#). Rinj (R8) must be populated.

EMI Filter

The EMI filter is not populated on the EVM. To test the EMI filter, components L2, C1 and C2 must be populated, while R12 must be removed. Typical values of these components are given in [Section 4.3](#).

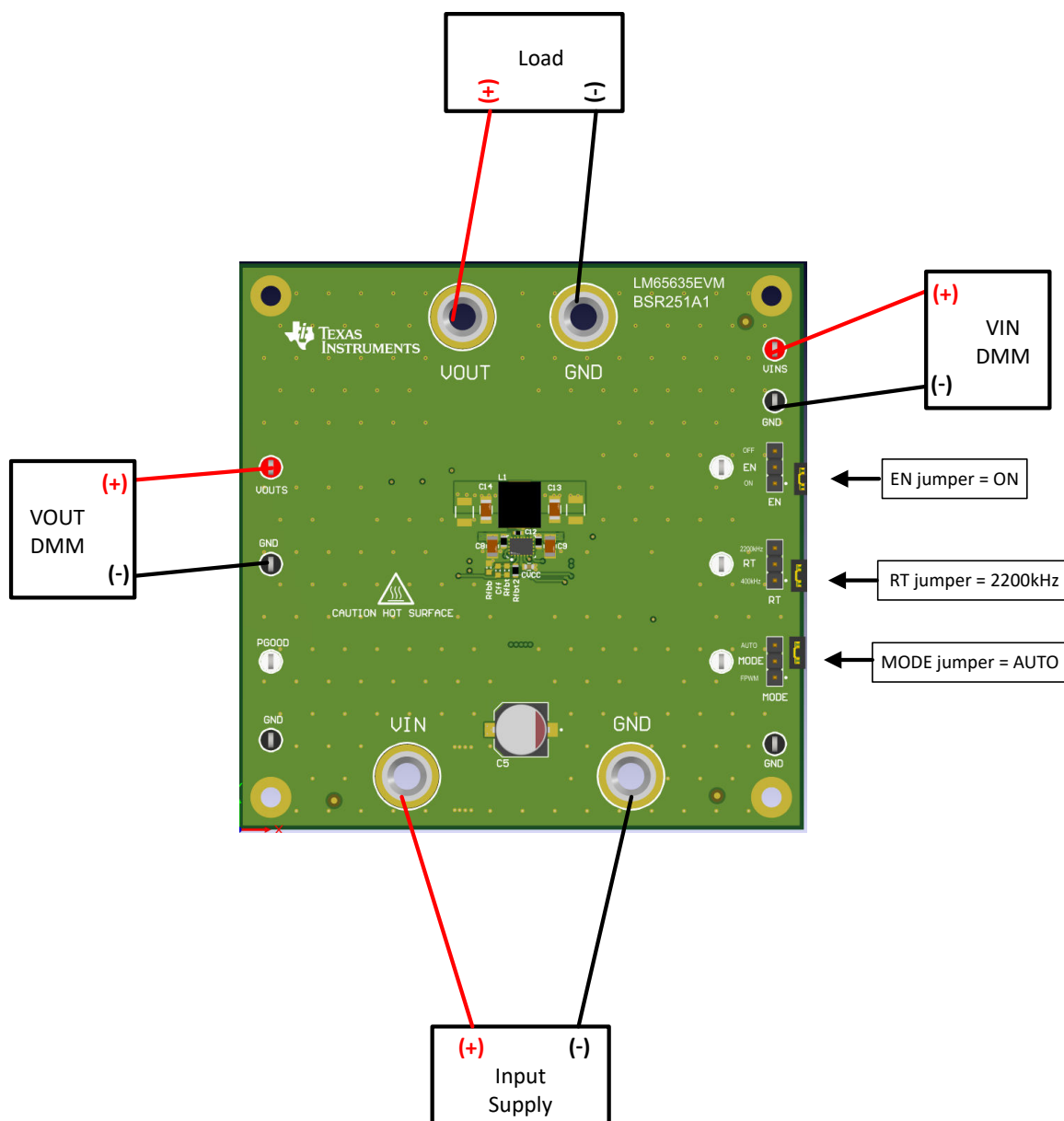


Figure 2-3. LM65635EVM Setup



3 Implementation Results

3.1 Evaluation Setup

The LM65635VM was used to take the following data with the set-up shown in [Figure 2-3](#).

3.2 Performance Data and Results

Unless otherwise specified the following condition apply: $T_A = 25^\circ\text{C}$, $V_{OUT} = 5\text{V}$, $V_{IN} = 24\text{V}$, $F_{SW} = 2200\text{kHz}$.

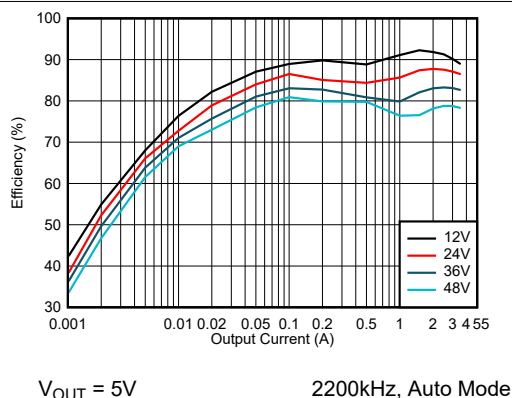


Figure 3-1. Efficiency

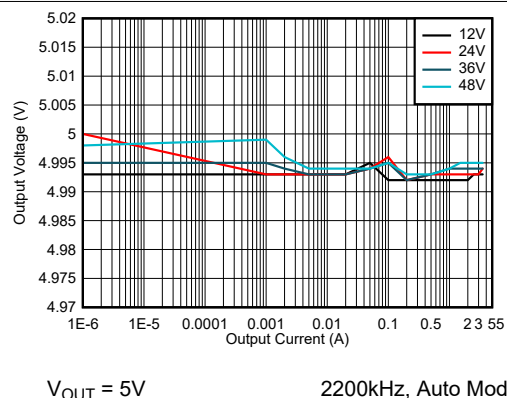


Figure 3-2. Line and Load Regulation

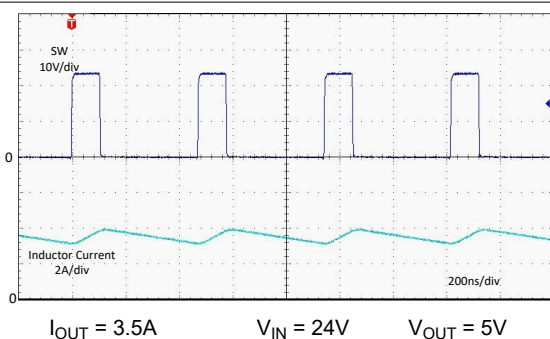


Figure 3-3. Typical Switching Waveform in PWM

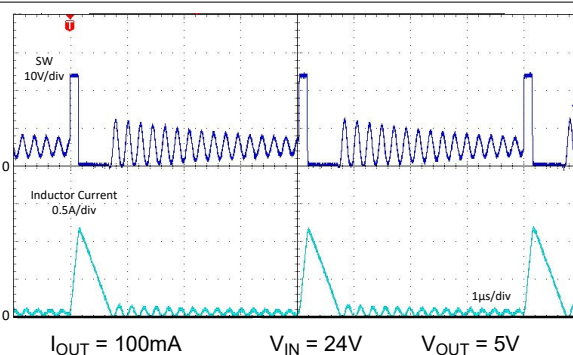


Figure 3-4. Typical Switching Waveform in PFM

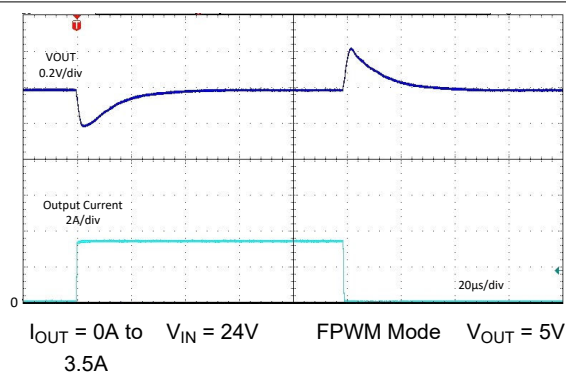


Figure 3-5. Load Transient

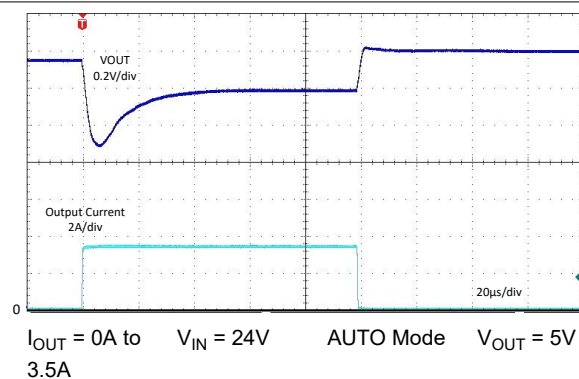


Figure 3-6. Load Transient

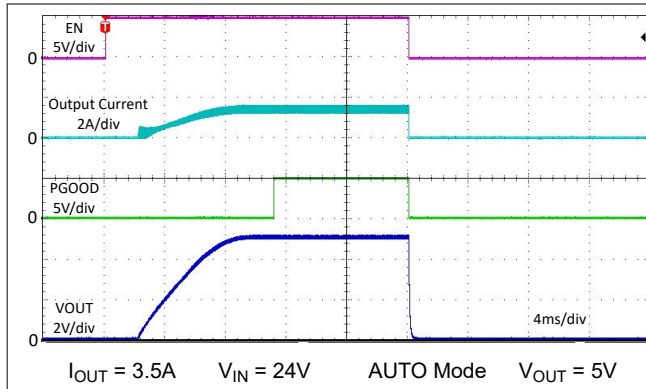
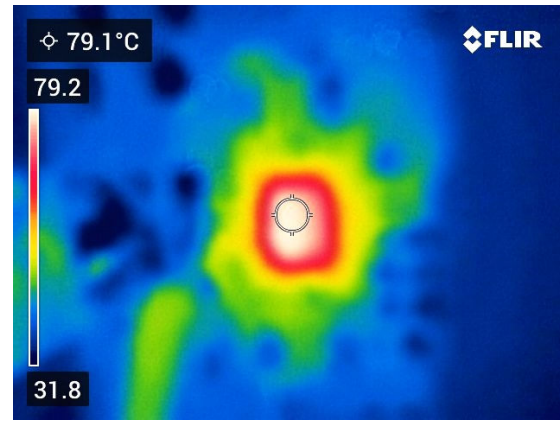


Figure 3-7. Start Up



$V_{OUT} = 5V$ $V_{IN} = 24V$ $I_{OUT} = 3.5A$

Figure 3-8. Thermal Image

4.2 PCB Layouts

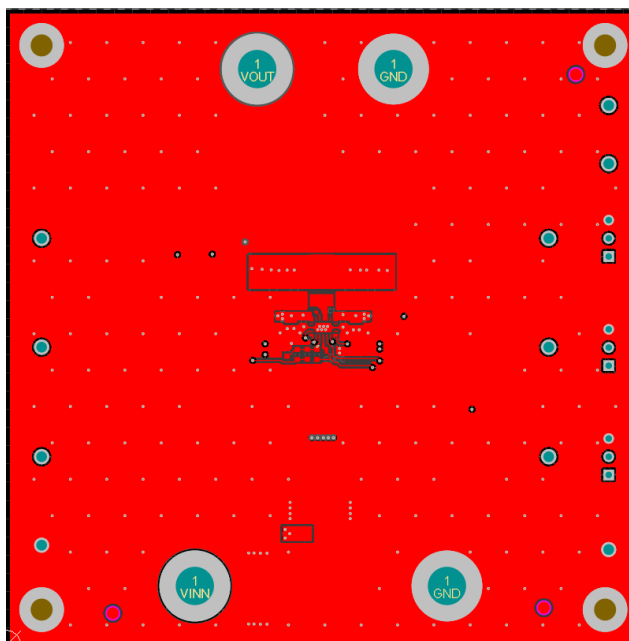


Figure 4-2. PCB Top Layer

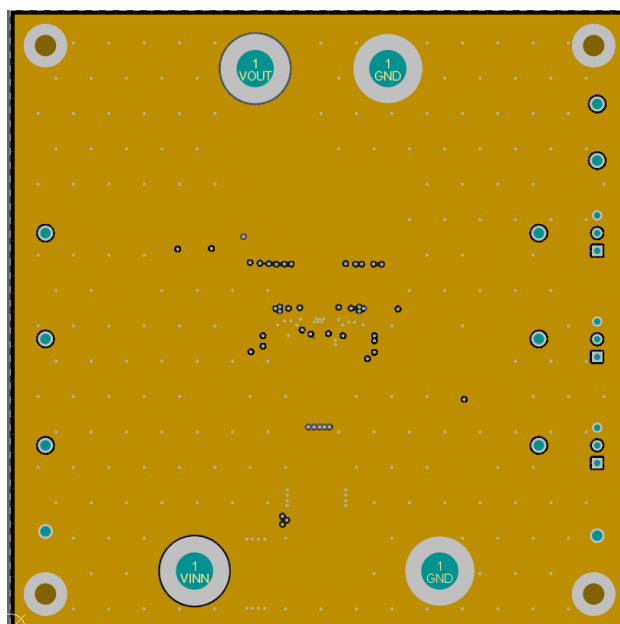


Figure 4-3. PCB Ground Layer (Directly Below Top Layer)

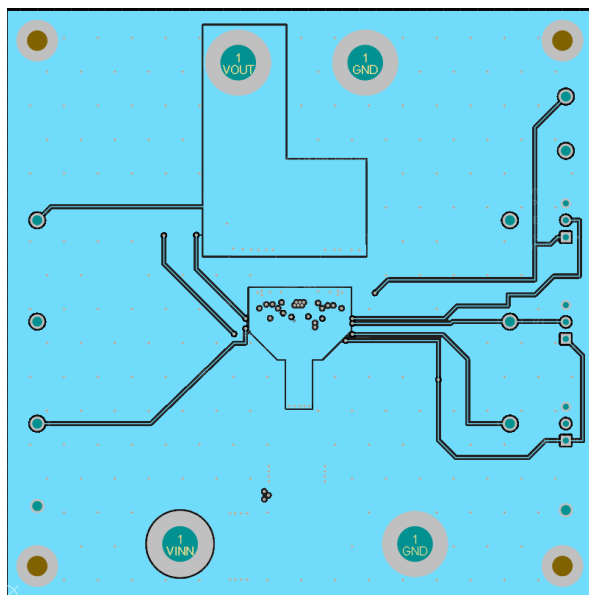


Figure 4-4. PCB Signal Layer

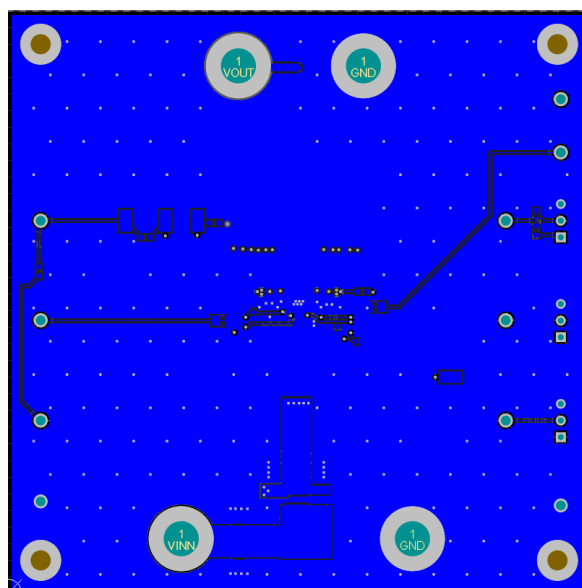


Figure 4-5. PCB Bottom Layer

4.3 Bill of Materials (BOM)

Table 4-1. LM65635EVM BOM (with options)

Designator	Alias	QTY	Value	Description	Part Number
C3, C4		2	2.2μF	Chip Multilayer Ceramic Capacitor for General Purpose 2.2μF ±20% 100V X7T SMD 0805	GRM21BD72A225ME01K
C5		1	33μF	33μF 80V Aluminum - Polymer Capacitors Radial, Can - SMD 45mOhm 2000 Hrs at 105°C	A767KS336M1KLAE045
C6, C7		2	0.1μF	CAP, CERM, 0.1μF, 100V, +/- 10%, X7R, AEC-Q200 Grade 1, 0603	HMK107B7104KAHT
C8, C9		2	4.7μF	4.7μF ±10% 100V Ceramic Capacitor X7S 1206 (3216 Metric)	GRM31CC72A475KE11L
C10	CVCC	1	1μF	Cap Ceramic 1μF 16V X7R 20% Pad SMD 0603 +125°C Automotive T/R	CGA3E1X7R1C105M080AC
C12	CBOOT	1	0.1μF	CAP, CERM, 0.1μF, 25V, +/- 20%, X7R, 0402	C1005X7R1E104M050BB
C13, C14		2	22μF	Chip Multilayer Ceramic Capacitors for General Purpose, 1206, 22μF, X7R, 15%, 20%, 16V	GRM31CZ71C226ME15L
C17		1	100nF	Chip Multilayer Ceramic Capacitors for General Purpose, 0402, 0.10μF, X7R, 15%, 10%, 25V	GRM155R71E104KE14J
L1		1	2.2μH	Inductor, Shielded Drum Core, Powdered Iron, 2.2μH, 8A, 0.018 ohm, SMD	IHLP2525CZER2R2M01
R1	Rpg	1	100k	RES, 100 k, 1%, 0.1 W, 0603	RC0603FR-07100KL
R2	Rvin	1	0	RES, 0, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	RMCF0603ZT0R00
R7	Rfbt2	1	0	RES, 0, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	RMCF0603ZT0R00
R11	Rbias	1	0	RES, 0, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	RMCF0603ZT0R00
R12		1	0	RES, 0, 1%, 0.5 W, 1206	5108
U1		1		Synchronous Step-Down Voltage Regulator, WQFN-FCRLF20	LM65635SRZTRQ1
C1, C2		0	2.2μF	Chip Multilayer Ceramic Capacitor for General Purpose 2.2μF ±20% 100V X7T SMD 0805	GRM21BD72A225ME01K
C11	Cbias	0	0.1μF	CAP, CERM, 0.1μF, 25V, +/- 10%, X7R, 0603	06033C104KAT2A
C15, C16		0	22μF	CAP, CERM, 22μF, 16V, +/- 20%, X7R, AEC-Q200 Grade 1, 1210	CGA6P1X7R1C226M250AC
C18	Cff	0	3pF	CAP, CERM, 3pF, 50V, +/- 8.3%, C0G/NP0, 0603	C0603C309C5GACTU
L2		0	1.5μH	Shielded Power Inductors 1.5μH 10.2A 10.5mOhm Max Nonstandard	XGL4030-152MEC
R3	Rent	0	100k	RES, 100 k, 1%, 0.1 W, 0603	RC0603FR-07100KL
R9	Rfbt	0	100k	RES, 100 k, 1%, 0.1 W, 0603	RC0603FR-07100KL
R4	Renb	0	18.7k	RES, 18.7 k, 1%, 0.1 W, 0603	RC0603FR-0718K7L
R5	RT	0	14.3k	RES, 14.3 k, 1%, 0.1 W, 0603	RC0603FR-0714K3L
R6	Rmode	0	150k	150kΩ ±1% 0.1W 0603 Thick Film Chip Resistor AEC-Q200 compliant	RMCF0603FT150K
R8	Rinj	0	51.0	RES, 51.0, 1%, 0.1 W, 0603	RC0603FR-0751RL
R10	Rfbb	0	0	RES, 0, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	RMCF0603ZT0R00

5 Additional Information

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 - 2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

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Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI's recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI's recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI's instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGRADATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

3 Regulatory Notices:

3.1 United States

3.1.1 Notice applicable to EVMs not FCC-Approved:

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

3.3 Japan

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/sds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。

<https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html>

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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西新宿三井ビル

3.3.3 *Notice for EVMs for Power Line Communication:* Please see http://www.tij.co.jp/sds/ti_ja/general/eStore/notice_02.page

電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。<https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-for-power-line-communication.html>

3.4 European Union

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):*

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

4 EVM Use Restrictions and Warnings:

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 Safety-Related Warnings and Restrictions:

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

5. *Accuracy of Information:* To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

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