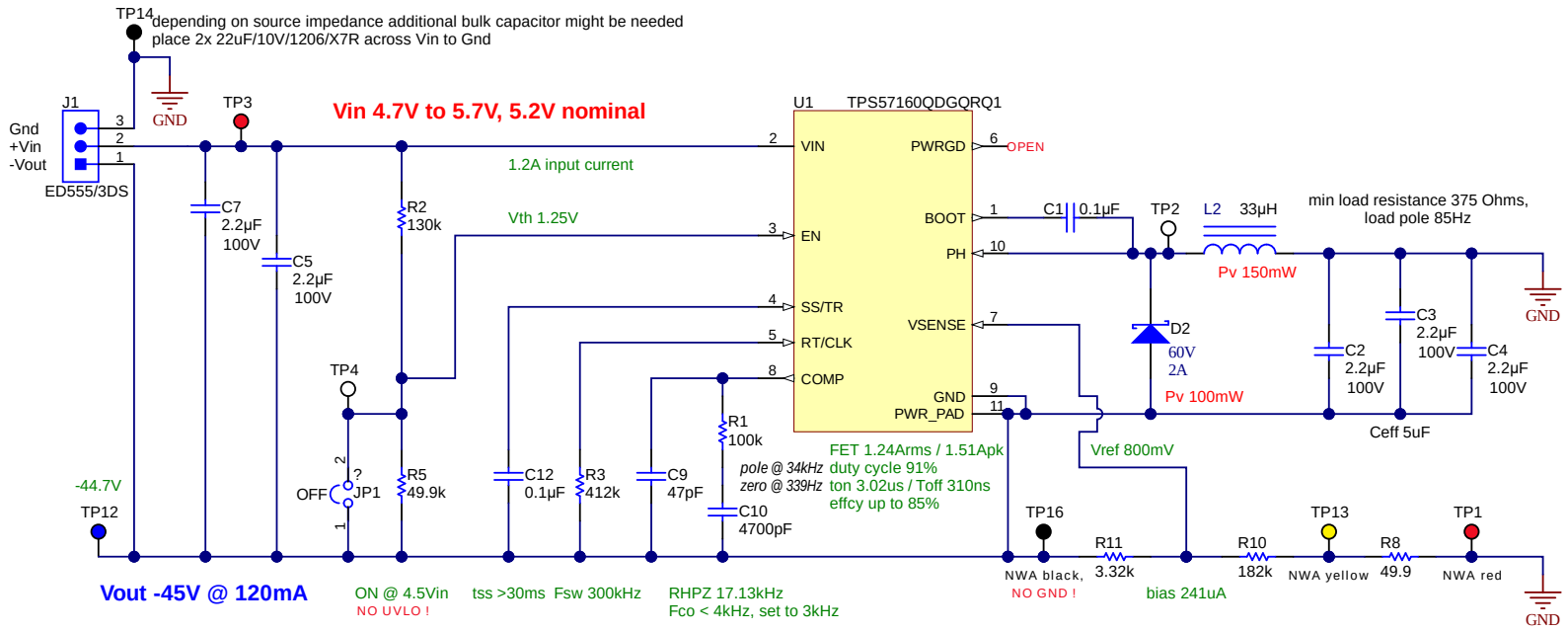

NOTES:

1) built on PCB PMP2763 RevA
2) inverting BuckBoost, refer to board connection
3) R8 for test purposes only
4) TP1/TP13/TP16 to connect Network Analyzer
5) stress analysis done for min. input voltage 4.7V
6) inductor XGL-6060-333 (33uH) offers low board space and reasonable efficiency

ASSEMBLY:

1) R7 SHORT
2) C8, C11 open
3) R4, R6, R9 open
4) L1, D1 open
5) TP5, TP7, TP8, TP11, TP15 open
6) C2, C3, C4, C5, C7 will not meet pattern
7) D2 - SOD-123 on SMB pattern
8) pattern L2 does not match for 6060 series

board connection for inverting BB:

1 = negative output

2 = positive input

3 = ground

Jack #1

1	2	3

Revision History

Revision	Notes
A	LMR16030
B	TPS57160
C	tuned loop to bw 3kHz

Orderable:		Designed for: Public Release	Mod. Date: 3/31/2022
TID #:		Project Title: 5W inverting BuckBoost	
Number: PMP30942	Rev: C	Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: [No Variations]	Sheet: 1 of 1
Drawn By:		File: PMP30942RevC_Schematic.SchDoc	Size: A4
Engineer: B.Geck		Contact: http://www.ti.com/support	

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