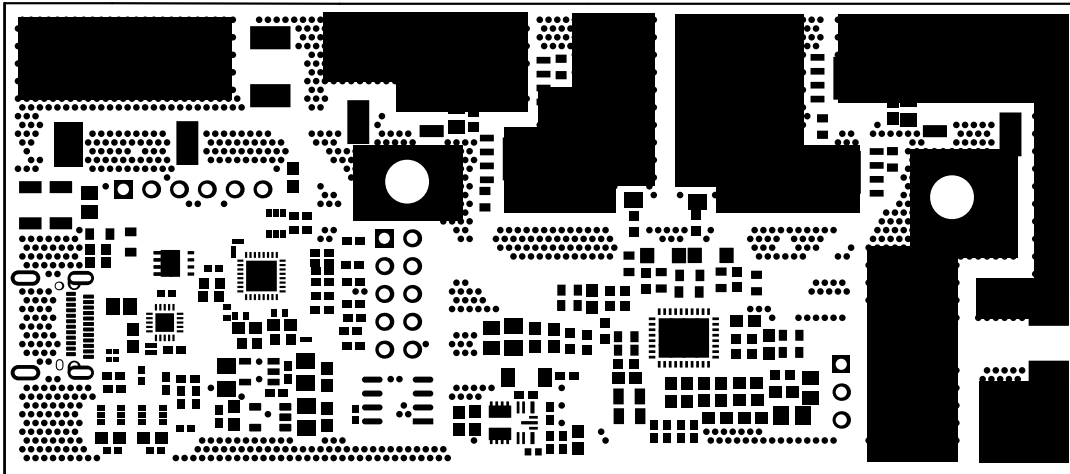
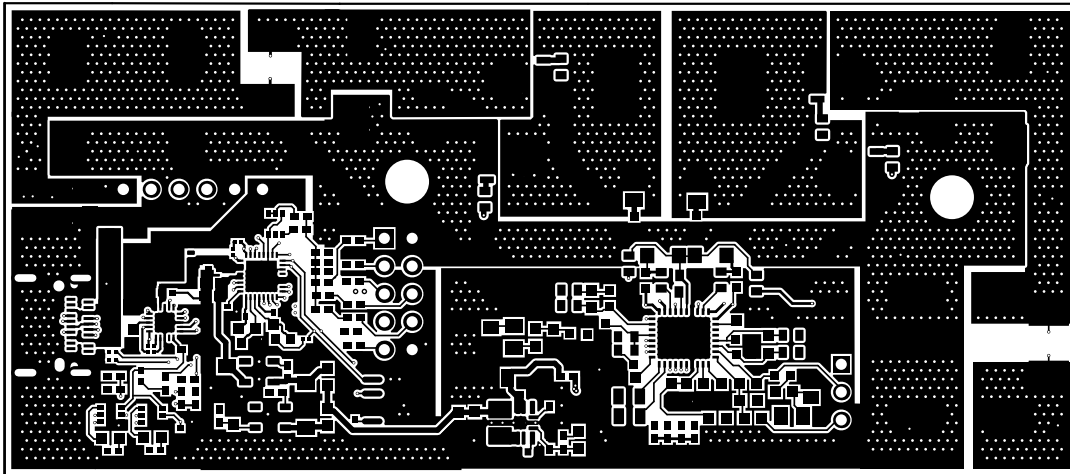


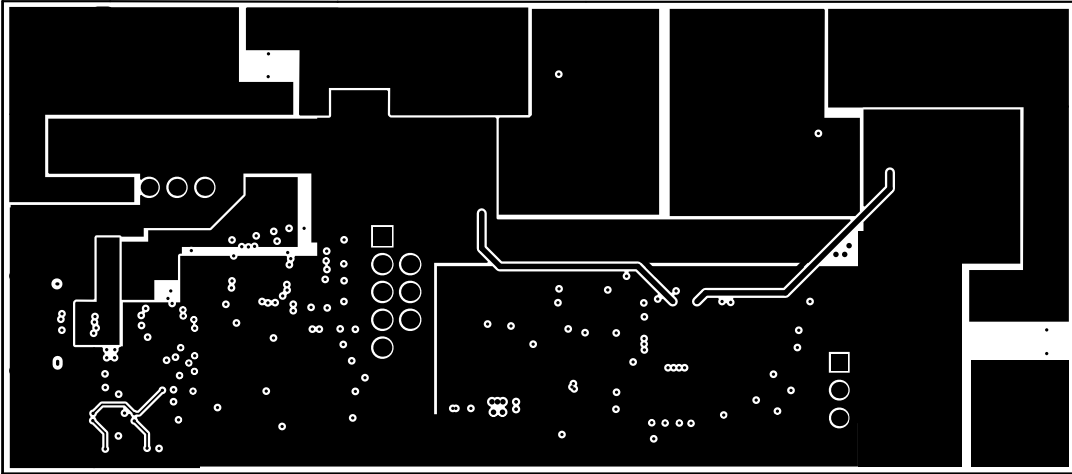
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41115	REV:	SVN REV: Not in version control
LAYER NAME = Top Overlay	TID #: N/A		
PLOT NAME = Top Overlay	GENERATED : 10/3/2024 3:08:36 PM		TEXAS INSTRUMENTS



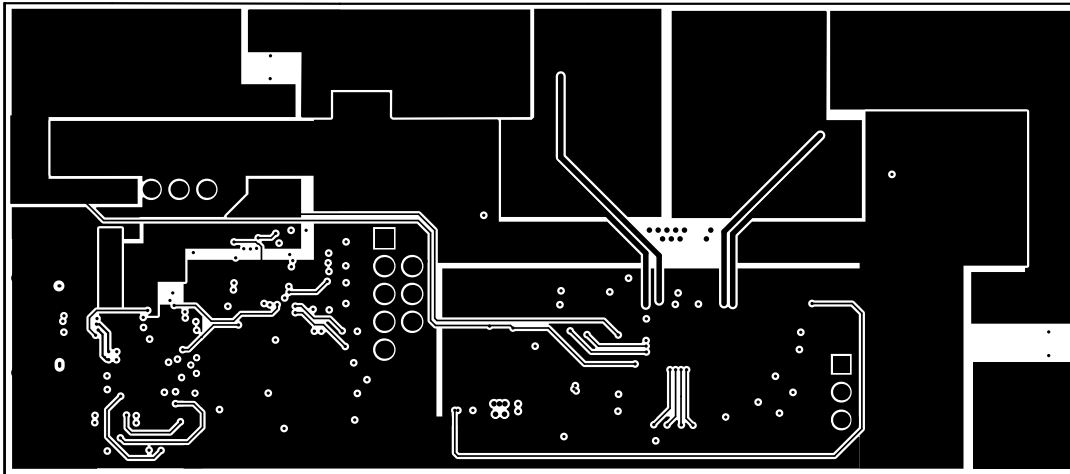
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41115	REV:	SVN REV: Not in version control
LAYER NAME = Top Solder	TID #: N/A		
PLOT NAME = Top Solder Mask	GENERATED : 10/3/2024 3:08:36 PM	TEXAS INSTRUMENTS	



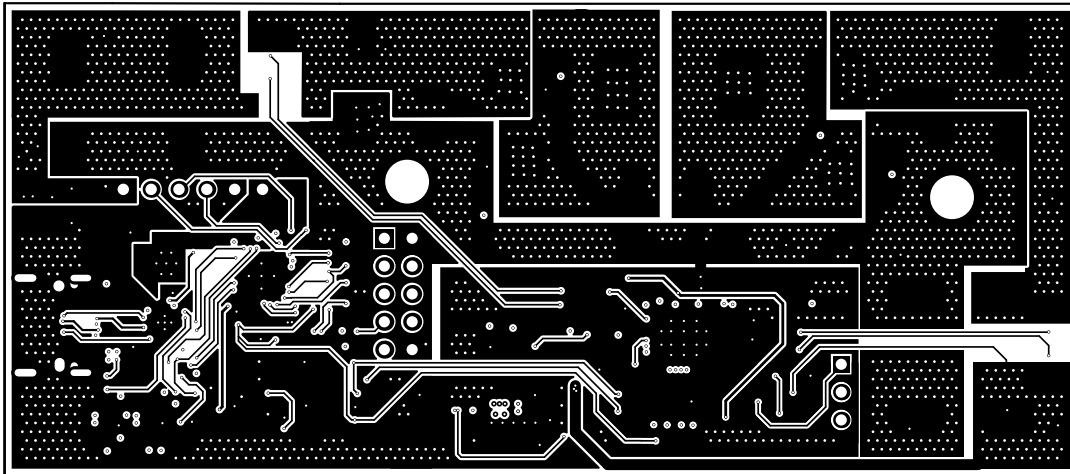
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41115	REV:	SVN REV: Not in version control
LAYER NAME = Top Layer	TID #: N/A		
PLOT NAME = Top Layer	GENERATED : 10/3/2024 3:08:37 PM	TEXAS INSTRUMENTS	



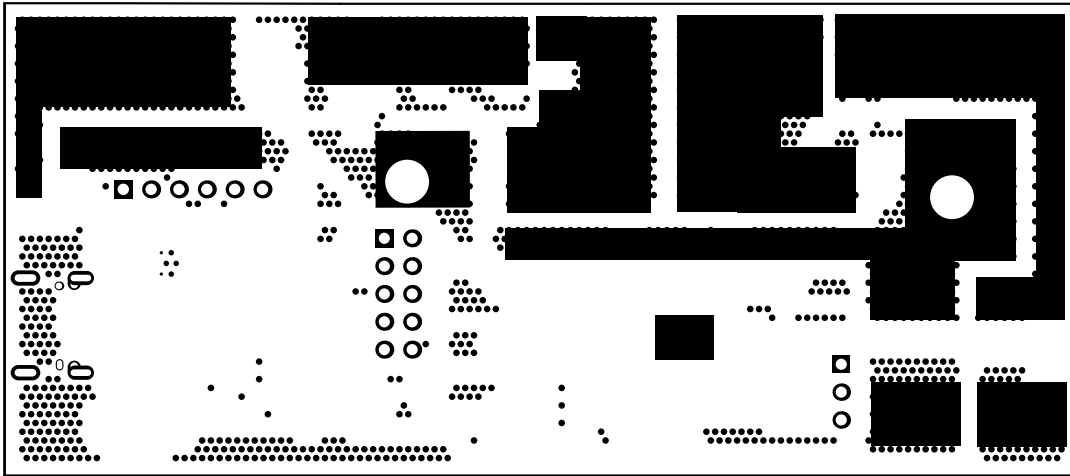
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41115	REV:	SVN REV: Not in version control
LAYER NAME =	TID #: N/A		
PLOT NAME = Signal 1	GENERATED : 10/3/2024 3:08:37 PM	TEXAS INSTRUMENTS	



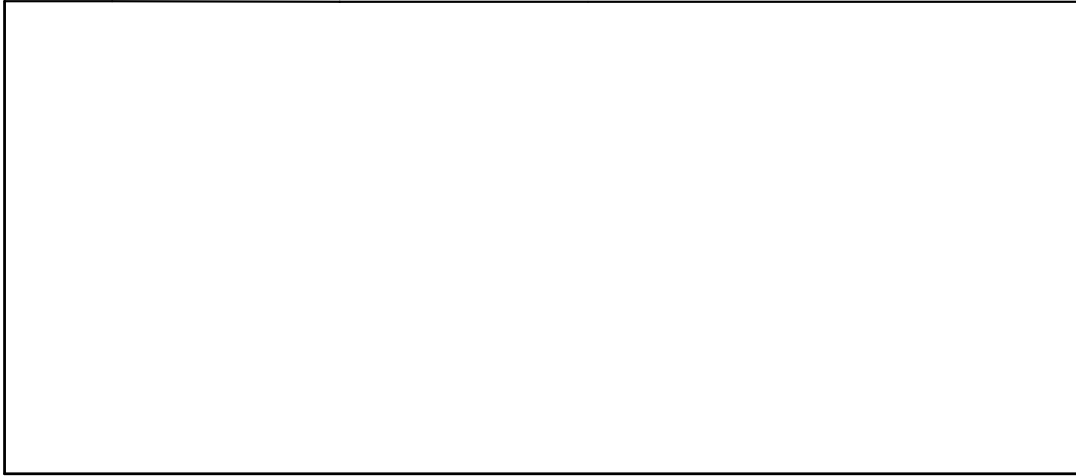
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41115	REV:	SVN REV: Not in version control
LAYER NAME =	TID #: N/A		
PLOT NAME = Signal 2	GENERATED : 10/3/2024 3:08:38 PM	TEXAS INSTRUMENTS	



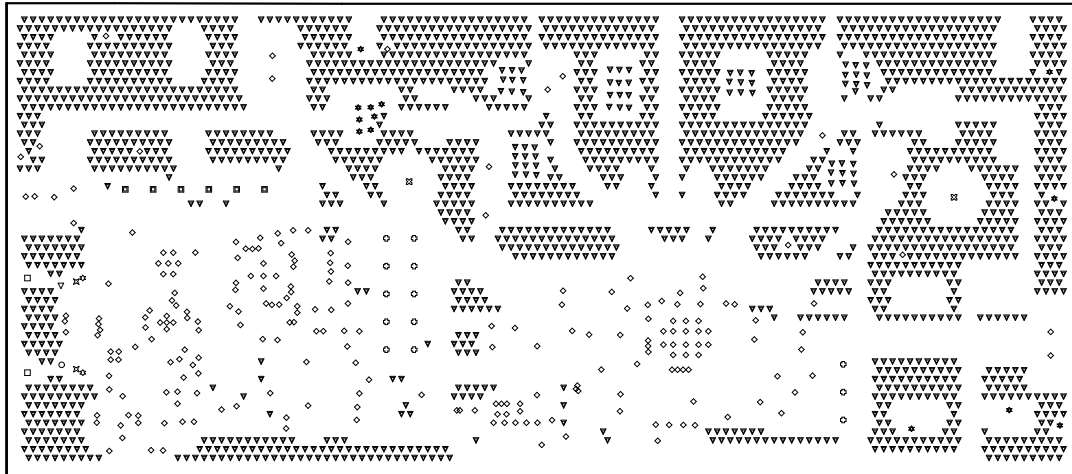
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41115	REV:	SVN REV: Not in version control
LAYER NAME = Bottom Layer	TID #: N/A		
PLOT NAME = Bottom Layer	GENERATED : 10/3/2024 3:08:38 PM	TEXAS INSTRUMENTS	



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41115	REV:	SVN REV: Not in version control
LAYER NAME = Bottom Solder	TID #: N/A		
PLOT NAME = Bottom Solder Mask	GENERATED : 10/3/2024 3:08:38 PM	TEXAS INSTRUMENTS	



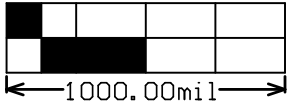
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41115	REV:	SVN REV: Not in version control
LAYER NAME = Bottom Overlay	TID #: N/A		
PLOT NAME = Bottom Overlay	GENERATED : 10/3/2024 3:08:39 PM	TEXAS INSTRUMENTS	



Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
▽	1	23.62mil (0.600mm)	NPTH	Round	Top Layer - Bottom Layer	+/-1.18mil
◇	238	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
✱	13	8.00mil (0.203mm)	PTH	Round	Top Layer - Bottom Layer	
▽	2208	9.84mil (0.250mm)	PTH	Round	Top Layer - Bottom Layer	
⊕	13	40.00mil (1.016mm)	PTH	Round	Top Layer - Bottom Layer	
⊞	6	40.95mil (1.040mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	2	157.48mil (4.000mm)	PTH	Round	Top Layer - Bottom Layer	
○	1	19.69mil (0.500mm)	NPTH	Slot	Top Layer - Bottom Layer	+/-1.18mil
⊗	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer	+/-1.97mil
⊗	2	27.56mil (0.700mm)	PTH	Slot	Top Layer - Bottom Layer	+/-1.97mil
□	2	27.56mil (0.700mm)	PTH	Slot	Top Layer - Bottom Layer	+/-1.97mil
	2488 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41115	REV:	SUN REV: Not in version control
LAYER NAME = Drill Drawing	TID #: N/A		
PLOT NAME = Drill Drawing	GENERATED : 10/3/2024	3:08:39 PM	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41115	REV:	SVN REV: Not in version control
LAYER NAME = M2 Board Dimensions	TID #: N/A		
PLOT NAME = Board Dimensions	GENERATED : 10/3/2024 3:08:41 PM	TEXAS INSTRUMENTS	

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2024, Texas Instruments Incorporated