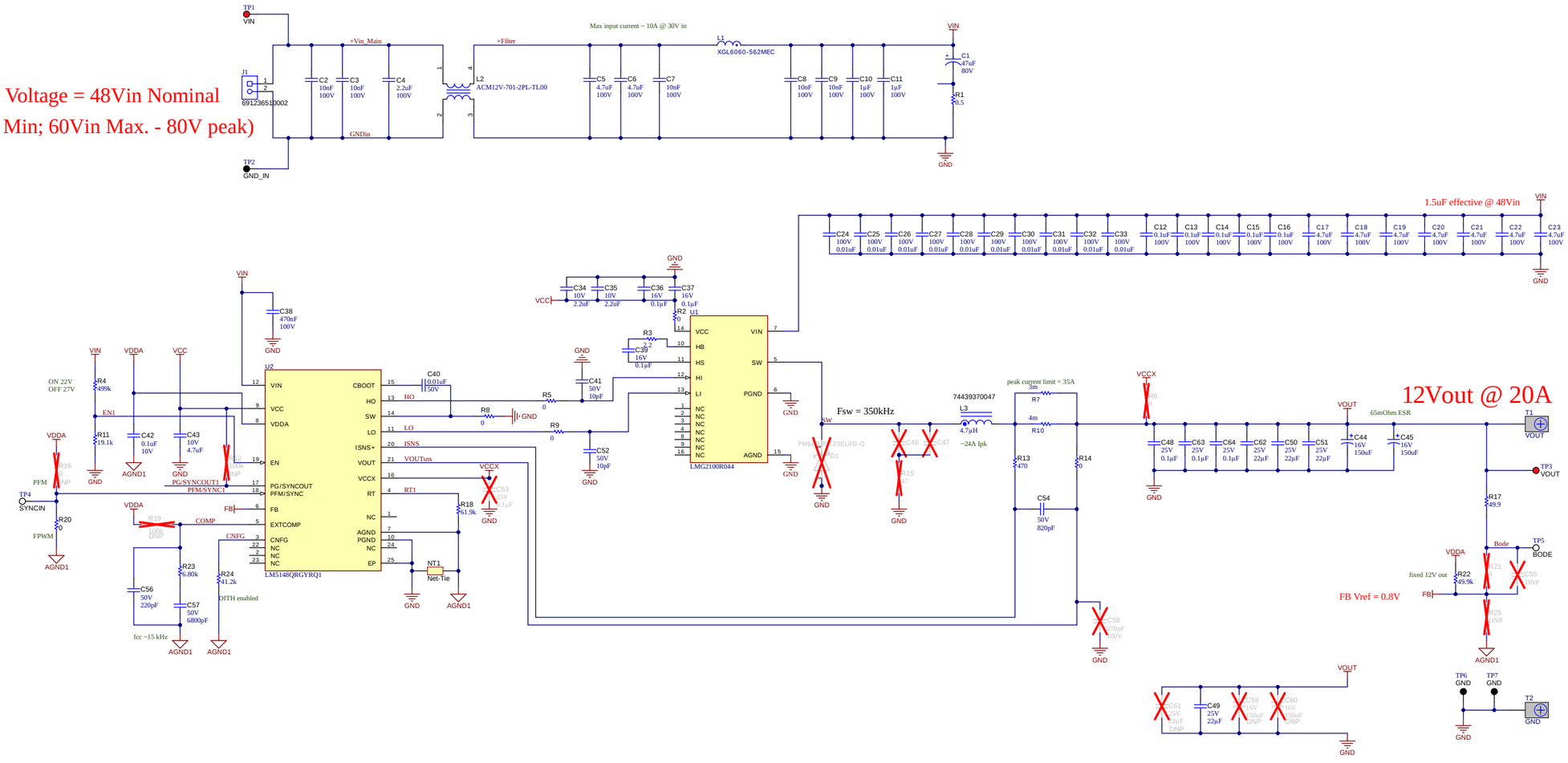
Input Voltage = 48Vin Nominal
(30Vin Min; 60Vin Max. - 80V peak)



DESIGN NOTES:

- Output capacitors and inductor have to be optimized according to ripple, load transient, rms currents, thermals
- Compensation network / snubbers have to be optimized in the lab
- The design has to be optimized according to customers' requirements

Orderable: ChangeMe in variant		Designed for: Public Release	Mod. Date: 11/27/2024
TID #: N/A		Project Title: 240W GaN Board	
Number: PMP31349	Rev: B	Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 1 of 2
Drawn By:		File: PMP31349.SchDoc	Size: C
Engineer: S.Panaro		Contact: http://www.ti.com/support	

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Logo1

CAUTION HOT SURFACE

Logo2

CAUTION HOT SURFACE

LOGO3

DANGER HIGH VOLTAGE

PCB
LOGO
WEEE logo

Orderable: ChangeMe in variant		Designed for: Public Release	Mod. Date: 11/12/2024
TID #: N/A		Project Title: 240W GaN Board	
Number: PMP31349	Rev: B	Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 2 of 2
Drawn By:		File: PMP31349_Hardware.SchDoc	Size: B
Engineer: S.Panaro		Contact: http://www.ti.com/support	

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