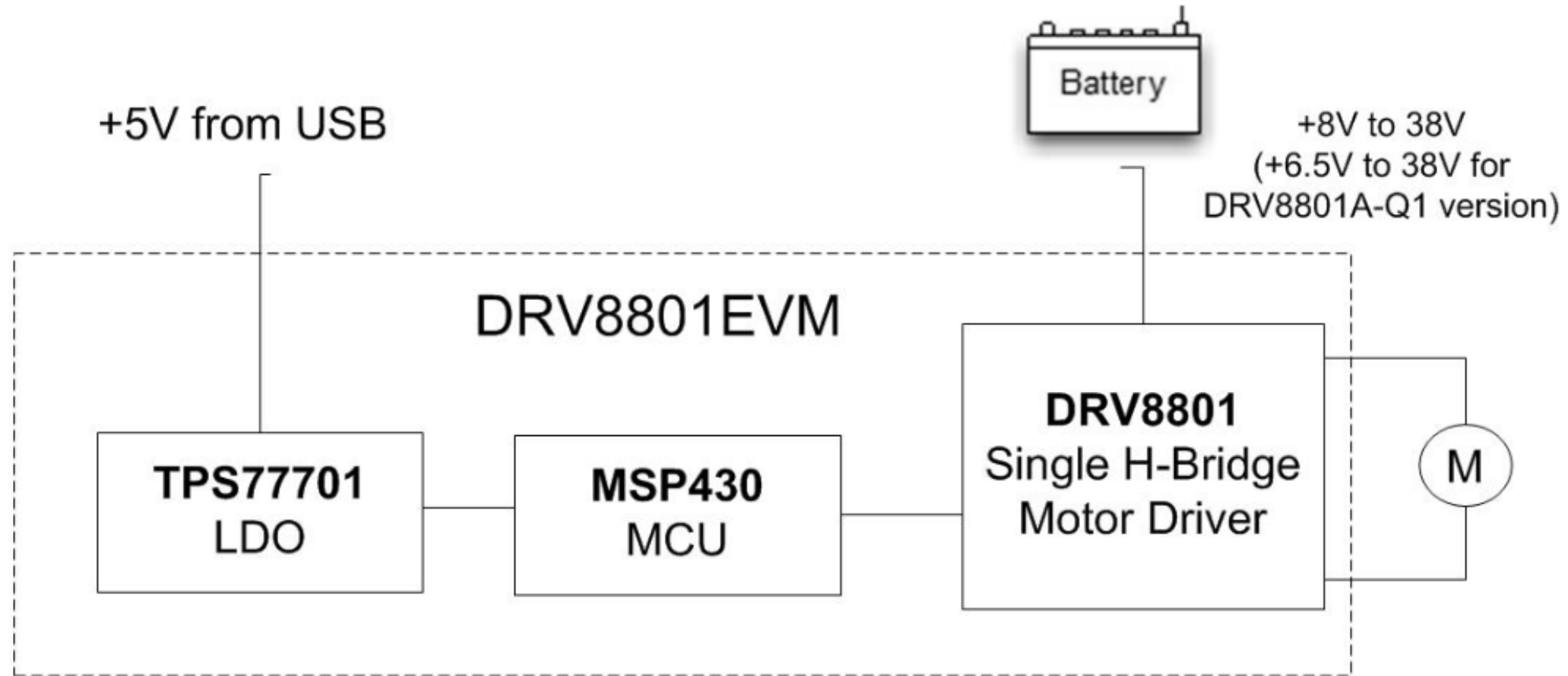
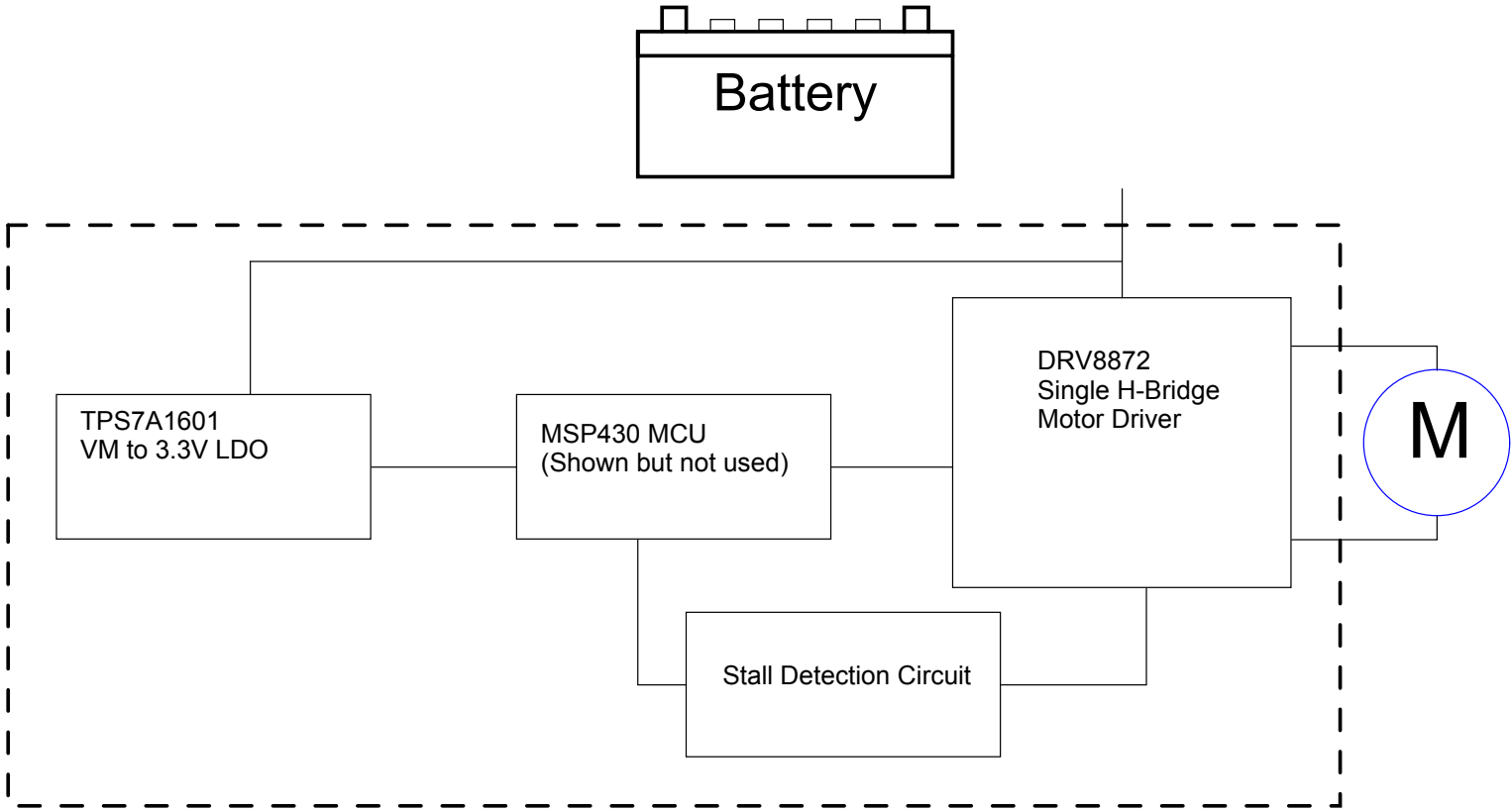
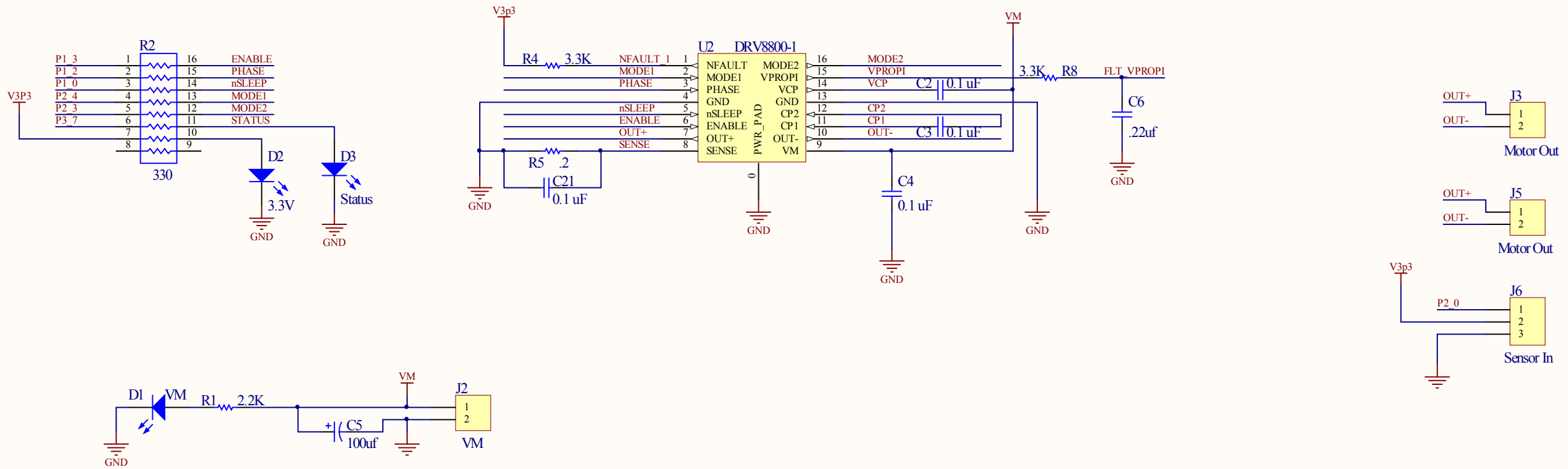
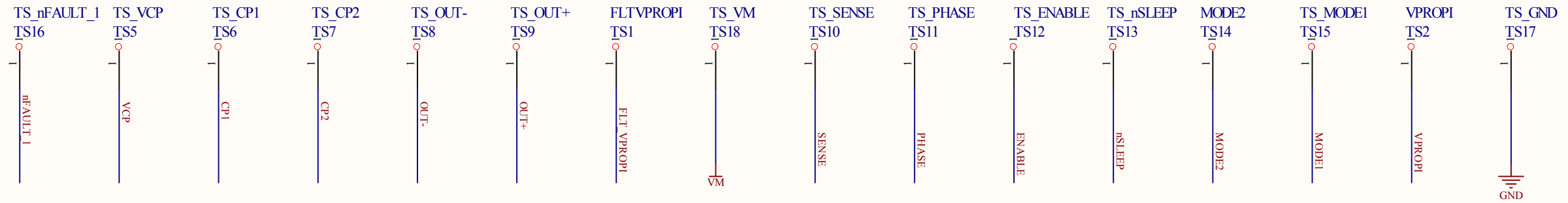


Automotive Brushed Motor Driver for Folding Mirrors Block Diagram



Automotive Brushed Motor Driver for Folding Mirrors Block Diagram



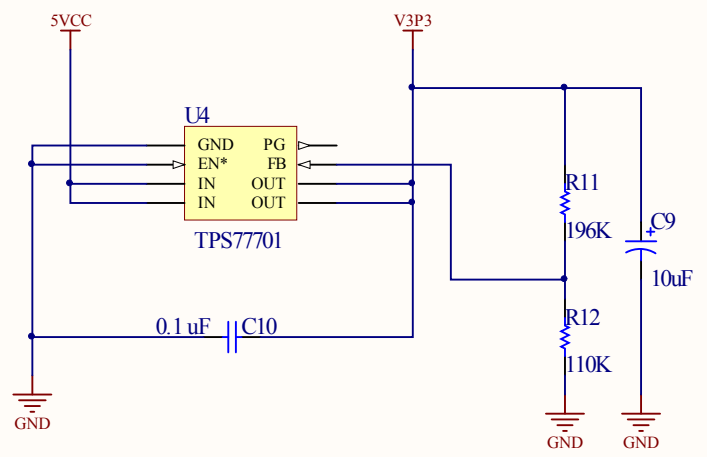
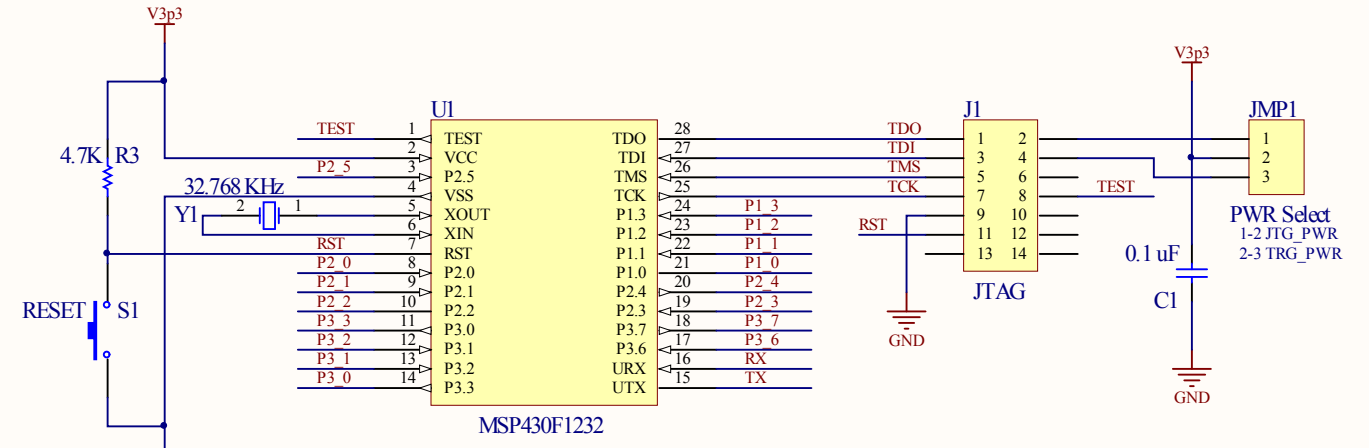
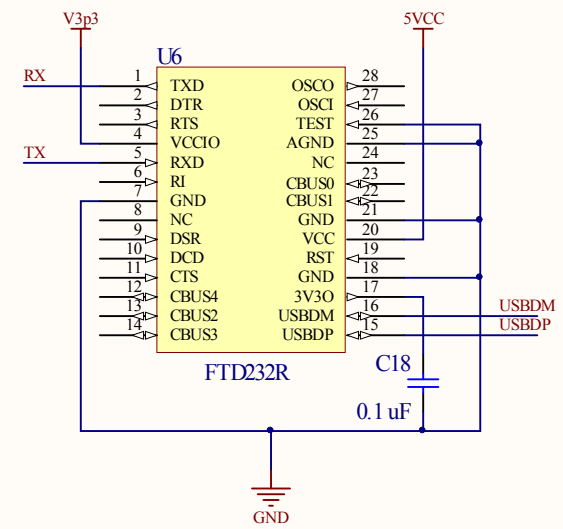
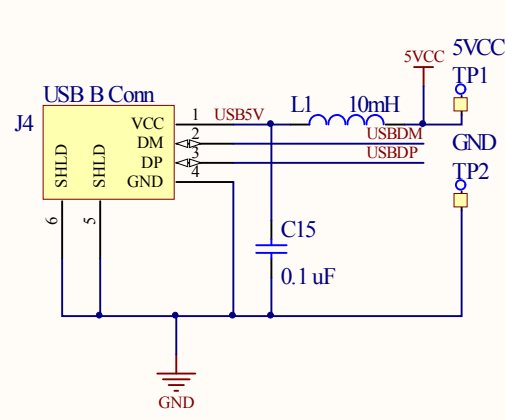


DRV8801EVM-001
Input Voltage VM: 8V to 38V

Texas Instruments

DRV8801EVM-001 Single DC Motor Driver

Size B	FCSM No.	DWG No. CPG002	Rev A
Scale	Sheet 1 of 2		



CPG002 DRV8801 EVM

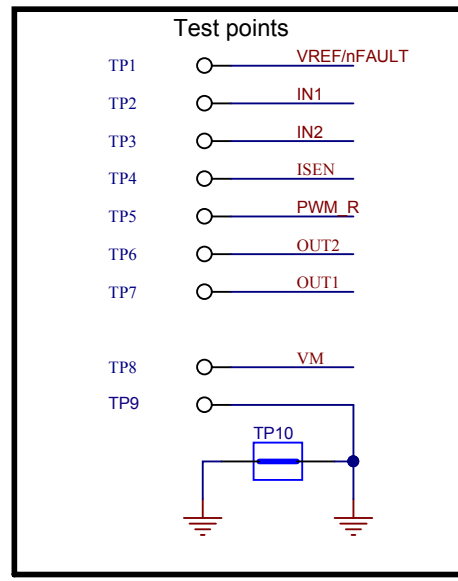
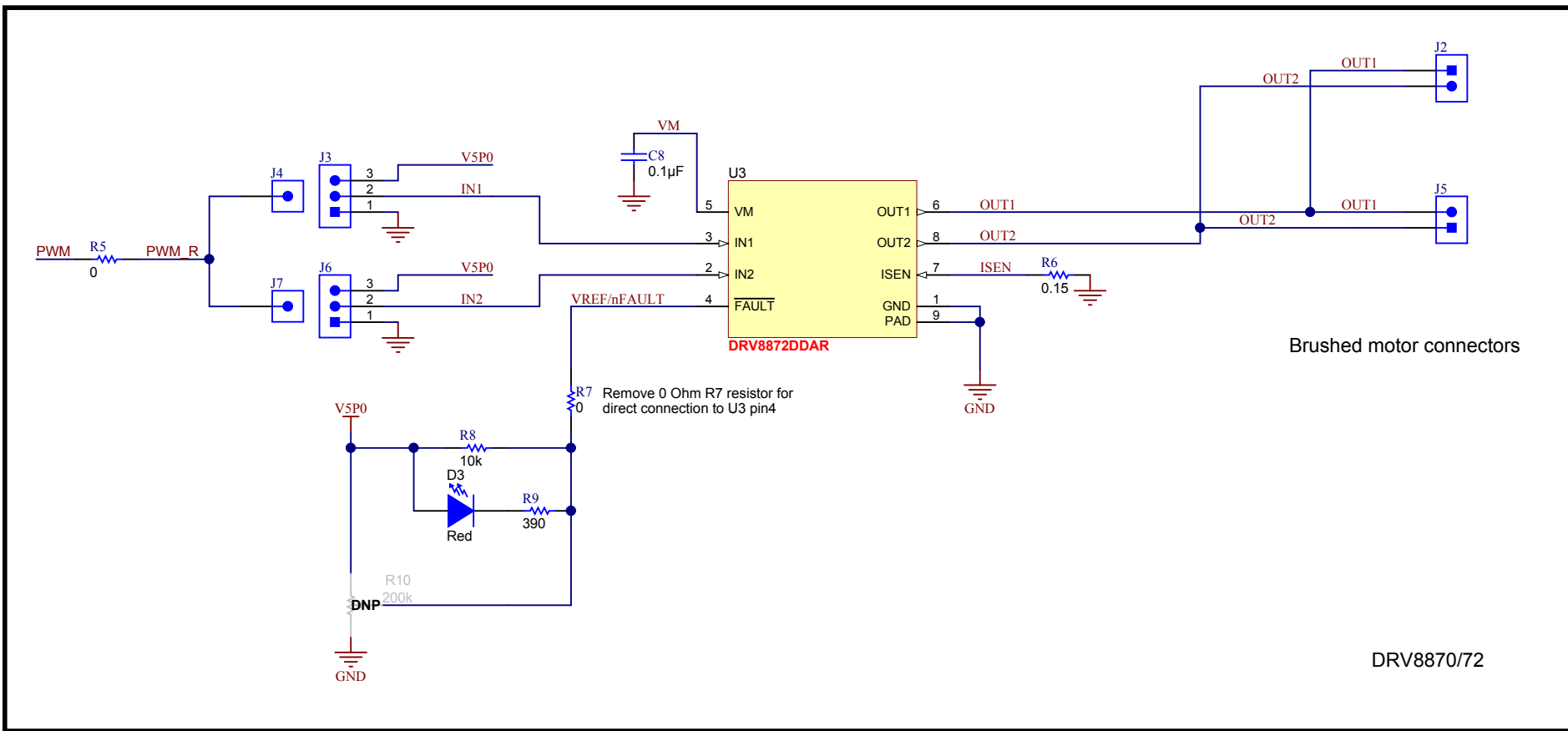
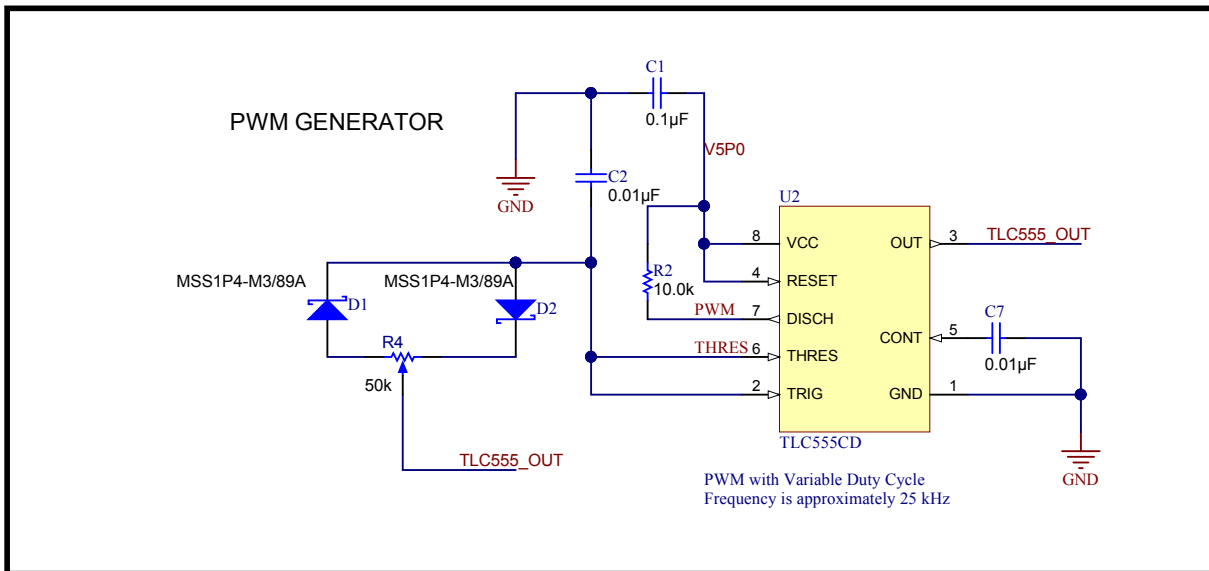
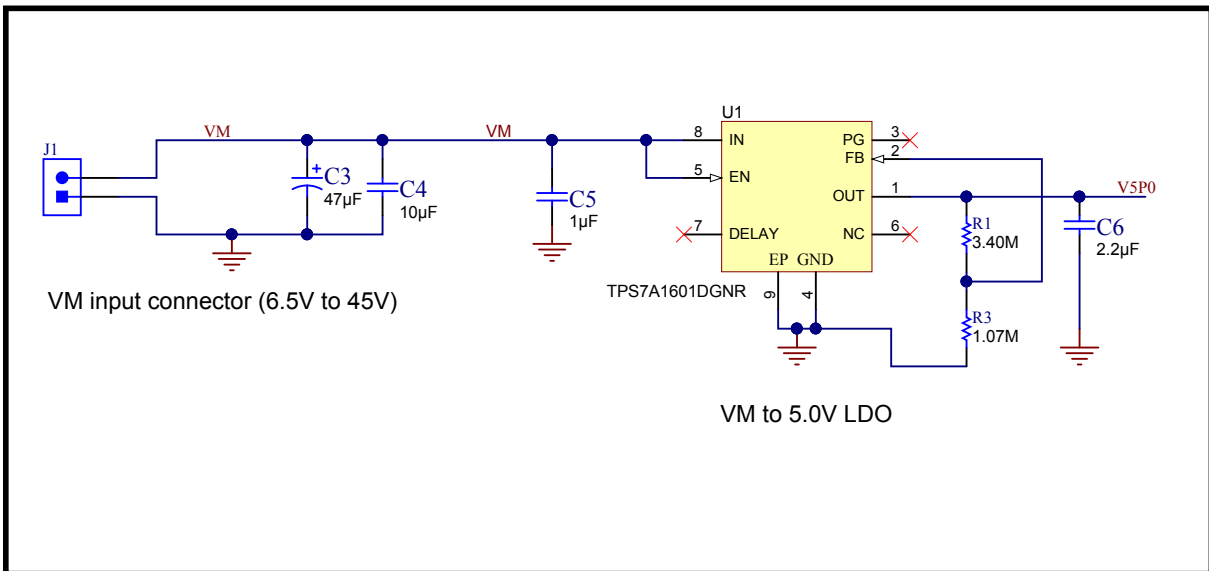


DRV8801EVM-001
Input Voltage VM: 8V to 38V

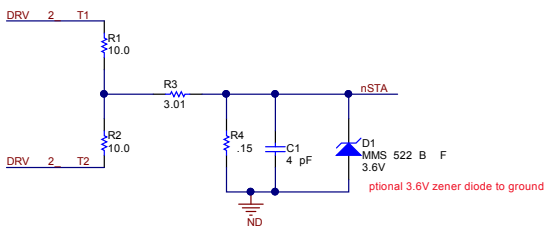
Texas Instruments

DRV8801EVM-001 Single DC Motor Driver

Size B	FCSM No.	DWG No. CPG002	Rev A
Scale	Sheet 2 of 2		



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.



Stall detection circuit

During normal operation, the motor will run with one output high and the other low. This will create a logic high as an input to the mcu.

At startup and stall, the current regulation circuit will activate. This will create a periodic logic low as an input to the mcu. This high to low transition can be used as an interrupt to the mcu to signal a possible stall.

At 10. V, nSTA toggles from 0 to 2.5V
 At 14.4V nSTA toggles from 0 to 3.3V

Additional protection circuitry can include a zener diode from nSTA to ground shown below

Orderable: EVM_orderable	Designed for: PRJ_Customer	Mod. Date: 11/ /2016	 TEXAS INSTRUMENTS http://www.ti.com <small>© Texas Instruments Copyright 2016</small>
TID: TID	Project Title: PRJ Title		
Number: PRJ_Number Rev: SCH	Sheet Title:		
Rev: Version control disabled	Assembly Variant: No Variations	Sheet: 2 of 3	
Drawn By: PRJ_Engineer	File: Stall_Detector_SchDoc	Size: B	Contact: TechSupport

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your applic.

H1 SJ-5303 (CLEAR) H2 SJ-5303 (CLEAR) H3 SJ-5303 (CLEAR) H4 SJ-5303 (CLEAR)



DNP FID1 DNP FID2 DNP FID3



PCB Number: MDBU006
PCB Rev: A

PCB LOGO Texas Instruments PCB LOGO Pb-Free Symbol PCB LOGO FCC disclaimer

Label Table

Variant	Label Text
001	DRV8870EVM
002	DRV8872EVM

LBL1
PCB Label
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ5
Assembly Note
Shorting plug TP10 shall be raised to minimum height of 250mils above PCB

Orderable: DRV8872EVM	Designed for: Public Release	Mod. Date: 7/17/2015	<p>TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments 2015</p>
TID #: N/A	Project Title: DRV8870/2EVM		
Number: MDBU006	Rev: A	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: 002	Sheet 3 of 3	
Drawn By:	File: MDBU006A_Hardware_SchDoc	Size: B	
Engineer: Rick Duncan	Contact: http://www.ti.com/support		

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

IMPORTANT NOTICE FOR TI REFERENCE DESIGNS

Texas Instruments Incorporated ("TI") reference designs are solely intended to assist designers ("Designer(s)") who are developing systems that incorporate TI products. TI has not conducted any testing other than that specifically described in the published documentation for a particular reference design.

TI's provision of reference designs and any other technical, applications or design advice, quality characterization, reliability data or other information or services does not expand or otherwise alter TI's applicable published warranties or warranty disclaimers for TI products, and no additional obligations or liabilities arise from TI providing such reference designs or other items.

TI reserves the right to make corrections, enhancements, improvements and other changes to its reference designs and other items.

Designer understands and agrees that Designer remains responsible for using its independent analysis, evaluation and judgment in designing Designer's systems and products, and has full and exclusive responsibility to assure the safety of its products and compliance of its products (and of all TI products used in or for such Designer's products) with all applicable regulations, laws and other applicable requirements. Designer represents that, with respect to its applications, it has all the necessary expertise to create and implement safeguards that (1) anticipate dangerous consequences of failures, (2) monitor failures and their consequences, and (3) lessen the likelihood of failures that might cause harm and take appropriate actions. Designer agrees that prior to using or distributing any systems that include TI products, Designer will thoroughly test such systems and the functionality of such TI products as used in such systems. Designer may not use any TI products in life-critical medical equipment unless authorized officers of the parties have executed a special contract specifically governing such use. Life-critical medical equipment is medical equipment where failure of such equipment would cause serious bodily injury or death (e.g., life support, pacemakers, defibrillators, heart pumps, neurostimulators, and implantables). Such equipment includes, without limitation, all medical devices identified by the U.S. Food and Drug Administration as Class III devices and equivalent classifications outside the U.S.

Designers are authorized to use, copy and modify any individual TI reference design only in connection with the development of end products that include the TI product(s) identified in that reference design. HOWEVER, NO OTHER LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE TO ANY OTHER TI INTELLECTUAL PROPERTY RIGHT, AND NO LICENSE TO ANY TECHNOLOGY OR INTELLECTUAL PROPERTY RIGHT OF TI OR ANY THIRD PARTY IS GRANTED HEREIN, including but not limited to any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services, or a warranty or endorsement thereof. Use of the reference design or other items described above may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

TI REFERENCE DESIGNS AND OTHER ITEMS DESCRIBED ABOVE ARE PROVIDED "AS IS" AND WITH ALL FAULTS. TI DISCLAIMS ALL OTHER WARRANTIES OR REPRESENTATIONS, EXPRESS OR IMPLIED, REGARDING THE REFERENCE DESIGNS OR USE OF THE REFERENCE DESIGNS, INCLUDING BUT NOT LIMITED TO ACCURACY OR COMPLETENESS, TITLE, ANY EPIDEMIC FAILURE WARRANTY AND ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

TI SHALL NOT BE LIABLE FOR AND SHALL NOT DEFEND OR INDEMNIFY DESIGNERS AGAINST ANY CLAIM, INCLUDING BUT NOT LIMITED TO ANY INFRINGEMENT CLAIM THAT RELATES TO OR IS BASED ON ANY COMBINATION OF PRODUCTS AS DESCRIBED IN A TI REFERENCE DESIGN OR OTHERWISE. IN NO EVENT SHALL TI BE LIABLE FOR ANY ACTUAL, DIRECT, SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF THE REFERENCE DESIGNS OR USE OF THE REFERENCE DESIGNS, AND REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

TI's standard terms of sale for semiconductor products (<http://www.ti.com/sc/docs/stdterms.htm>) apply to the sale of packaged integrated circuit products. Additional terms may apply to the use or sale of other types of TI products and services.

Designer will fully indemnify TI and its representatives against any damages, costs, losses, and/or liabilities arising out of Designer's non-compliance with the terms and provisions of this Notice.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2016, Texas Instruments Incorporated