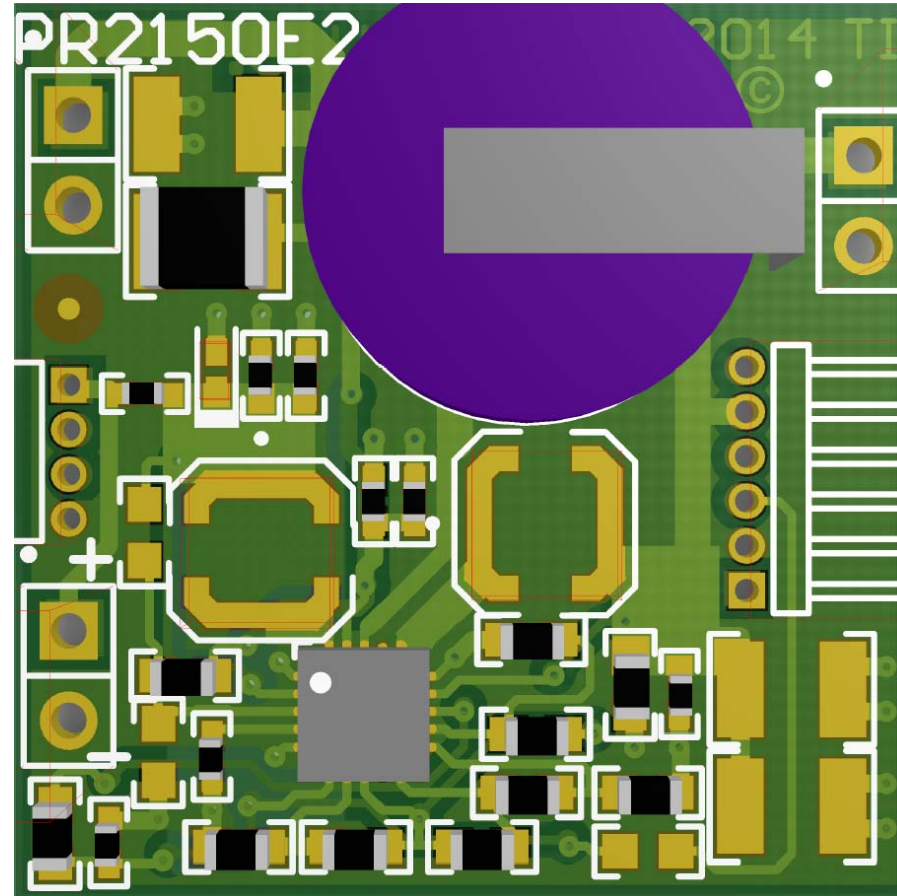
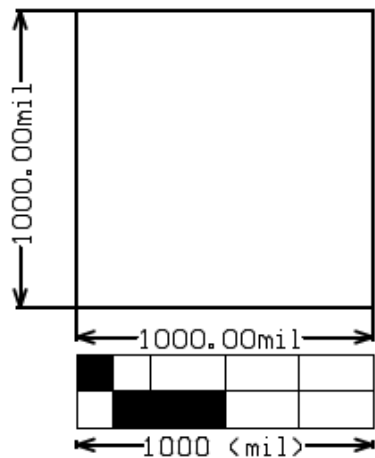


TI Designs: Solar Energy Harvester Schematic and PCB layers



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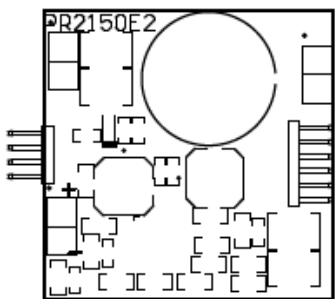
TI Designs: Solar Energy Harvester Schematic and PCB layers



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00242	REV:	SUN REV: Not In VersionControl
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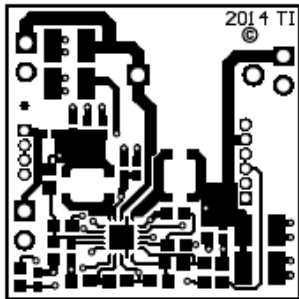
TI Designs: Solar Energy Harvester Schematic and PCB layers



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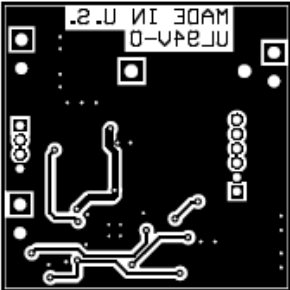
TI Designs: Solar Energy Harvester Schematic and PCB layers



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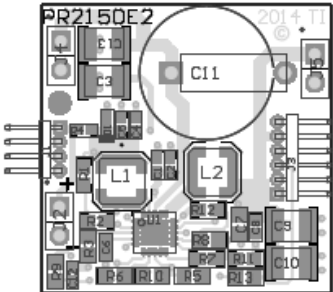
TI Designs: Solar Energy Harvester Schematic and PCB layers



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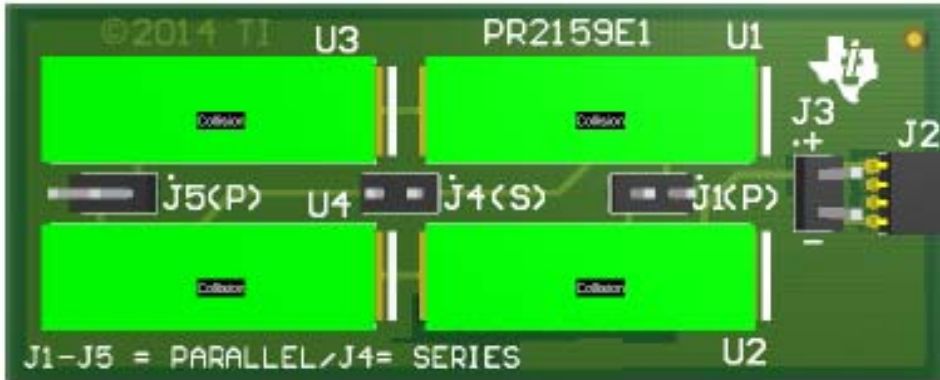
TI Designs: Solar Energy Harvester Schematic and PCB layers



PCB VIEWED FROM TOP SIDE	BOARD #: TIDA-00242	REV:	SUN REV: Not In VersionControl
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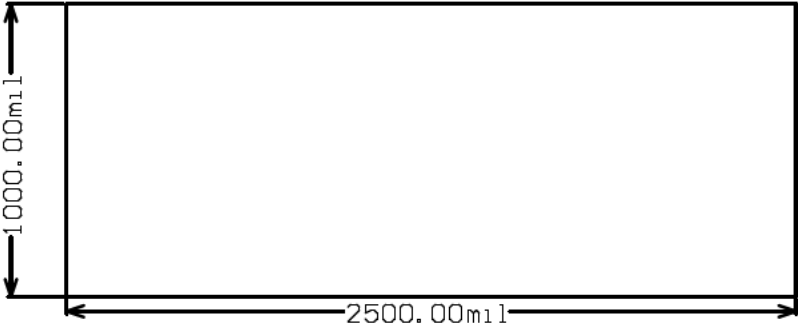
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TI Designs: Solar Source Schematic and PCB layers



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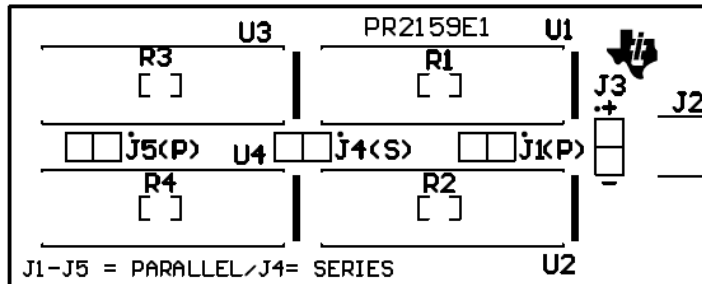
TI Designs: Solar Source Schematic and PCB layers



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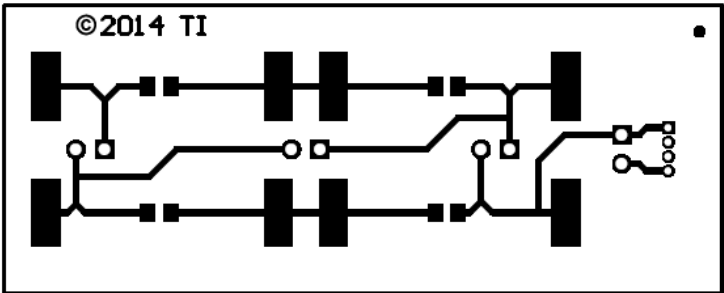
TI Designs: Solar Source Schematic and PCB layers



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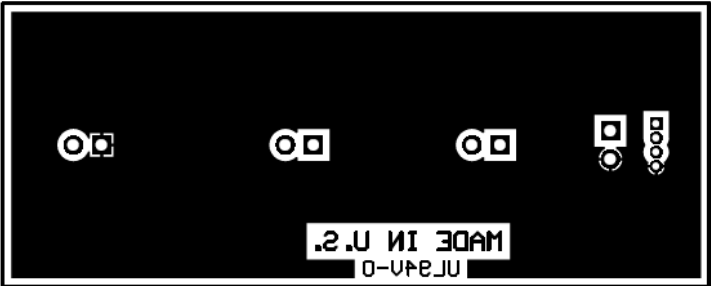
TI Designs: Solar Source Schematic and PCB layers



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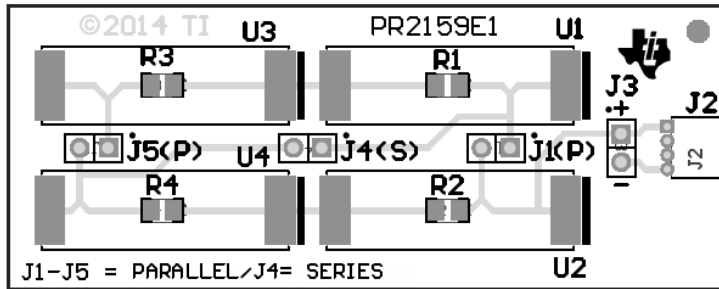
TI Designs: Solar Source Schematic and PCB layers



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00242	SOL REV:	SUN REV: Not In VersionControl
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PLOT NAME = Bottom Layer	GENERATED : 6/2/2014	9:33:27 AM	TEXAS INSTRUMENTS

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TI Designs: Solar Source Schematic and PCB layers



PCB VIEWED FROM TOP SIDE	BOARD #: TIDA-00242	SOL REV:	SUN REV: Not In VersionControl
IGNORE -> Top Overlay			
PLOT NAME = Top Components and Silkscreen	GENERATED : 6/2/2014	9:33:32 AM	TEXAS INSTRUMENTS

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