

Charger Booster Pack schematic



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<p>PCB Number: TIDA-00587 PCB Rev: A</p> <p>PCB LOGO Texas Instruments</p> <p>CAUTION HOT SURFACE</p> <p>PCB LOGO CAUTION: READ USER GUIDE BEFORE USE</p>					
<p>J8: PCB Label Use 3.95" x 3.25" "PR2192E3 Charger Booster Pack"</p>					
<p>J21: Label Assembly Note This Assembly Note is for PCB labels only</p>					
<p>J22: Assembly Note Mating Plug</p>					
<p>J23: Assembly Note Voigen America (WALL POWER SUPPLY) KUS81052 Transformer: Vwall MNT 5V 2A USB</p>					
<p>J24: Assembly Note CBL: USB A/MINI B CON 3' 28/28 AWG Quarter (USB CABLE) 2021002403 CBL: USB A/MINI B CON 3' 28/28 AWG</p>					
<p>J25: Assembly Note J7, J8 Must be inserted from the bottom with the pins facing up. DO NOT CUT PINS.</p>					
<p>J26: Assembly Note Place a 200mil spot of Locktite Output 315 over RT1 and exposed area on the heatsink (You must use the recommended activator)</p>					
<p>Thermal conductive glue, electrically isolating, must cover RT1 and exposed cooper</p>					
<p>Designed for Public Release Mod. Date: 6/22/2018 Project Title: Charger Booster Pack Number: TIDA-00587 Rev: A Sheet Title: Hardware @Hotkey: Version control disabled Assembly: User (No Variations) Sheet 2 of 4 Cable: Gordon Varney PCB: TIDA-00587 - Home Schematic Size: B Engineer: Gordon Varney Contact: http://www.ti.com/support © Texas Instruments 2018</p>					
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