

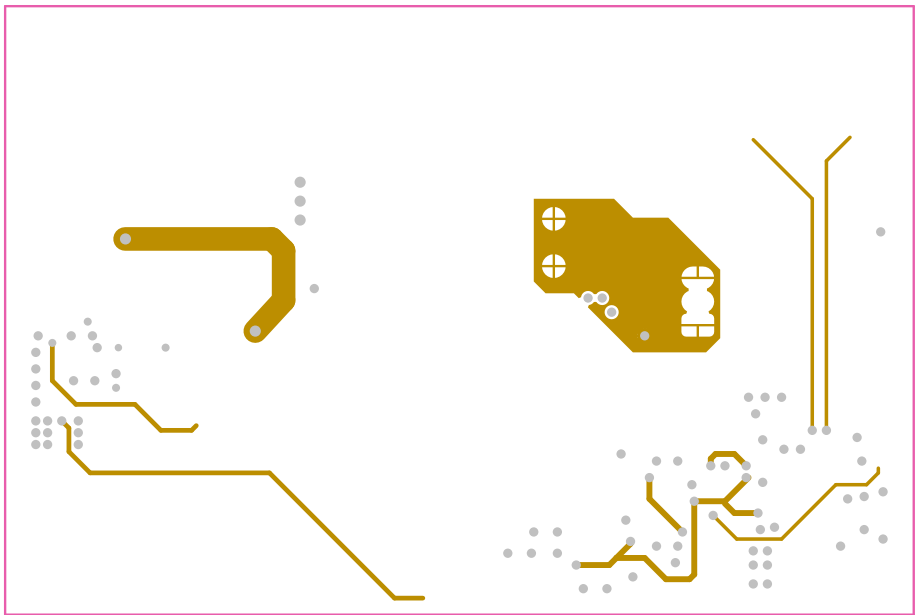
Top Overlay

M1 Board Outline

Top Layer

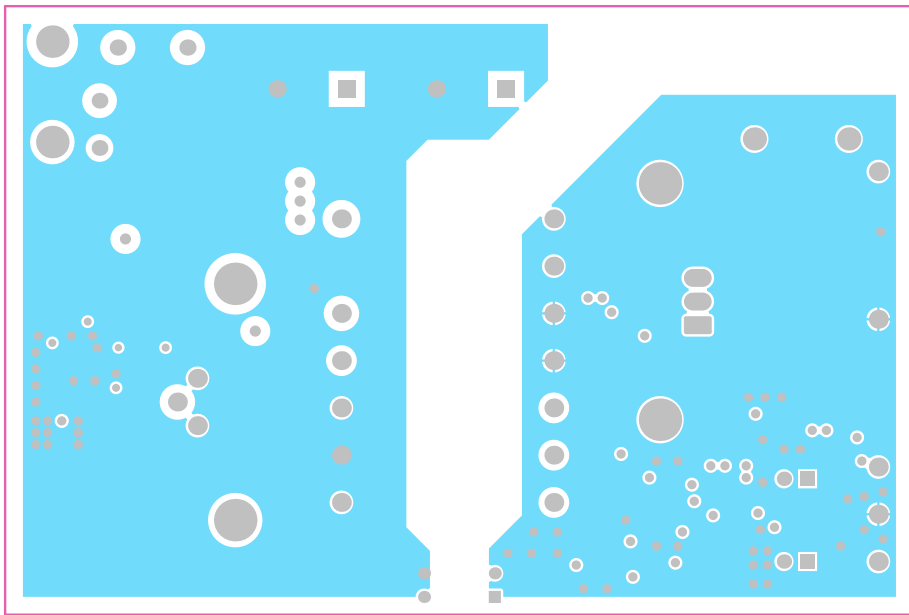
M2 Board Dimensions





M1 Board Outline

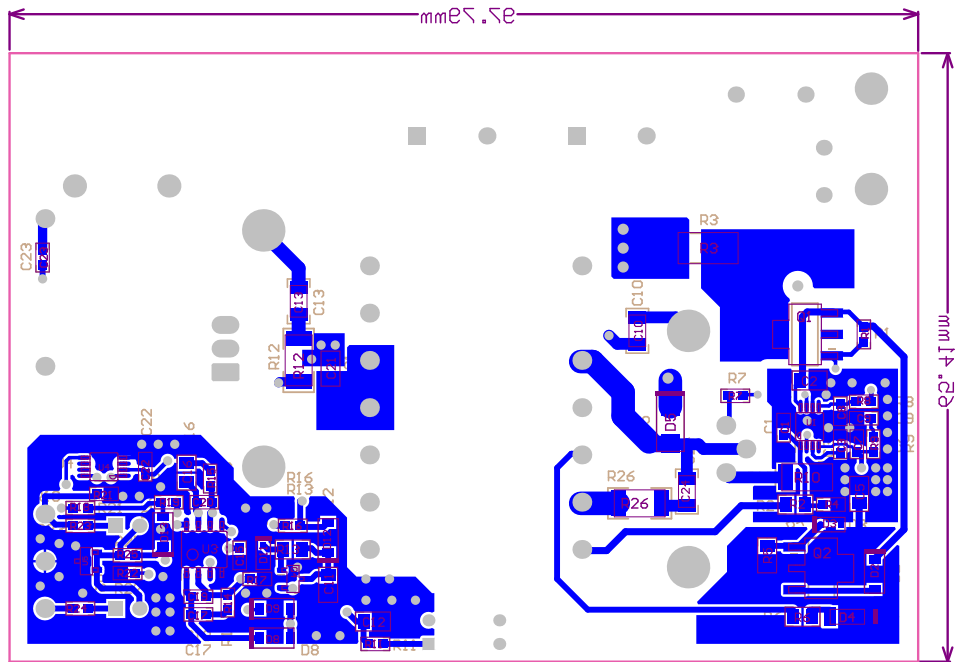
Signal Layer 1



M1 Board Outline

Signal Layer 2

M1 Board Outline
 M2 Board Dimensions
 Bottom Overlay
 Bottom Layer



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.
 ASSEMBLY VARIANT: [No Variations]



PCB VIEWED FROM BOTTOM SIDE	BOARD #: PMP30047	REV:	SUN REV: Not In VersionControl
	TID #: .TID		
PLOT NAME = Bottom Copper	GENERATED : 7/25/2016 10:42:21 AM		TEXAS INSTRUMENTS

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