

$I_{peak} @ \text{Min } V_{in} @ 10.3A \text{ load current} = 18.3A$

$R_{sense} = V_{cl}/I_{peak} = 65.5mV/18.3A = 3.58m\Omega$

Input Voltage = -36V to -60V

Vout = 14.5V @ 9A

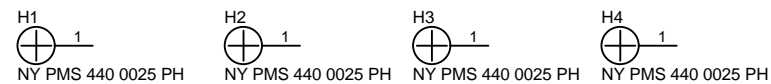
UVLO Rising = -30Vin
UVLO Falling = -29Vin

Fsw = 150kHz

Orderable: N/A	Designed for: Public Release	Mod. Date: 10/5/2016
TID #: PMP20371	Project Title: LM5122 Single Phase Inverting Buck Boost	
Number: PMP20371	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 2
Drawn By:	File: PMP20371_REVA.SchDoc	Size: B
Engineer: Xinyu Dai	Contact: http://www.ti.com/support	

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PCB Number: PMP20371
PCB Rev: A

Label Table	
Variant	Label Text
001	

ZZ1
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Orderable: N/A	Designed for: Public Release	Mod. Date: 7/26/2016
TID #: PMP20371	Project Title: LM5122 Single Phase Inverting Buck Boost	
Number: PMP20371	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 2
Drawn By:	File: PMP20371_REVA_Hardware.SchDoc	Size: B
Engineer: Xinyu Dai	Contact: http://www.ti.com/support	

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Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
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