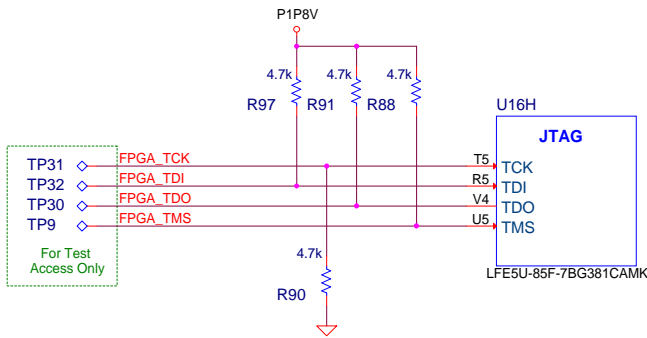
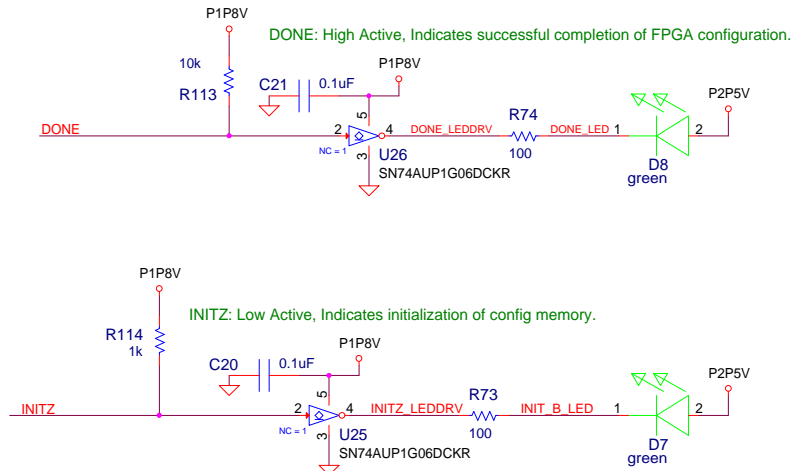
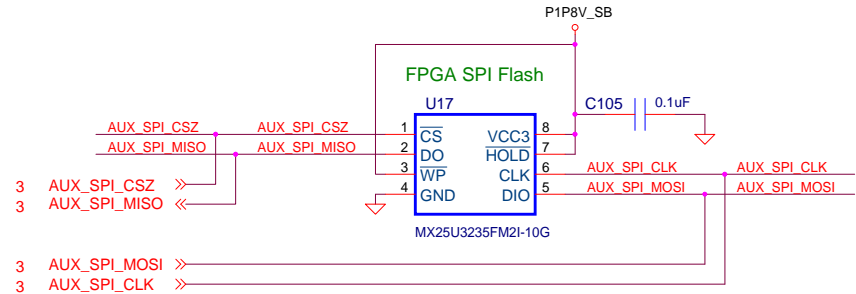
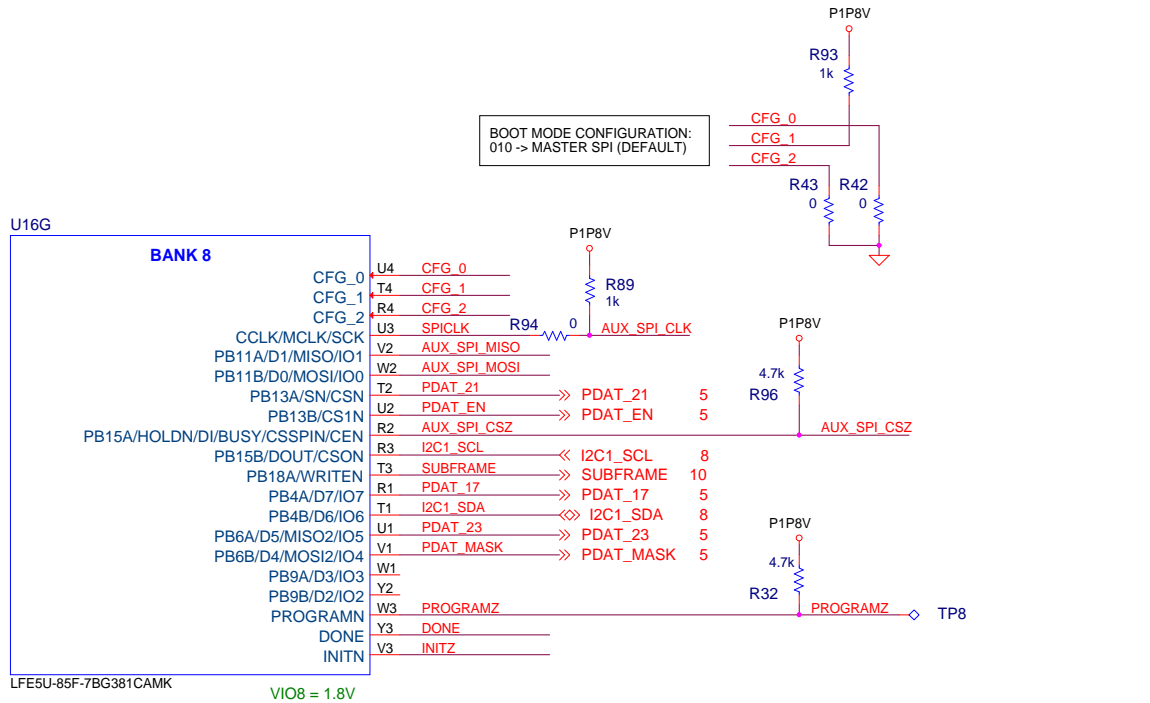


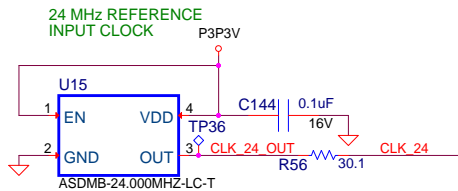
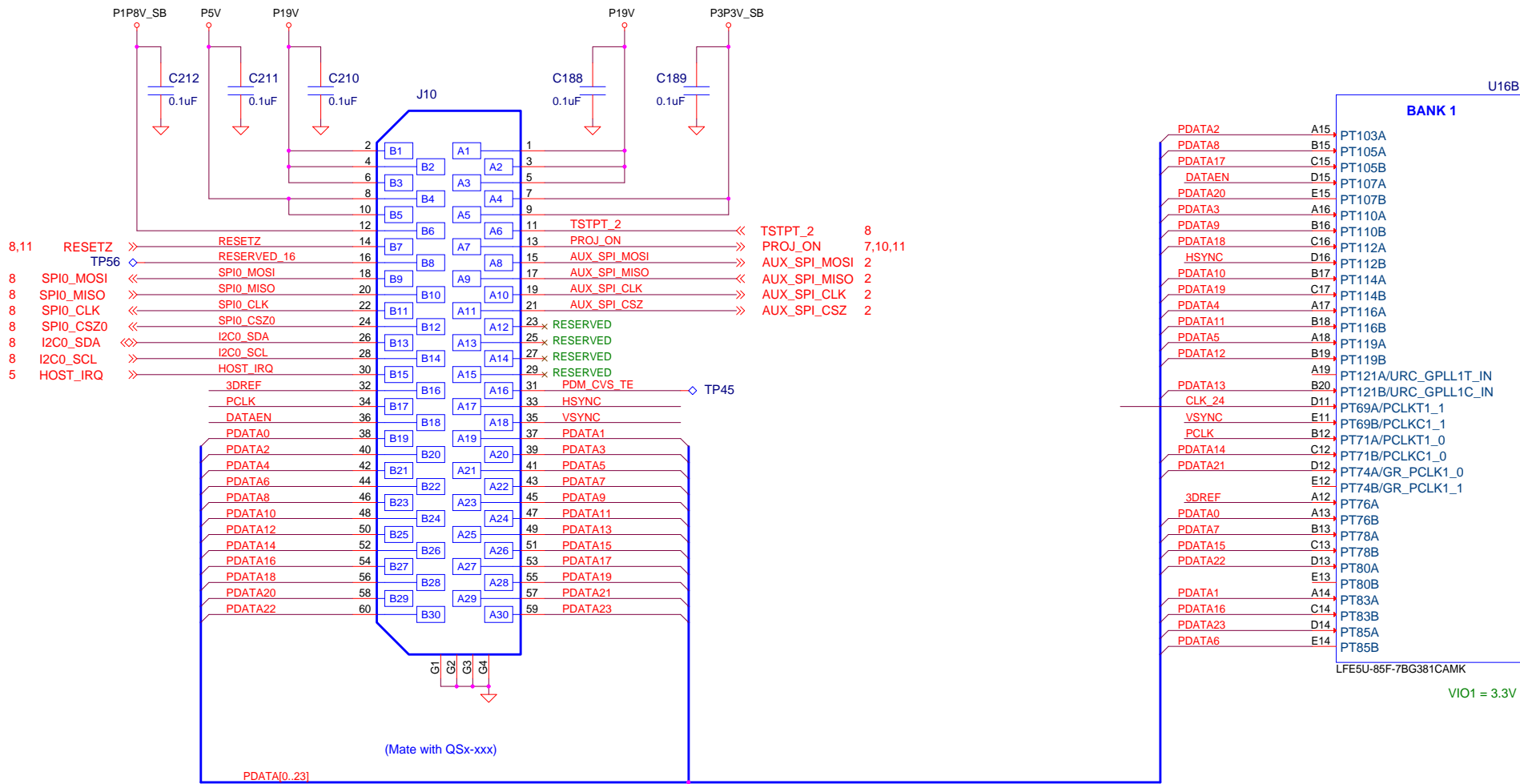
# DLPC3434 DISPLAY BOARD REFERENCE DESIGN



## FPGA Boot, Flash, Reset

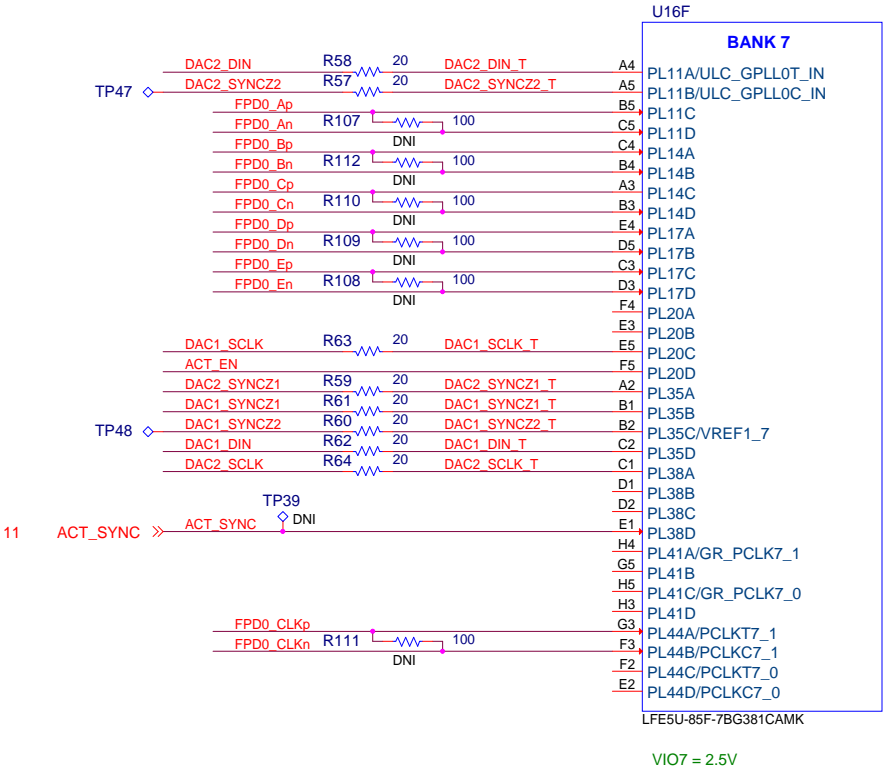
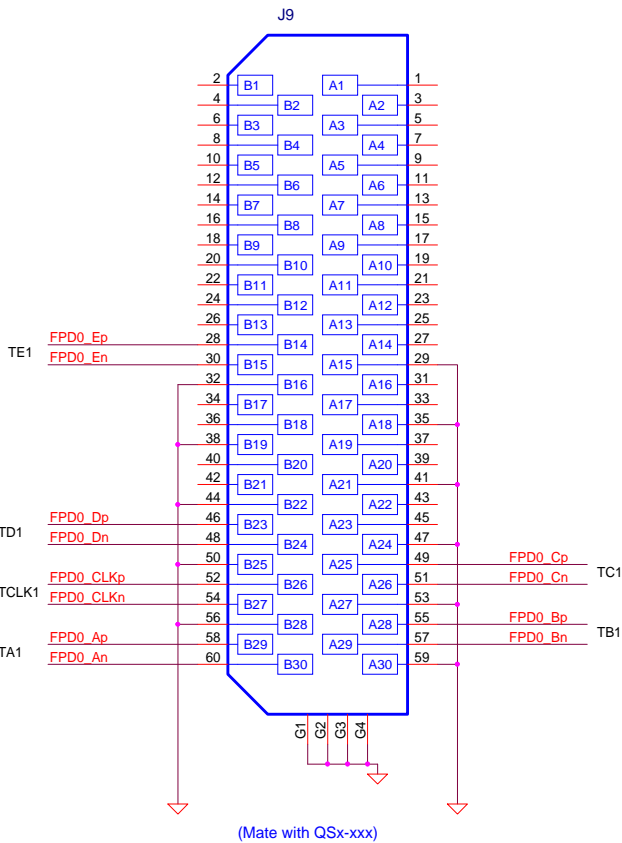
TEXAS INSTRUMENTS	DWN	DATE	DRAWING NO	REV
	Roger Perry	03/29/2019		
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SIZE		D	SHEET 2 OF 15	

CONNECTION TO FRONT END PROVIDING PARALLEL RGB VIDEO INPUT  
(PLACE ON BOTTOM SIDE)



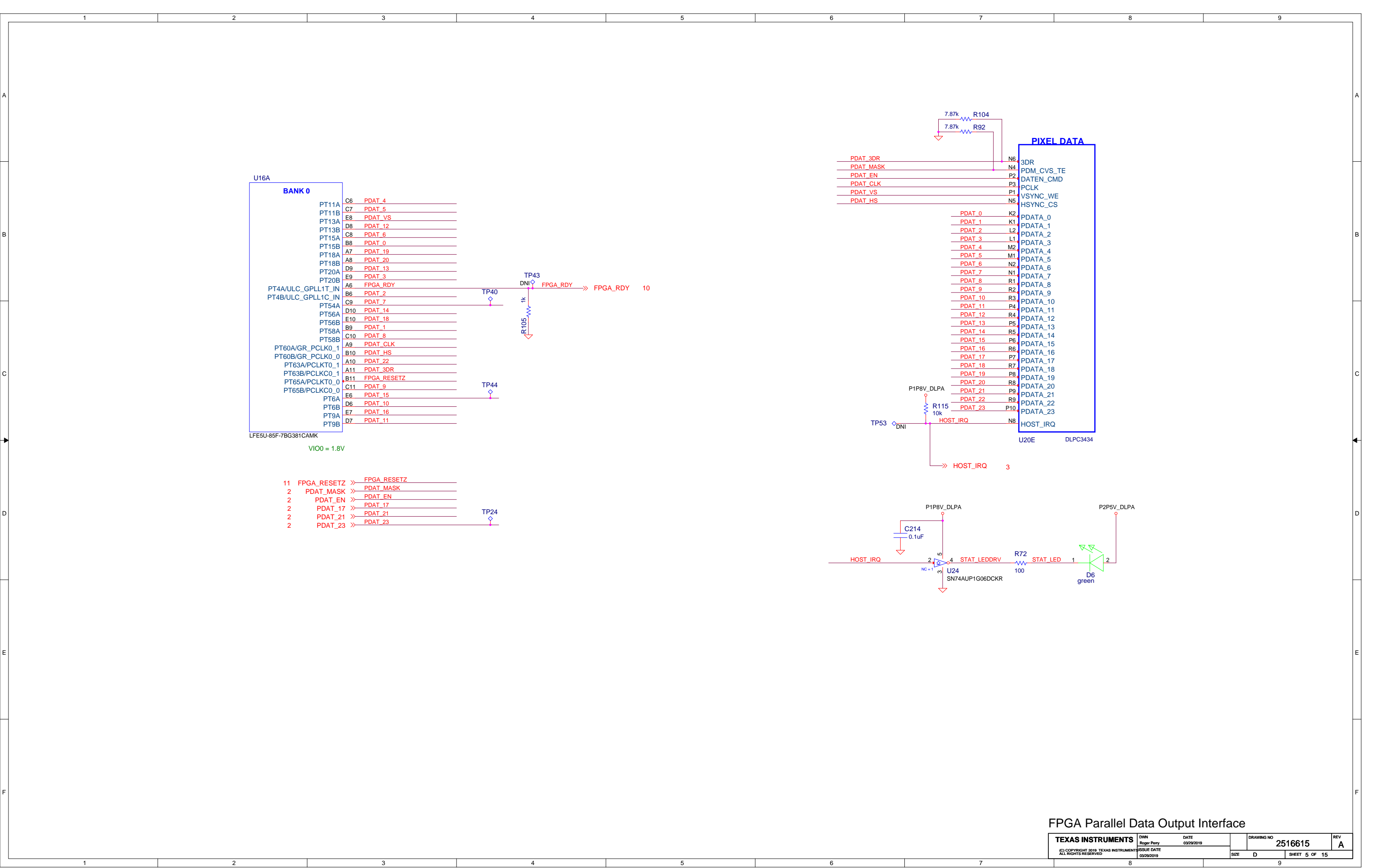
FPGA Parallel Data Input Interface

CONNECTION TO FPD LINK VIDEO INPUT  
(PLACE ON TOP SIDE)



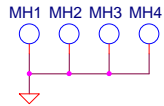
DAC1_SCLK	»	DAC1_SCLK	13
DAC1_SYNCZ1	»	DAC1_SYNCZ1	13
DAC1_DIN	»	DAC1_DIN	13
DAC2_SCLK	»	DAC2_SCLK	13
DAC2_SYNCZ1	»	DAC2_SYNCZ1	13
DAC2_DIN	»	DAC2_DIN	13
ACT_EN	»	ACT_EN	13

FPD-Link Video Input

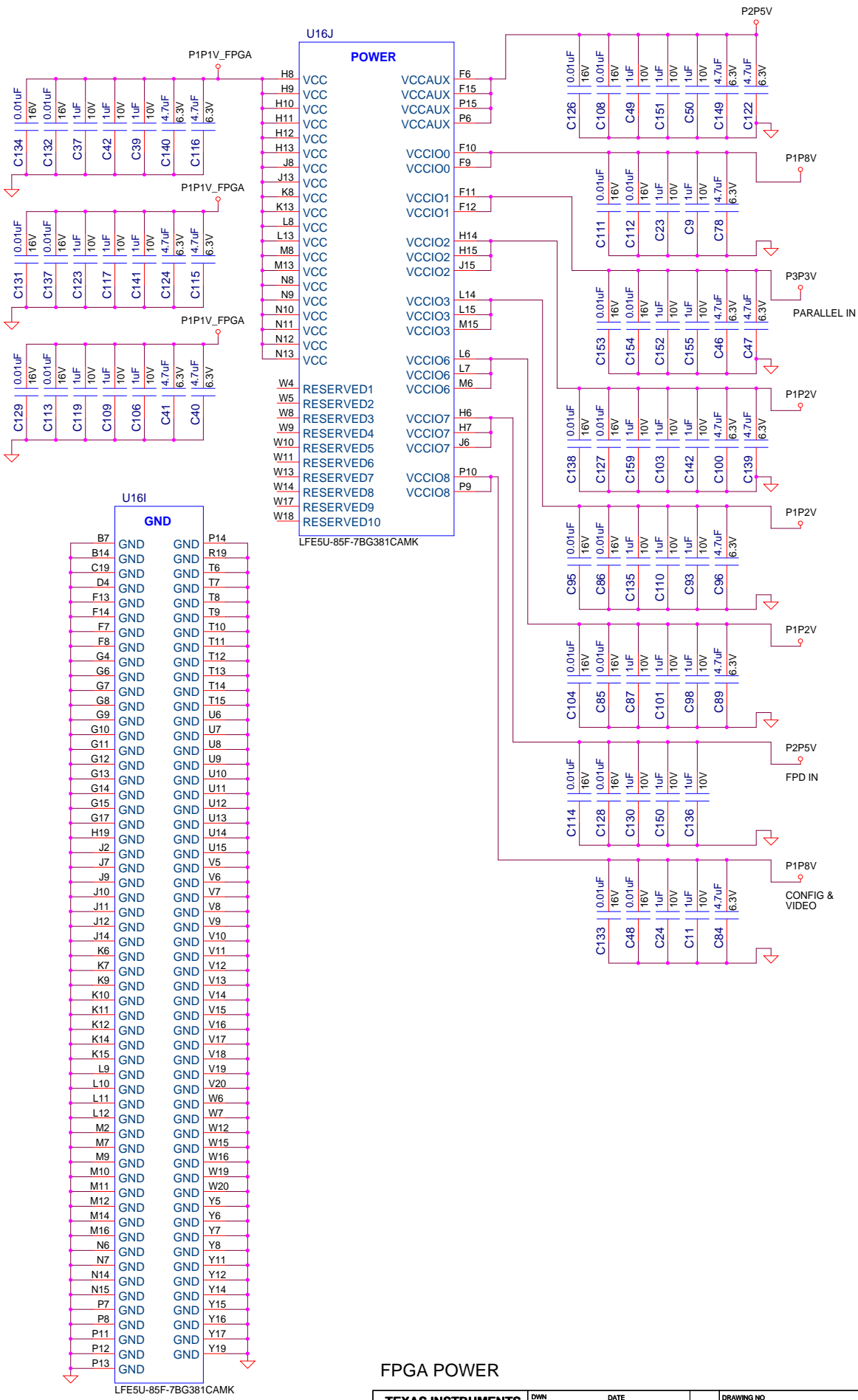
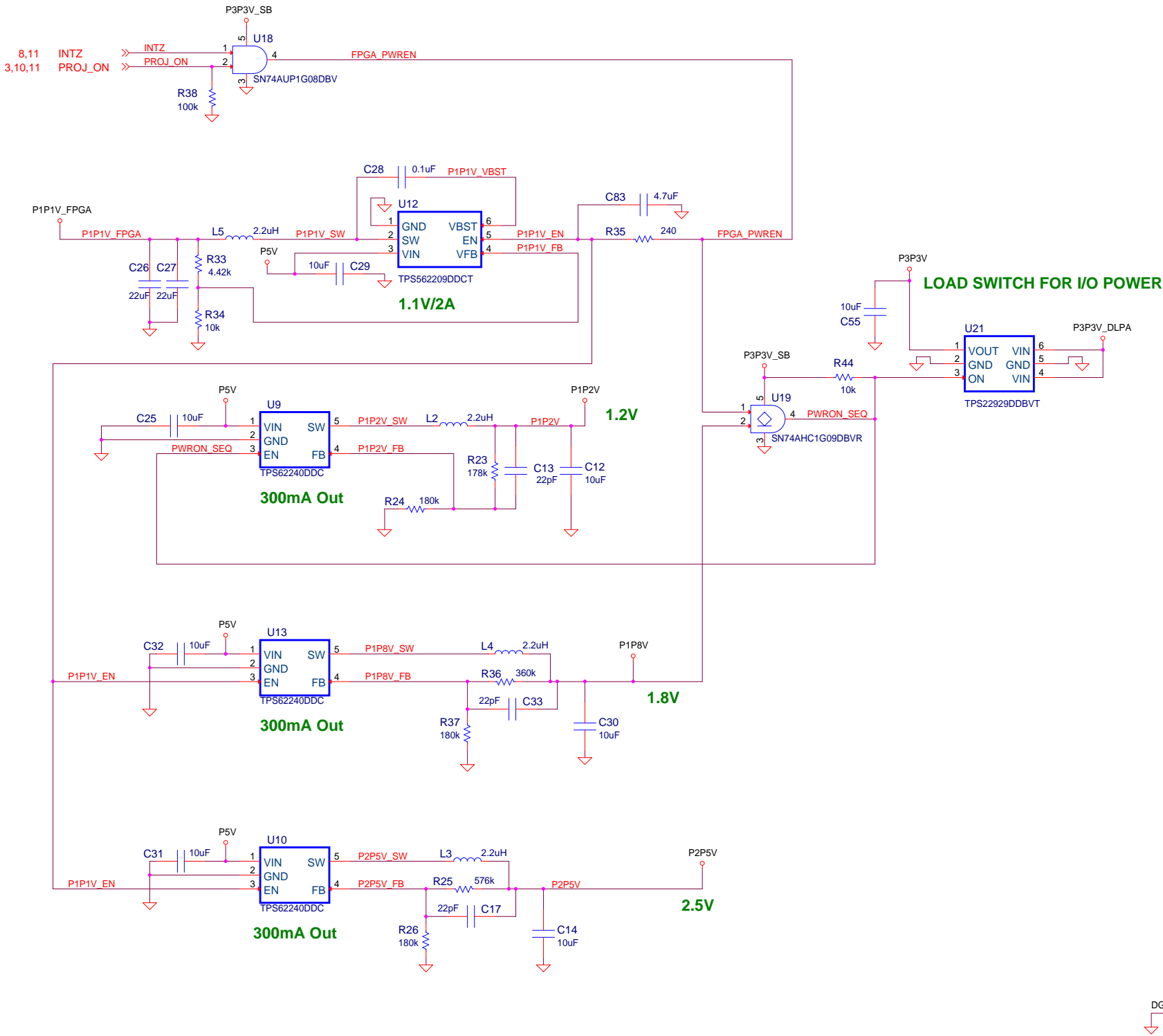




BOARD MOUNTING HOLES



- SHUTDOWN WHEN FAST PARK DETECTED.
- INTZ GOING HIGH ENABLES POWER TO FPGA.

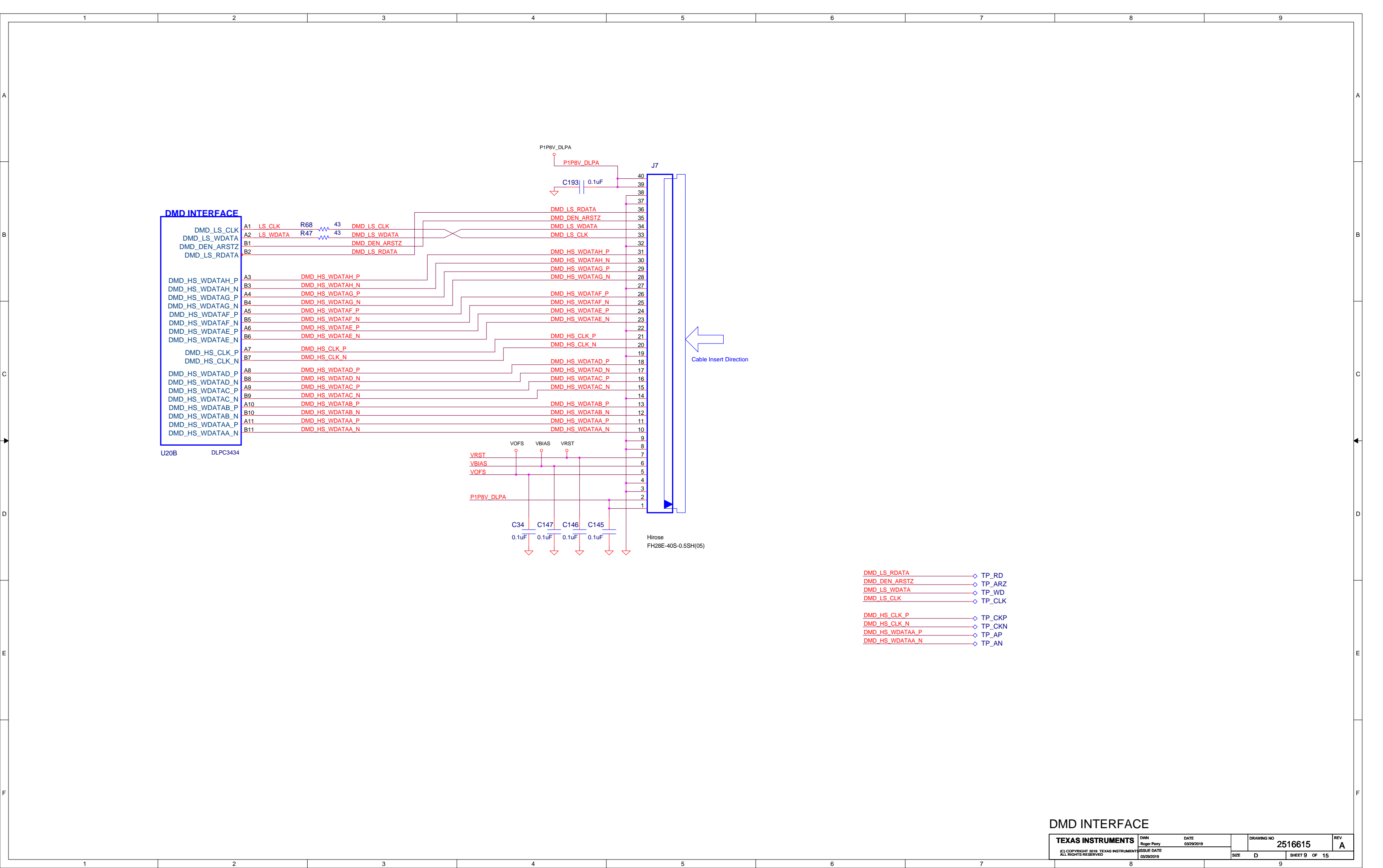


FPGA POWER

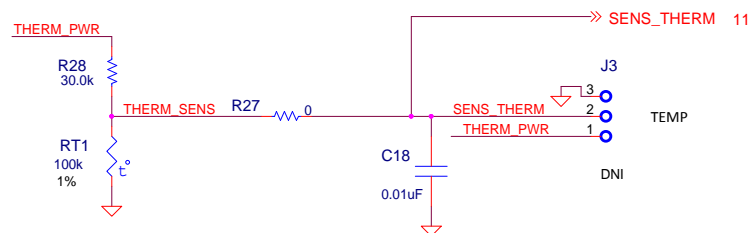
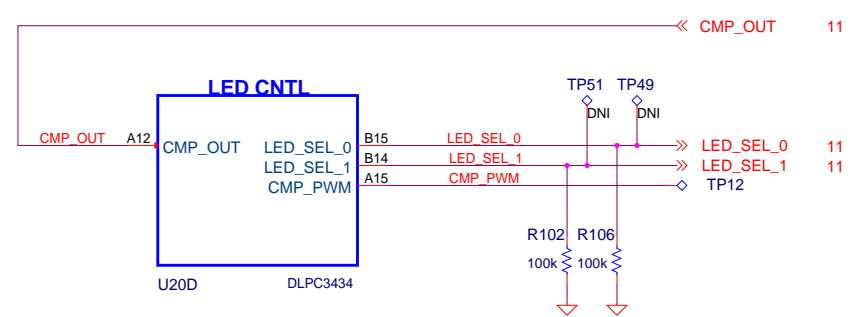
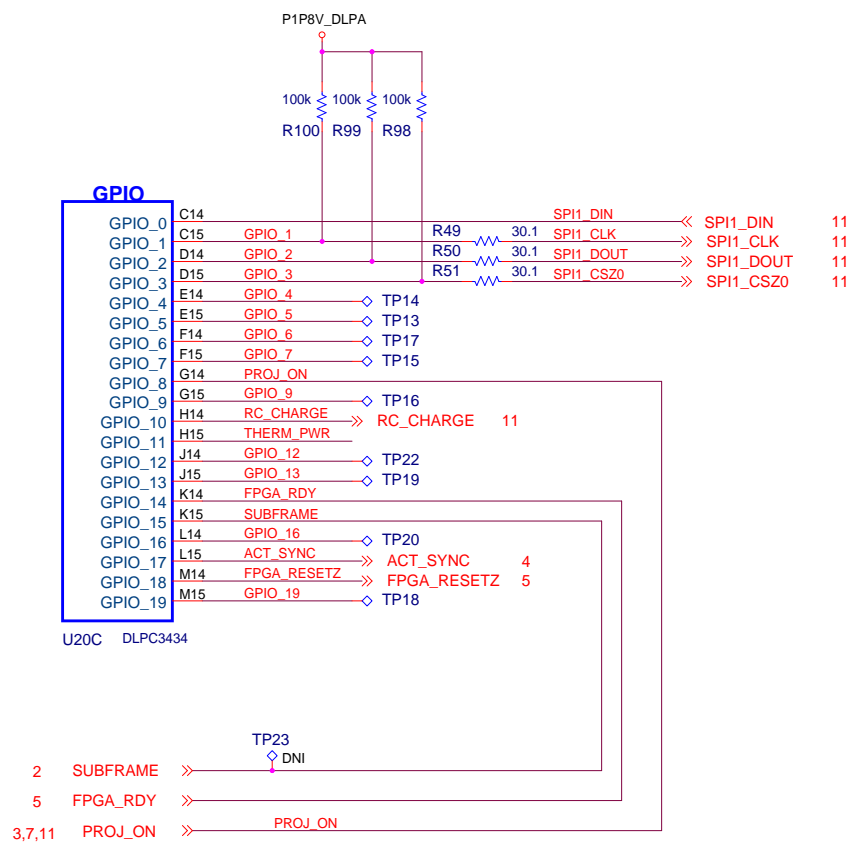
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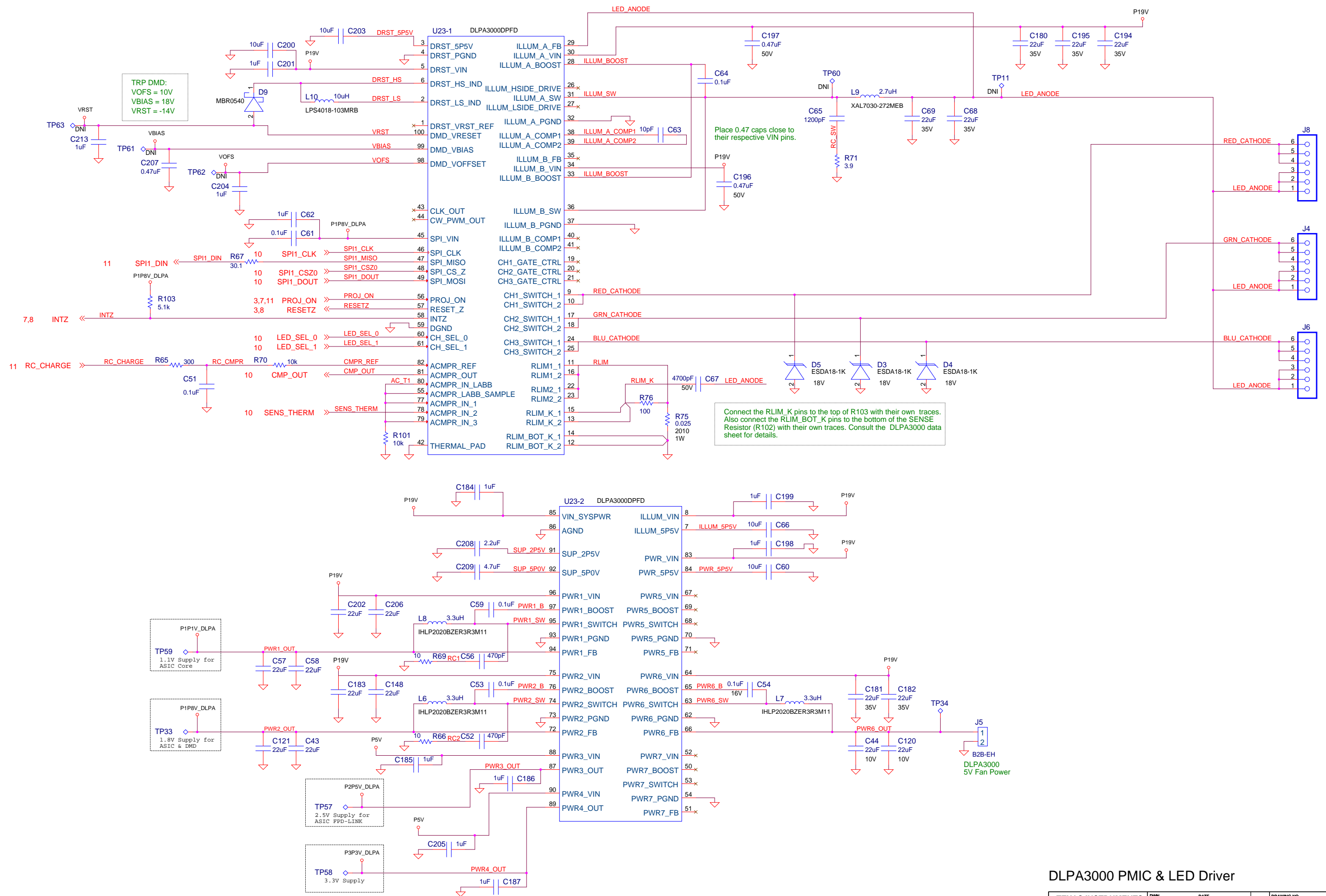




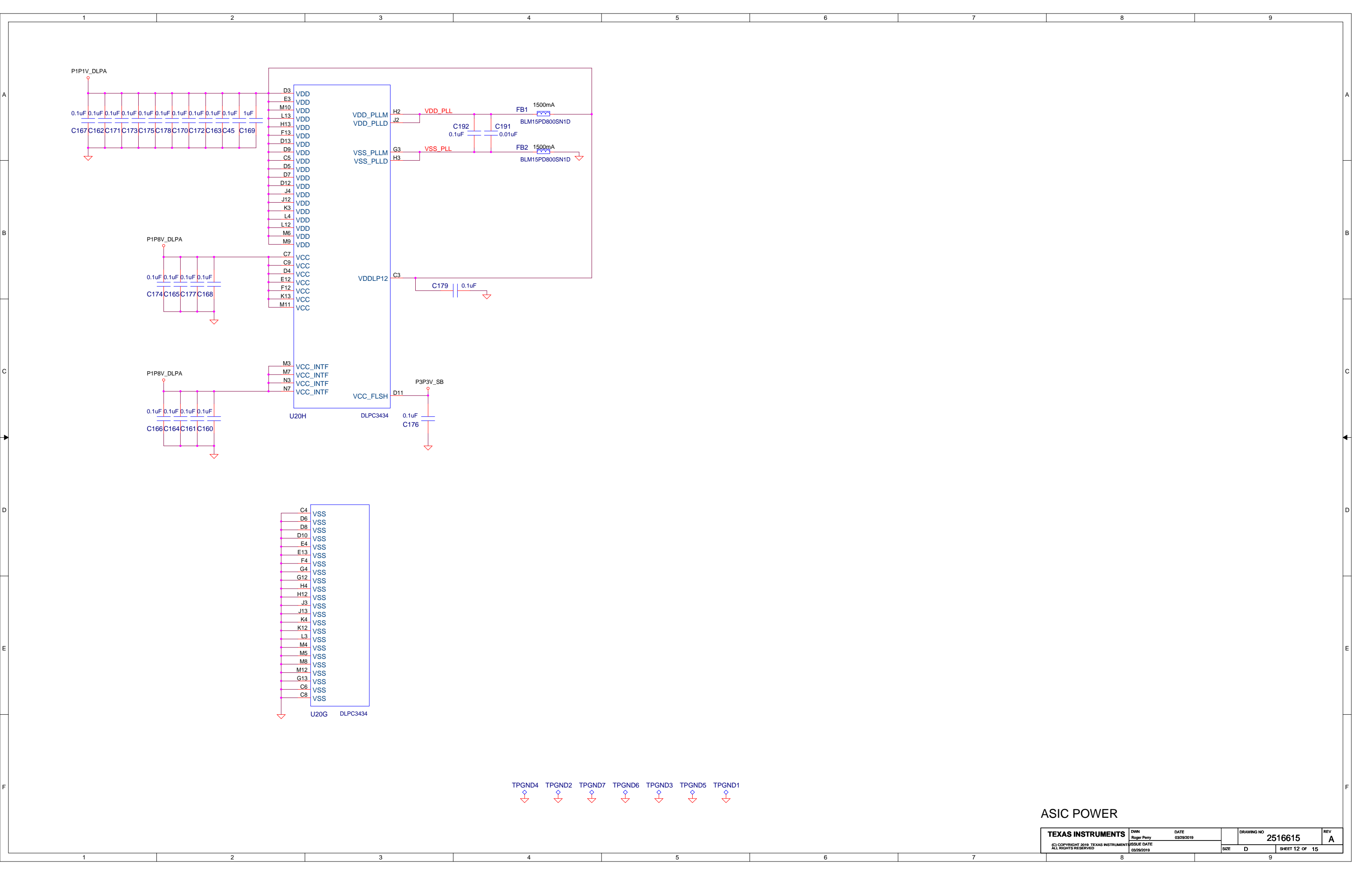


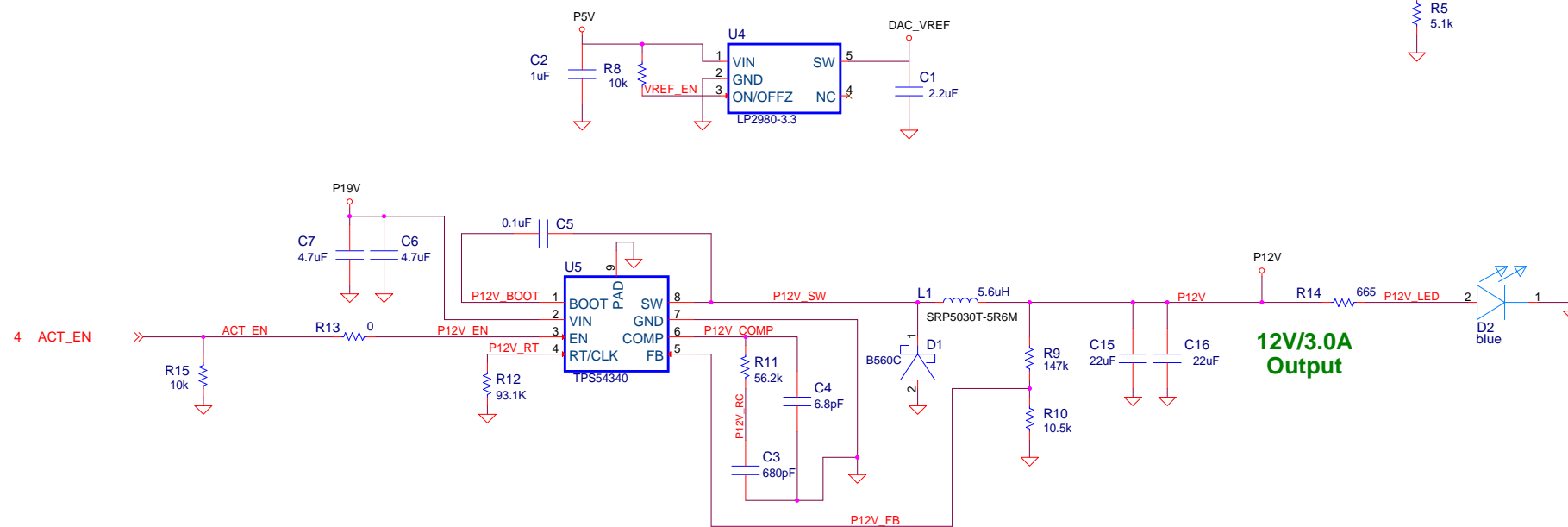
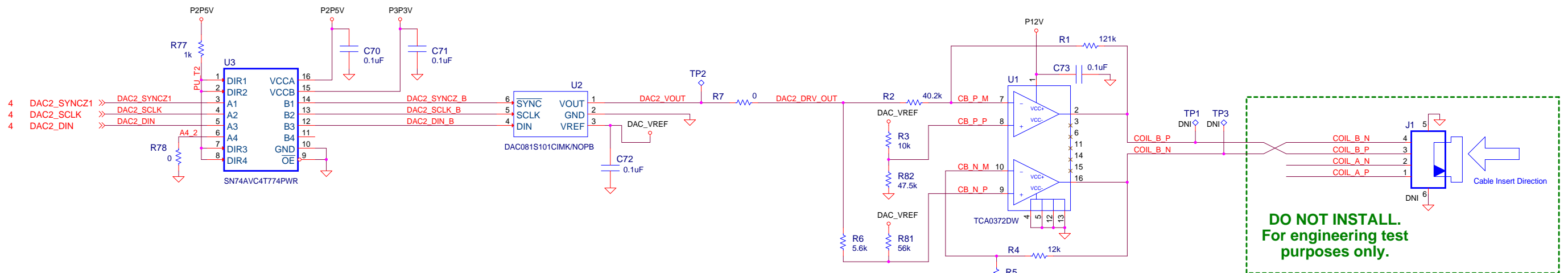
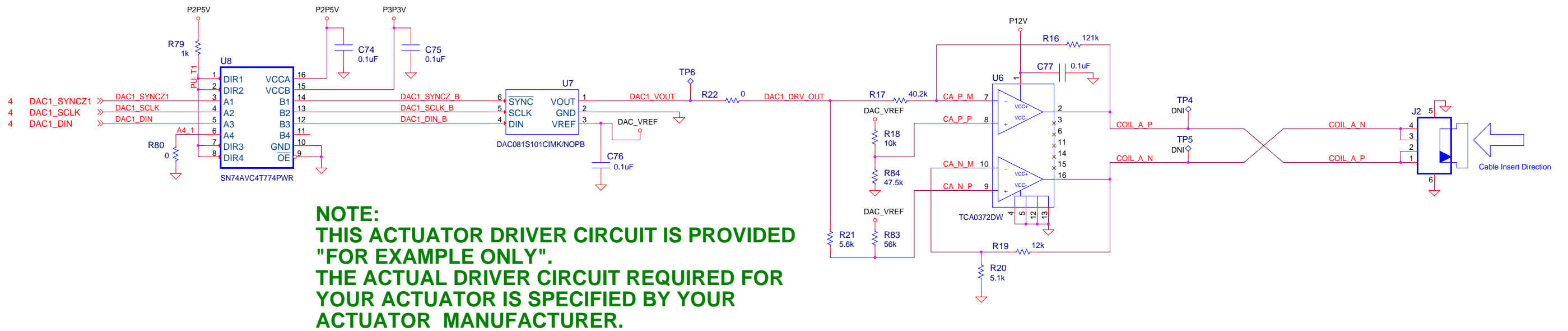
DMD INTERFACE





DLPA3000 PMIC &amp; LED Driver





ACTUATOR DRIVER

1	2	3	4	5	6	7	8	9	
A	<div>Revisions</div> <div>Rev. A: Initial Release</div>								A
B									B
C									C
D									D
E									E
F									F
1	2	3	4	5	6	7	8	9	

REVISION NOTES

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	ISSUE DATE 03/29/2019		SIZE D	SHEET 14 OF 15

1	2	3	4	5	6	7	8	9	
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B									B
C									C
D									D
E									E
F									F

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