

TS3USB3000

High Speed Signal Conditioners

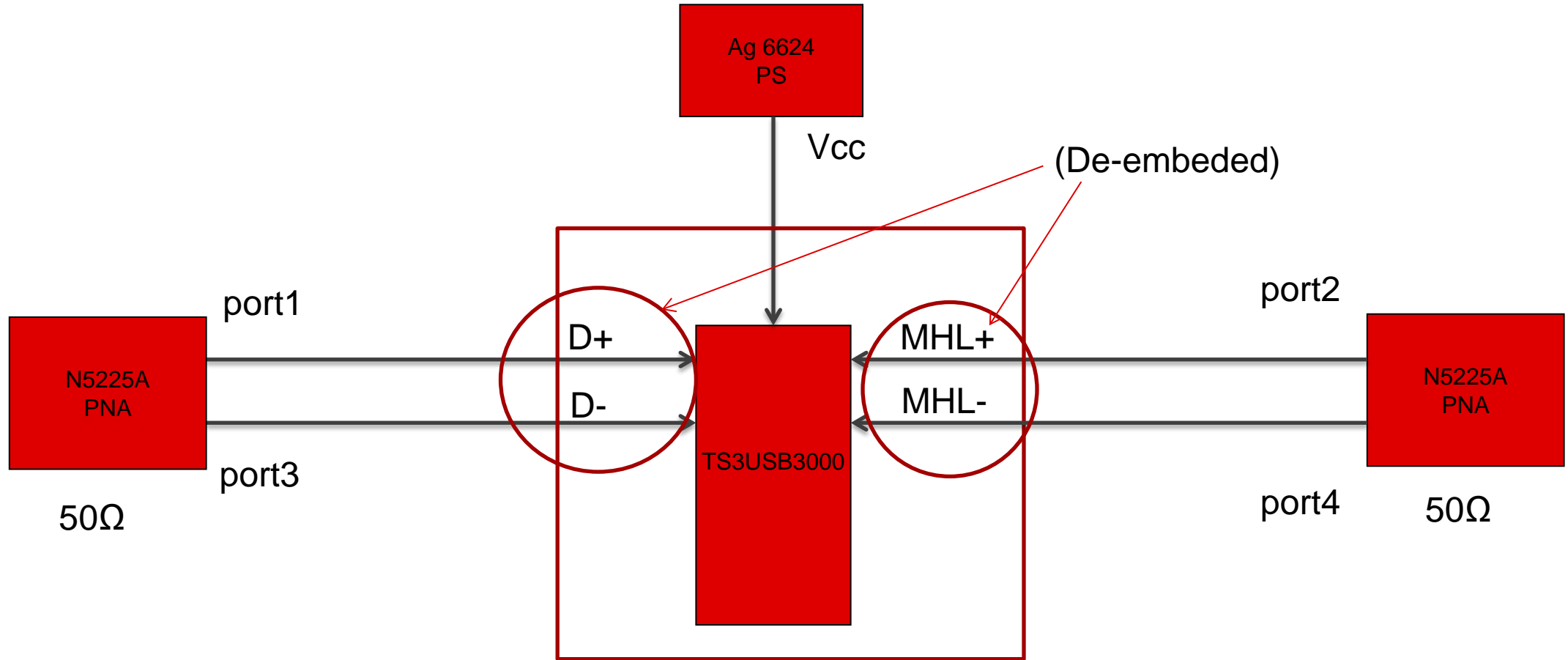
Setup Notes:

- Bench board traces have been de-embedded at the VNA (Ag N5225A)
- Using RF power of -10dBm
- VNA start freq: 10 MHz, stop freq: 10 GHz, IF BW:1kHz
- Port order: (1,3,2,4)

S4P File Description

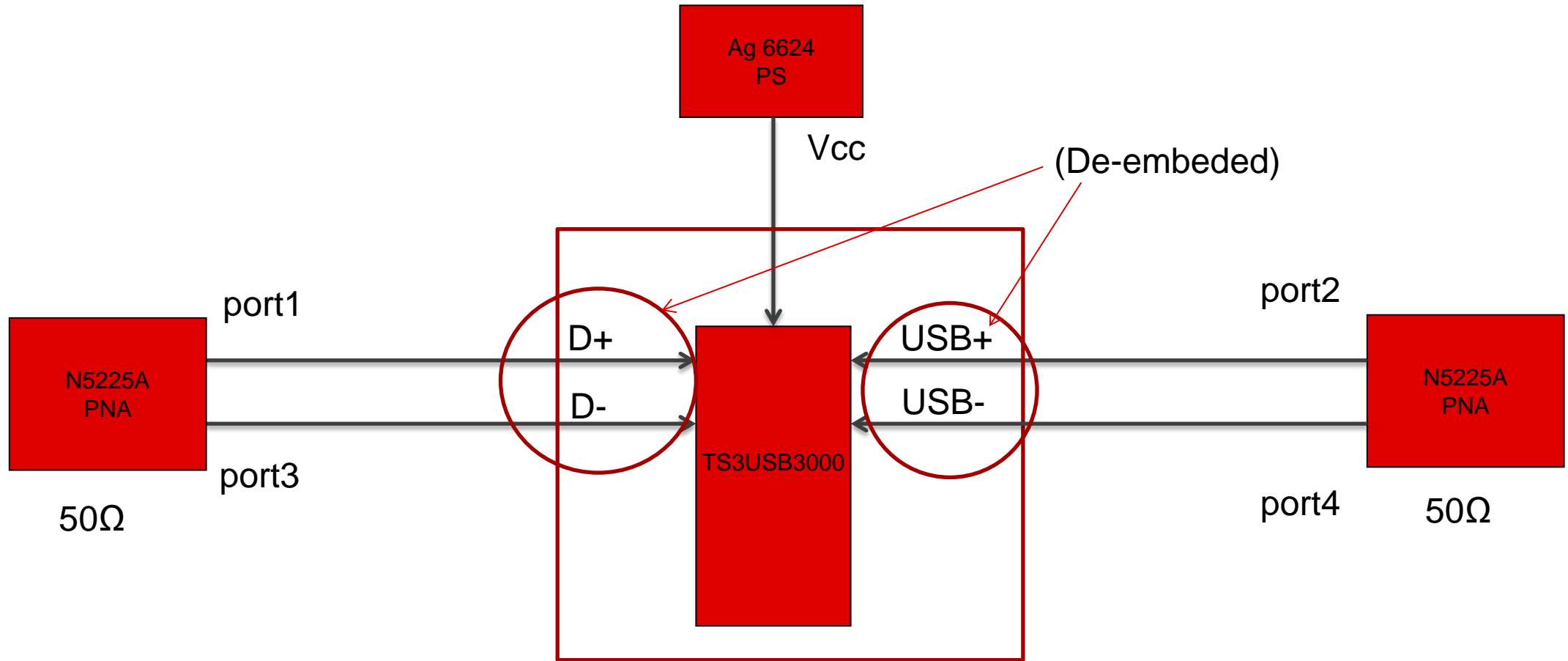
- TS3USB3000-D-to-MHL-3.3v-25-sel-hi.s4p
 - VCC = 3.3V; Temperature = 25C
 - D to MHL path with switch closed
- TS3USB3000-D-to-MHL-3.3v-25-sel-low.s4p
 - VCC = 3.3V; Temperature = 25C
 - D to MHL path with switch open
- TS3USB3000-D-to-USB-3.3v-25-sel-hi.s4p
 - VCC = 3.3V; Temperature = 25C
 - D to USB path with switch open
- TS3USB3000-D-to-USB-3.3v-25-sel-low.s4p
 - VCC = 3.3V; Temperature = 25C
 - D to USB path with switch closed

TS3USB3000 test setup: D to MHL path



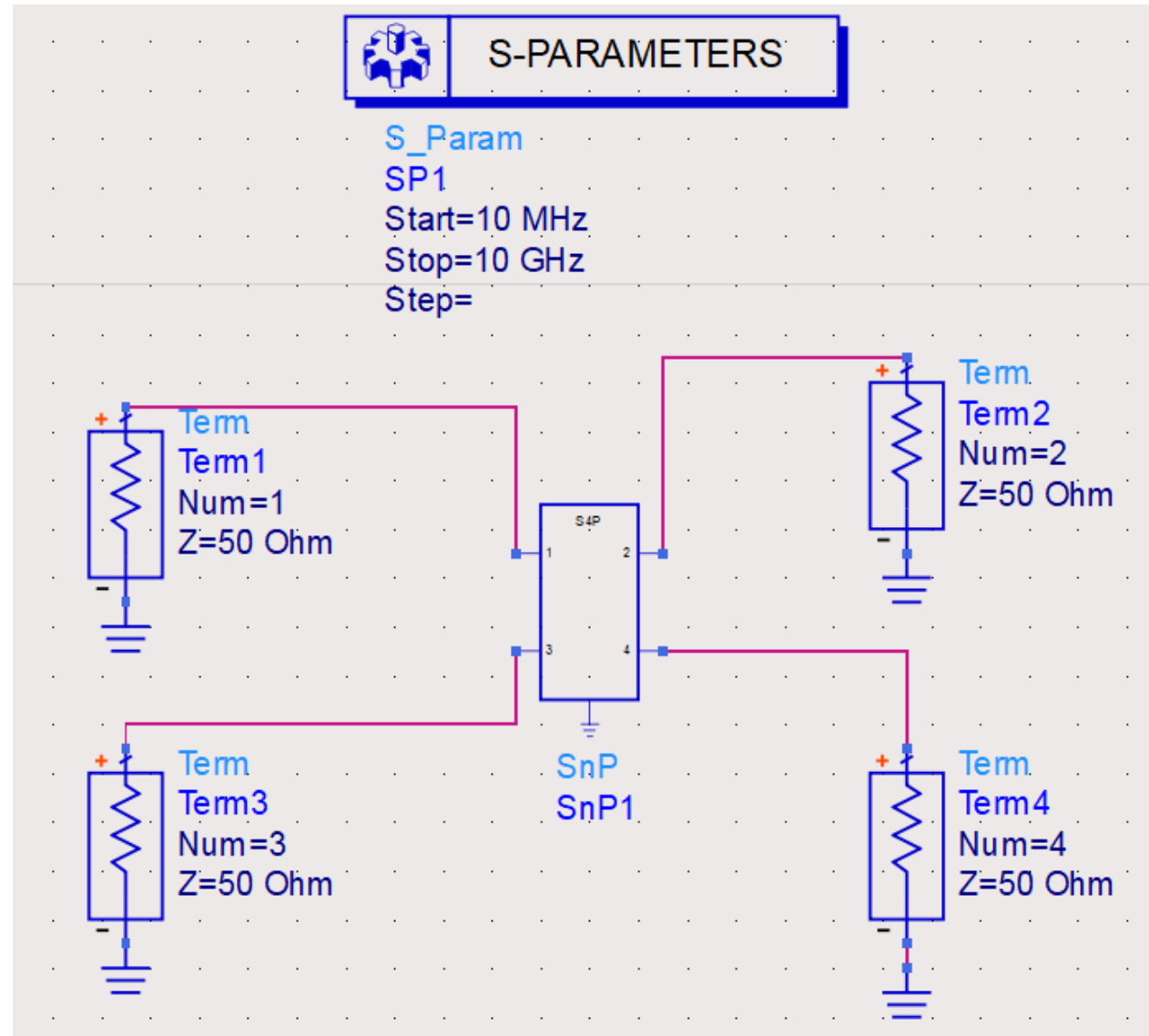
- Unused Ports terminated 50-ohms to GND.

TS3USB3000 test setup: D to USB path

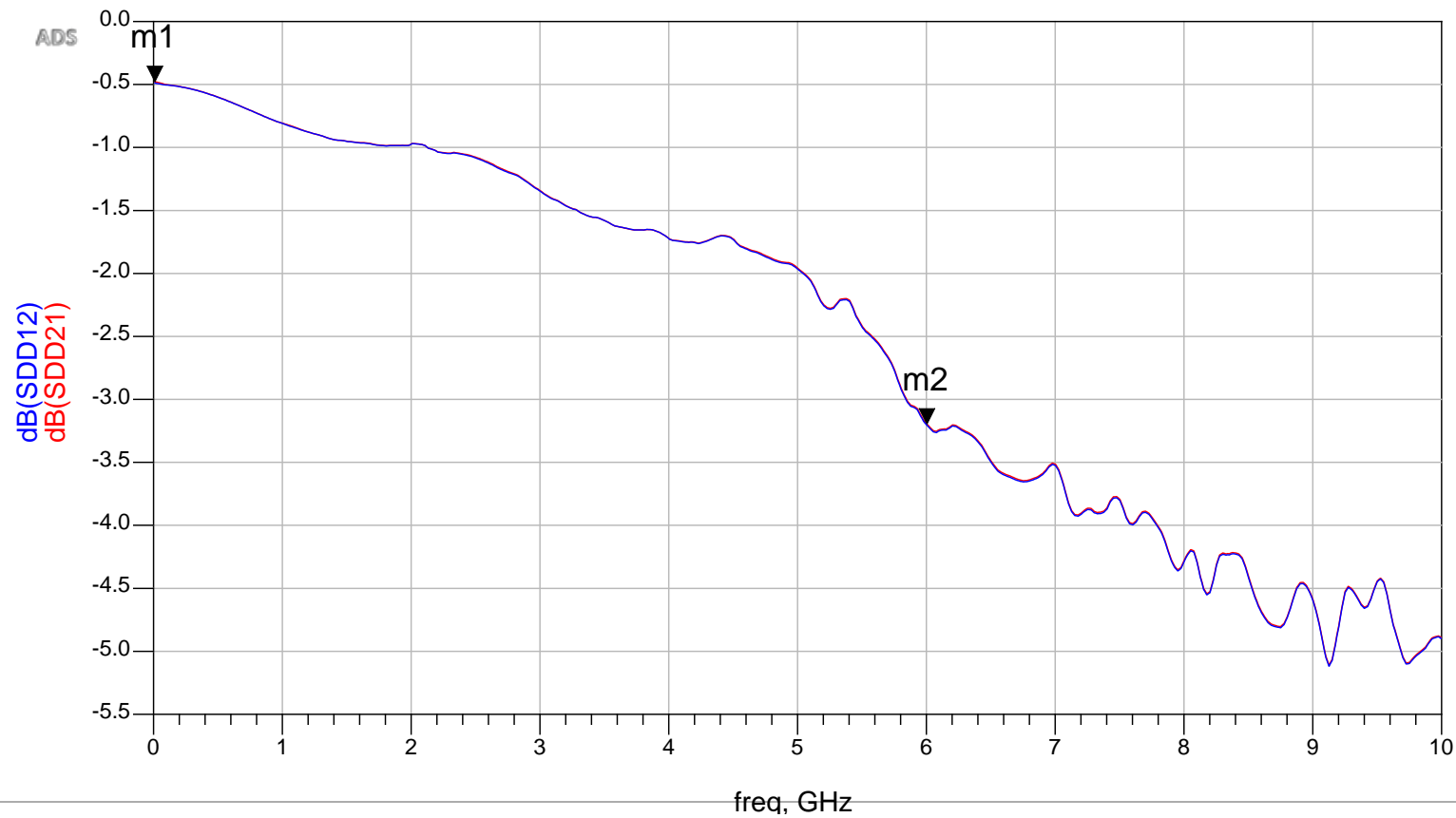


- Unused Ports terminated 50-ohms to GND.

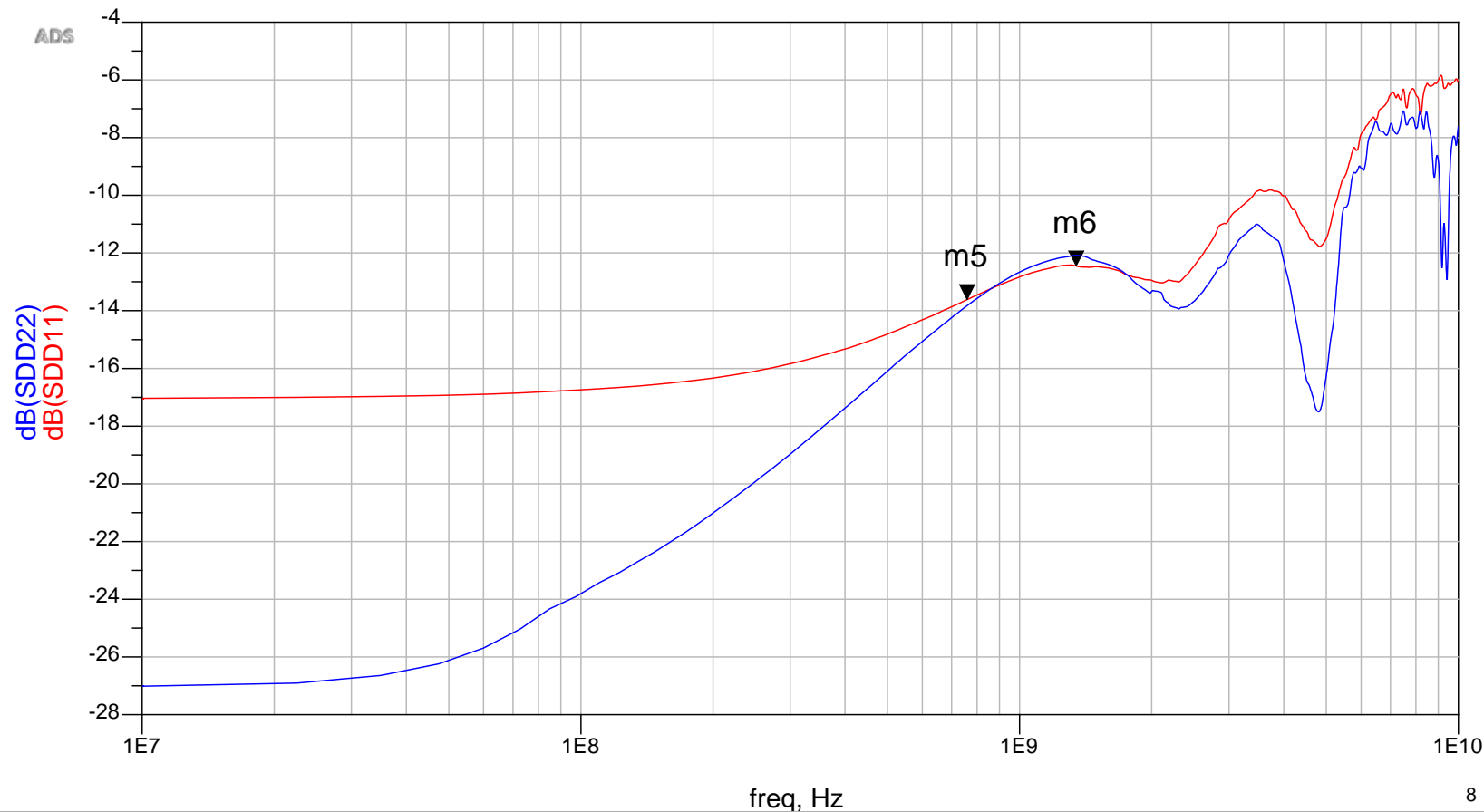
ADS Test Bench



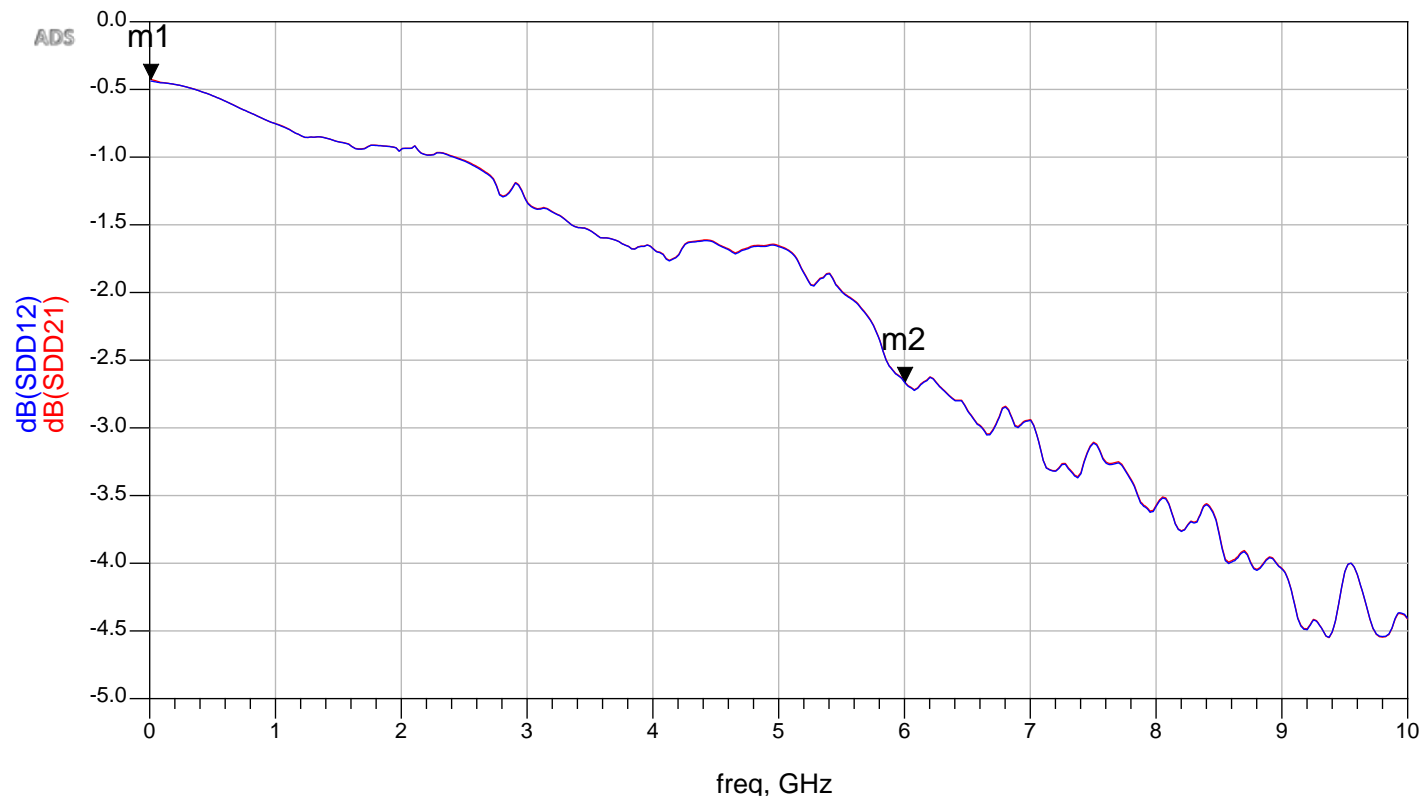
D to MHL: Insertion Loss



D to MHL: Return Loss



D to USB: Insertion Loss



D to USB: Return Loss

