

H1  
DNP  
NY PMS 440 0025 PH

H2  
DNP  
NY PMS 440 0025 PH

H3  
TTL-232R-3V3

H4  
ESD Cover

H5  
Printed Warranty notice

H6  
Printed QSG

DNP  
FID1

DNP  
FID2

DNP  
FID3

DNP  
FID4

DNP  
FID5

DNP  
FID6

PCB Number: TIDA-00299  
PCB Rev: E2

PCB LOGO  
Texas Instruments

PCB LOGO  
Pb-Free Symbol

PCB LOGO  
FCC disclaimer

PCB LOGO  
ESD Susceptible

PCB LOGO  
ESD Susceptible

Variant/Label Table

Variant	Label Text
001	All Populated with DNP option

LBL1  
PCB Label  
Size: 0.65" x 0.20"

ZZ1  
Label Assembly Note  
This Assembly Note is for PCB labels only

ZZ2  
Assembly Note  
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3  
Assembly Note  
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4  
Assembly Note  
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: TMDXICE110-BTA	Designed for: N/A	Mod. Date: 4/4/2017
TID #: N/A	Project Title: AMIC110 ICE EVM	
Number: PROC040	Rev: 1.1	Sheet Title: Misc
SVN Rev: N/A	Assembly Variant: N/A	Sheet: 1 of 10
Drawn By: N/A	File: AMIC110 ICE EVM_HW_SchDoc	Size: B
Engineer: N/A	Contact: N/A	

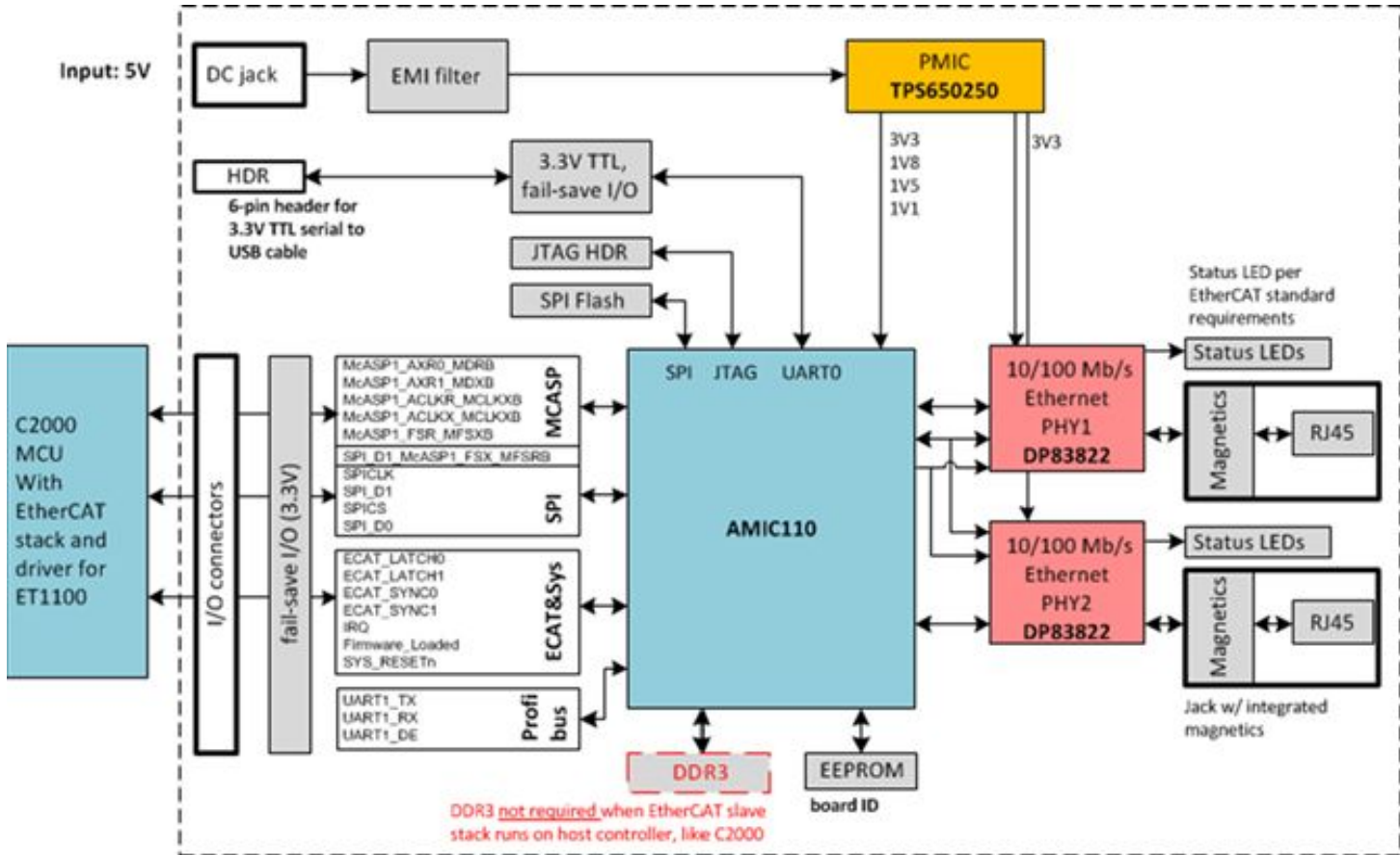
TEXAS  
INSTRUMENTS

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Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

AMIC110 ICE Functional Block Diagram

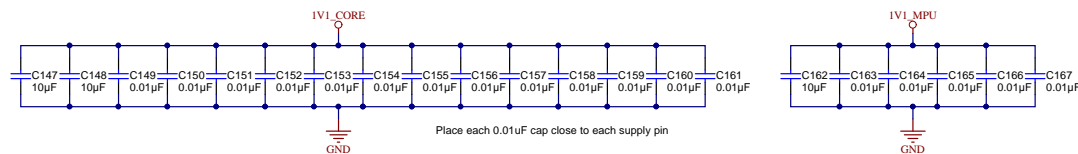
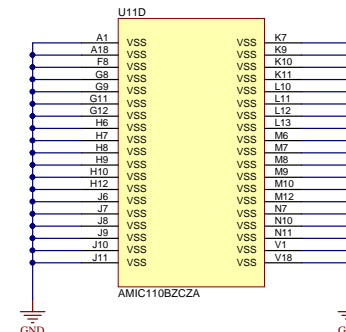
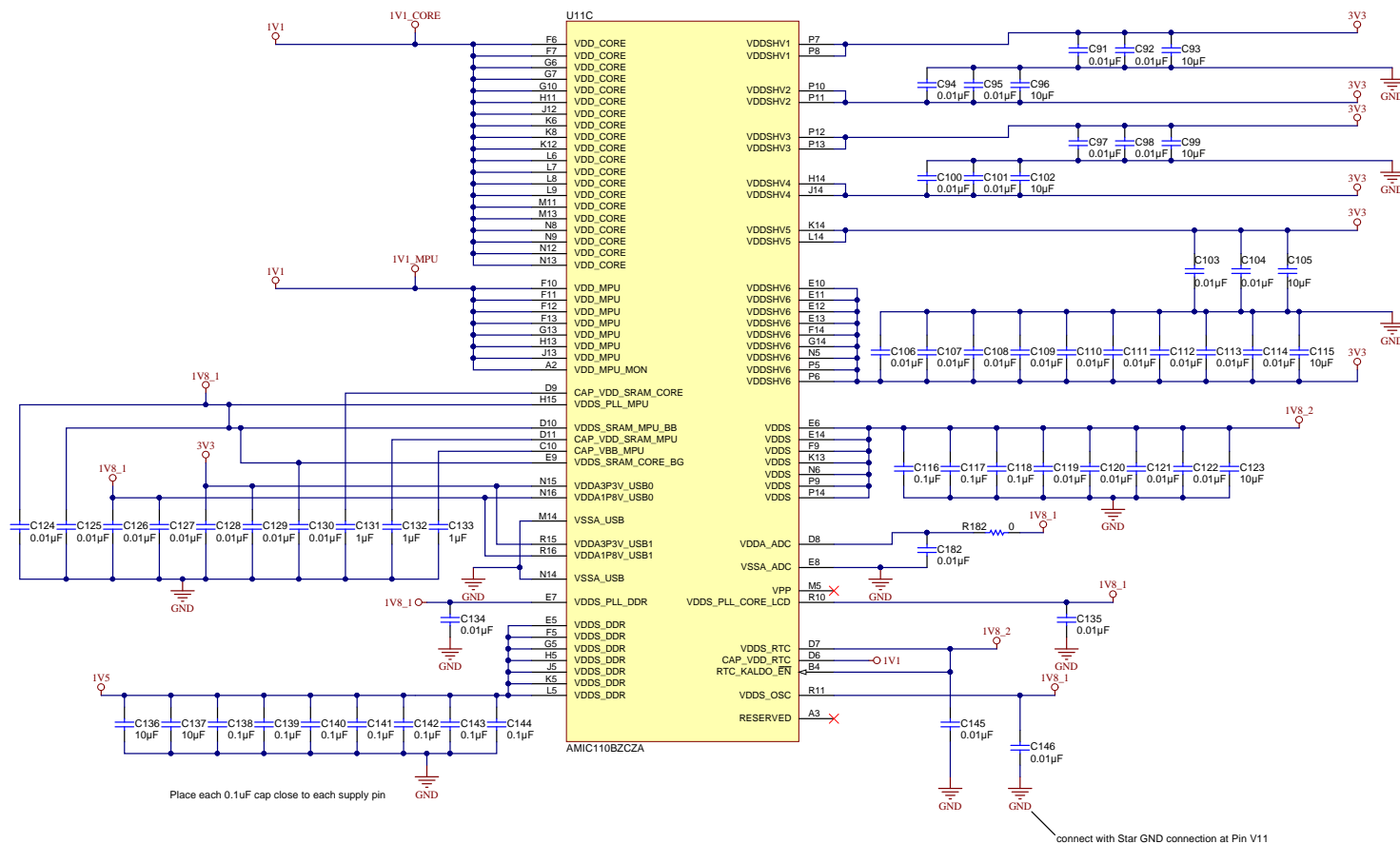


Orderable: TMDXICE110-BTA	Designed for: N/A	Mod. Date: 4/4/2017
TID #: N/A	Project Title: AMIC110 ICE EVM	
Number: PROC040   Rev: 1.1	Sheet Title: Block Diagram	
SVN Rev: N/A	Assembly Variant: N/A	Sheet 2 of 10
Drawn By: N/A	File: AMIC110 ICE EVM_CS.SchDoc	Size: B
Engineer: N/A	Contact: N/A	



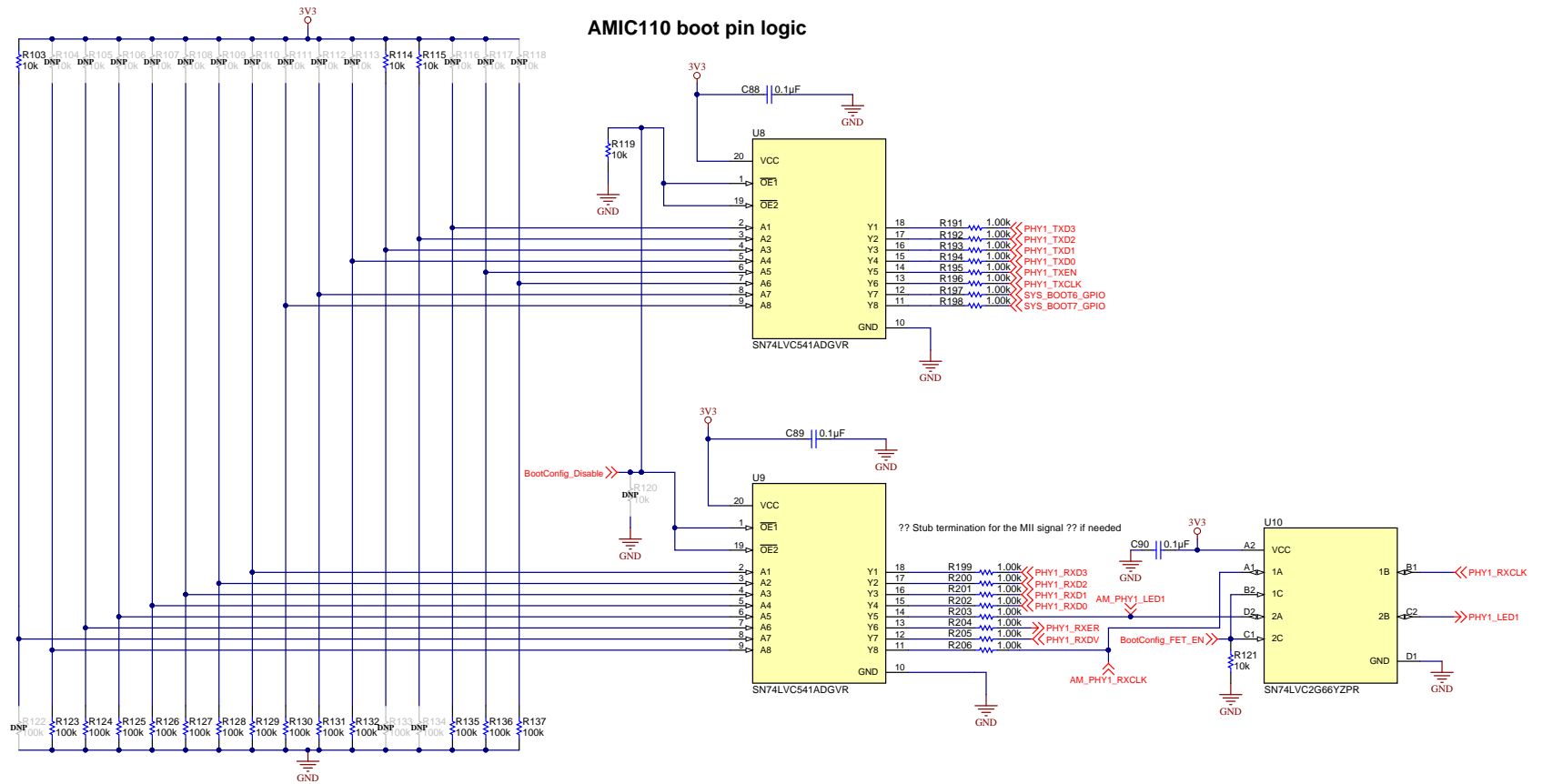






Orderable: TMDXICE110-BTA	Designed for: N/A	Mod. Date: 4/4/2017
TID #: N/A	Project File: AMIC110 ICE EVM	
Number: PROC040   Rev: 1.1	Sheet Title: Processor Power	
SVN Rev: N/A	Assembly Variant: N/A	Sheet: 6 of 10
Drawn By: N/A	File: AMIC110 ICE EVM_PS.SchDoc	Size: B
Engineer: N/A	Contact: N/A	

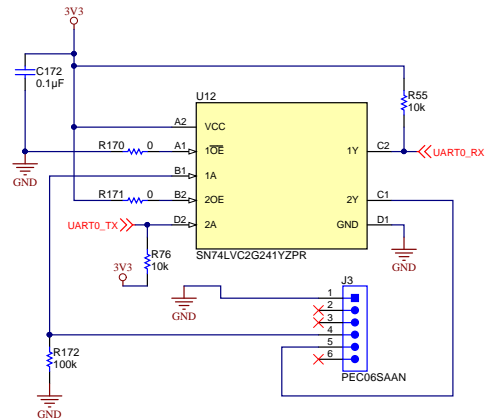
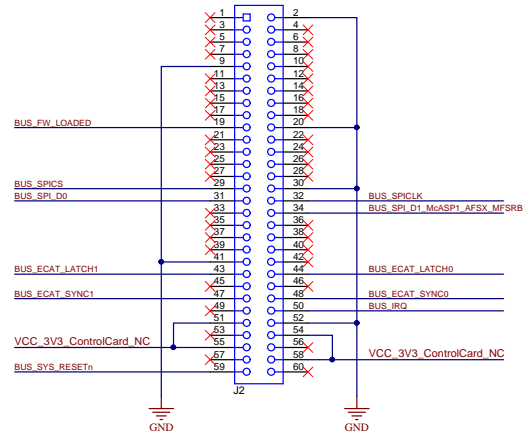
# AMIC110 boot pin logic



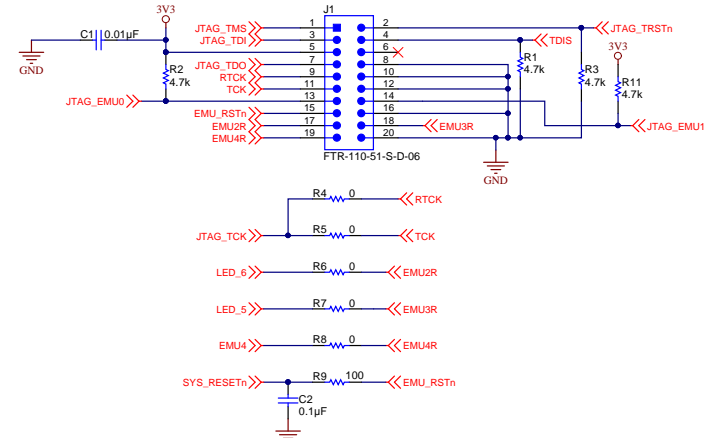
McASP A and B not supported on interface

McASP A and B not supported on interface

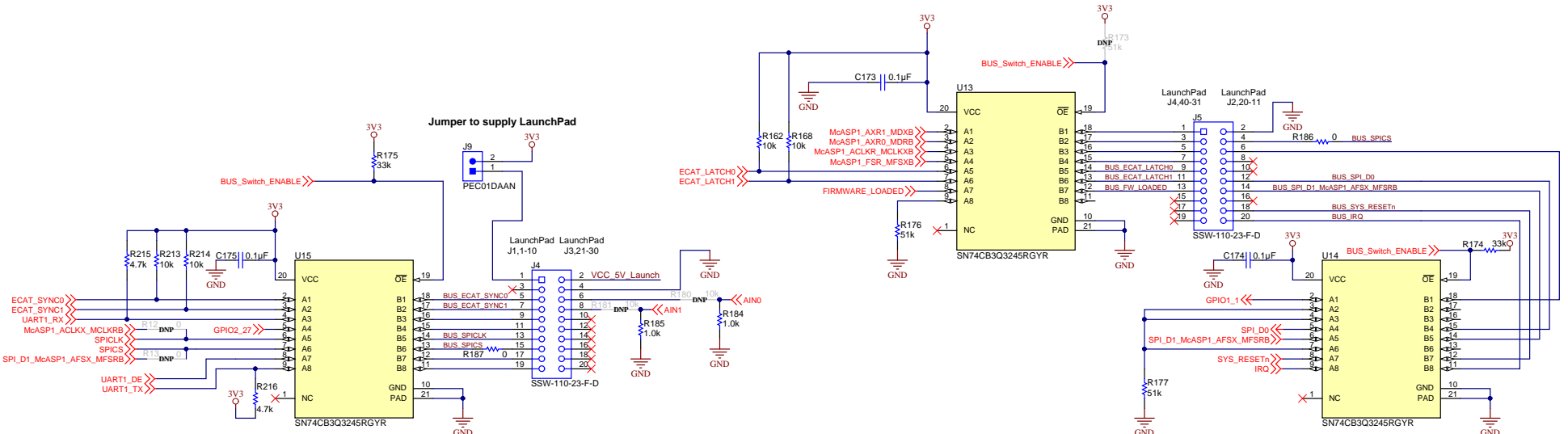
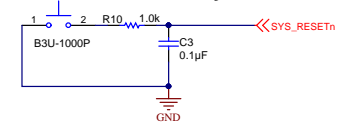
Please note due to symbol definition the connector pin assignment numbering is swapped vs the f28779d ControlCard connector




## 41



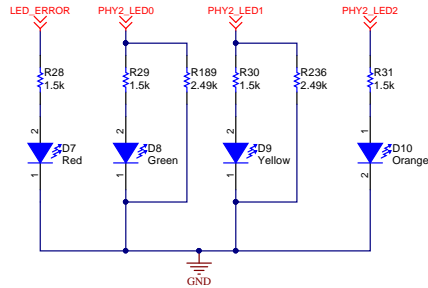
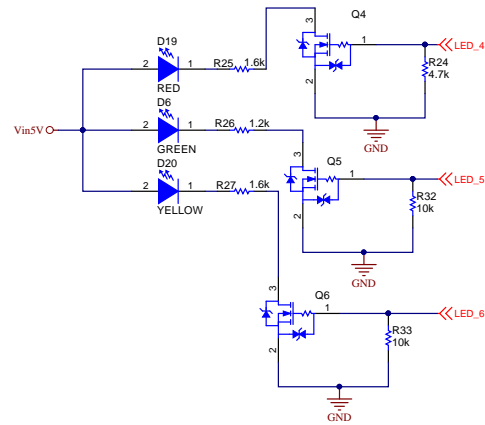
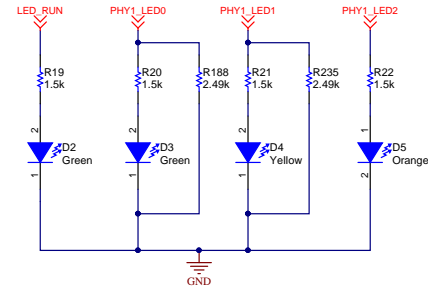
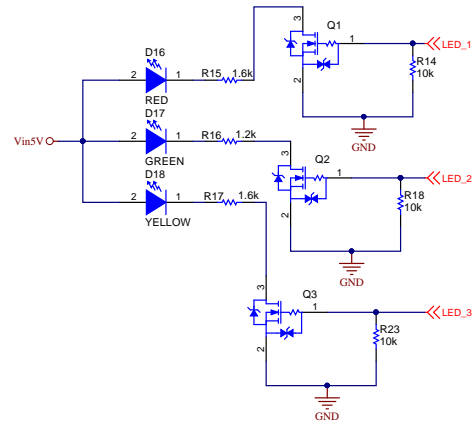
## R10 1.0k 11



Orderable: <b>TMDXICE110-BTA</b>	Designed for: <b>N/A</b>	Mod. Date: 4/4/2017	 <b>TEXAS</b> <b>INSTRUMENTS</b>
Number: <b>PROC040</b>	Project Title: <b>AMIC110 ICE EVM</b>		
SVN Rev: <b>1</b>	Sheet Title: <b>Connectors</b>		
Drawn By: <b>N/A</b>	Assembly Variant: <b>N/A</b>	Sheet: <b>8</b> of <b>10</b>	
Engineer: <b>N/A</b>	File: <b>AMIC110 ICE EVM Interfaces SchDoc</b>	Size: <b>B</b>	
Contact: <b>N/A</b>			<a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2017

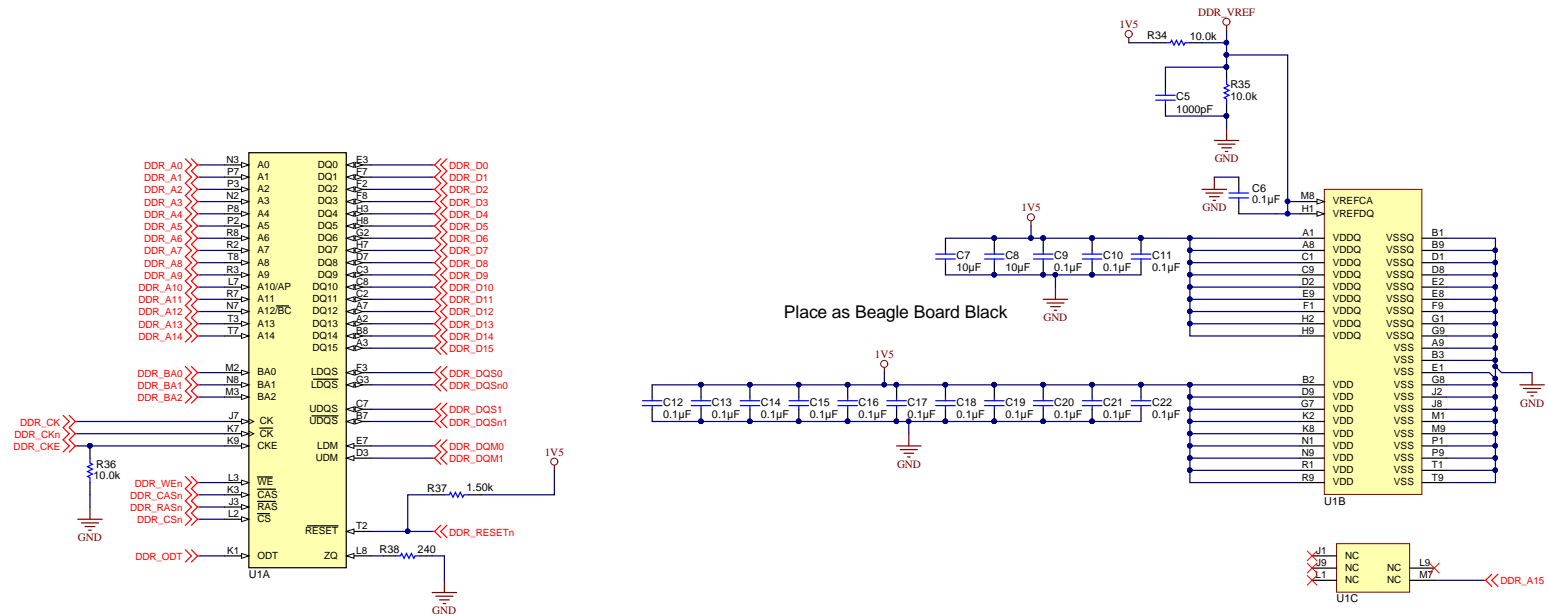


# Real Time Ethernet LEDs

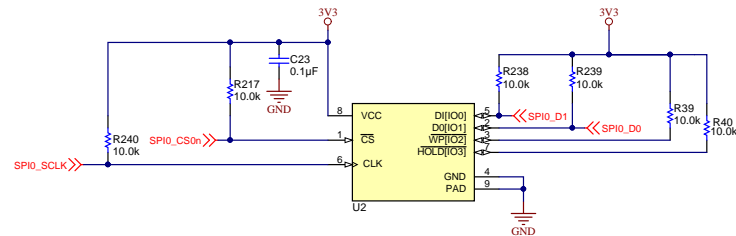


Orderable: TMDXICE110-BTA	Designed for: N/A	Mod. Date: 4/4/2017
TID #: N/A	Project Title: AMIC110 ICE EVM	
Number: PROC040   Rev: 1.1	Sheet Title: LEDs	
SVN Rev: N/A	Assembly Variant: N/A	Sheet: 9 of 10
Drawn By: N/A	File: AMIC110 ICE EVM_Led.SchDoc	Size: B
Engineer: N/A	Contact: N/A	

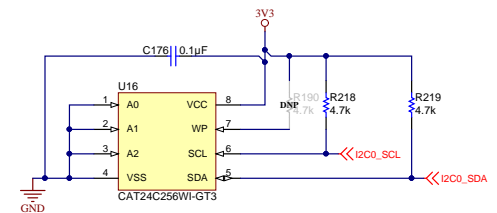
## DDR3 Interface




## SPI Boot Flash



## Board ID EEPROM



Order: <b>TMDXICE110-BTA</b>		Designed for: <b>N/A</b>		Mod. Date: 4/4/2017		 <b>TEXAS INSTRUMENTS</b> <a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2017
Title: <b>N/A</b>		Project Title: <b>AMC110 ICE EVM</b>				
Number: <b>PROC040</b> [Rev: 1.1]		Sheet Title: <b>Memory</b>				
SVN Rev: <b>N/A</b>		Assembly Variant: <b>N/A</b>		Sheet: <b>10 of 10</b>		
Drawn By: <b>N/A</b>		File: <b>AMC110 ICE EVM_Memory_SchDoc</b>		Size: <b>B</b>		
Engineer: <b>N/A</b>		Contact: <b>N/A</b>				