

AM263P CC

PROC159A(001)

TABLE OF CONTENTS

PAGE	CONTENTS
01	TABLE OF CONTENTS
02	REVISION HISTORY
03	SYSTEM BLOCK DIAGRAM
04	POWER FLOW DIAGRAM
05	POWER SEQUENCING
06	I2C TREE
07	GPIO_MAPPING_TABLE
08	POWER INPUT
09	PMIC
10	POWER SUPPLY #1
11	CURRENT MONITORING DEVICES
12	ETHERNET POWERS
13	SOC-POWER and GND
14	SOC-QSPI & OSPI INTERFACE
15	SOC-MMC0 INTERFACE
16	SOC-IO INTERFACES
17	SOC-JTAG, RESET and CLK
18	MCAN AND FSI MUX
19	SOC-PRU0 ICSS MII0, CPSW RGMII/MII Ethernet
20	SOC-PRU1 ICSS MII1 Ethernet
21	ICSS ETHERNET/HSEC MUXES
22	ICSS ON-BOARD/ADD-ON PHY & MII0/MII1 MUXES
23	CLOCKS AND LIN1
24	TEST AUTOMATION HEADER
25	BOOTMODE BUFFER AND SWITCH
26	XDS110 DEBUGGER
27	AUTOMATION SIGNALS BUFFER
28	LED DRIVER AND IO EXPANDER
29	SOC-ADC & DAC INTERFACE
30	ADC MUXES
31	HSEC CONNECTOR
32	RESET SWITCHES
33	CC EVM NOTES, HW and LABELS

Designed for TI by Mistral Solutions Pvt Ltd



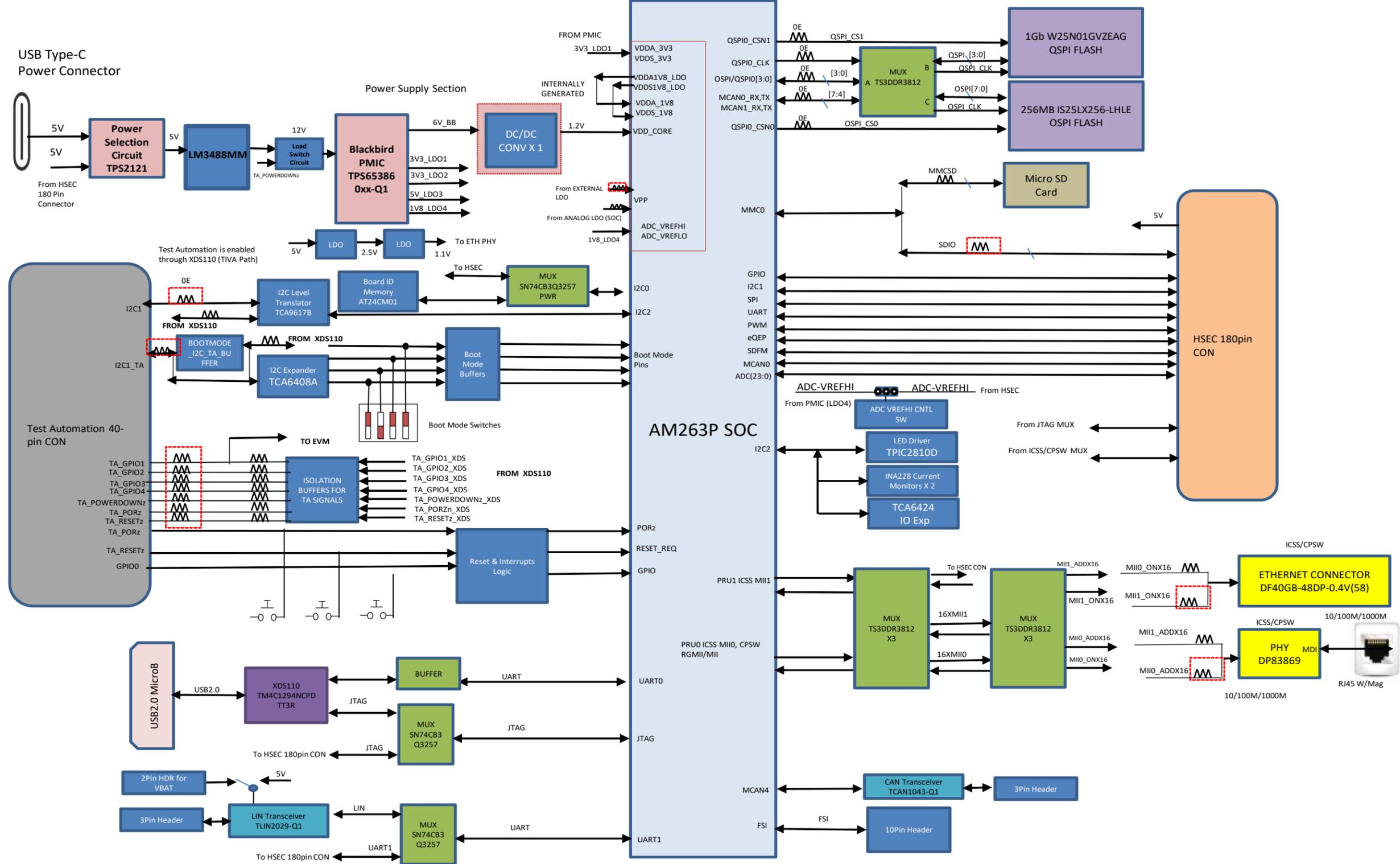
Title TABLE OF CONTENTS

Size	Variant Name = PROC159A(001)	Rev
C		A
Date:	Thursday, April 04, 2024	Sheet 1 of 33

REVISION HISTORY

VER #	DATE	DESCRIPTION OF CHANGES	AUTHOR	REVIEWED BY	APPROVED BY
0.01	12 FEB 2024	Drafted from REV E2 Schematics. DNI'd R372, OSPI reset circuit updated, Updated Ethernet connector I2C address configuration and pin addition	Mistral Design Team		
0.02	20 FEB 2024	Added a Schematic note for Ethernet connector	Mistral Design Team		
0.03	4 APR 2024	I2C Addressing pull downs for Eth connector changed to 1K. Added Silkscreen label for Eth Connector.	Mistral Design Team		
0.04	25 APR 2024	Updated R23 = 49.9kOhm, C22 = 560pF, C23 = 1.0uF in order to change switching frequency of LM3488 to 333KHz	Mistral Design Team		

SYSTEM BLOCK DIAGRAM



Designed for TI by Mistral Solutions Pvt Ltd



Title SYSTEM BLOCK DGM

Size	Variant Name = PROC159A(001)	Rev
C		A
Date:	Thursday, April 04, 2024	Sheet 3 of 33

POWER SEQUENCE

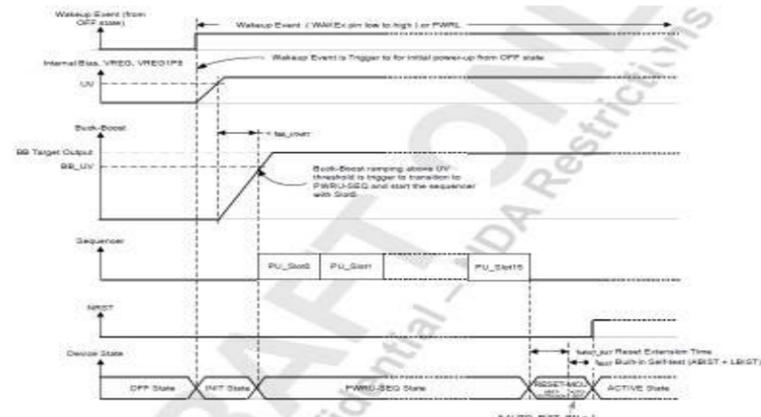
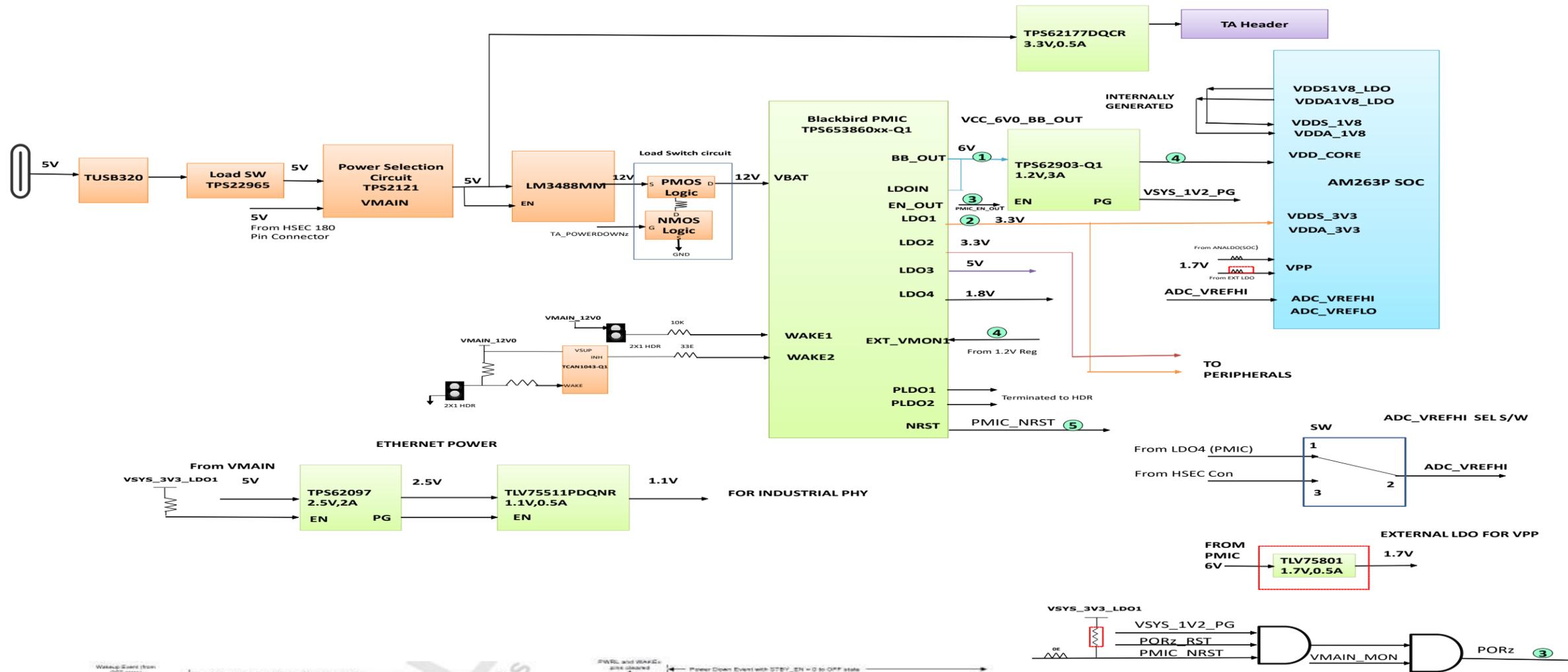


Figure 9-6. Power-Up from OFF state Example

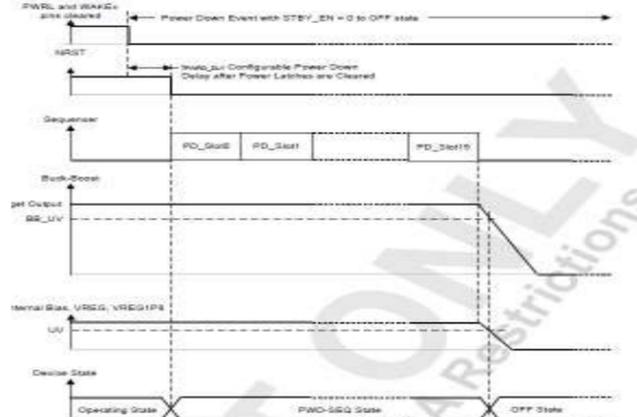


Figure 9-8. Power-Down to OFF state Example



Designed for TI by Mistral Solutions Pvt Ltd



Title POWER SEQUENCE

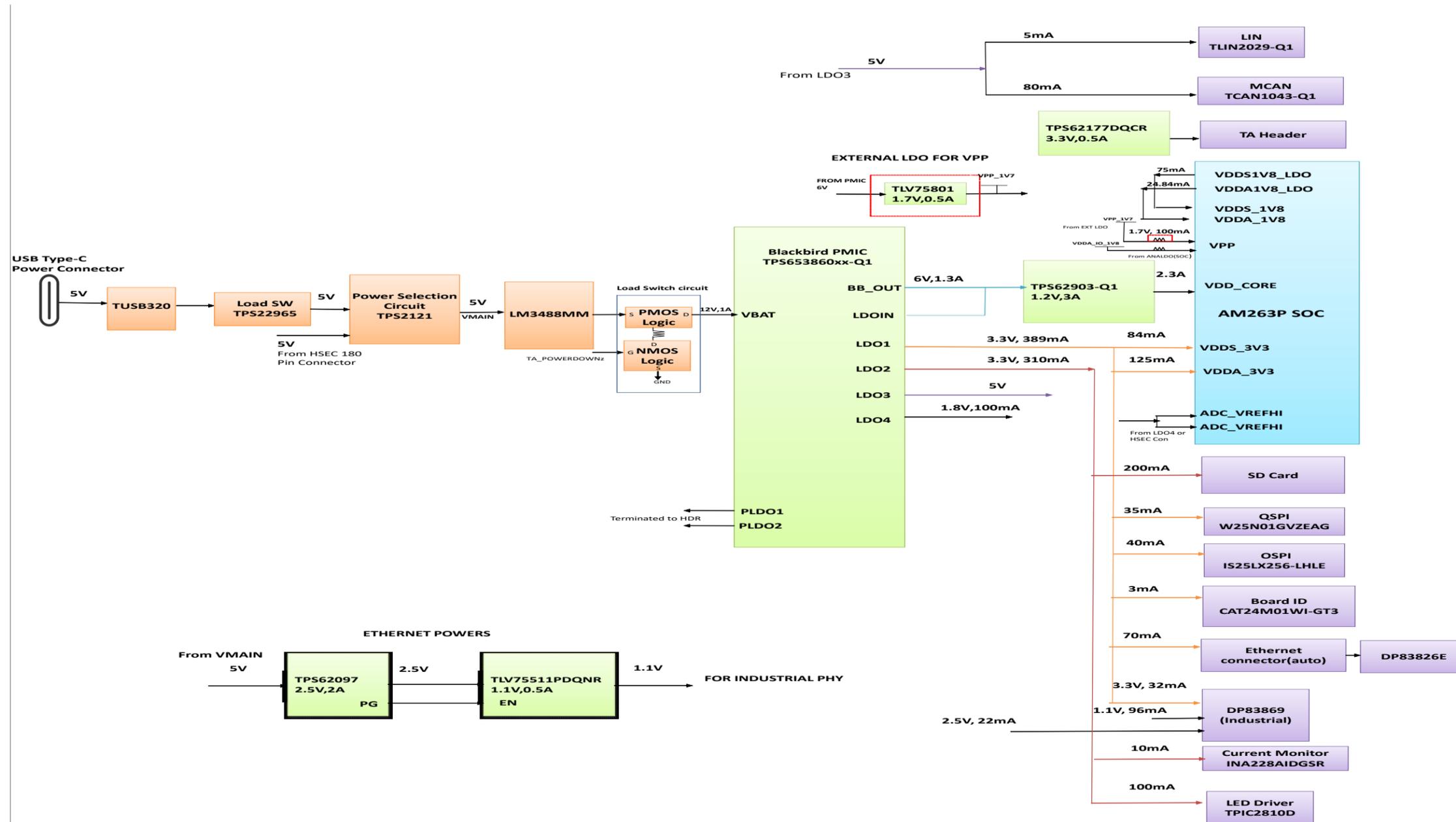
Size Variant Name = PROC159A(001)

Date: Thursday, April 04, 2024

Rev A

Sheet 4 of 33

POWER FLOW DIAGRAM



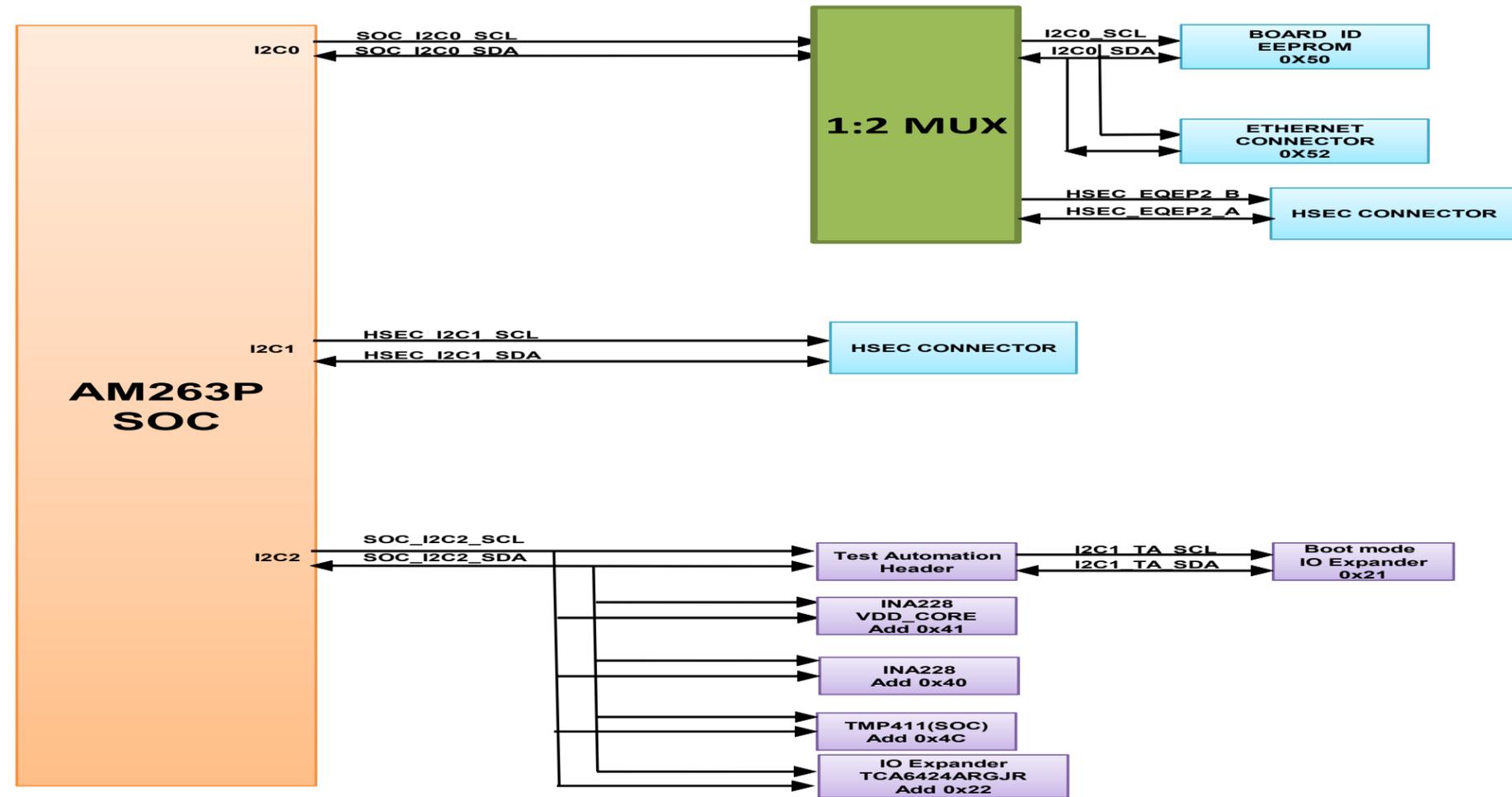
Designed for TI by Mistral Solutions Pvt Ltd



Title POWER FLOW DIAGRAM

Size		Rev
C	Variant Name = PROC159A(001)	A
Date:	Thursday, April 04, 2024	Sheet 5 of 33

I2C TREE DIAGRAM



GPIO MAPPING TABLE

SI No.	GPIO DESCRIPTION	GPIO	Pin Name	FUNCTIONALITY	Net Name	ACTIVE STATE
1	Interrupt To SoC	GPIO21	LIN2_RXD	Interrupt	SOC_INTn	LOW
2	Interrupt To DP83826E/DP83TG720	GPIO67	EPWM12_A	Interrupt	ICSSM2_PWDN/INTn	LOW
3	User Defined LED	GPIO66	EPWM11_B	GPIO	USER_LED1	PREFERABLE
4	Interrupt To DP83869	GPIO68	EPWM12_B	Interrupt	ICSSM1_INT	LOW
5	User Defined LED	GPIO22	LIN2_TXD	GPIO	USER_LED0	PREFERABLE
6	Reset input to DP83869	GPIO35	RGMII1_TXC	Reset	GPIO_ICSSM1_RST	LOW
9	Reset input to Ethernet connector	GPIO36	RGMII1_TX_CTL	Reset	GPIO_ICSSM2_RST	LOW
10	Interrupt To SoC from PMIC	GPIO29	RGMII1_RXC	Interrupt	PMIC_INTn	LOW
11	Select line for OSPI and QSPI	GPIO37	RGMII1_TDO	Mux Selection	OSPI/QSPI_MUX_SEL	PREFERABLE
IO Expander 01						
13	Enable control to clock buffer		P01	Enable	CLK_BUF_EN	HIGH
14	Select line for ICSSM Ethernet/HSEC Mux (PRU1 signals)		P02	Mux Selection	ICSSM1_MUX_SEL	PREFERABLE
15	Select line for ICSSM ON-Board/ADD-ON PHY Mux		P03	Mux Selection	ICSSM2_MUX_SEL	PREFERABLE
16	Select line for MCAN and FSI MUX		P04	Mux Selection	FSI_MUX_SEL	PREFERABLE
17	Select line for ADC MUX 3		P05	Mux Selection	ADC3_MUX_SEL	PREFERABLE
18	Select line for ADC MUX 4		P06	Mux Selection	ADC4_MUX_SEL	PREFERABLE
19	Enable control to SD load switch		P07	Load SW Enable	GPIO_uSD_PWR_EN	HIGH
20	Select line for ADC MUX 5		P10	Mux Selection	ADC5_MUX_SEL	PREFERABLE
21	Select line for I2C0 MUX		P11	Mux Selection	I2C0_MUX_SEL	PREFERABLE
22	Select line for SPI1 MUX		P12	Mux Selection	SPI1_MUX_SEL	PREFERABLE
23	Select line for UART2 MUX		P13	MUX Selection	UART2_MUX_SEL	PREFERABLE
24	Enable control to 1.7V LDO		P14	LDO Enable	VPP_LDO_EN	PREFERABLE
25	Select line for LIN/UART MUX		P15	Mux Selection	LIN_MUX_SEL	PREFERABLE
26	Select line for ADC MUX 1		P16	Mux Selection	ADC1_MUX_SEL	PREFERABLE
27	Select line for ADC MUX 2		P17	Mux Selection	ADC2_MUX_SEL	PREFERABLE
28	GPIO to HSEC		P20	GPIO	HSEC_GPIO	PREFERABLE
29	Standby input to CAN transceiver		P21	GPIO	MCAN1_STB	HIGH
30	Select line for MDIO/MDC Mux sel 1		P22	Mux Selection	MDIO/MDC_MUX_SEL1	PREFERABLE
31	Select line for MDIO/MDC Mux sel 2		P23	Mux Selection	MDIO/MDC_MUX_SEL2	PREFERABLE
32	Select line for ICSSM Ethernet/HSEC Mux (PRU0 signals)		P24	Mux Selection	ICSSM0_MUX_SEL	PREFERABLE

Designed for TI by Mistral Solutions Pvt Ltd



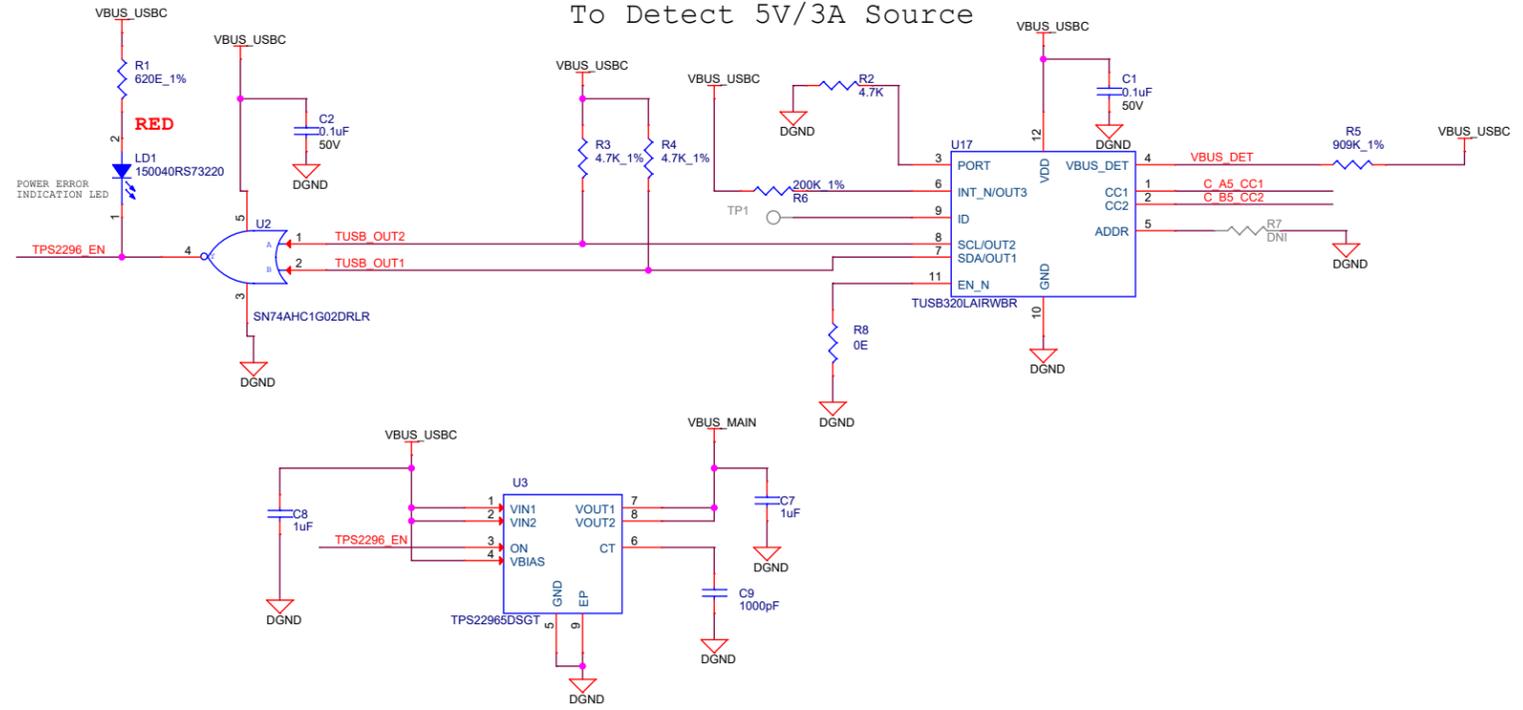
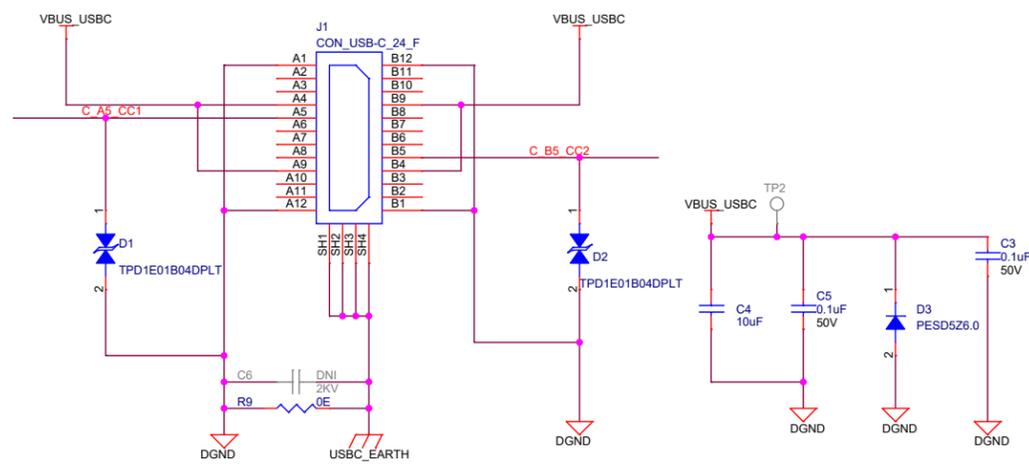
Title: GPIO MAPPING TABLE

Size	Variant Name = PROC159A(001)	Rev
C		A
Date:	Thursday, April 04, 2024	Sheet 7 of 33

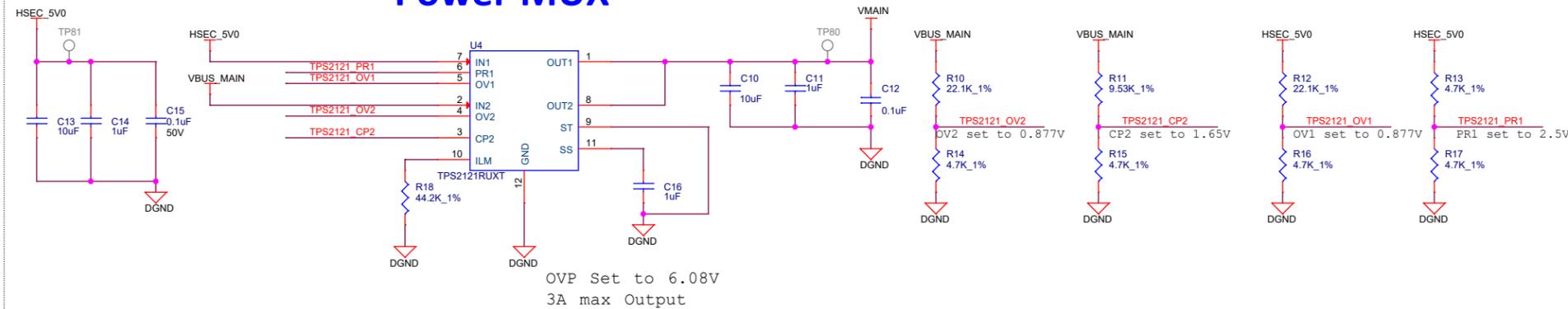
USB-C Power

Configured as UFP MODE
To Detect 5V/3A Source

USB-C Connector



Power MUX

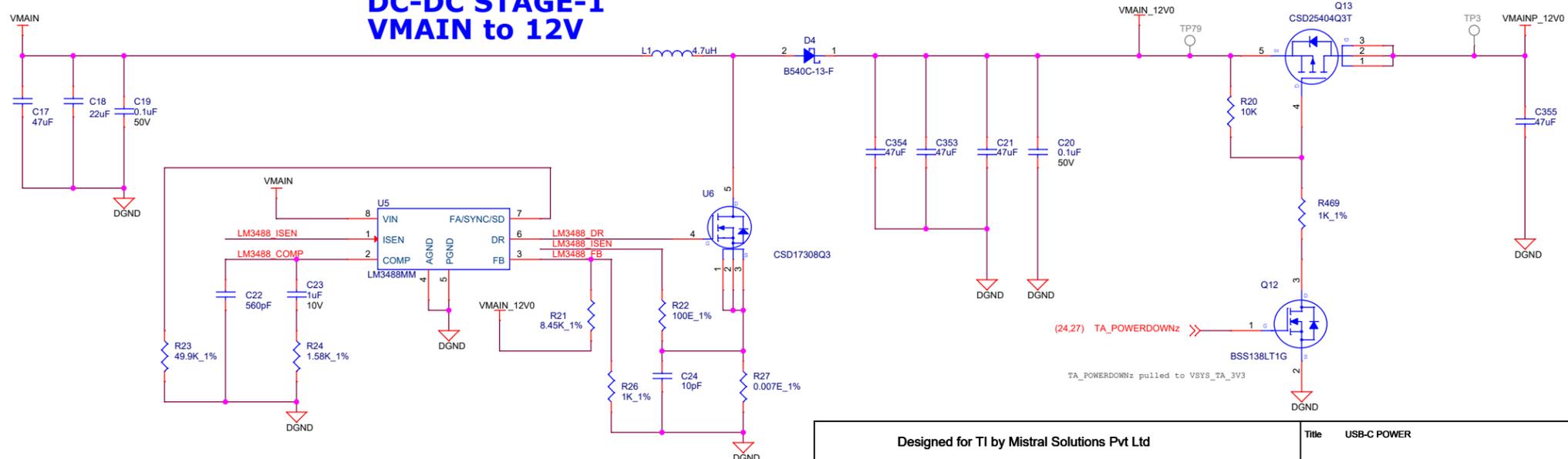


XCOMP	OUTPUT
PR1 > CP2	HSEC_5V0
PR1 = < CP2	VBUS_MAIN

OVP Set to 6.08V
3A max Output

Designed as per reference sch

DC-DC STAGE-1 VMAIN to 12V



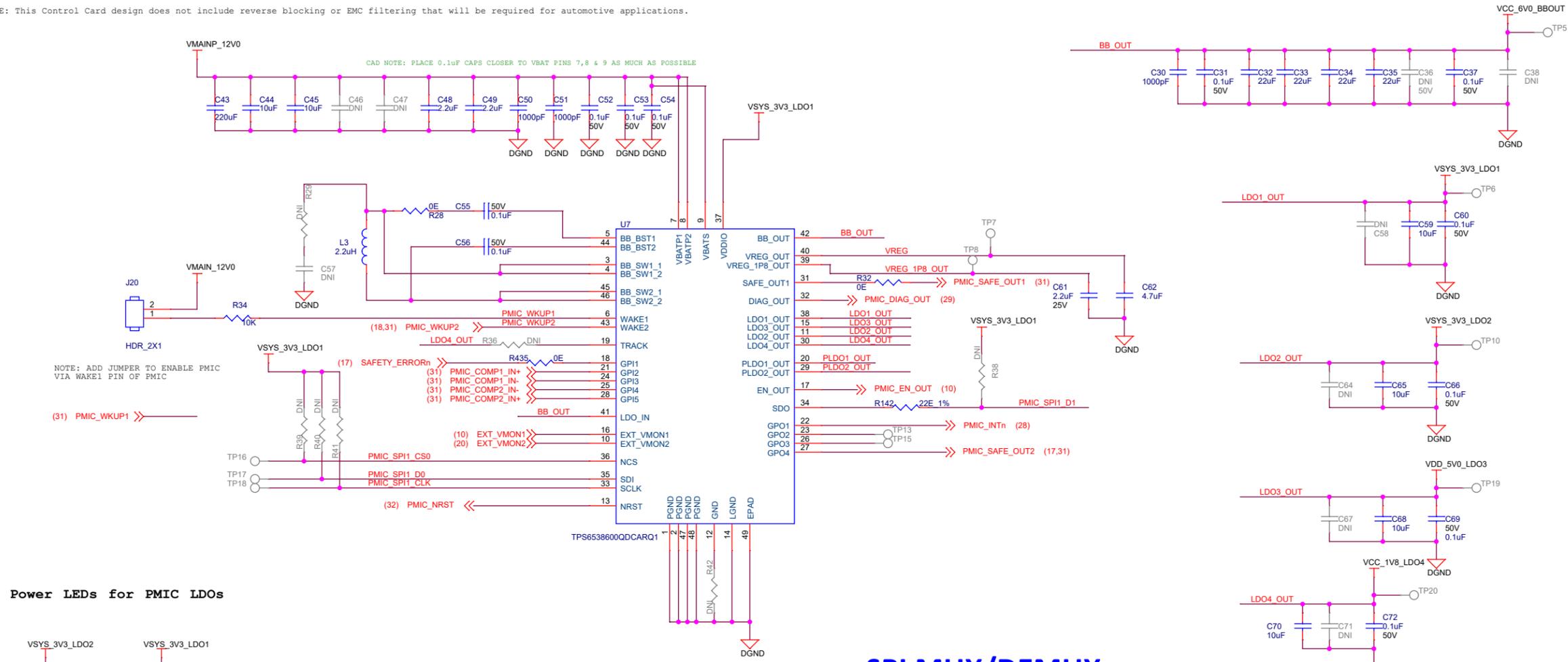
Designed for TI by Mistral Solutions Pvt Ltd



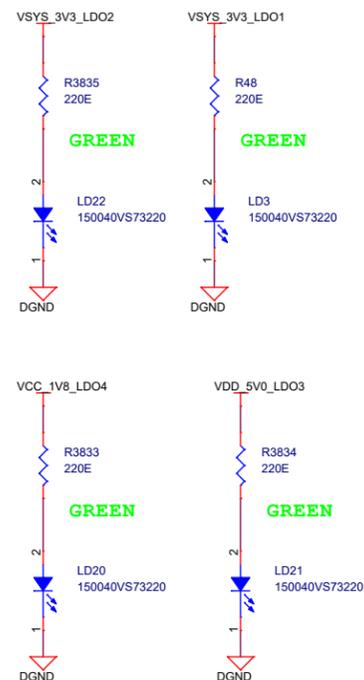
Title		USB-C POWER	
Size	Variant Name = PROC159A(001)	Rev	
C		A	
Date:	Thursday, April 25, 2024	Sheet	8 of 33

BLACKBIRD PMIC

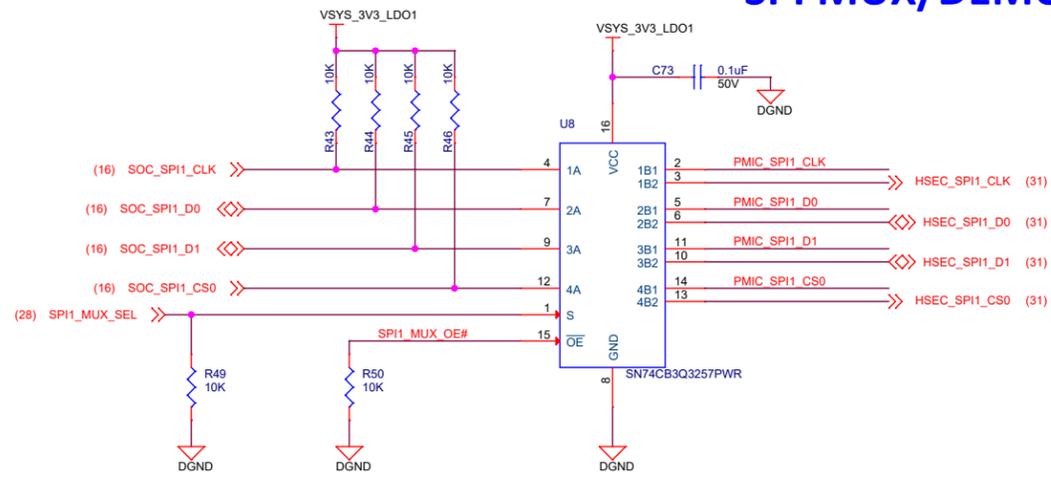
NOTE: This Control Card design does not include reverse blocking or EMC filtering that will be required for automotive applications.



Power LEDs for PMIC LDOs



SPI MUX/DEMUX



SPI1 - 1:2 MUX

SEL	CONDITION	FUNCTION
LOW	PMIC SELECTED	A-->B1 port
HIGH	HSEC SPI1 selected	A-->B2 port

Designed for TI by Mistral Solutions Pvt Ltd



Title BLACKBIRD PMIC

Size Variant Name = PROC159A(001)

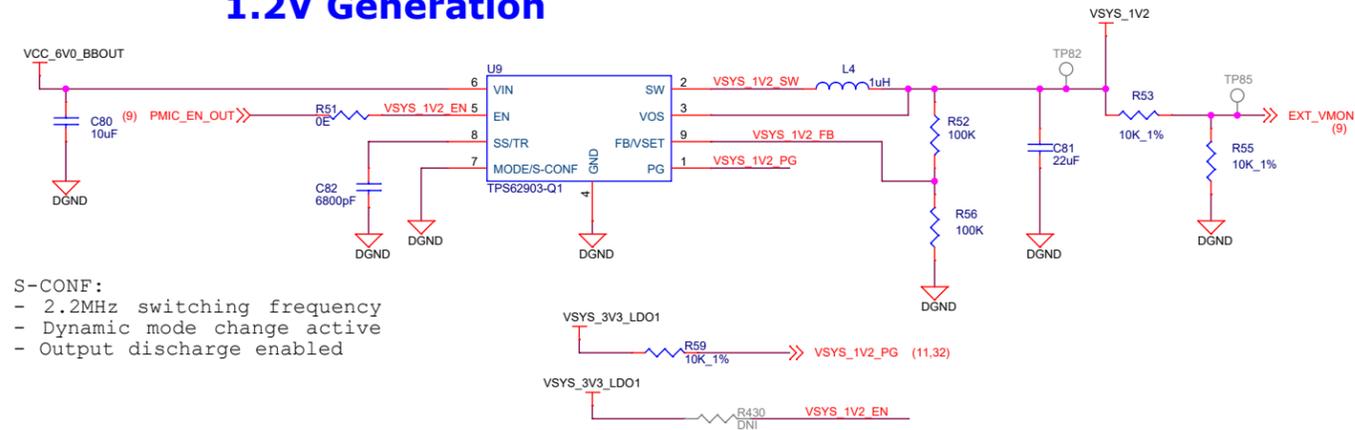
Date: Thursday, April 04, 2024

Sheet 9 of 33

Rev A

Power Supply #1

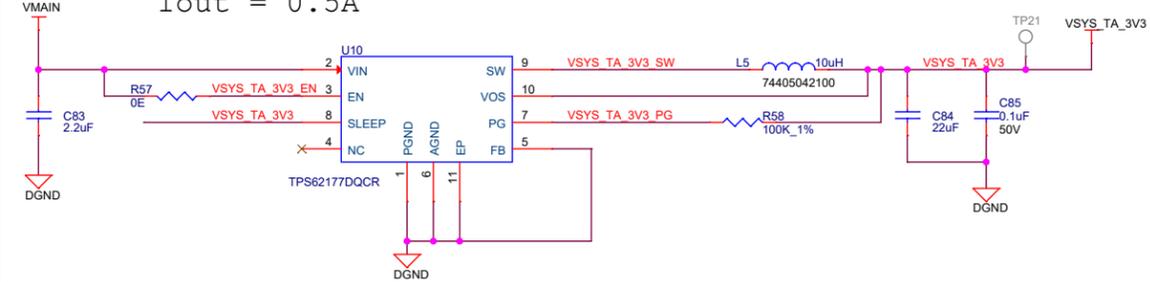
1.2V Generation



S-CONF:
 - 2.2MHz switching frequency
 - Dynamic mode change active
 - Output discharge enabled

Test Automation Header Supply

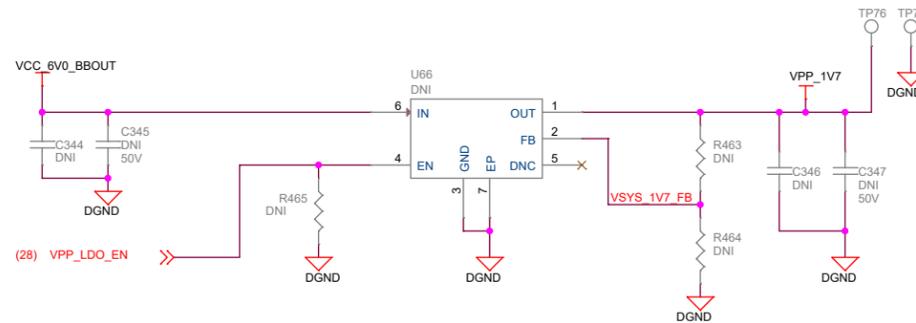
TPS62177 3.3V BUCK REGULATOR
 Vout = 3.3V
 Iout = 0.5A



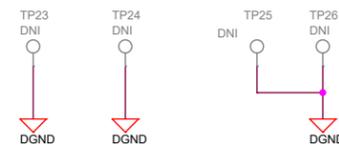
TLV75801
 Vout=1.7V
 Iout = .5A

1.7V VPP Generation

Place testpoints with
 100mils spacing to
 insert external jumper



Ground Test Points



Designed for TI by Mistral Solutions Pvt Ltd



Title POWER SUPPLY #1

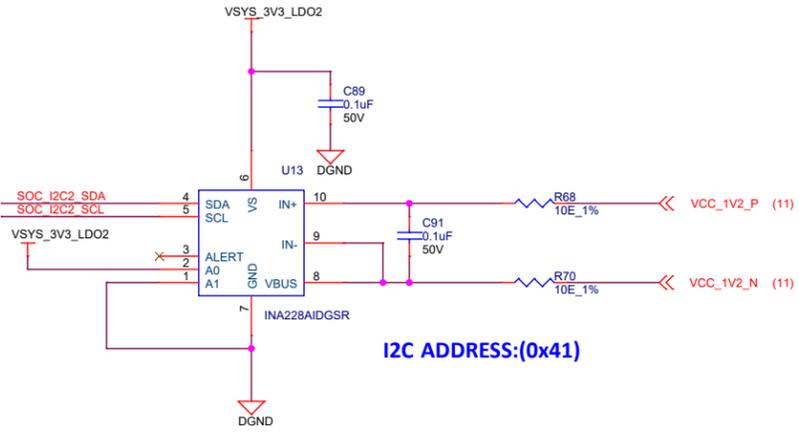
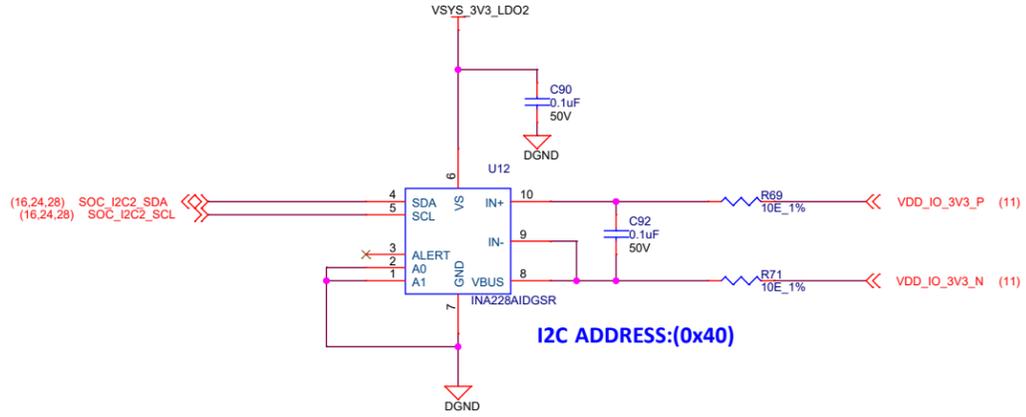
Size C Variant Name = PROC159A(001)

Date: Thursday, April 04, 2024

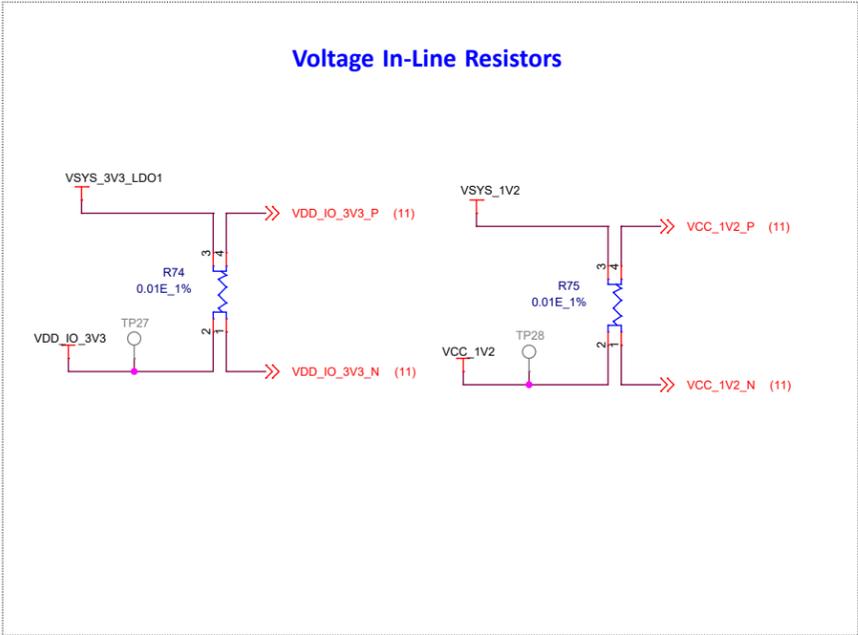
Sheet 10 of 33

Rev A

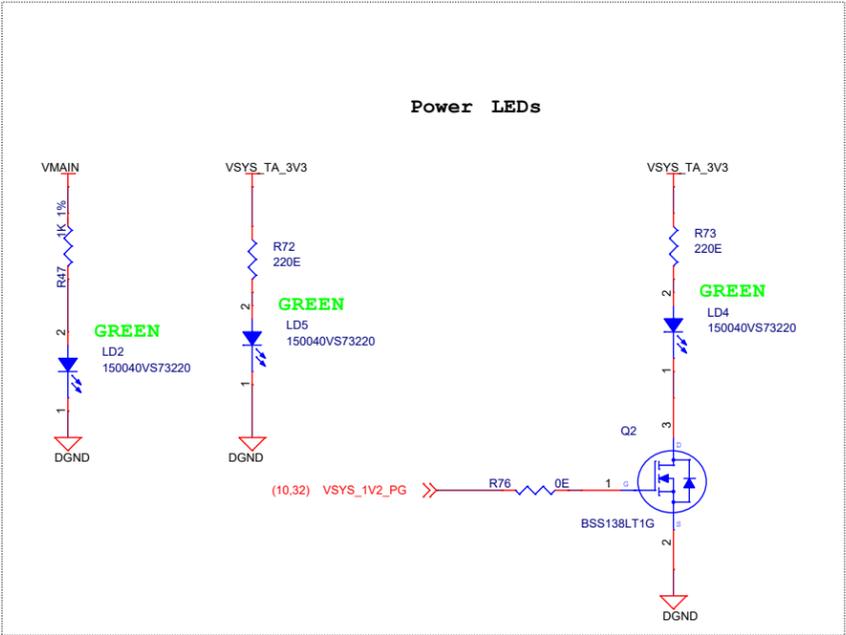
Current Monitors



Voltage In-Line Resistors

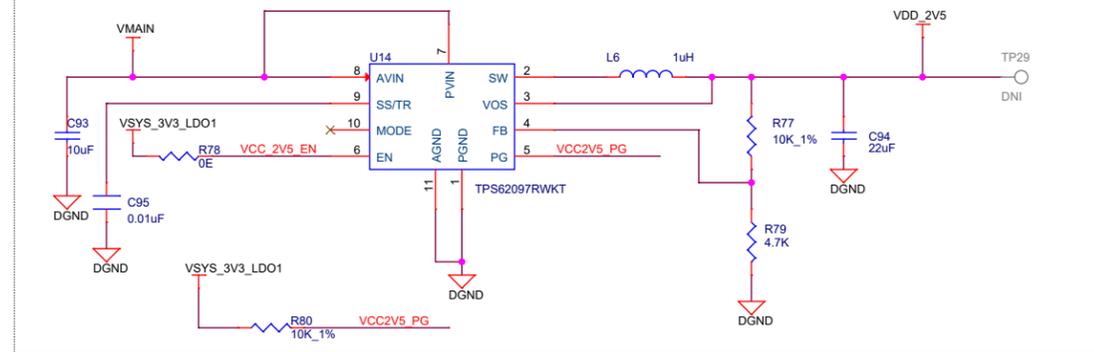


Power LEDs

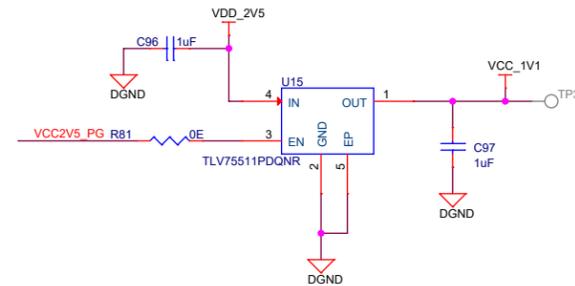


Ethernet Powers

2.5V ETHERNET PHY POWER SUPPLY

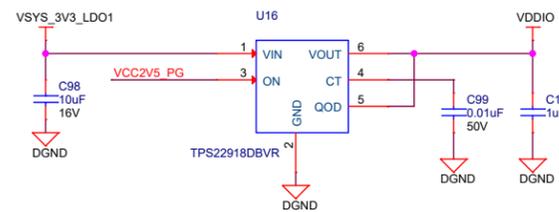


1.1V, 0.5AMPS SUPPLY



FOR INDUS PHY

Load Switch



Designed for TI by Mistral Solutions Pvt Ltd



Title ETHERNET POWERS

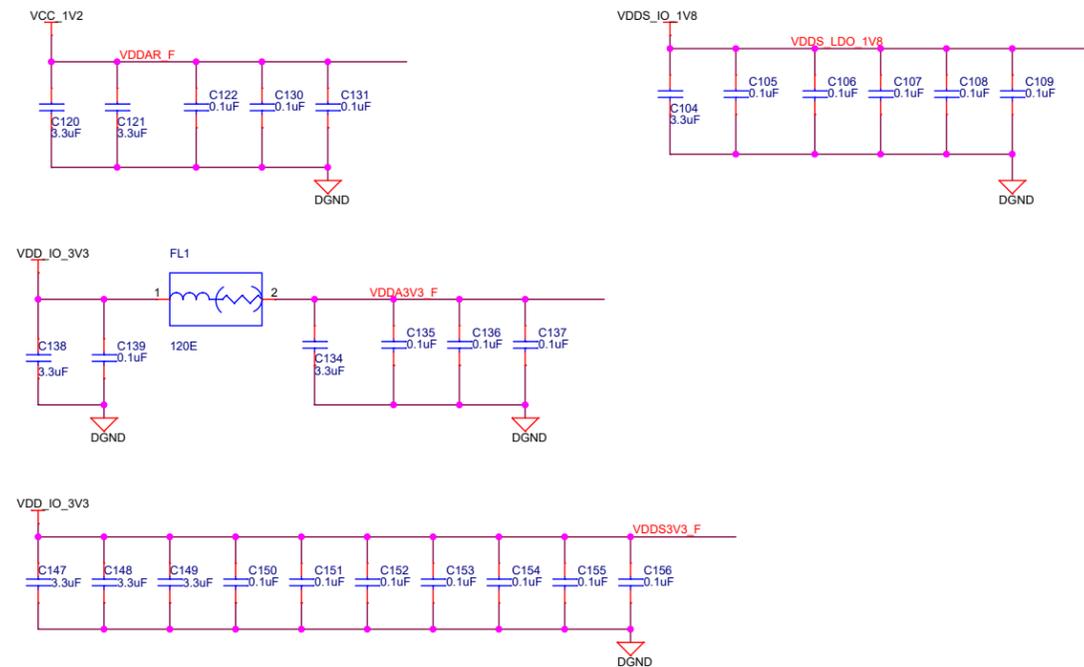
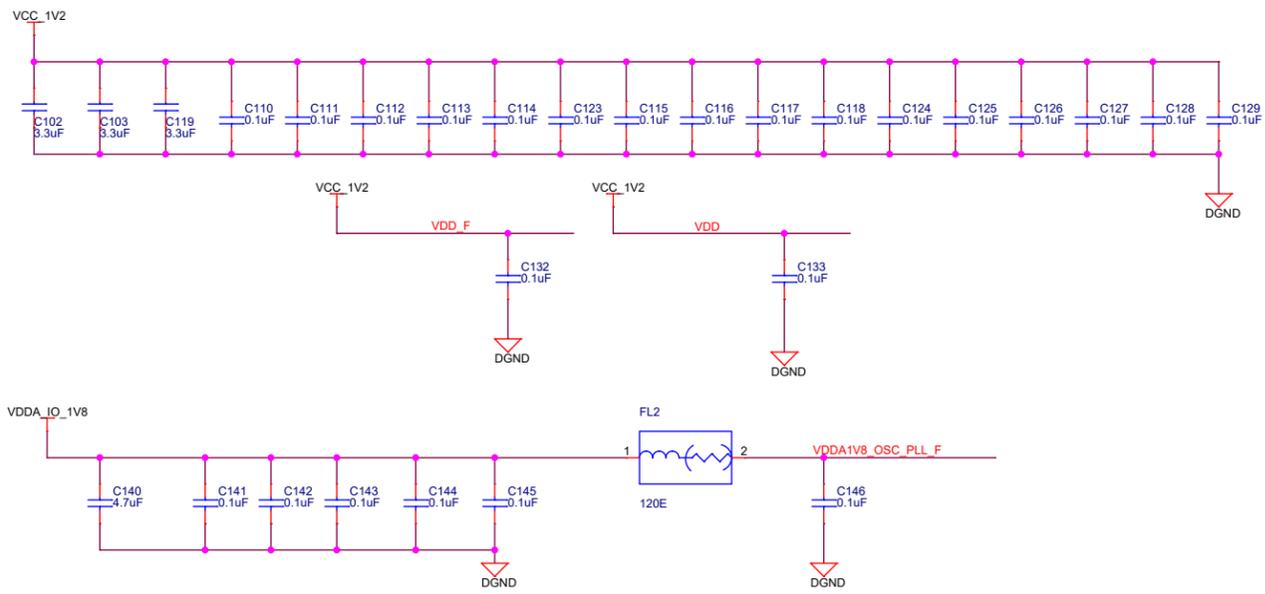
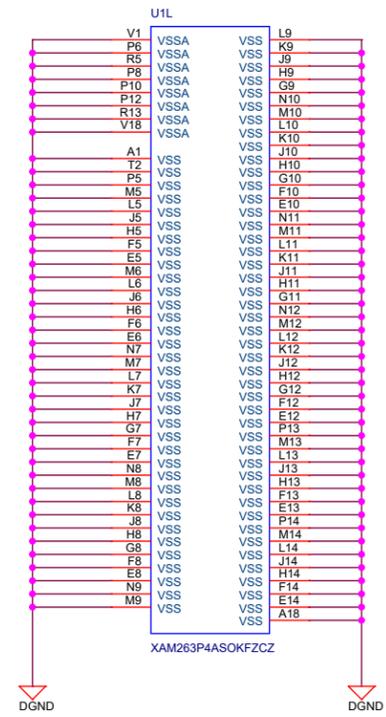
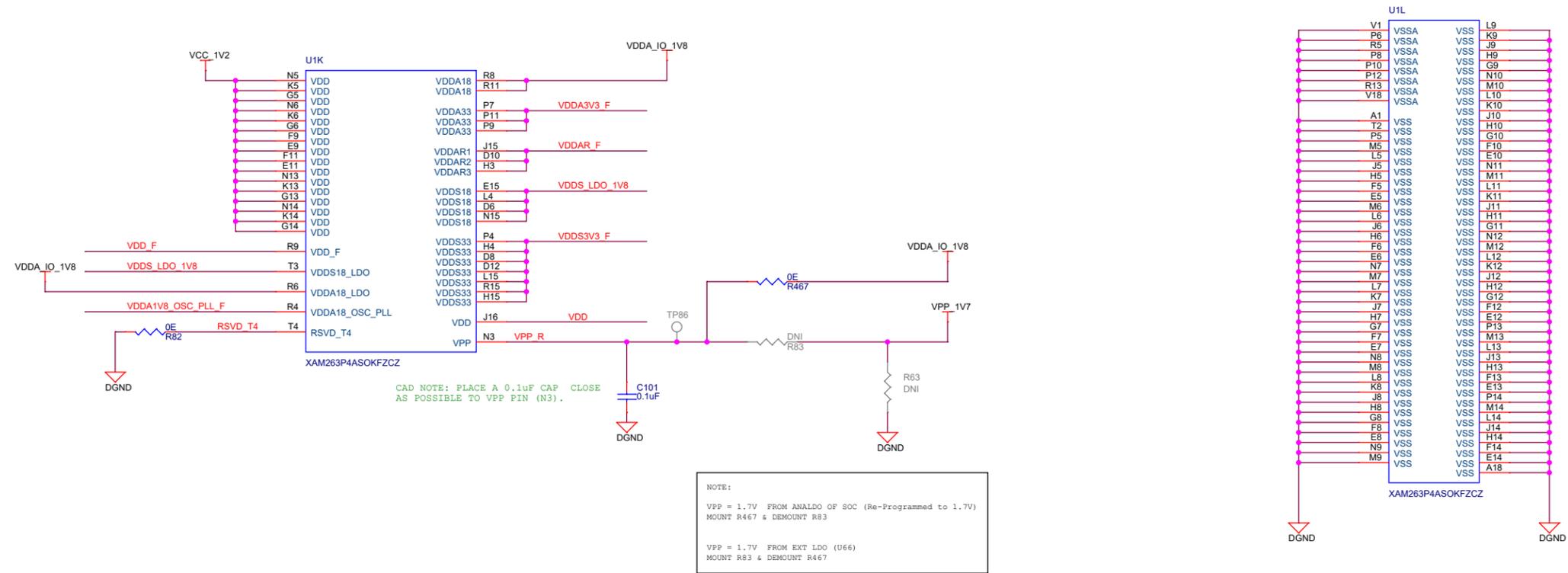
Size C Variant Name = PROC159A(001)

Rev A

Date: Thursday, April 04, 2024

Sheet 12 of 33

SOC-POWER and GND



Designed for TI by Mistral Solutions Pvt Ltd



Title SOC POWER AND GND

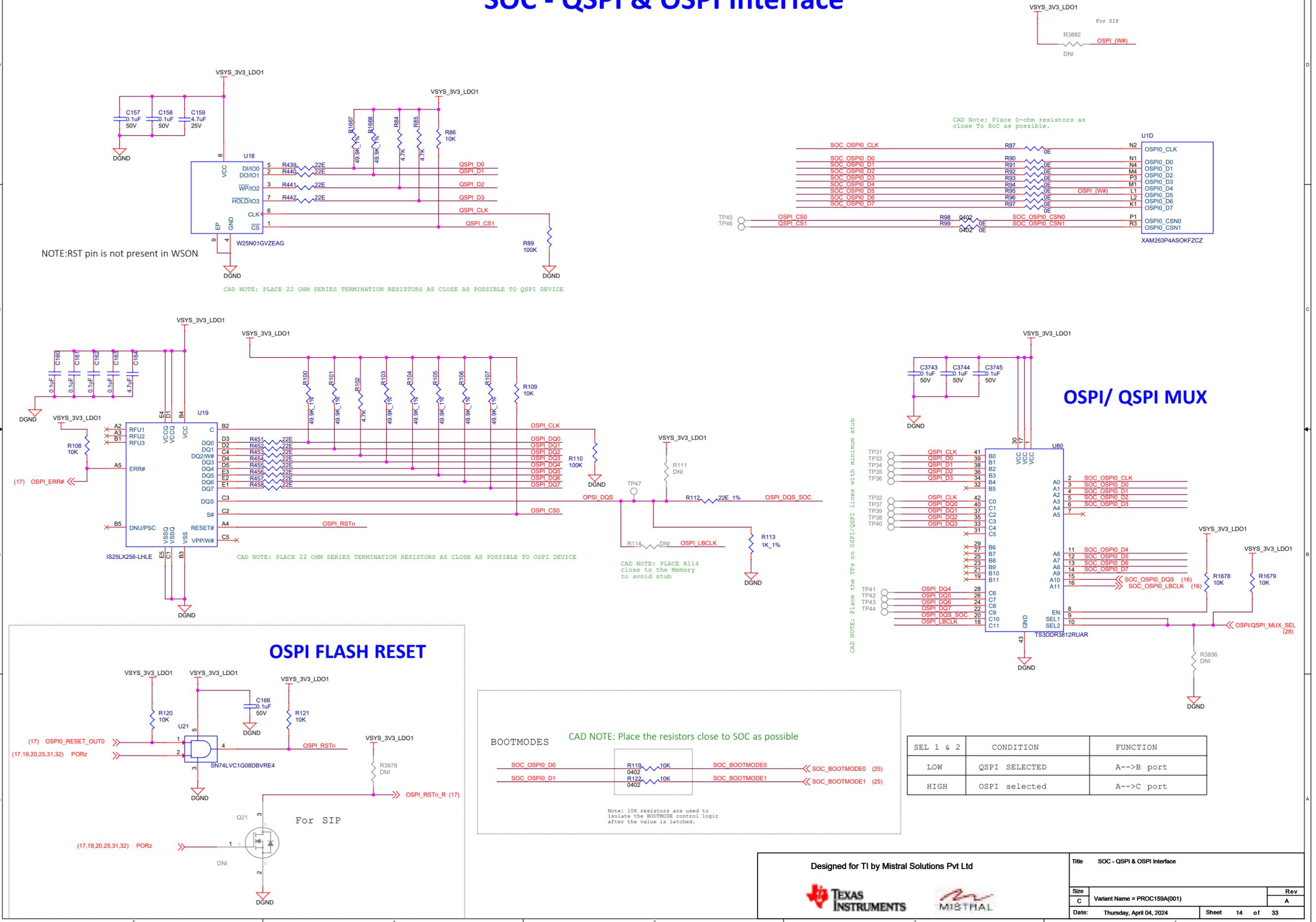
Size Variant Name = PROC159A(001)

Date: Thursday, April 04, 2024

Rev A

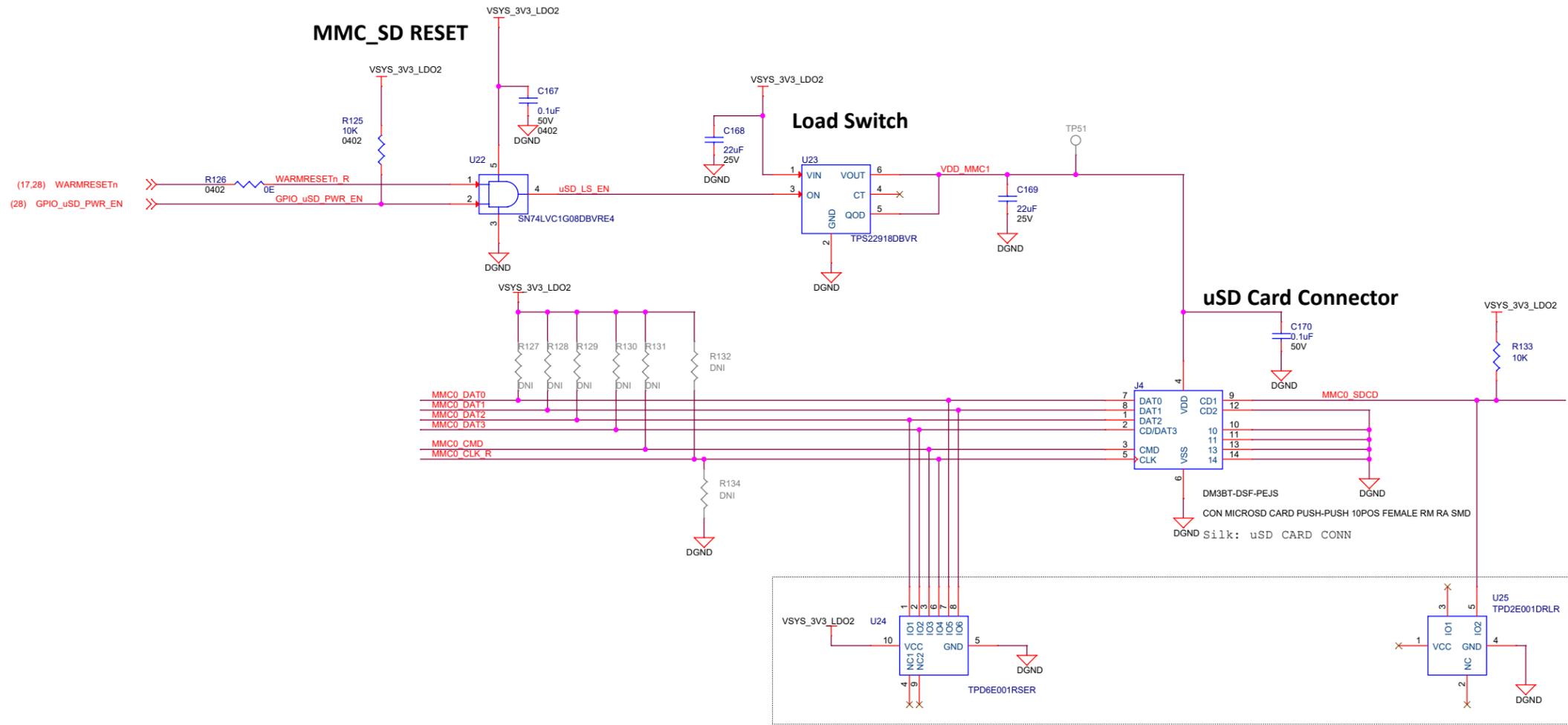
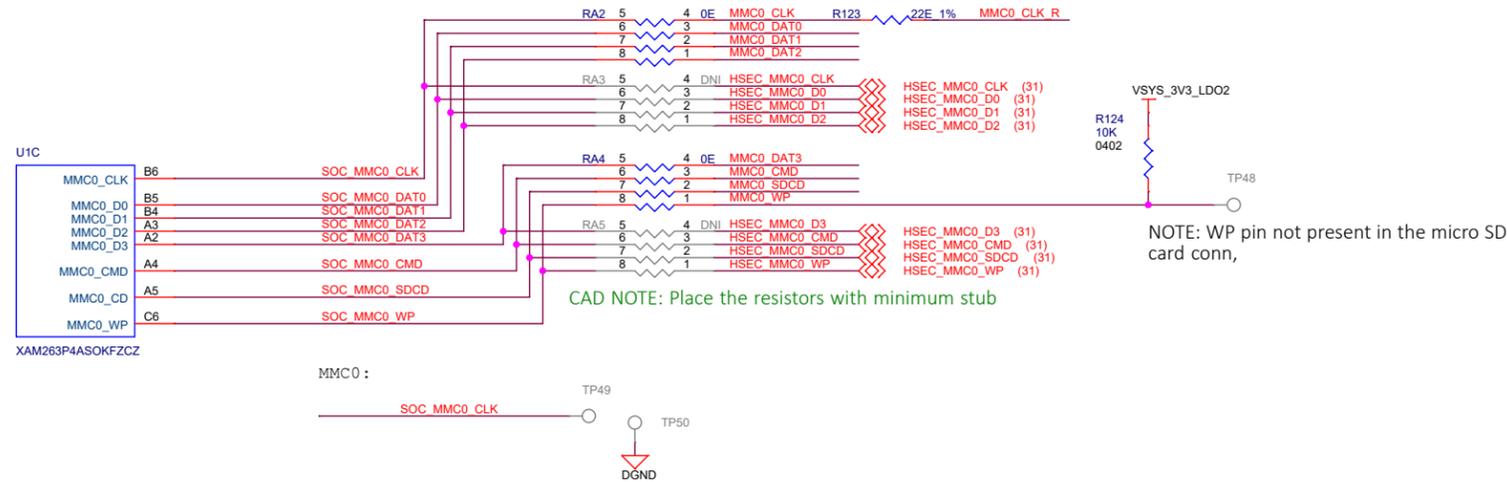
Sheet 13 of 33

SOC - QSPI & OSPI Interface



SEL 1 & 2	CONDITION	FUNCTION
LOW	QSPI SELECTED	A-->B port
HIGH	OSPI selected	A-->C port

SOC- MMC0 Interface



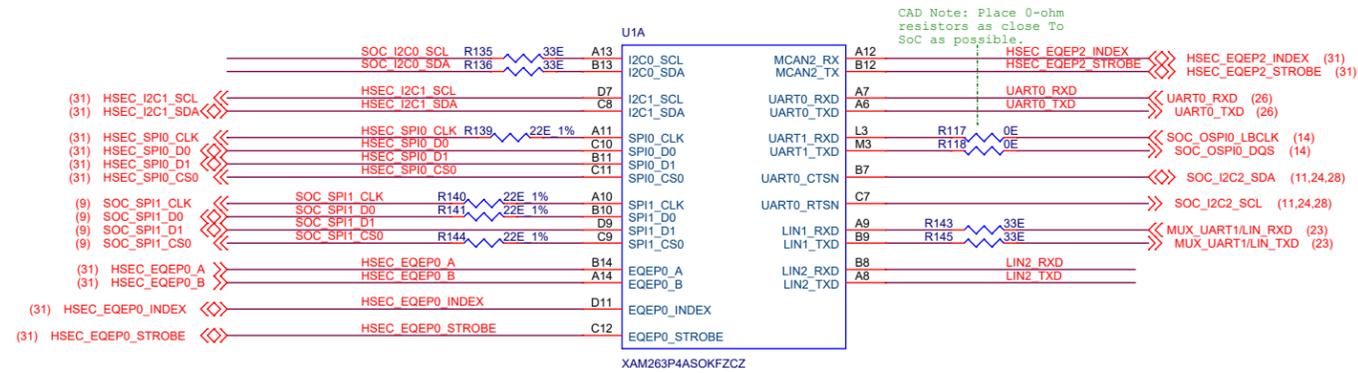
Designed for TI by Mistral Solutions Pvt Ltd



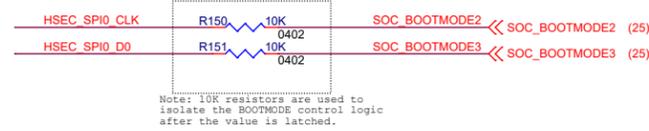
Title SOC- MMC0 Interface

Size	Variant Name = PROC159A(001)	Rev
C		A
Date:	Thursday, April 04, 2024	Sheet 15 of 33

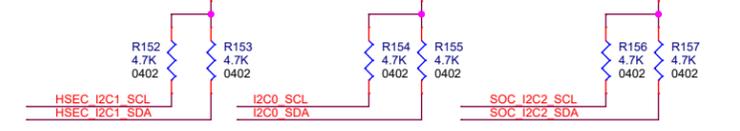
SOC-IO Interfaces



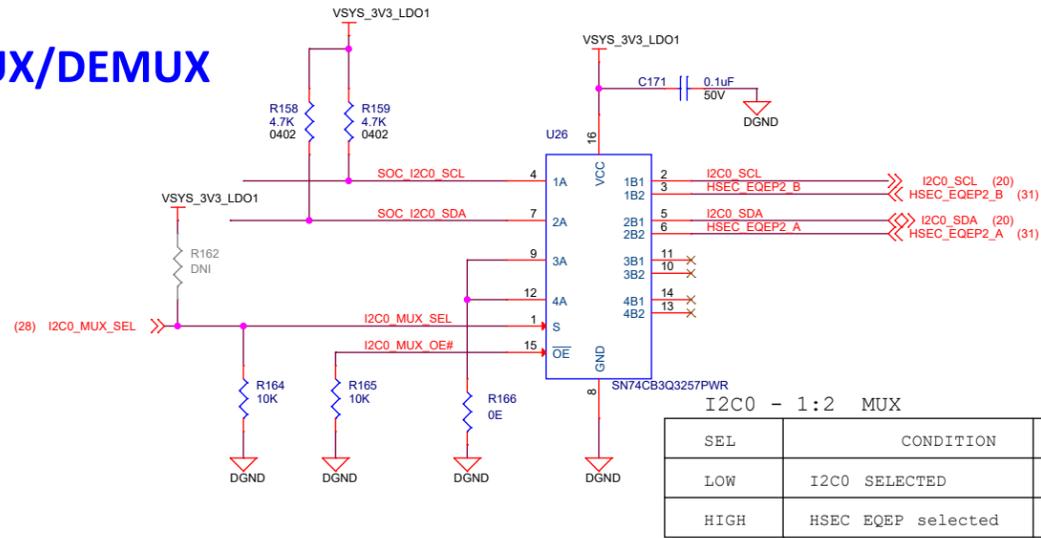
BOOTMODES



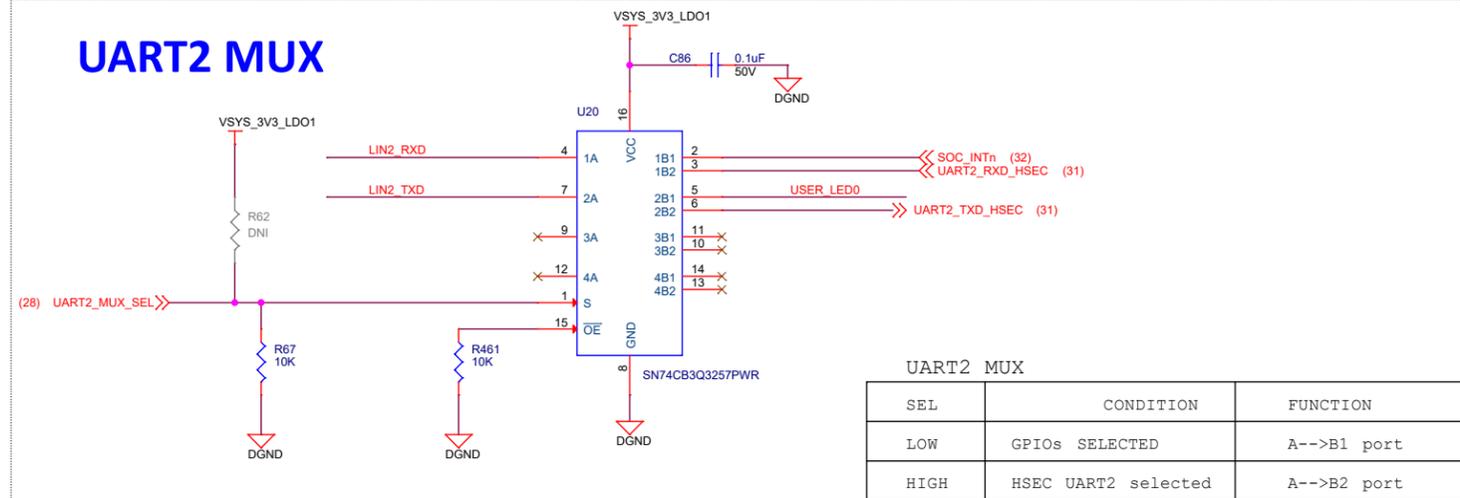
I2C Pull-ups



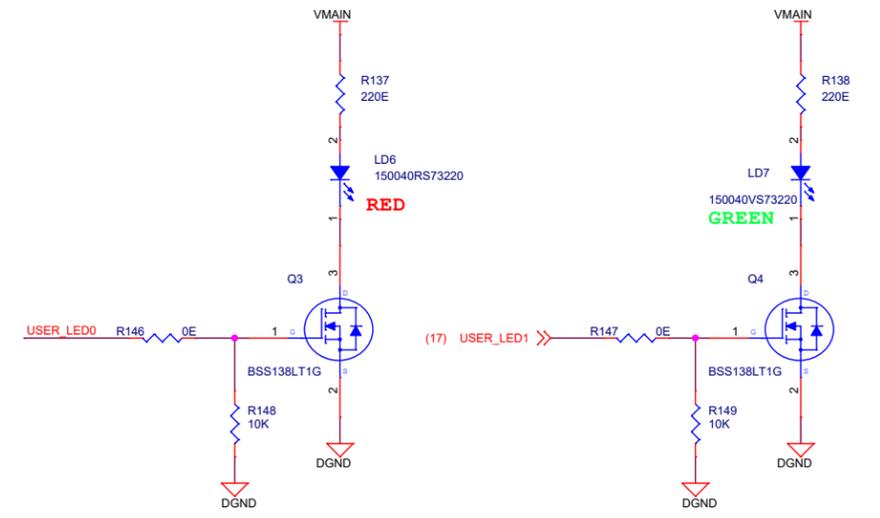
I2C0 MUX/DEMUX



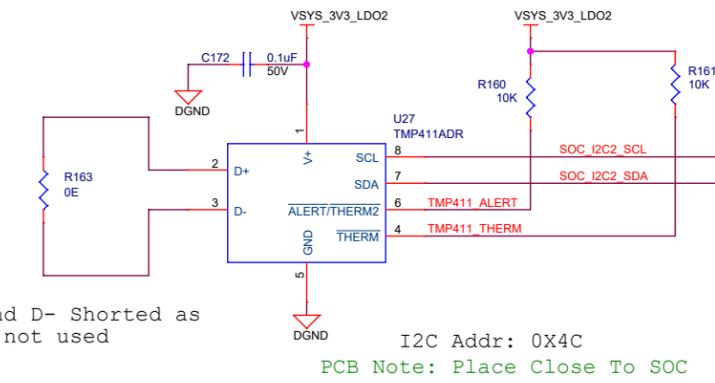
UART2 MUX



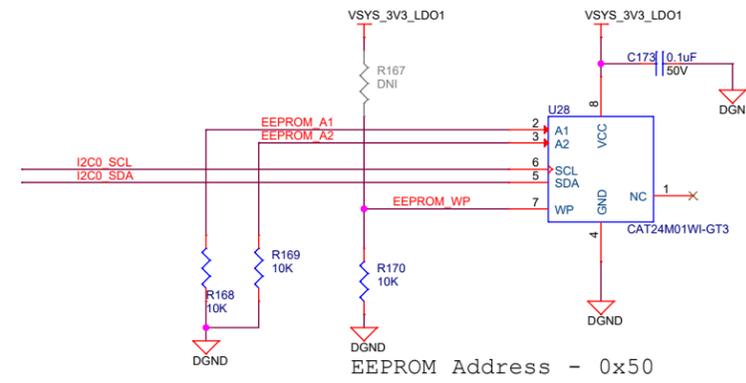
USER LEDs



Temperature Sensor



Board ID EEPROM

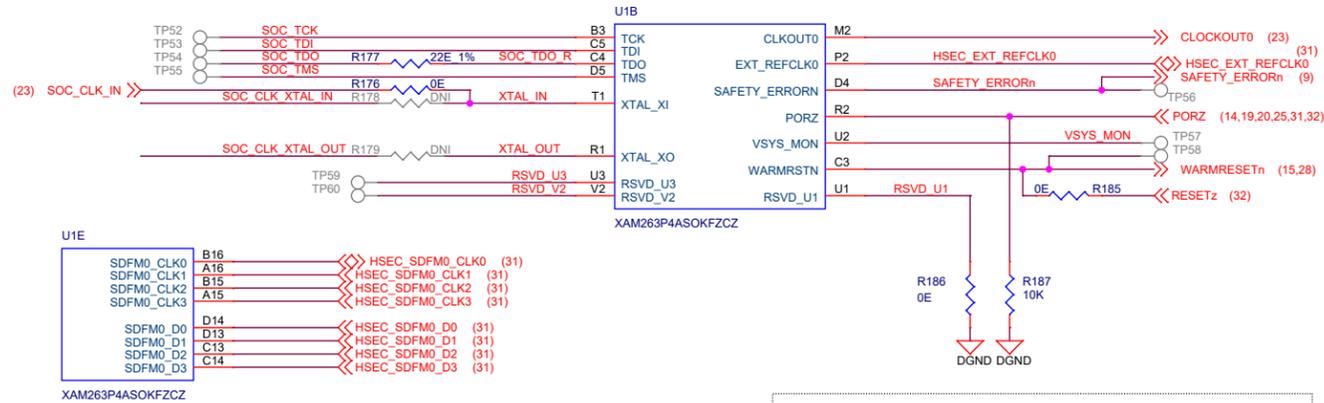


Designed for TI by Mistral Solutions Pvt Ltd

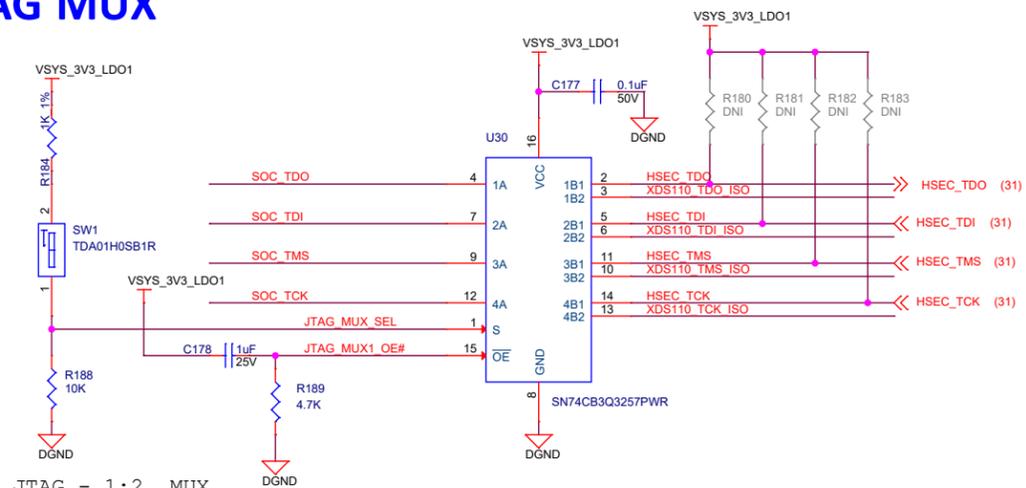


Title		SOC-IO Interfaces	
Size	Variant Name = PROC159A(001)	Rev	
C		A	
Date:	Thursday, April 04, 2024	Sheet	16 of 33

SOC JTAG, RESET and CLKS



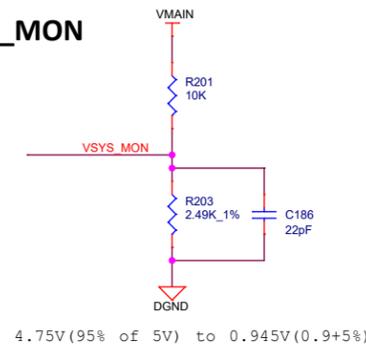
JTAG MUX



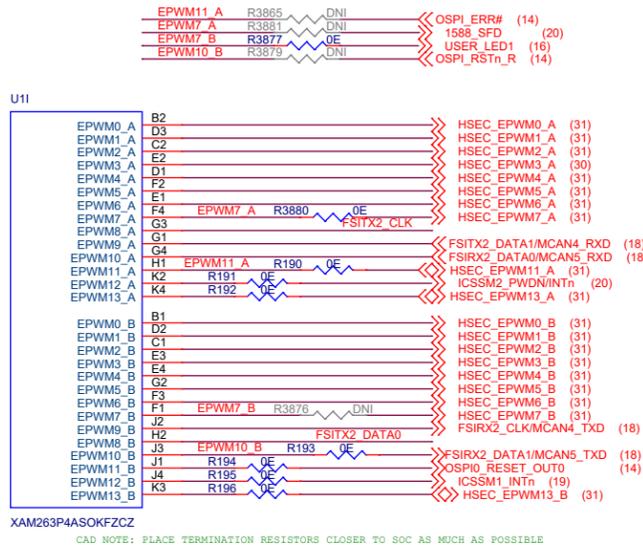
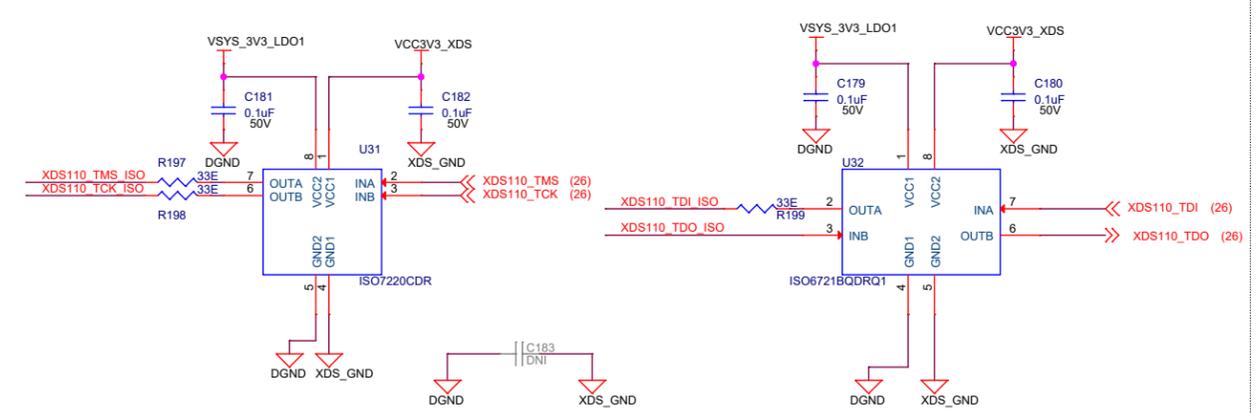
JTAG - 1:2 MUX

SW1.1	CONDITION	FUNCTION
LOW	HSEC EMU selected	A-->B1 port [EXTERNAL EMU]
HIGH	XDS110 selected	A-->B2 port [ON Board EMU]

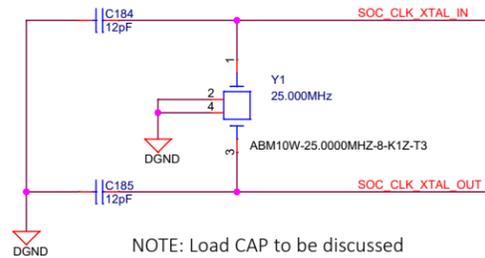
VSYS_MON



ISOLATION FOR XDS110

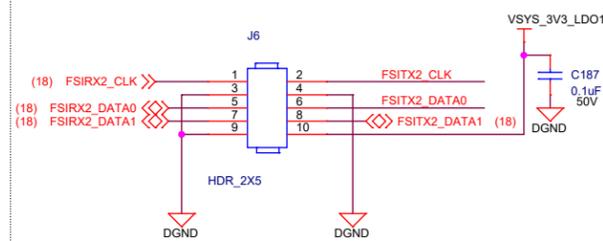


SOC Clock

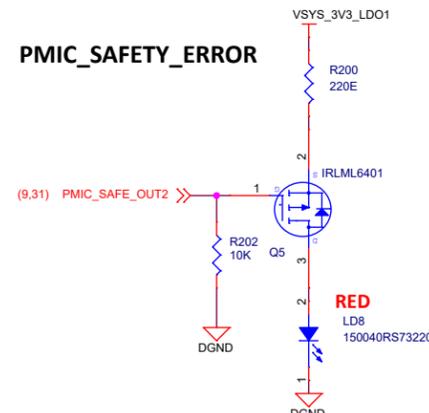


NOTE: Load CAP to be discussed

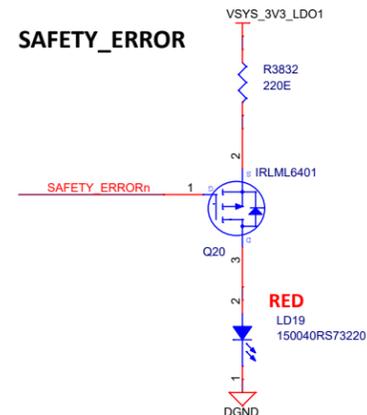
FSI Header



PMIC_SAFETY_ERROR



SAFETY_ERROR



Designed for TI by Mistral Solutions Pvt Ltd



Title SOC JTAG, RESET and CLKS

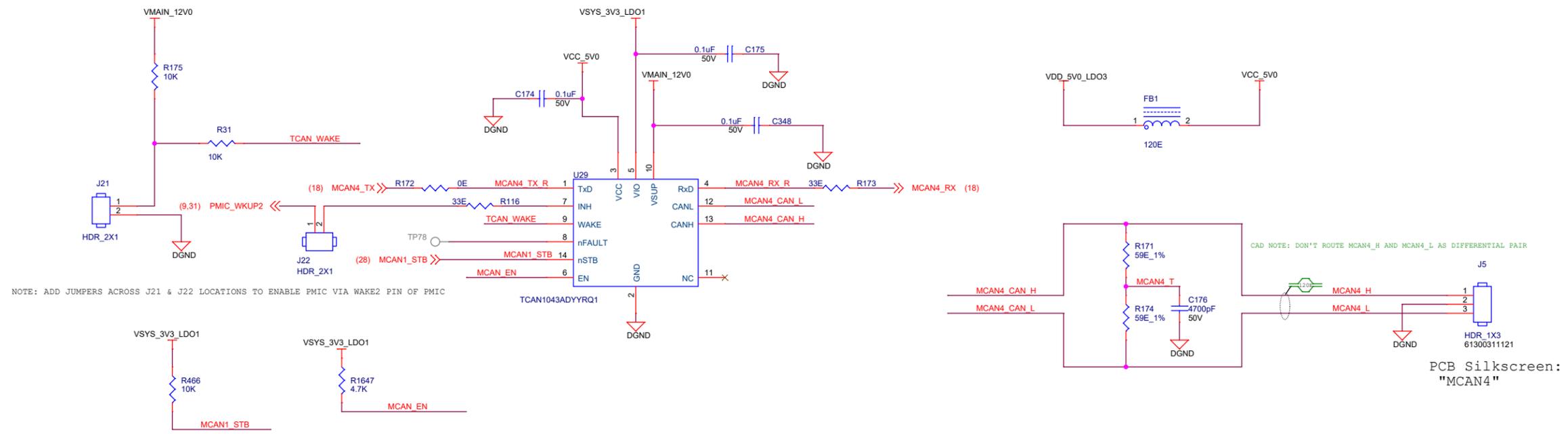
Size Variant Name = PROC159A(001)

Date: Thursday, April 04, 2024

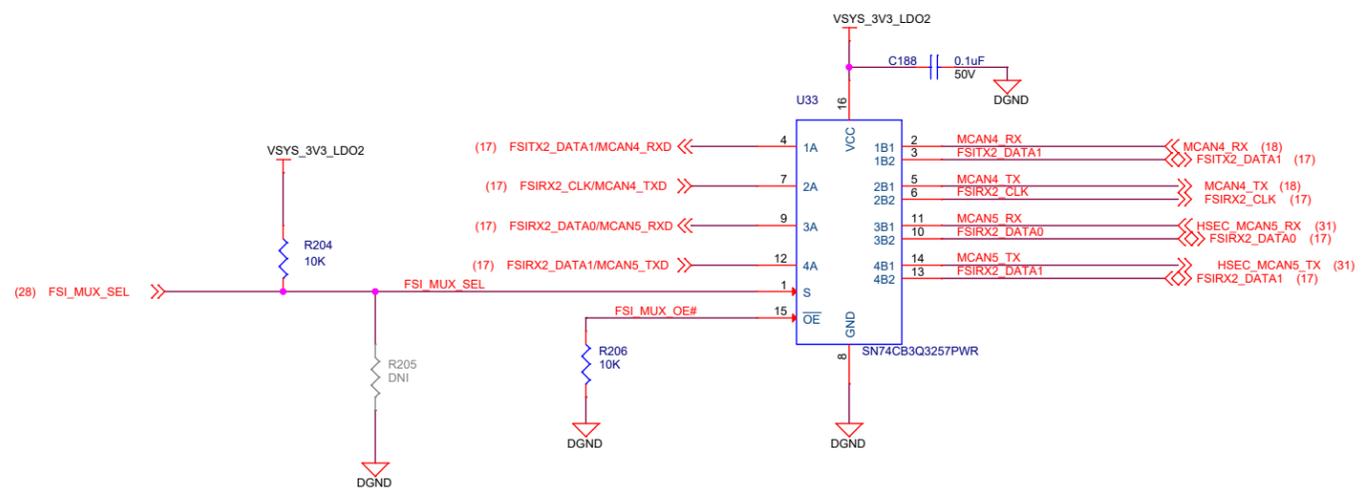
Sheet 17 of 33

Rev A

MCAN Interface



MCAN AND FSI MUX



FSI MUX

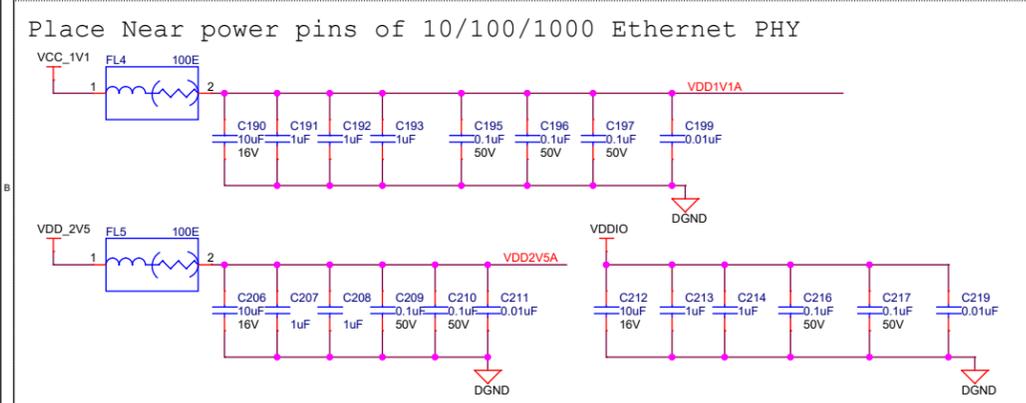
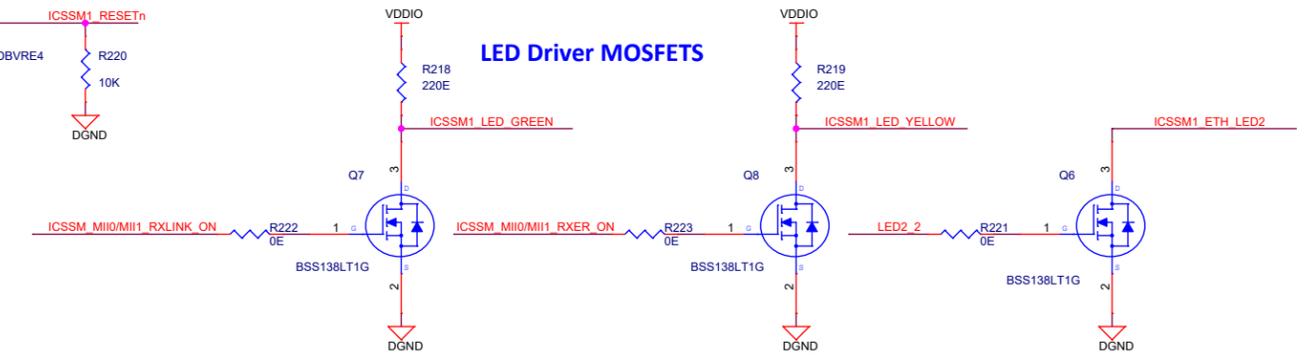
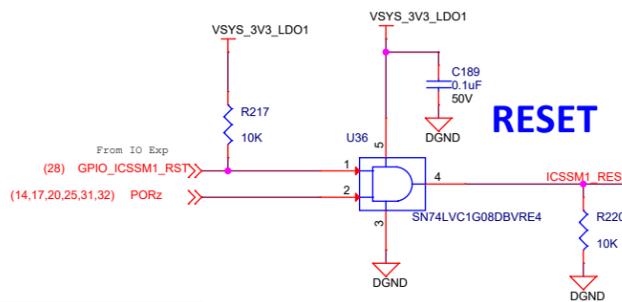
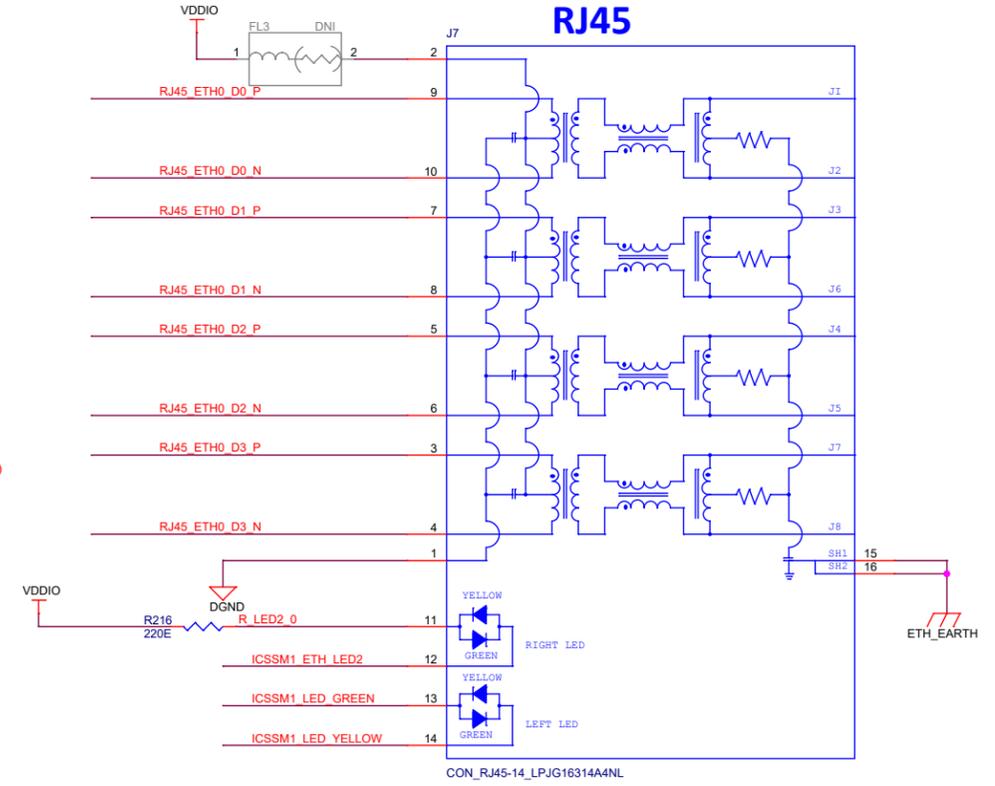
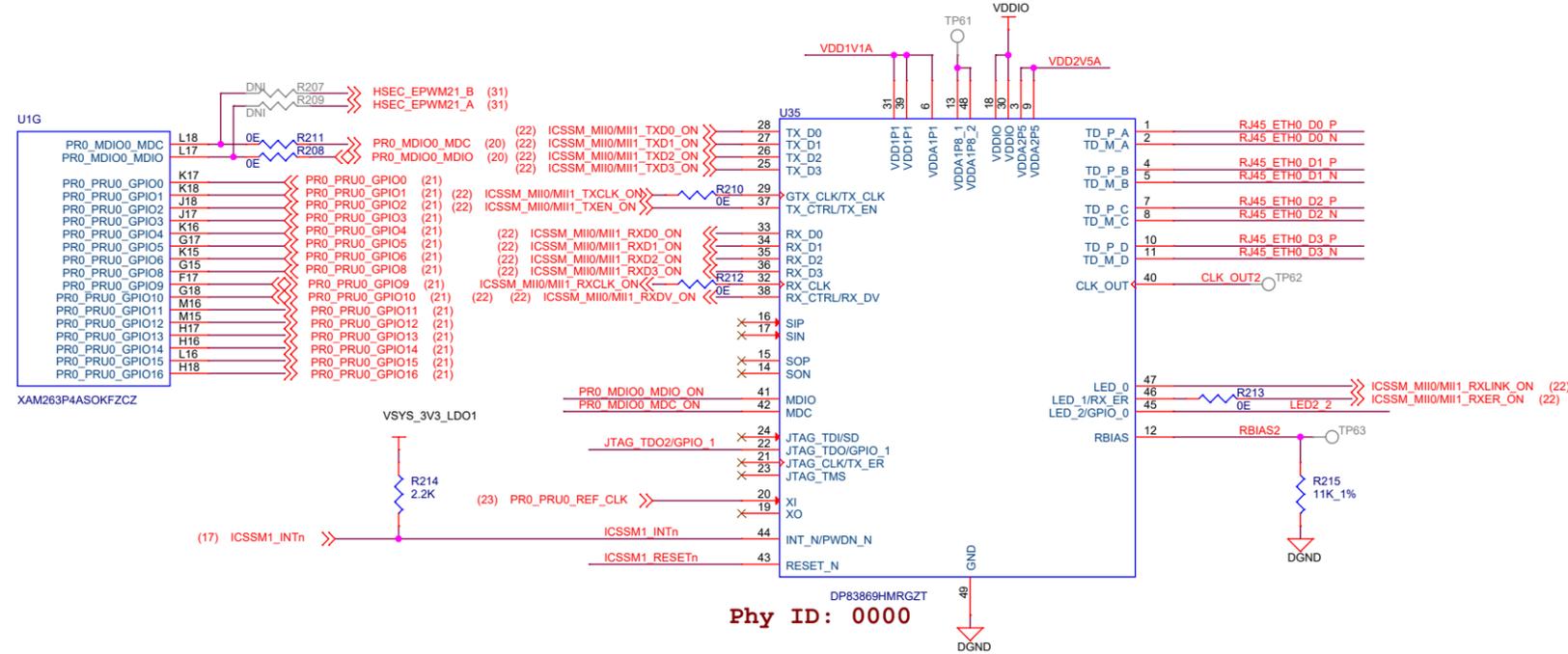
SEL	CONDITION	FUNCTION
LOW	MCAN for CAN & HSEC SELECTED	A-->B1 port
HIGH	FSI SELECTED	A-->B2 port

Designed for TI by Mistral Solutions Pvt Ltd

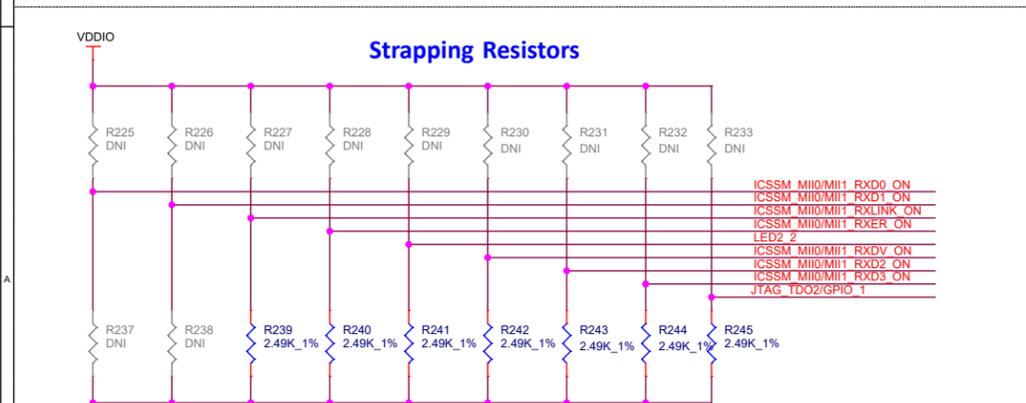
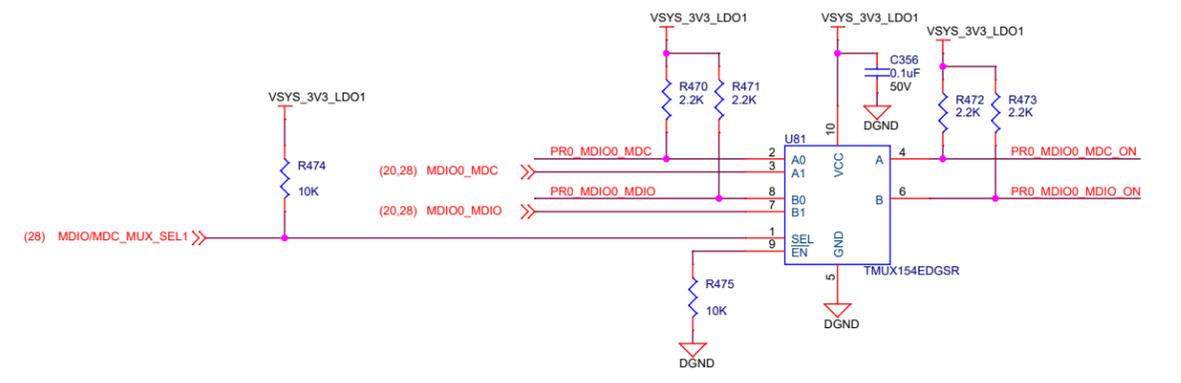


Title		MCAN AND FSI MUX	
Size	Variant Name = PROC159A(001)	Rev	A
Date:	Thursday, April 04, 2024	Sheet	18 of 33

PRU0 ICSS MII0, CPSW RGMII/MII Ethernet



MDIO/MDC MUX FOR ON-BOARD PHY



PHY ADDRESS = 00000
 Auto-negotiation, 10/100/1000 advertised, Auto-MDI-X
 RGMII to Copper (1000BaseT/100Base-TX/10Base-Te)

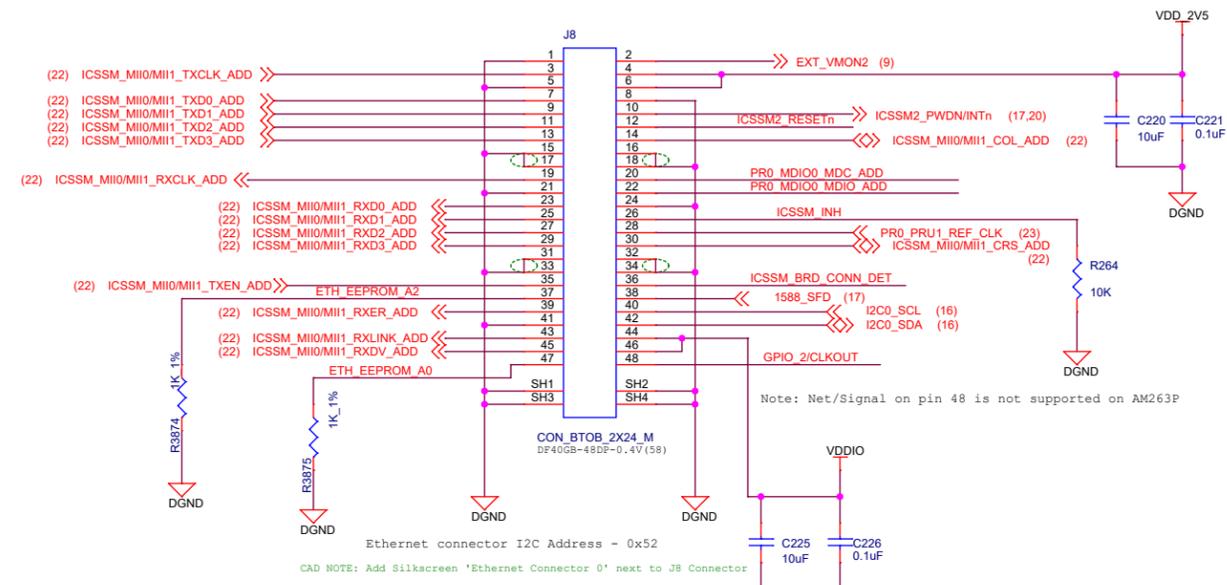
PRU1 ICSS MII1 Ethernet

U1H

PR0_PRU1_GPIO0	F18	R247	33E	PR0_PRU1_GPIO0	(21)
PR0_PRU1_GPIO1	G16	R248	33E	PR0_PRU1_GPIO1	(21)
PR0_PRU1_GPIO2	E17	R249	33E	PR0_PRU1_GPIO2	(21)
PR0_PRU1_GPIO3	E18	R250	33E	PR0_PRU1_GPIO3	(21)
PR0_PRU1_GPIO4	F16	R251	33E	PR0_PRU1_GPIO4	(21)
PR0_PRU1_GPIO5	F15	R252	33E	PR0_PRU1_GPIO5	(21)
PR0_PRU1_GPIO6	E16	R253	33E	PR0_PRU1_GPIO6	(21)
PR0_PRU1_GPIO8	D18	R254	33E	PR0_PRU1_GPIO8	(21)
PR0_PRU1_GPIO9	C18	R255	33E	PR0_PRU1_GPIO9	(21)
PR0_PRU1_GPIO10	D17	R256	33E	PR0_PRU1_GPIO10	(21)
PR0_PRU1_GPIO11	B18	R257	33E	PR0_PRU1_GPIO11	(21)
PR0_PRU1_GPIO12	B17	R258	33E	PR0_PRU1_GPIO12	(21)
PR0_PRU1_GPIO13	D16	R259	33E	PR0_PRU1_GPIO13	(21)
PR0_PRU1_GPIO14	C17	R260	33E	PR0_PRU1_GPIO14	(21)
PR0_PRU1_GPIO15	A17	R261	33E	PR0_PRU1_GPIO15	(21)
PR0_PRU1_GPIO16	C16	R262	33E	PR0_PRU1_GPIO16	(21)
PR0_PRU1_GPIO18	C15	R1652	33E	PR0_PRU1_GPIO18	(31)
PR0_PRU1_GPIO19	D15	R1653	33E	PR0_PRU1_GPIO19	(31)

XAM263P4ASOKFZCZ

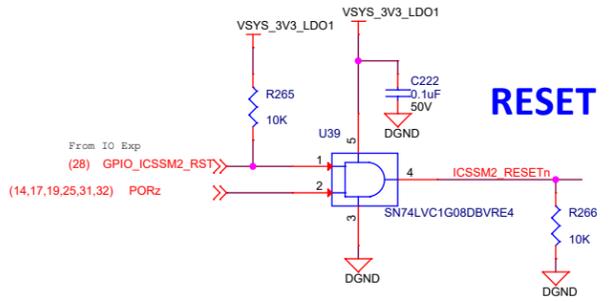
CAD NOTE: Place the resistors close to SoC with minimum stub



Ethernet connector I2C Address - 0x52

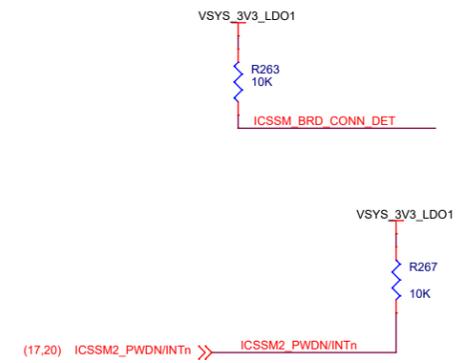
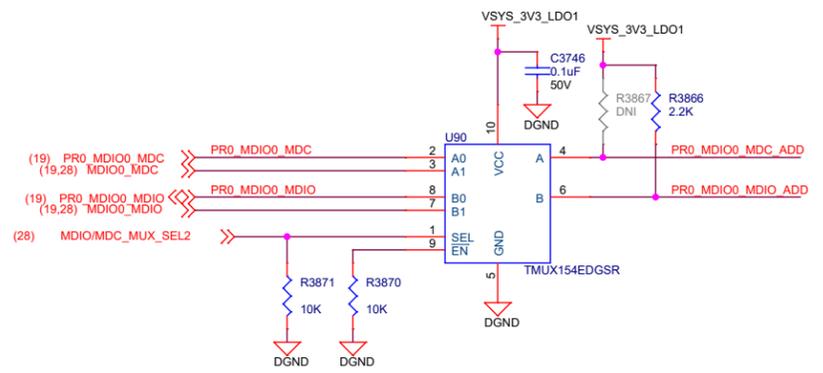
CAD NOTE: Add Silkscreen 'Ethernet Connector 0' next to J8 Connector

Note: Net/Signal on pin 48 is not supported on AM263P



RESET

MDIO/MDC MUX FOR ADD-ON BOARD CONN

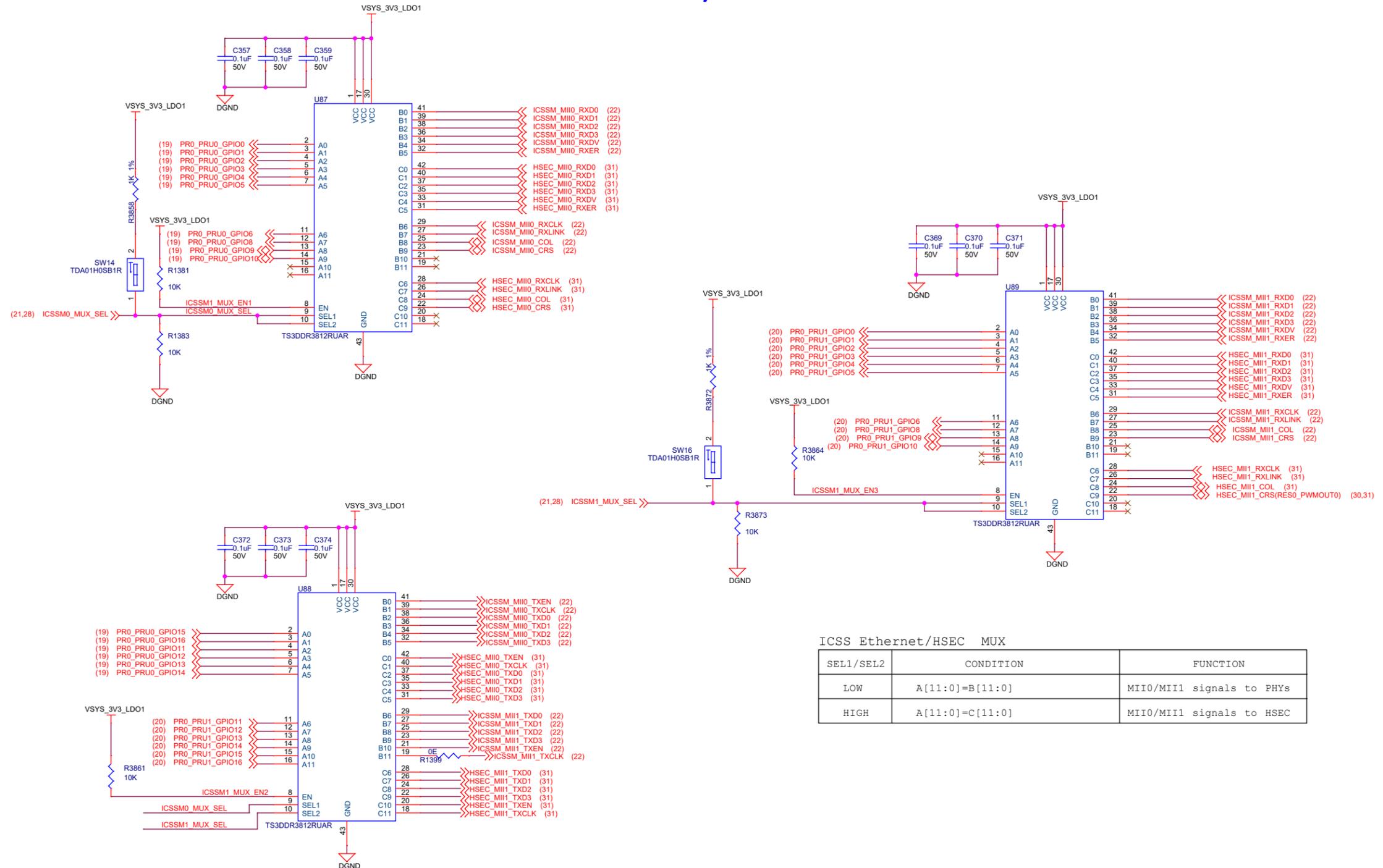


Designed for TI by Mistral Solutions Pvt Ltd



Title			SOC-ICSS ETHERNET CONN		
Size	Variant Name = PROC159A(001)				Rev
C					A
Date:	Thursday, April 04, 2024	Sheet	20	of	33

ICSS Ethernet/HSEC MUX

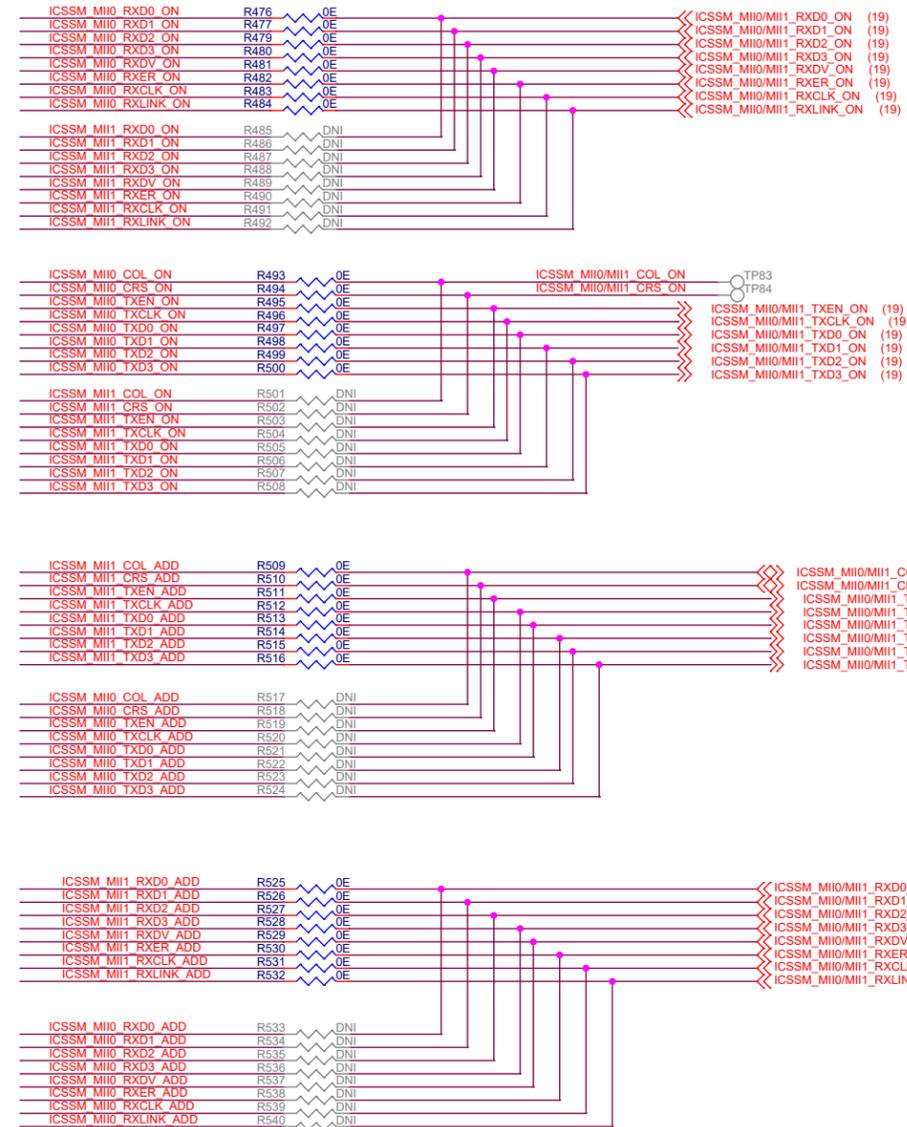
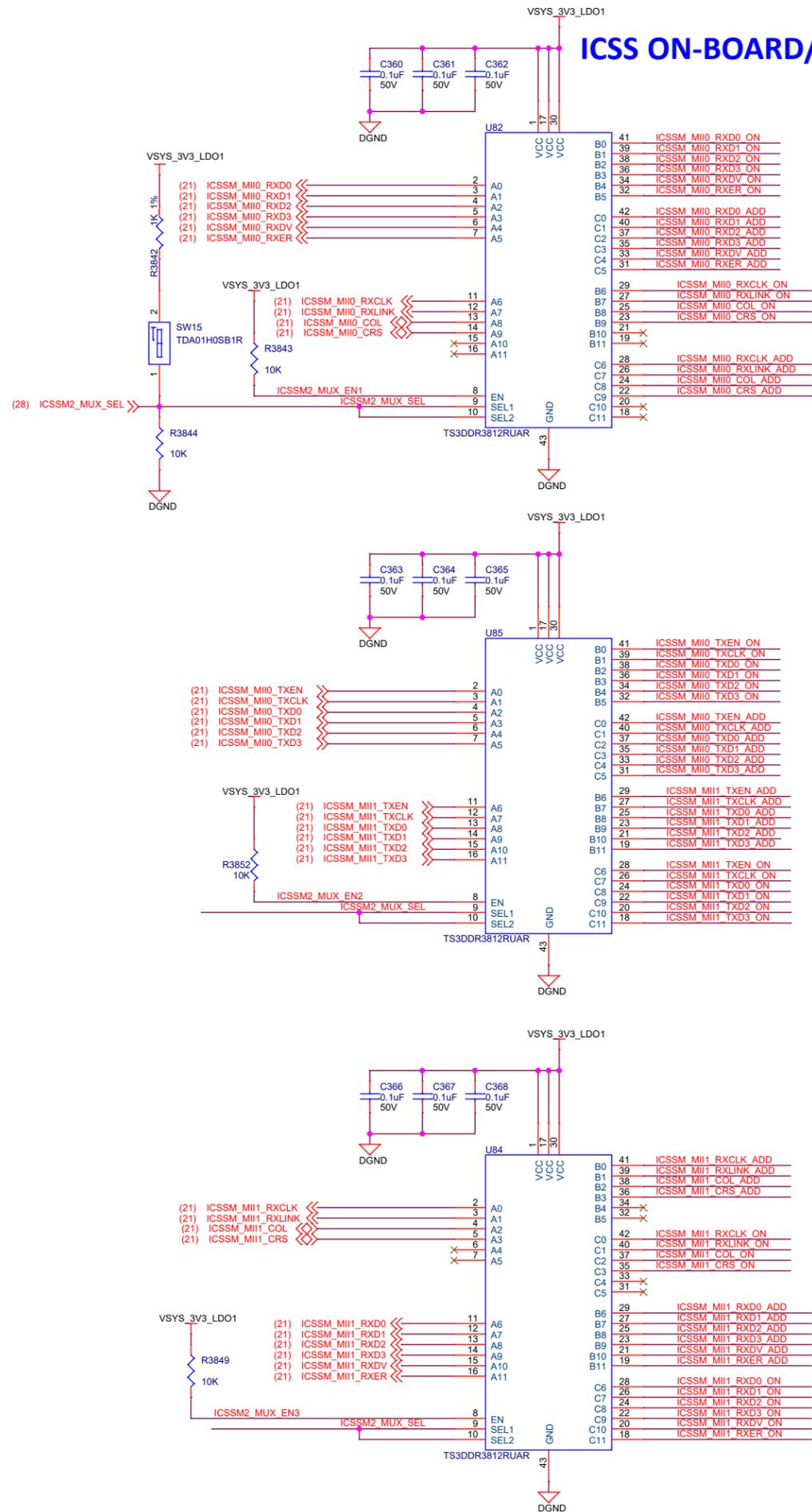


ICSS Ethernet/HSEC MUX

SEL1/SEL2	CONDITION	FUNCTION
LOW	A[11:0]=B[11:0]	MII0/MII1 signals to PHYs
HIGH	A[11:0]=C[11:0]	MII0/MII1 signals to HSEC

ICSS ON-BOARD/ADD-ON PHY MUX

ICSS MII0/MII1 - RESISTOR MUX



CAD NOTE: Place the resistors with minimum stub

ICSS MII0/MII1 - RESISTOR MUX

MODE	FUNCTION
Populate R476 to R484, R493 to R500, R509 to R516, R525 to R532 resistors (Default)	MII0 -> On-board PHY MII1 -> Add-on Board connector
Populate R485 to 492, R501 to R508, R517 to R524, R533 to R540 resistors	MII0 -> Add-on Board connector MII1 -> On-board PHY

ICSS ON-BOARD/ADD-ON PHY MUX

SEL1/SEL2	CONDITION	FUNCTION
LOW	A[11:0]=B[11:0]	MII0 signals to ON-BOARD PHY MII1 signals to ADD-ON BOARD
HIGH	A[11:0]=C[11:0]	MII0 signals to ADD-ON BOARD MII1 signals to ON-BOARD PHY

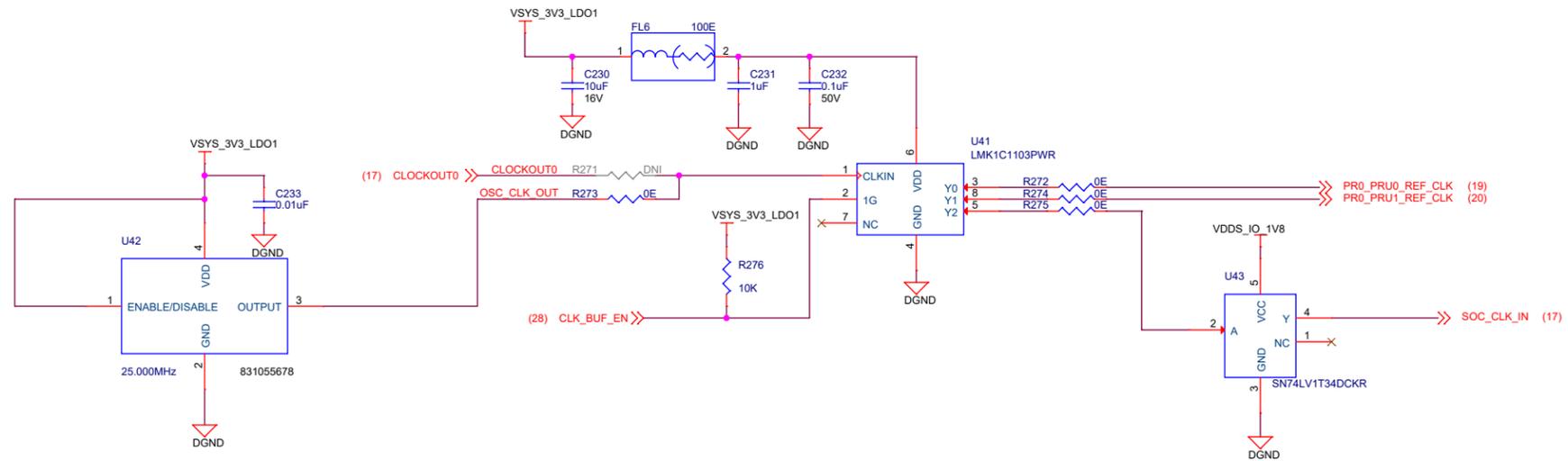
Designed for TI by Mistral Solutions Pvt Ltd



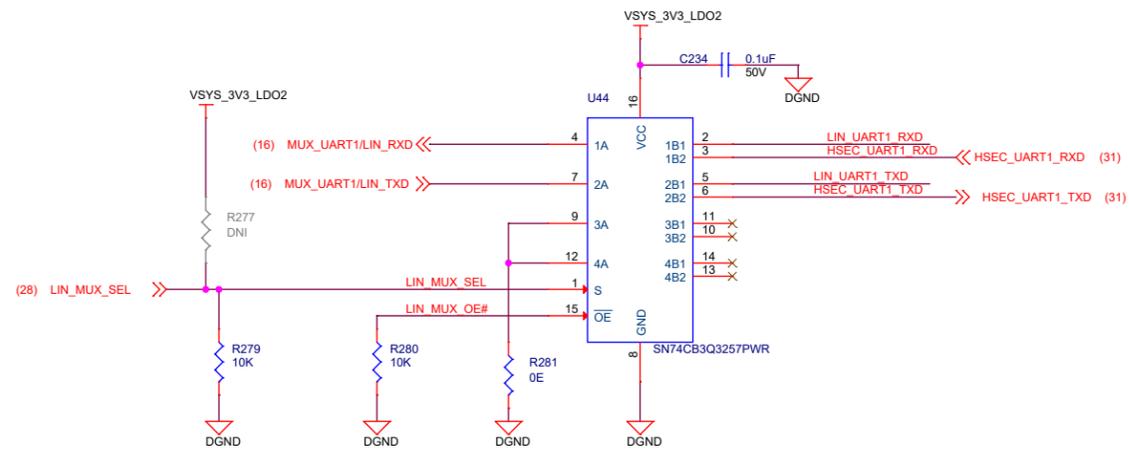
Title ICSS ON-BOARD/ADD-ON PHY & MII0/MII1 MUXES

Size	Variant Name = PROC159A(001)	Rev
C		A
Date:	Thursday, April 04, 2024	Sheet 22 of 33

CLOCKS



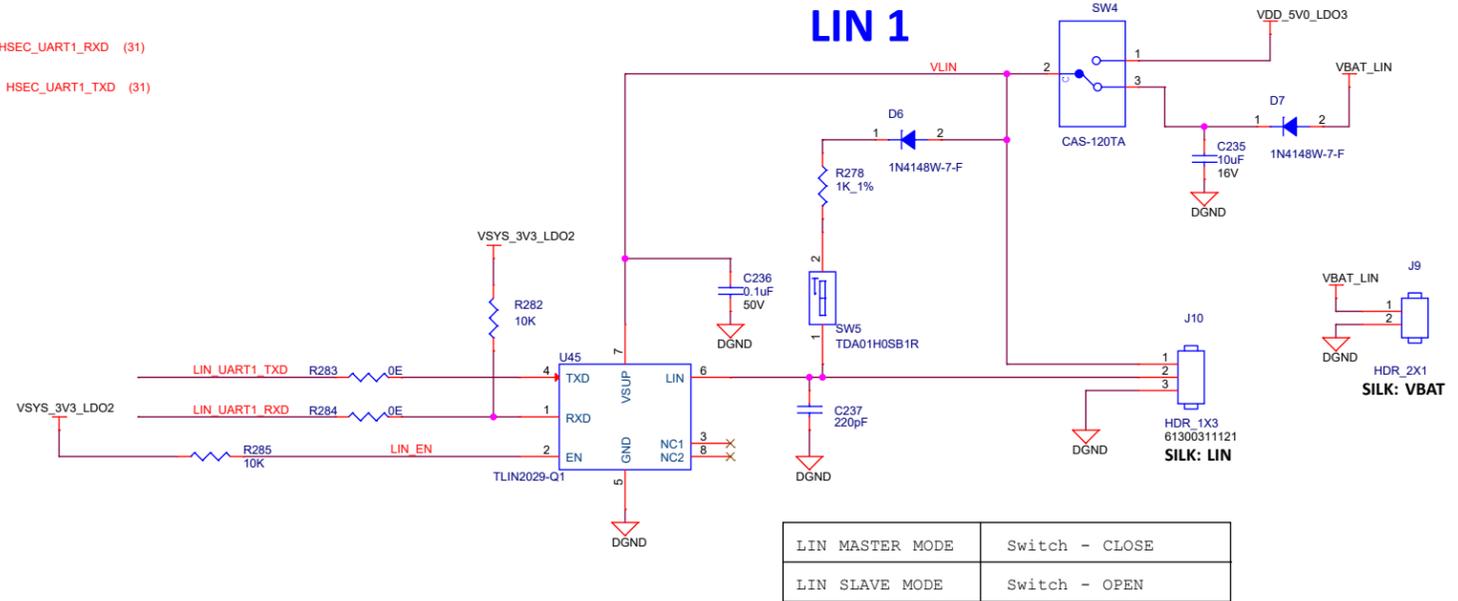
LIN MUX/DEMUX



I2C0 - 1:2 MUX

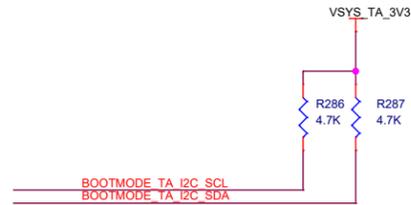
SEL	CONDITION	FUNCTION
LOW	LIN SELECTED	A-->B1 port
HIGH	HSEC UART selected	A-->B2 port

LIN 1



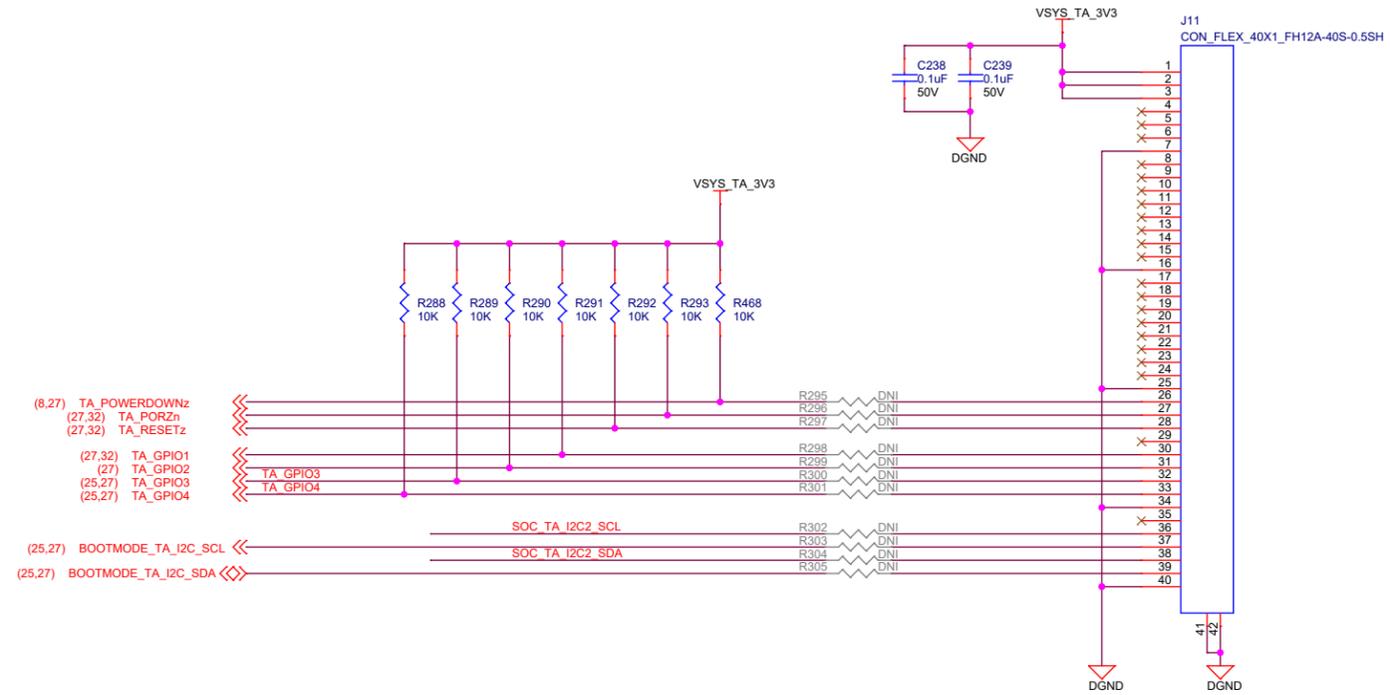
TEST AUTOMATION HEADER

I2C Pull Ups

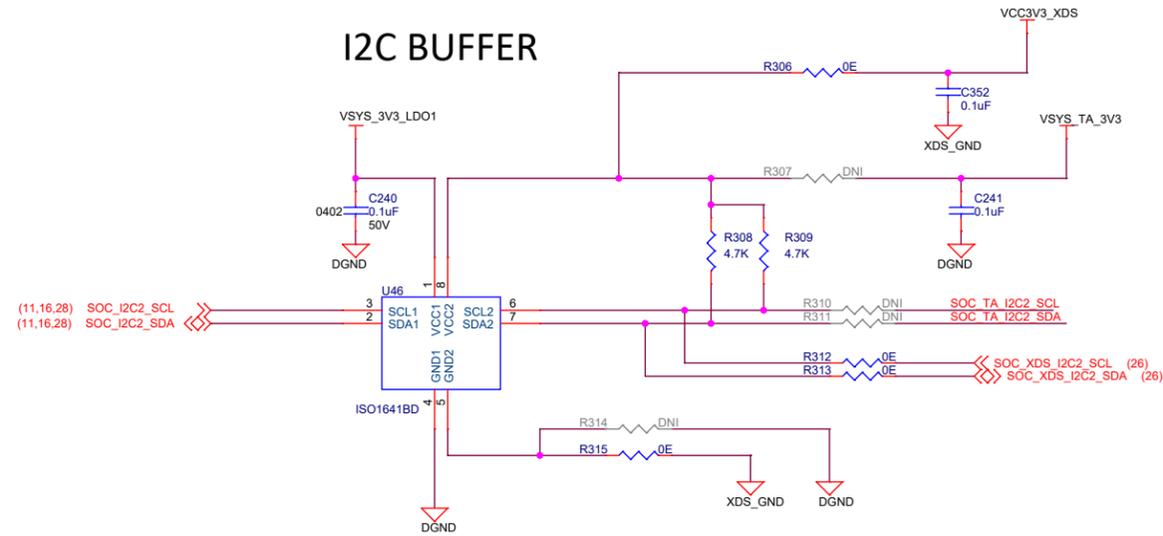


AUTOMATION INTERFACE

Cable : Parlex-050R40-76B, .5mm 3"



I2C BUFFER



TA Configuration
 Mount : R307, R310, R311, R314
 Demount : R306, R312, R313, R315

TEST AUTOMATION GPIO MAPPING

SIGNAL NAME	DESCRIPTION	Direction WRT CTRL	Internal/ External PU/PD states
TA_POWERDOWN	Used to Power down the system	OUTPUT	External Pullup
TA_PORZn	Used to Reset the SoC PORz	OUTPUT	External Pullup
TA_RESETz	SoC Warmreset	OUTPUT	External Pullup
TA_GPIO1	Interrupt to SOC	OUTPUT	External Pullup
TA_GPIO3	Used to Enable the BOOTMODE Buffer	OUTPUT	External Pullup
TA_GPIO4	Used Reset Bootmode IO Exp	OUTPUT	External Pullup

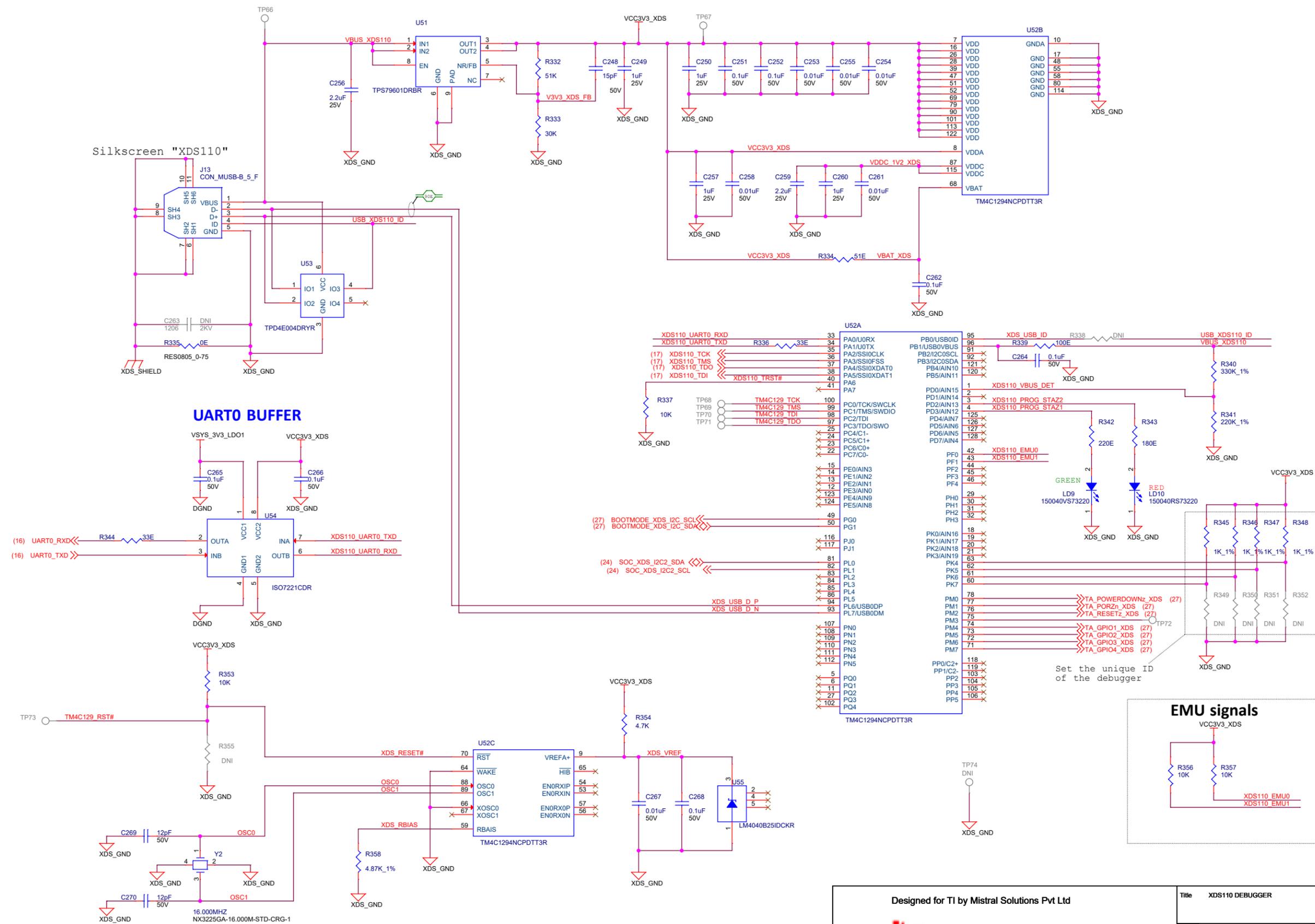
Designed for TI by Mistral Solutions Pvt Ltd



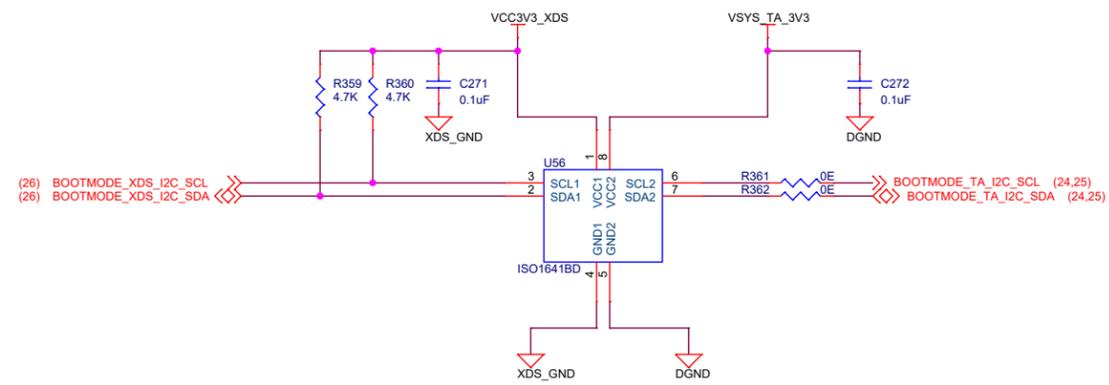
Title TEST AUTOMATION HEADER

Size	Variant Name = PROC159A(001)	Rev
C		A
Date:	Thursday, April 04, 2024	Sheet 24 of 33

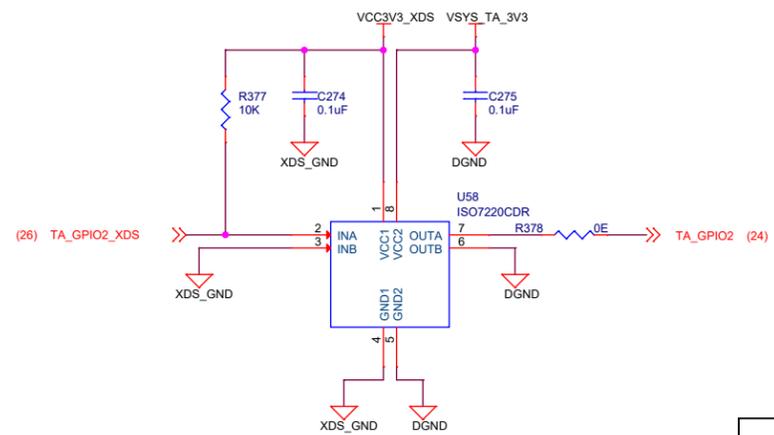
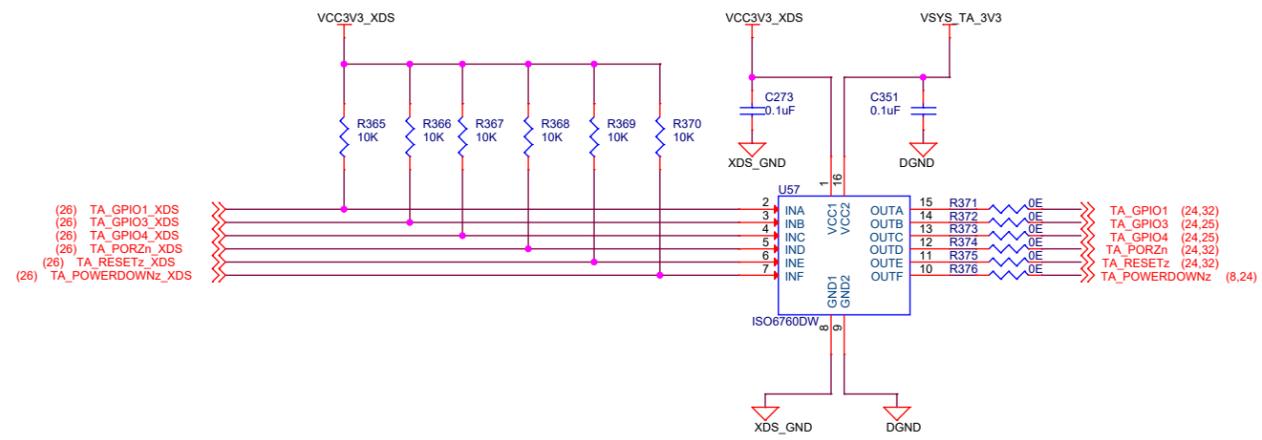
XDS110 DEBUGGER



BOOTMODE_I2C_TA BUFFER



ISOLATION BUFFERS FOR TA SIGNALS



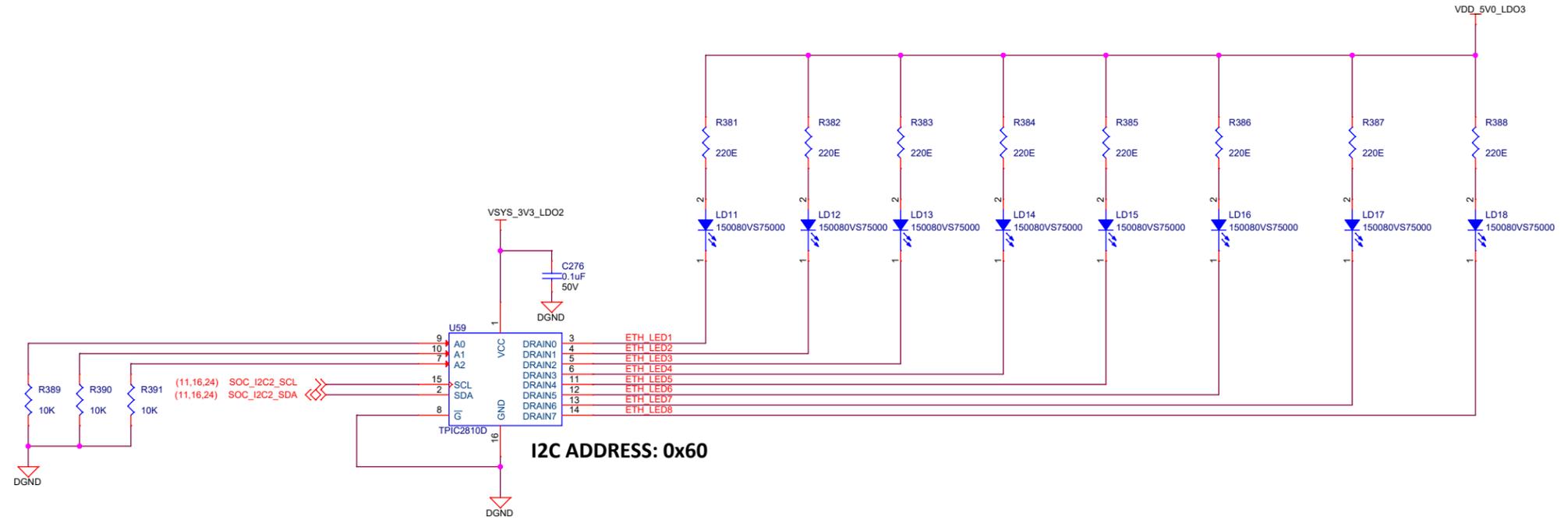
Designed for TI by Mistral Solutions Pvt Ltd



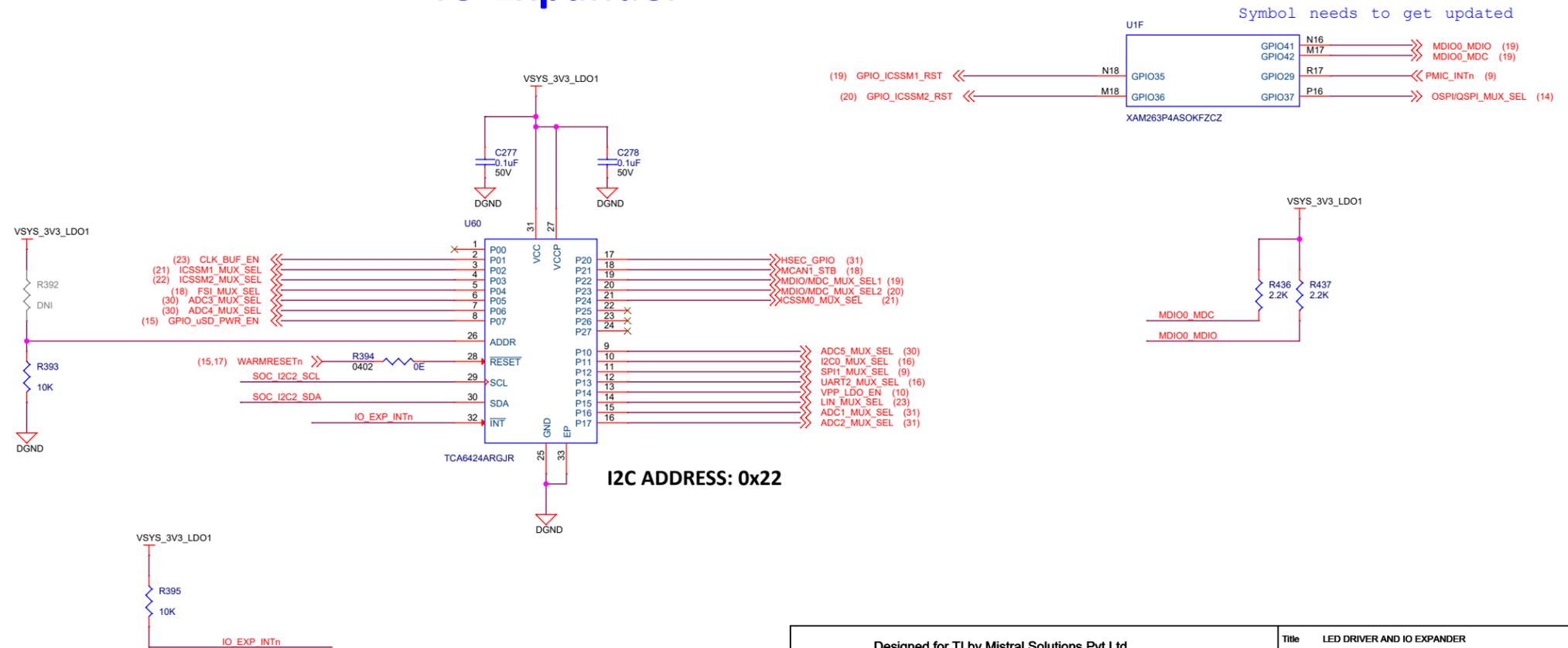
Title AUTOMATION SIGNALS BUFFER

Size	Variant Name = PROC159A(001)	Rev
C		A
Date:	Thursday, April 04, 2024	Sheet 27 of 33

LED Driver



IO Expander



Designed for TI by Mistral Solutions Pvt Ltd



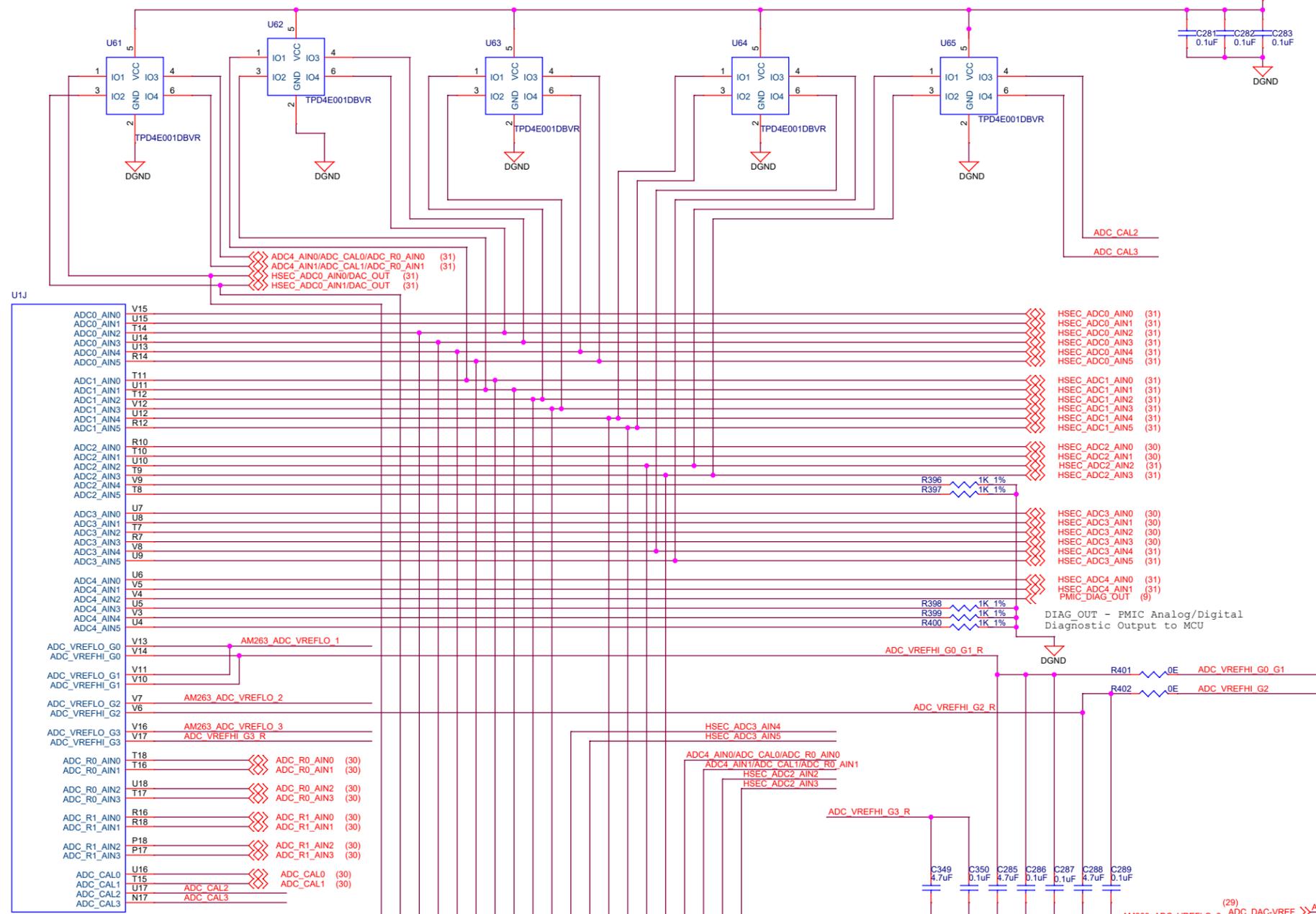
Title LED DRIVER AND IO EXPANDER

Size	Variant Name = PROC159A(001)	Rev
C		A

Date: Thursday, April 04, 2024 Sheet 28 of 33

SOC- ADC & DAC Interface

CAD NOTE: Place the ESDs close to connector

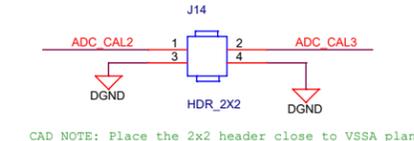


XAM263P4ASOKFZCZ

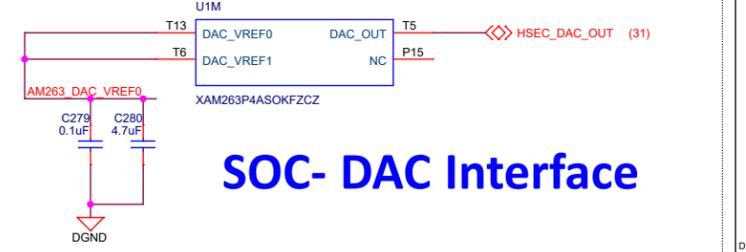
CAD NOTE: Place the CAPS close to each other

CAD NOTE: - Place decoupling at BGA between VREF and GND
Place GND short as close as possible to BGA and decoupling

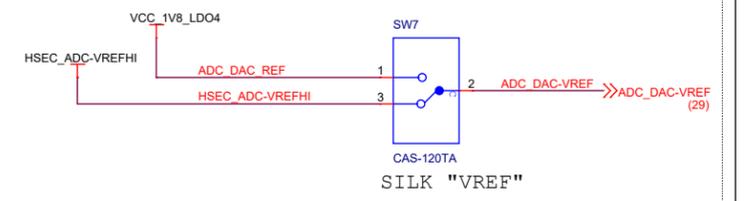
CAD NOTE: Place the CAPS close to the connector



CAD NOTE: Place the 2x2 header close to VSSA plane

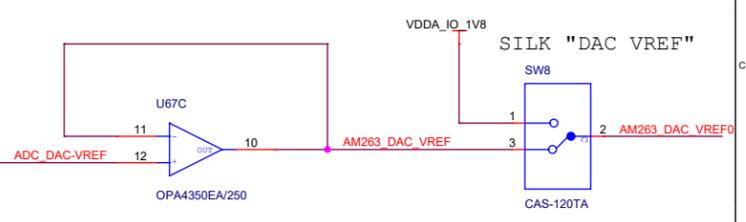


SOC- DAC Interface



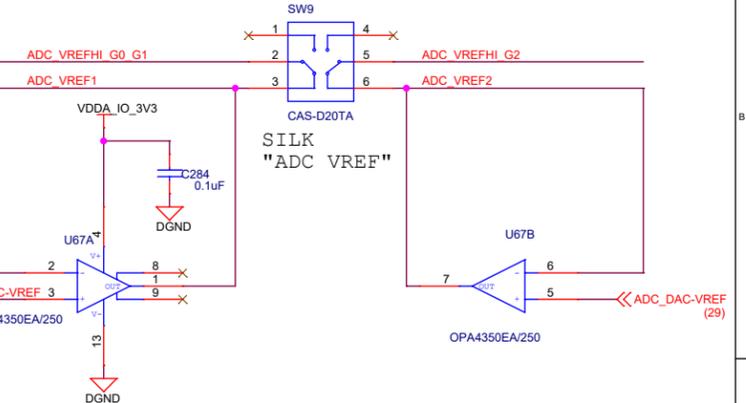
REF VOLTAGE SELECTION

SW POSITION	SUPPLY SELECTION
PIN 1-2	ON BOARD REF IS SELECTED
PIN 2-3	HSEC SUPPLY IS SELECTED



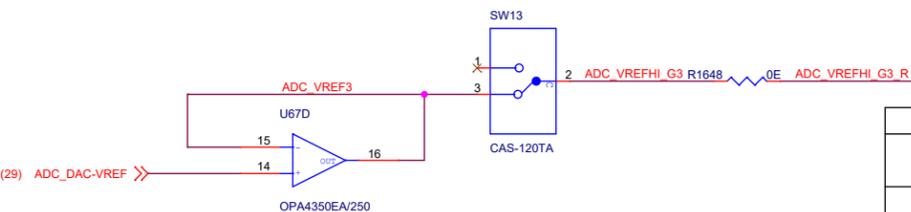
DAC REF VOLTAGE SELECTION

SW POSITION	SUPPLY SELECTION
PIN 1-2	Allows AM263P on-die LDO reference (Routed n PCB)
PIN 2-3	selects output of VREF switch



ADC REF VOLTAGE SELECTION

SW POSITION	SUPPLY SELECTION
PIN 1-2	OPEN - Allows AM263P on-die LDO reference (routed on-die)
PIN 2-3	selects output of VREF switch
PIN 4-5	OPEN - Allows AM263P on-die LDO reference (routed on-die)
PIN 5-6	selects output of VREF switch



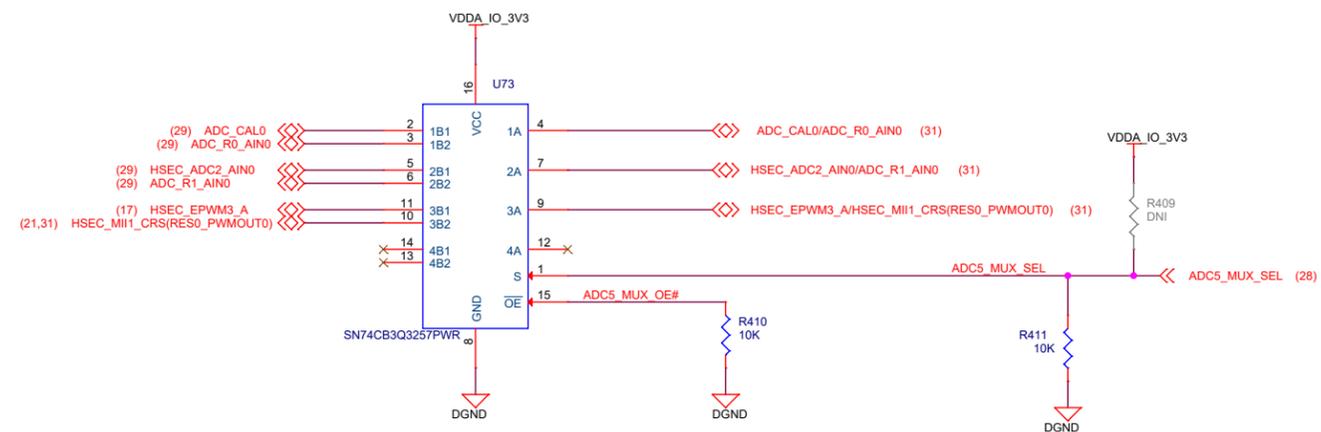
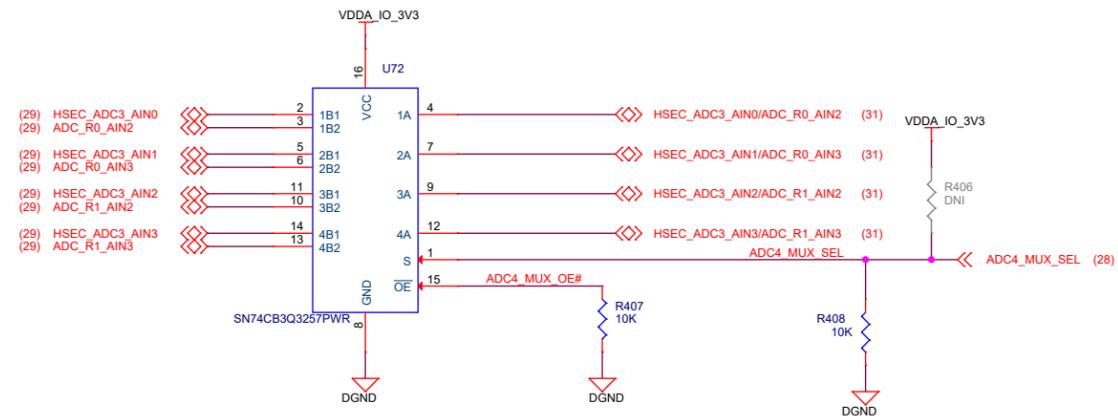
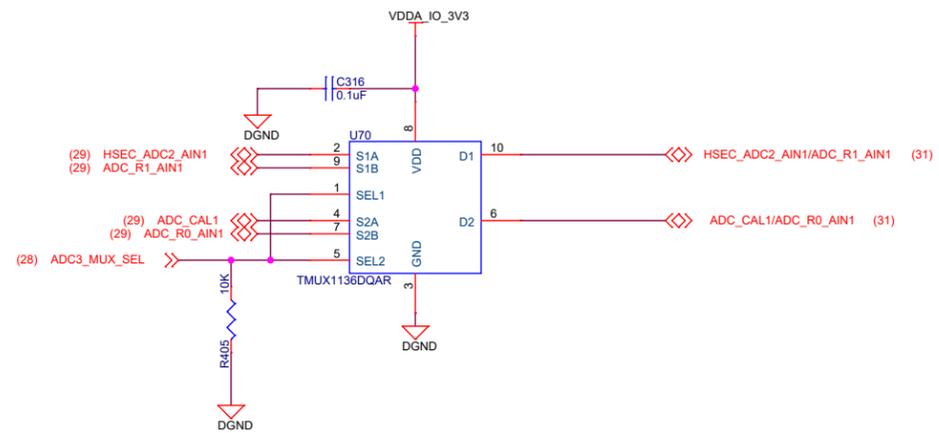
ADC REF VOLTAGE SELECTION

SW POSITION	SUPPLY SELECTION
PIN 1-2	OPEN - Allows AM263P on-die LDO reference (routed on-die)
PIN 2-3	selects output of VREF switch

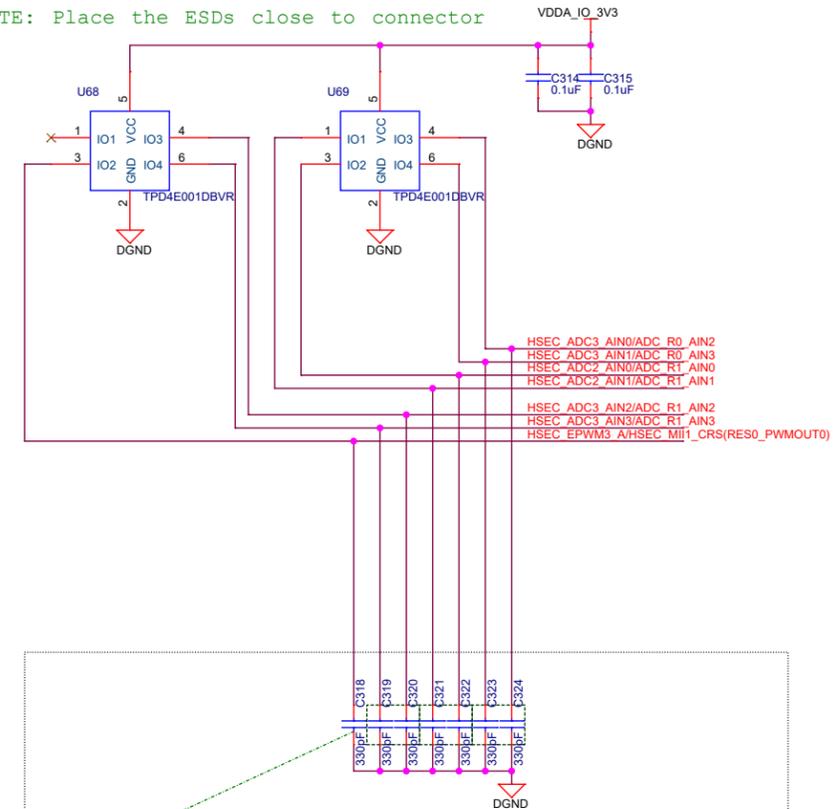
Designed for TI by Mistral Solutions Pvt Ltd



ADC MUXES



CAD NOTE: Place the ESDDs close to connector



CAD NOTE: Place the CAPs close to each other

CAD NOTE: Place the CAPs close to connector

Designed for TI by Mistral Solutions Pvt Ltd



Title ADC MUXES

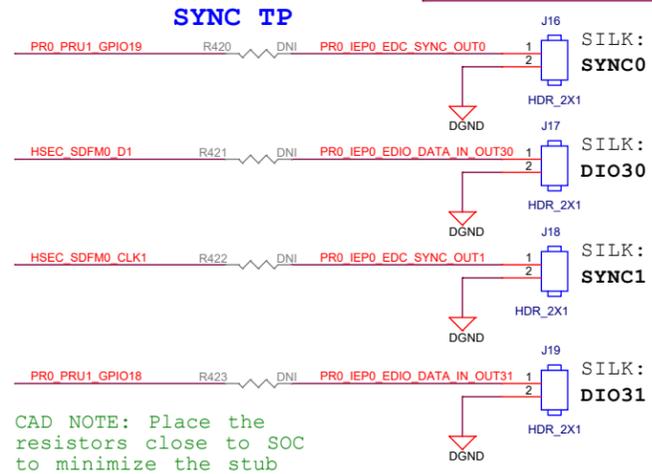
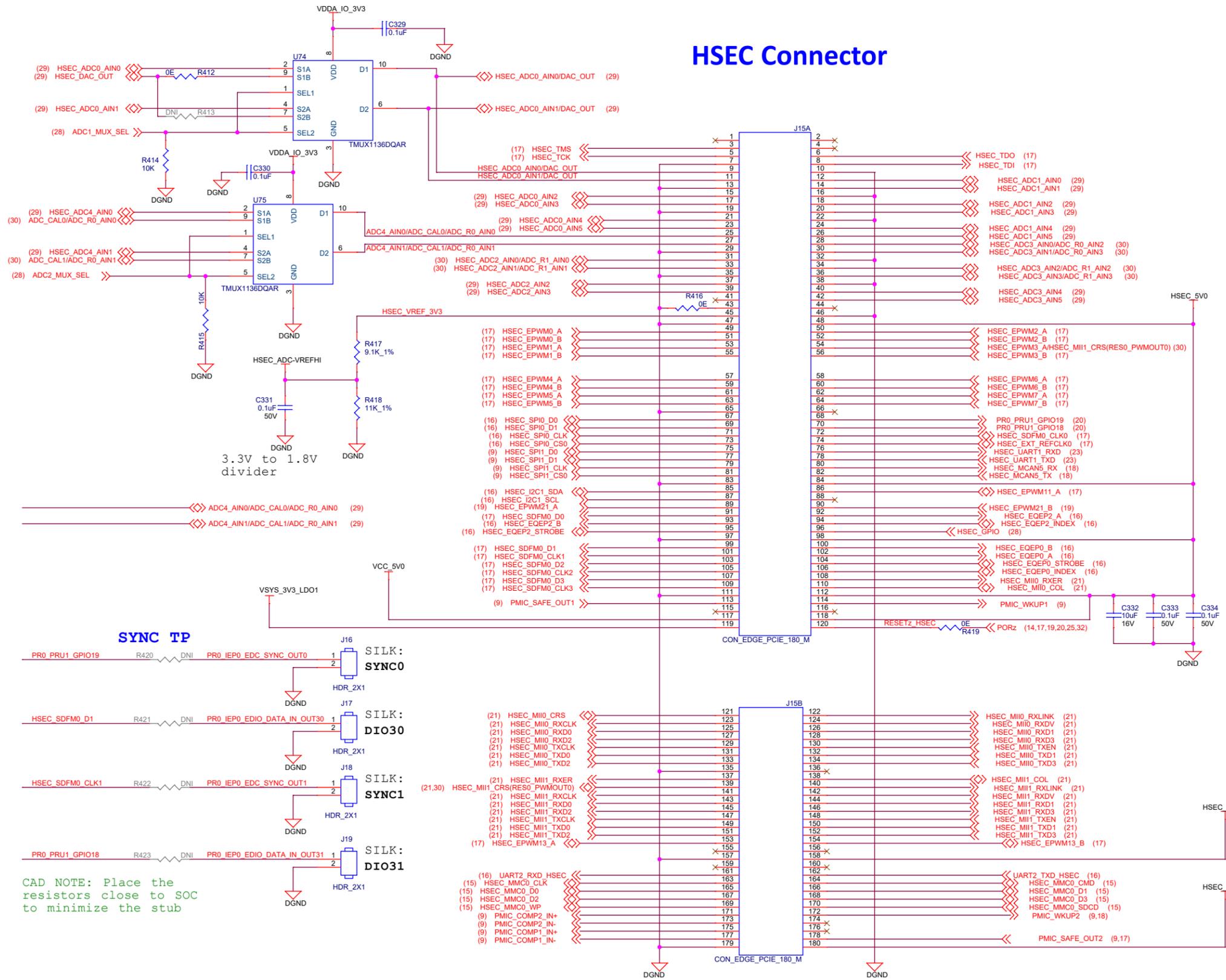
Size Variant Name = PROC159A(001)

Date: Thursday, April 04, 2024

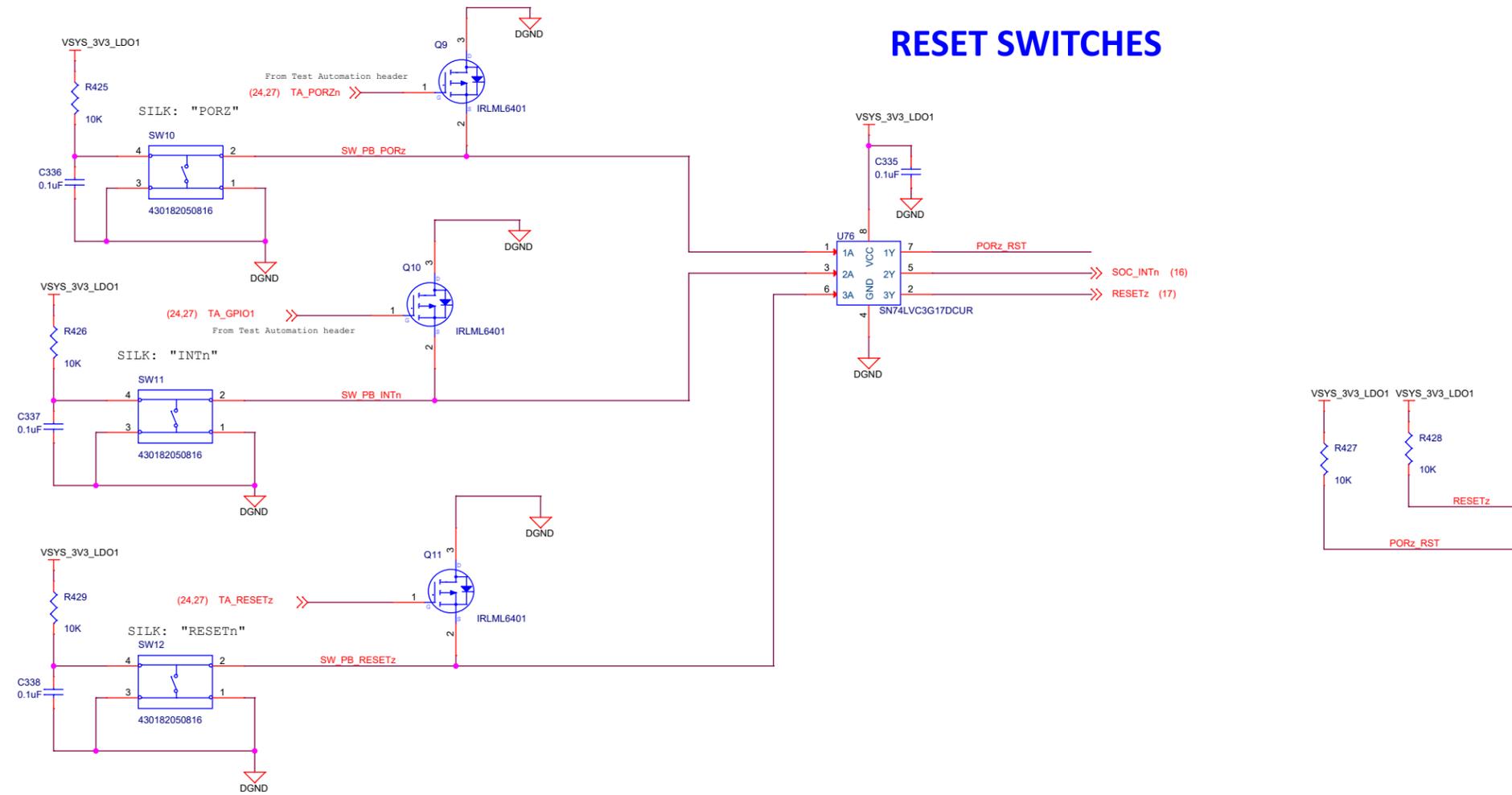
Sheet 30 of 33

Rev A

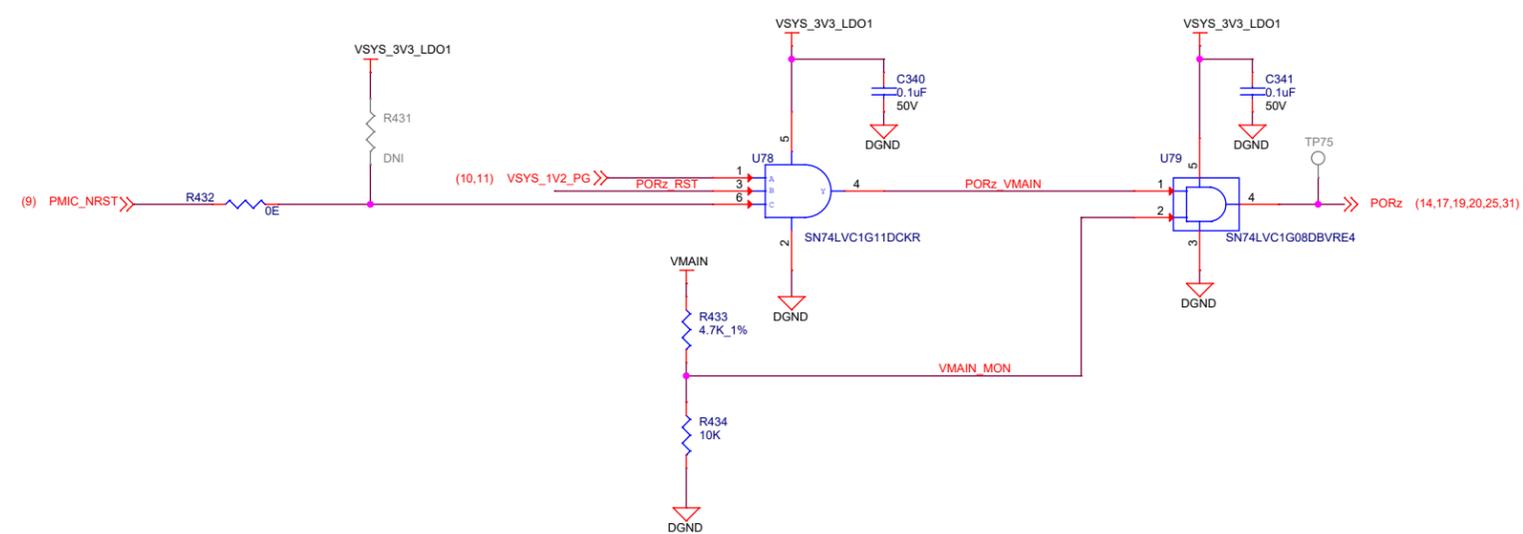
HSEC Connector



RESET SWITCHES



PORz



Designed for TI by Mistral Solutions Pvt Ltd



Title		RESET SWITCHES	
Size	Variant Name = PROC159A(001)	Rev	
C		A	
Date:	Thursday, April 04, 2024	Sheet	32 of 33

EVM Development & Evaluation test circuitry

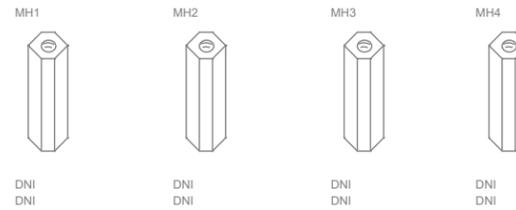
(TI EVM Only)

NOTES, HW & LABELS

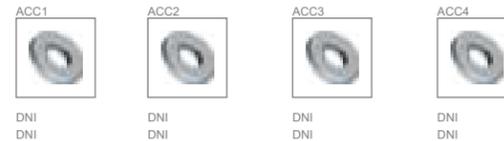
ASSEMBLY NOTES

1. All MSL components should be baked as per JEDEC standard.
2. PCB should be baked at 120 degree for 8 hours.
3. Board assembly must comply with workmanship standards. IPC-A-610 Class 2, unless otherwise specified.
4. These assemblies are ESD sensitive, ESD precautions shall be observed.
5. These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
6. Provide serial numbers to the assembled boards for identification.
7. The assembled board are wrapped in ESD Covers(individual) and packed securely before shipment.

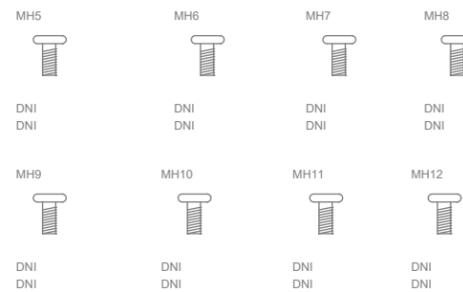
STANDOFFS



WASHER'S



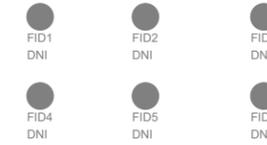
SCREWS



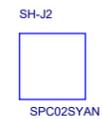
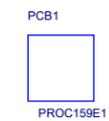
RUBBER FEET



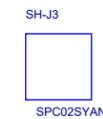
FIDUCIALS



BARE PCB



SH-J2 Shall be mounted on J20 to enable the PMIC VIA WAKE1 PIN OF PMIC.



SH-J3 Shall be mounted on J22 to enable the PMIC VIA WAKE2 PIN OF PMIC.



SH-J4 Shall be mounted on J21 to enable the TCAN WAKE

LABELS

Board Serial No.



Assembly Revision.



EVM Orderable No.



Orderable Part Numbers

Variant	Label Text
001	TMDSCNCD263P
002	TMDSCNCD263P-SIP

LOGOs



Designed for TI by Mistral Solutions Pvt Ltd



Title CC EVM NOTES,HW &LABELS

Size C Variant Name = PROC159A(001)

Date: Thursday, April 04, 2024

Rev A

Sheet 33 of 33