



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: N/A	Designed for: Public Release	Mod. Date: 8/14/2015
TID #: 142	Project Title: TIDP142 R-2R S&H Glitch Reduction	
Number: TIDP142	Rev: Rev 3	Sheet Title: TIDP142 Schematic
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 2 of 3
Drawn By:	File: TIDP142_Sch.SchDoc	Size: B
Engineer: Matthew Saucedo	Contact: http://www.ti.com/support	