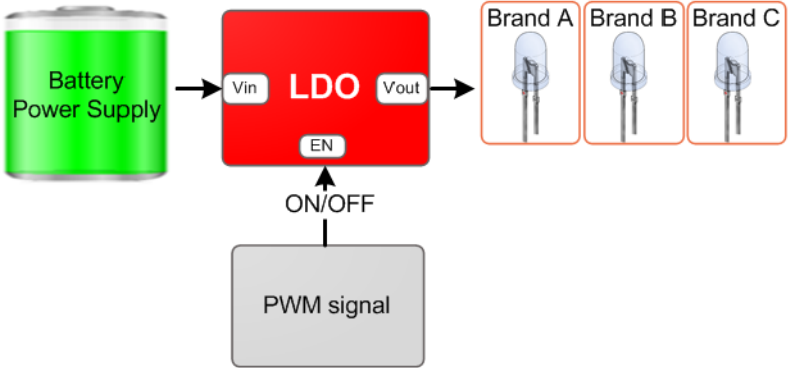
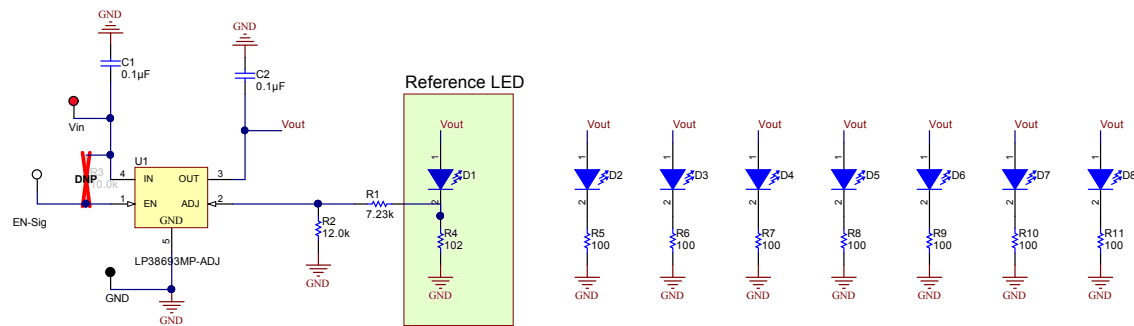
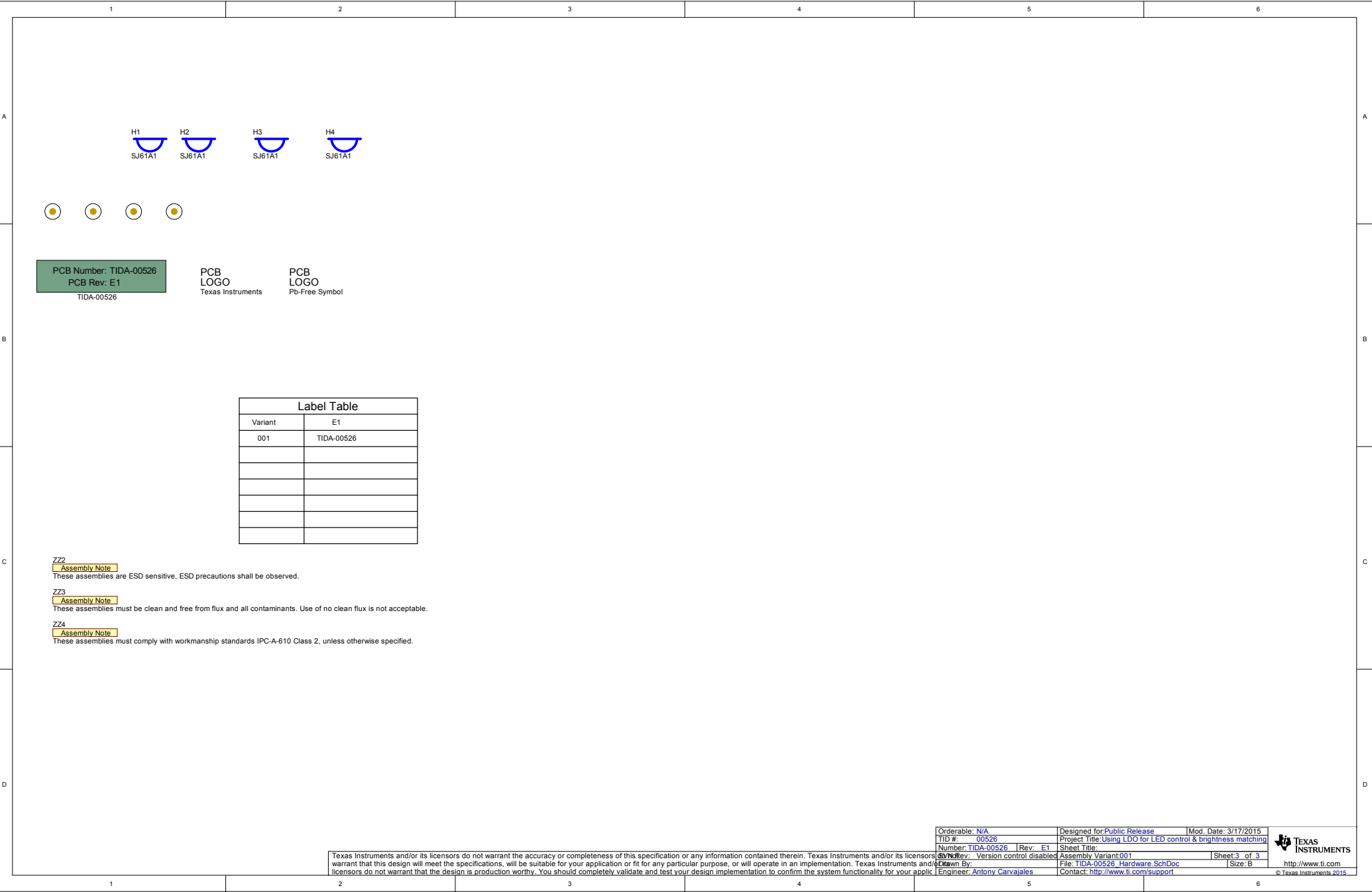


Revision History	
Revision	Notes





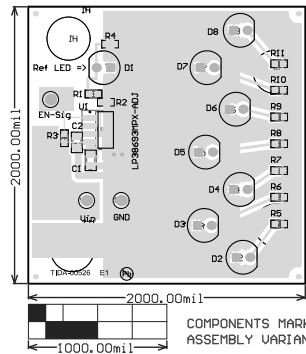


Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0.40mil	3.5	
3	Top Layer	Copper	1.40mil		
4	Dielectric1	FR-4	59.20mil	4.8	
5	Bottom Layer	Copper	1.40mil		
6	Bottom Solder	Solder Resist	0.40mil	3.5	
7	Bottom Overlay				


222 ■ These assemblies are ESD sensitive, ESD precautions shall be observed.

223 ■ These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

224 ■ These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.



DESIGN INFORMATION					
MIN. TRACK WIDTH:	8 MIL				
MIN. CLEARANCE:	0.2 mm				
MIN. VIA PAD SIZE:	24 MIL				
MINIMUM ANNUAL RING 0.05mm (2MIL) EXTERNAL					
PER IPC-D-275 CLASS 2 LEVEL C					
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL					
MATERIAL:					
<input type="checkbox"/> FR-408	<input checked="" type="checkbox"/> FR-4 High Tg	<input type="checkbox"/> OTHER _____			
THICKNESS:	<input checked="" type="checkbox"/> 62 MIL (1.6mm) +/-10%	<input type="checkbox"/> OTHER _____			
TOLERANCE:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2	<input type="checkbox"/> OTHER +/- _____			
BOW & TWIST:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2	<input type="checkbox"/> OTHER +/- _____			
DRILLING:					
REFERENCE:	<input checked="" type="checkbox"/> AS SHOWN	<input checked="" type="checkbox"/> NC_DRILL FILES			
PTH MIN COPPER THICKNESS:	<input checked="" type="checkbox"/> 1 MIL	<input type="checkbox"/> OTHER _____			
BOARD FINISH:					
SILKSCREEN:	<input checked="" type="checkbox"/> TOP	<input checked="" type="checkbox"/> BOTTOM			
SILKSCREEN COLOR:	<input checked="" type="checkbox"/> WHITE	<input type="checkbox"/> OTHER _____			
SOLDER RESIST COLOR:					
<input checked="" type="checkbox"/> GREEN	<input type="checkbox"/> BLUE	<input type="checkbox"/> OTHER _____			
SURFACE FINISH:					
<input checked="" type="checkbox"/> IMMERSION GOLD (ENIG)	<input type="checkbox"/> ENEPIG				
<input type="checkbox"/> IMM. TIN/SILVER OR EQUIV	<input type="checkbox"/> OTHER _____				
ARRAY/PANEL:	<input type="checkbox"/> CUT AND TRIM PER MECH LAYER 1				
	<input type="checkbox"/> N.C. ROUTE	<input checked="" type="checkbox"/> V. SCORE			
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:					
<input checked="" type="checkbox"/> ANSI PC-A-600F CLASS -->	<input type="checkbox"/> 1	<input checked="" type="checkbox"/> 2	<input type="checkbox"/> 3		
<input checked="" type="checkbox"/> UL 94V-0	<input checked="" type="checkbox"/> RoHS	<input type="checkbox"/> OTHER	PER ORDER		
ADDITIONAL REQUIREMENTS:					
MICROSECTION: <input type="checkbox"/> YES					
BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE				<input checked="" type="checkbox"/> REQUIRED	<input type="checkbox"/> PER ORDER
MANUFACTURER'S UL:					
<input type="checkbox"/> RAIL	<input type="checkbox"/> METAL	<input checked="" type="checkbox"/> SILK			

 TEXAS INSTRUMENTS	
PROJECT TITLE: Using LDO for LED control & brightness match	
DESIGN FOR: Public Release	
FILE NAME: TIDA-00526_TID.PcbDoc	
ENGINEER: Antony Carvajales	LAYOUT BY: Antony Carvajales
SCALE: 1.00	ALTIM DESIGNER VERSION: 14.3.16.37051

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ENGINEER: Antony Carvajales	LAYOUT BY: Antony Carvajales
SCALE: 1.00	ALTIM DESIGNER VERSION: 14.3.16.37051