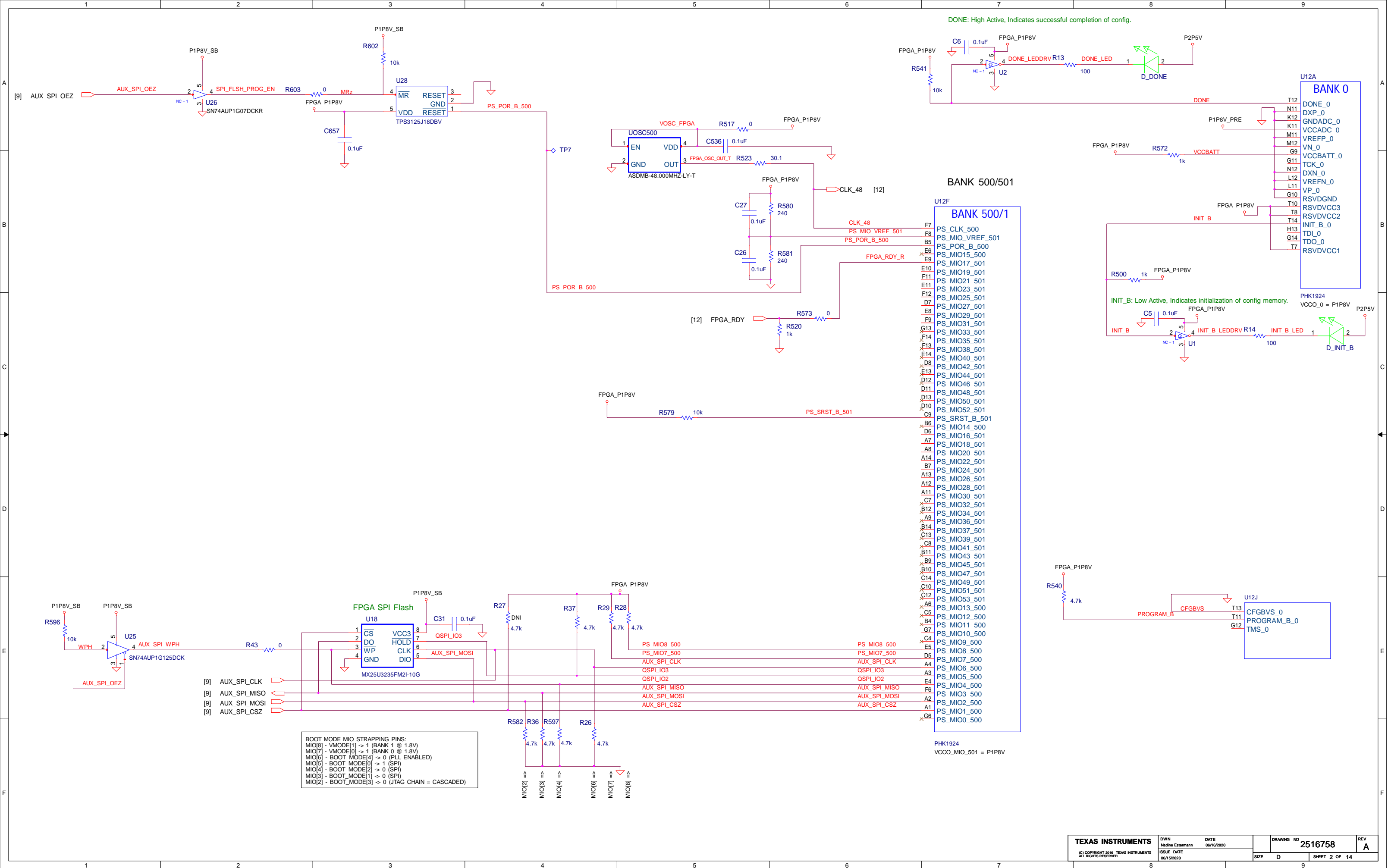


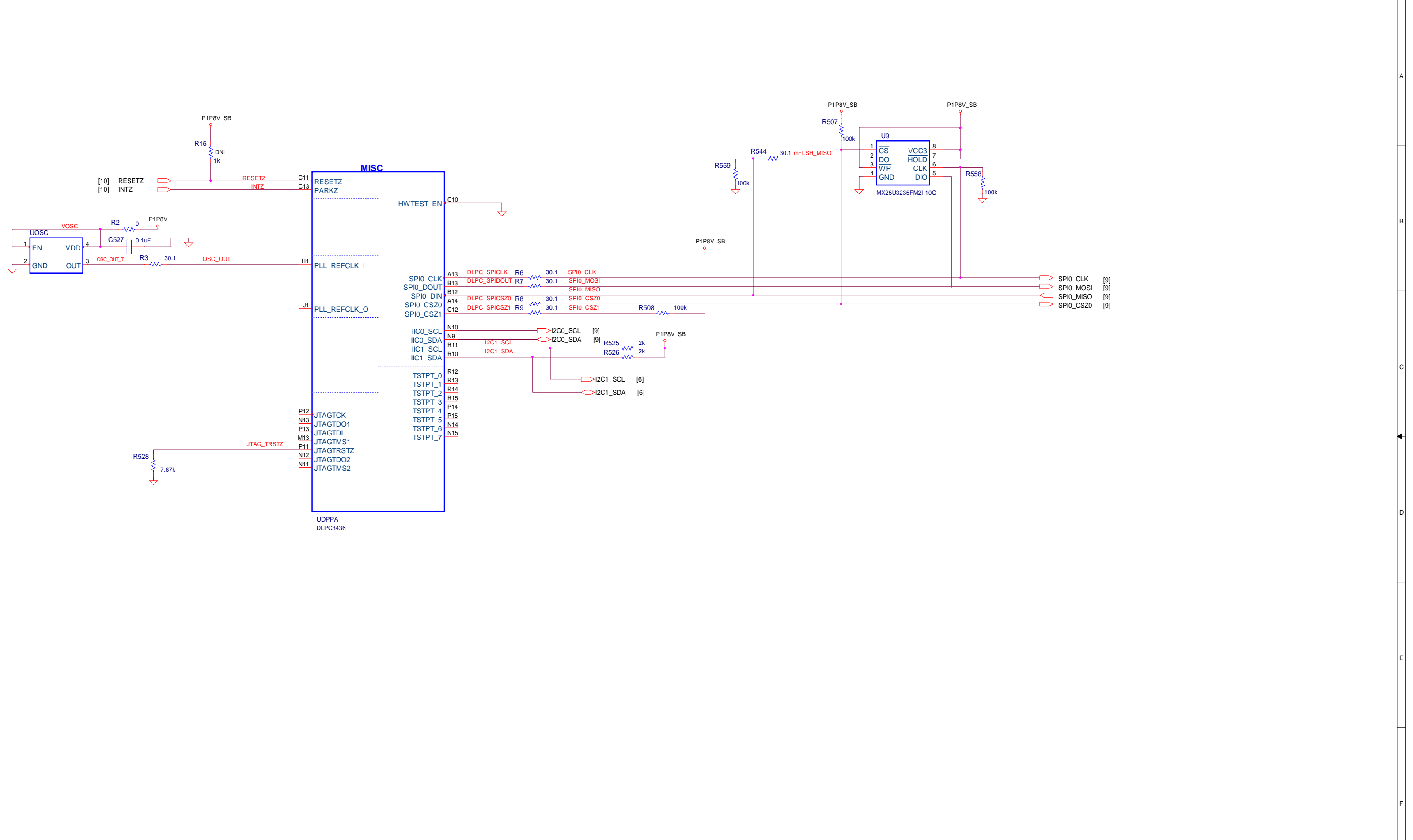
Revisions:

E.O: Engineering Release

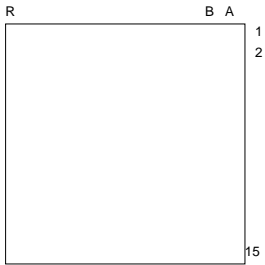
- A:
Changed resistor value for R628 from 20k to 1k reduction of IR drop
Changed resistor value for R626 from 100k to 1k reduction of IR drop
Add 0.01uF caps C666, C667, C668, C669 for Raspberry PI drive strength

DWN	Nadine Estermann	DATE	06/16/2020	TEXAS INSTRUMENTS - DLP Products		
ENGR	Nadine Estermann	06/16/2020		(C) COPYRIGHT 2009 Texas Instruments Inc. All Rights Reserved		
SYS	Paul Rancuret	---		TITLE TIDA-080009 DLPDLCR230NPEVM		
APVD	Jeff Dennis	---		DRAWING NO DLP038		REV A
QA	---	---		11 x 17	Orcad Capture 16.2	SHEET 1 of 14

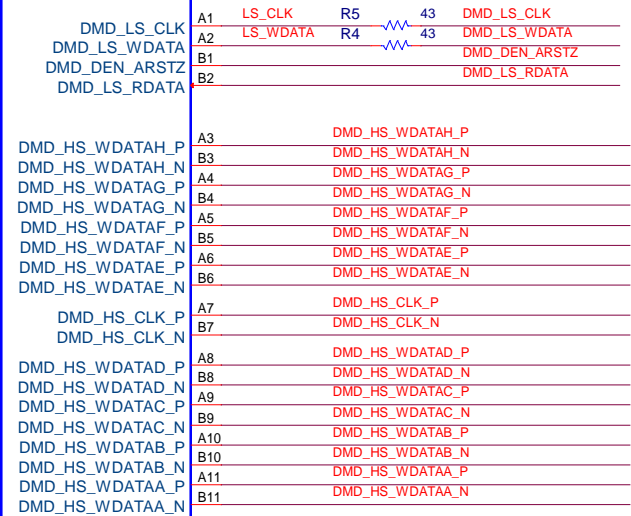




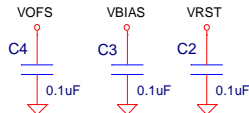
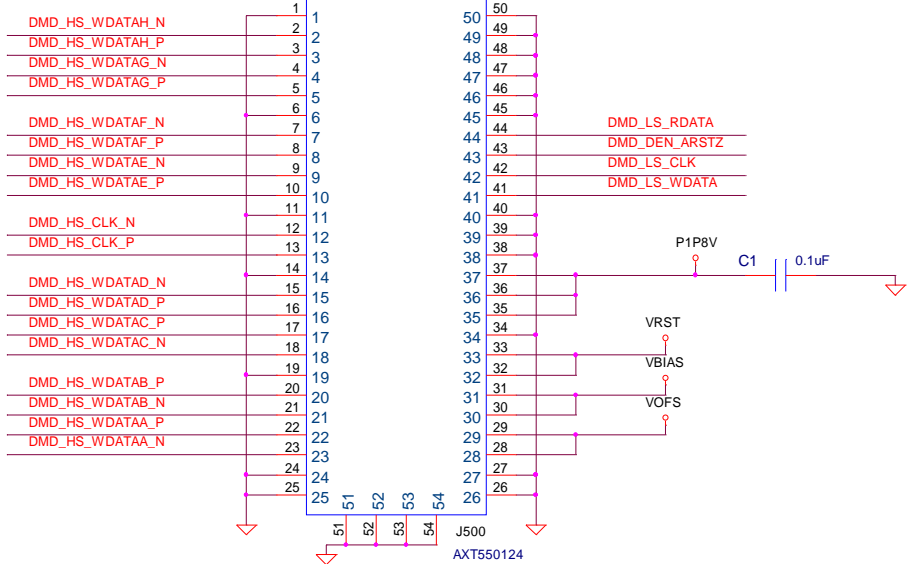
DLPC3436 Clock, Reset, SPI and Flash Memory



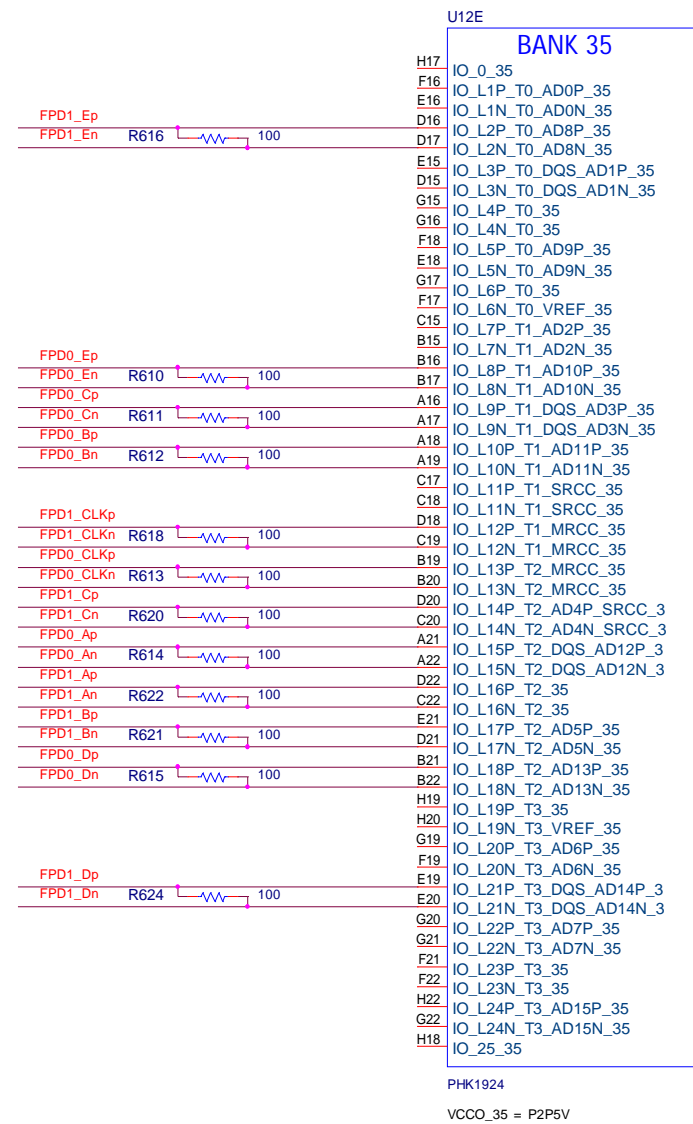
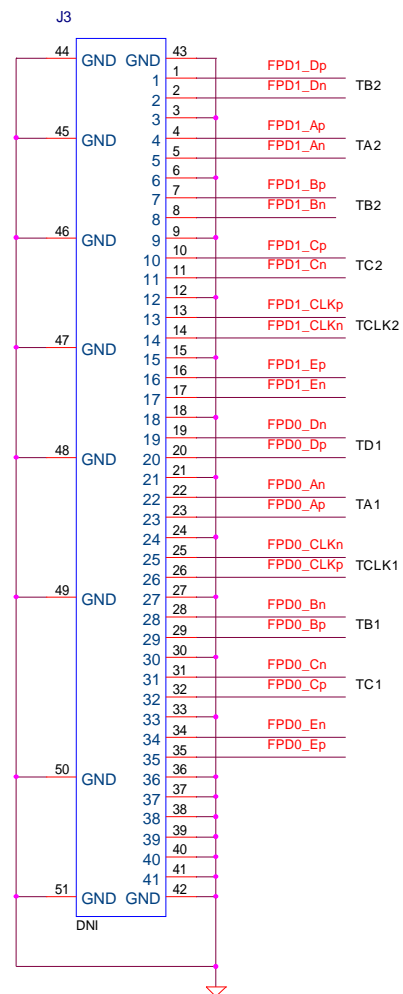
DMD INTERFACE

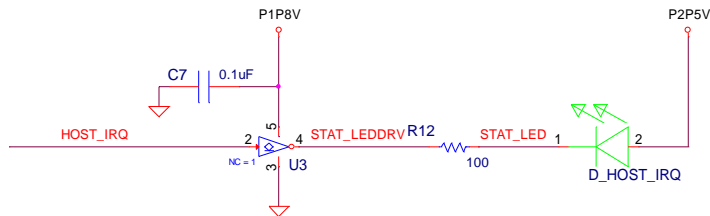
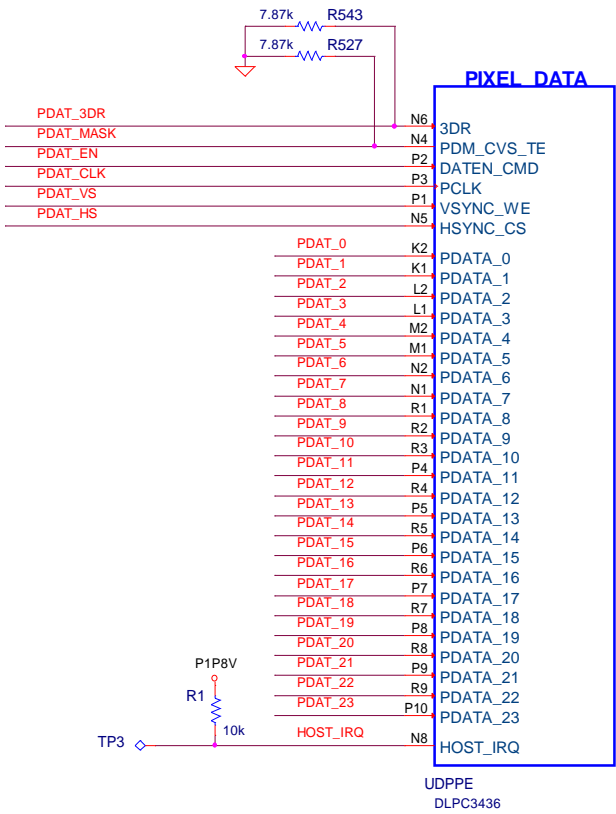
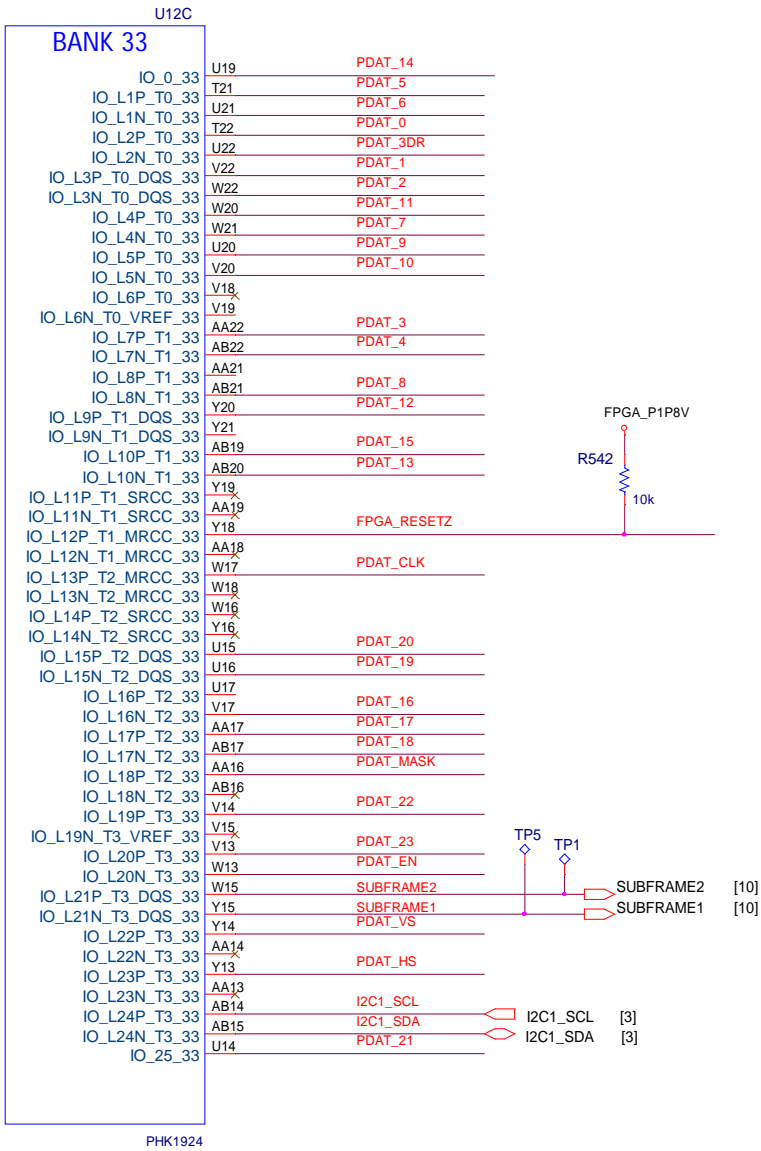


UDPPB
DLPC3436



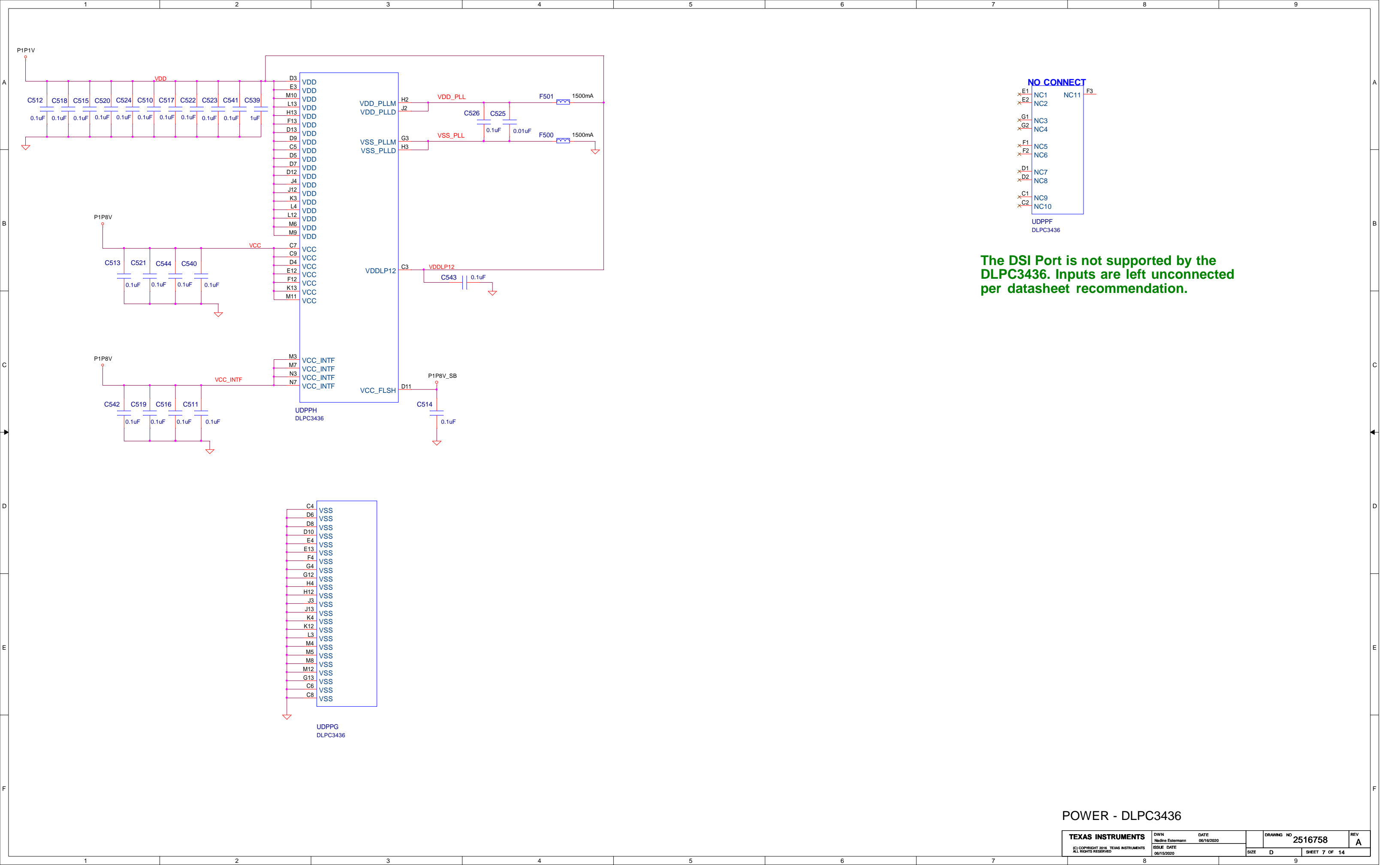
DMD INTERFACE



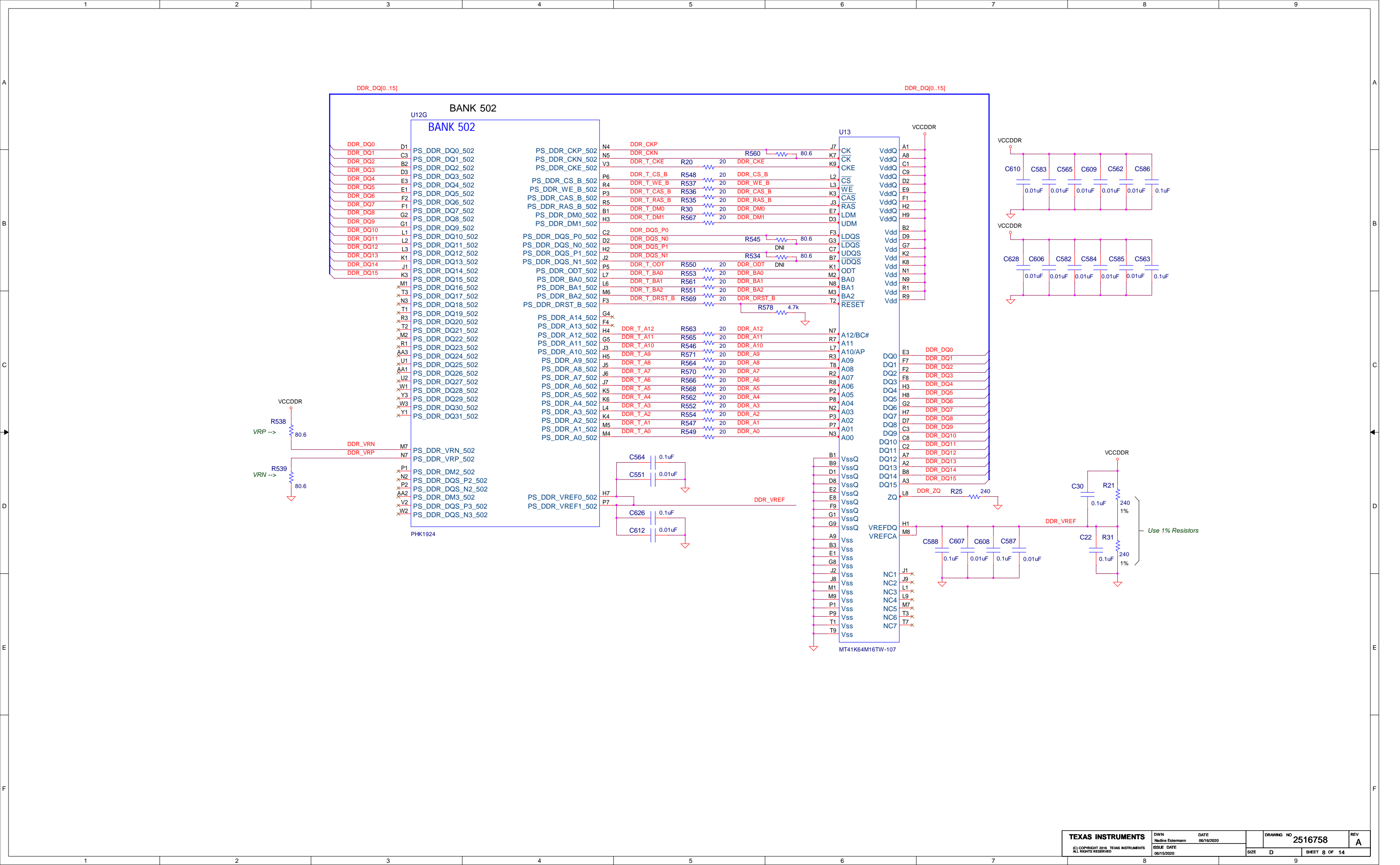


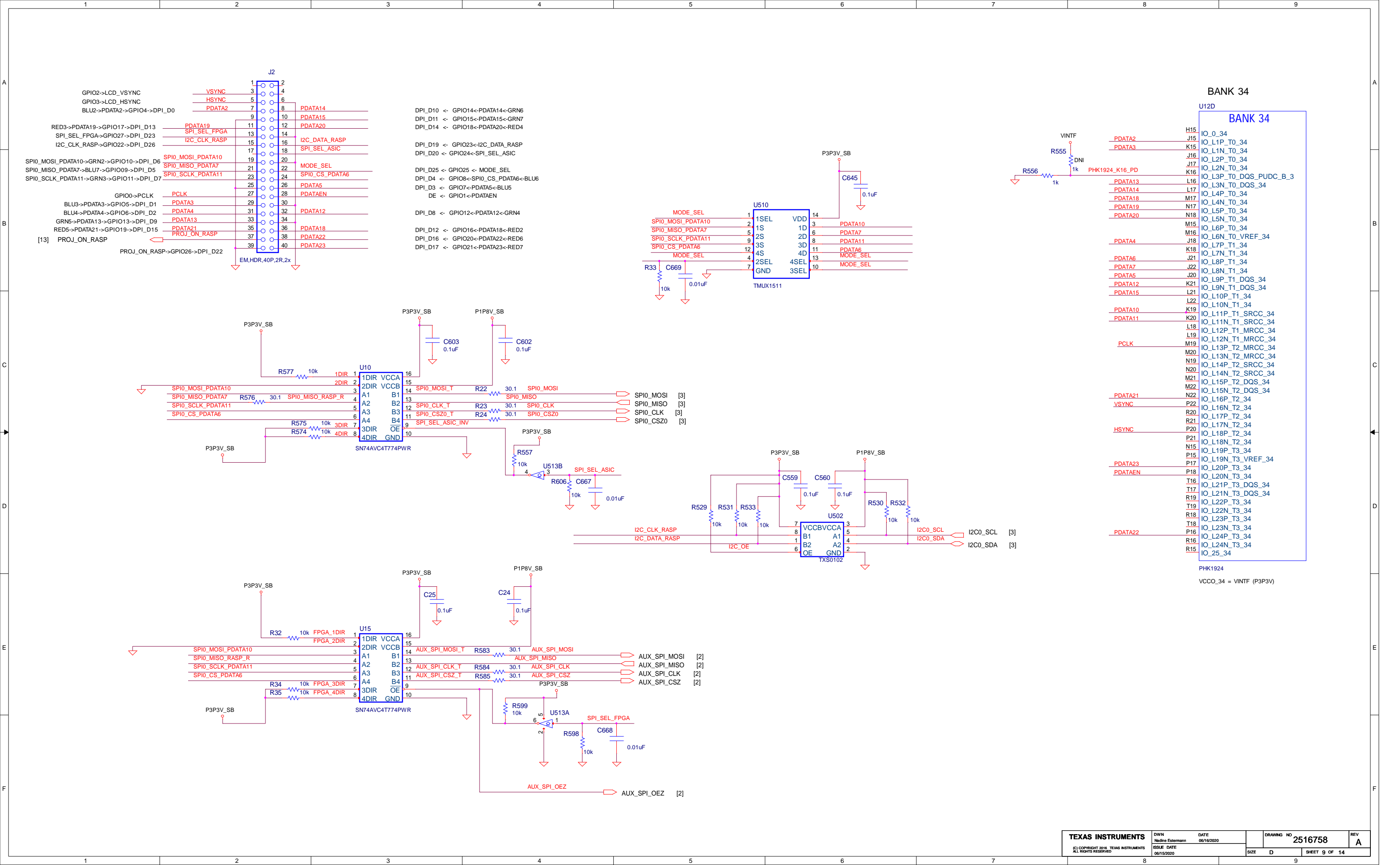
Parallel Port In

TEXAS INSTRUMENTS	DWVN	DATE	DRAWING NO	REV
	Nedine Estermann	06/16/2020		
ISSUE DATE			SIZE	SHEET 6 OF 14
06/15/2020			D	A

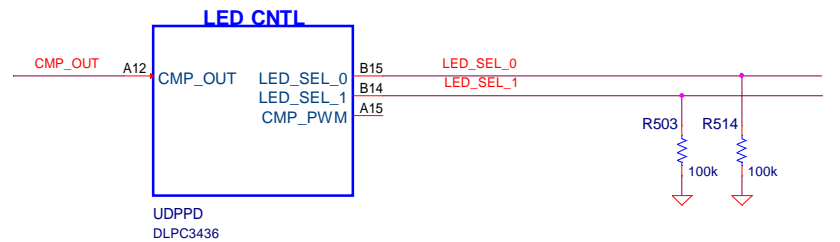
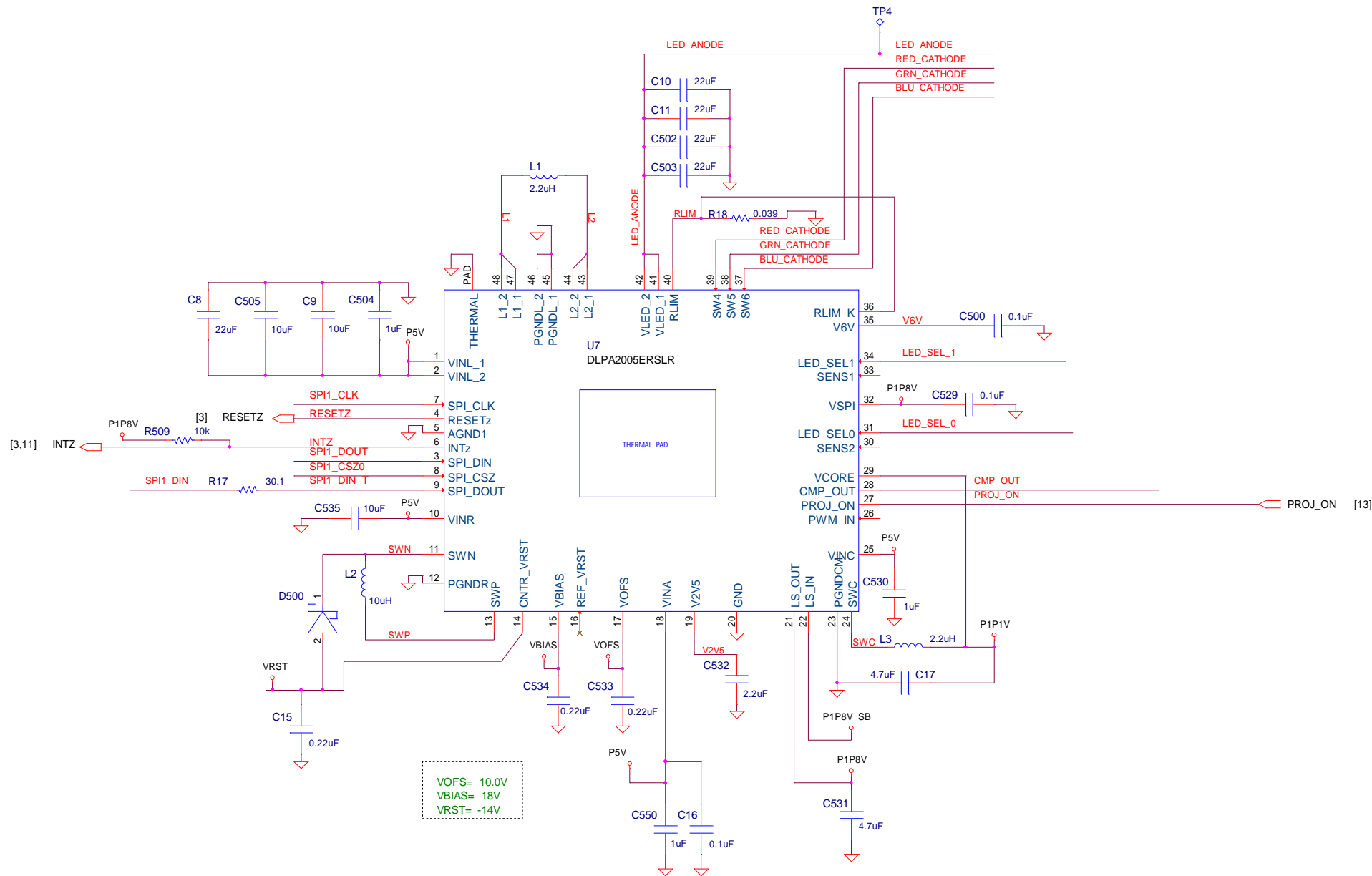
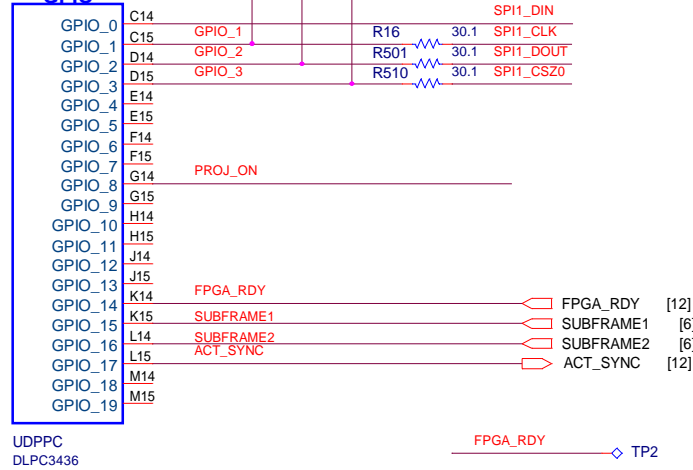


POWER - DLPC3436

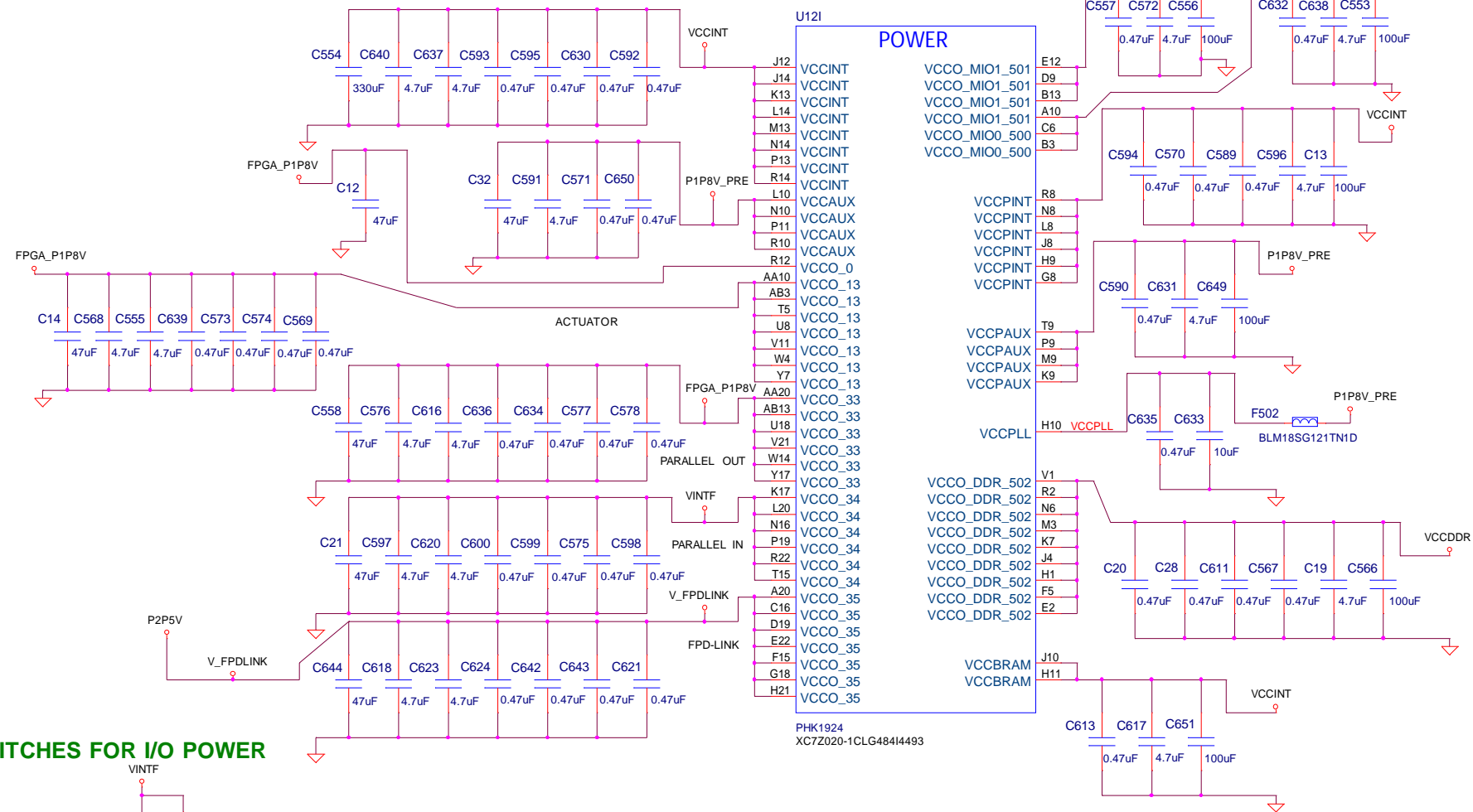




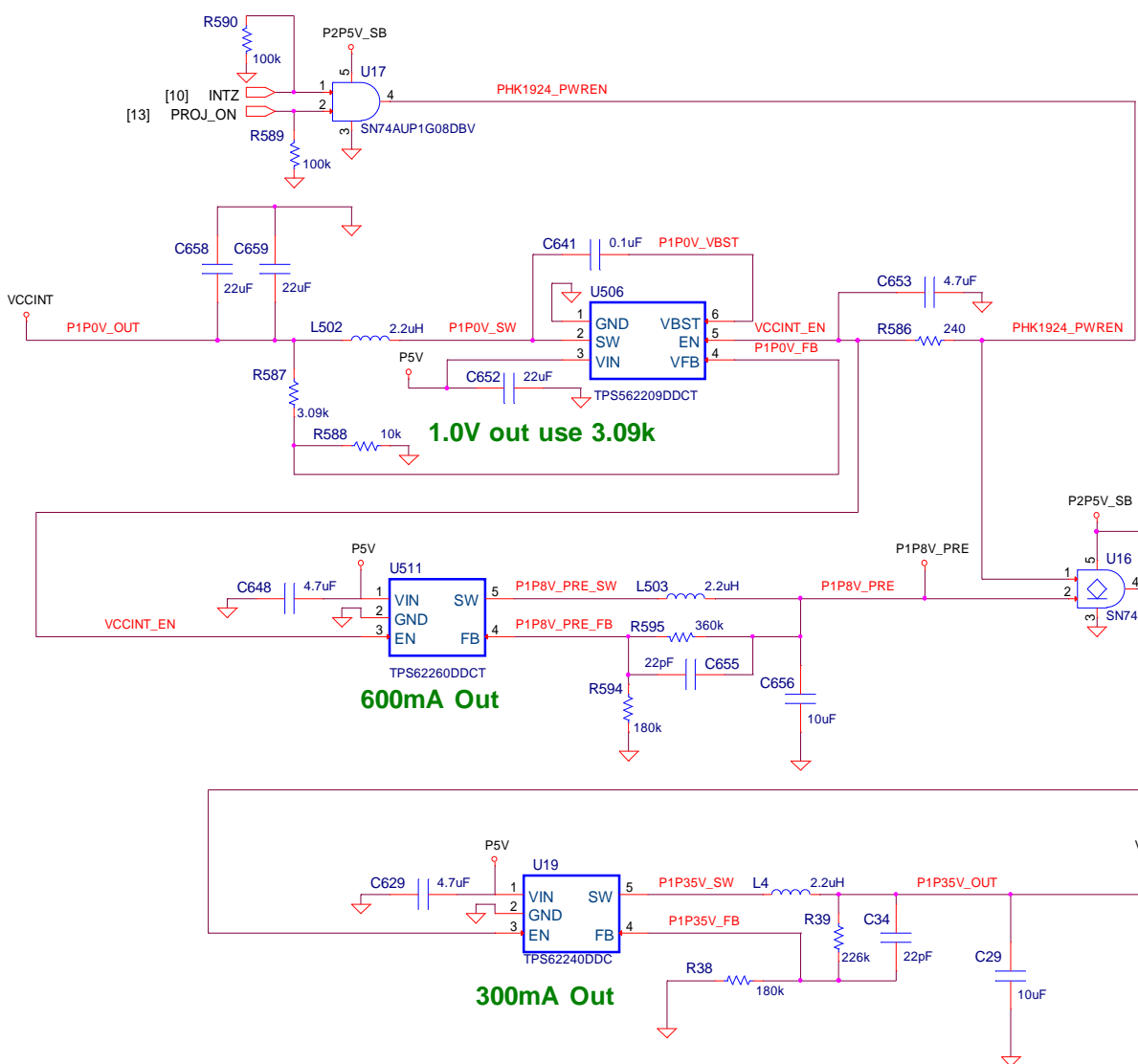
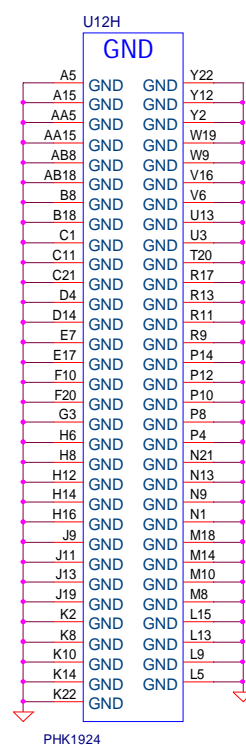
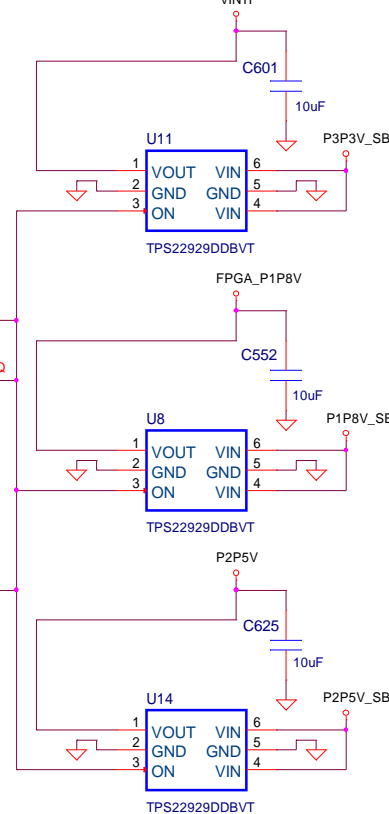
MASTER
GPIO



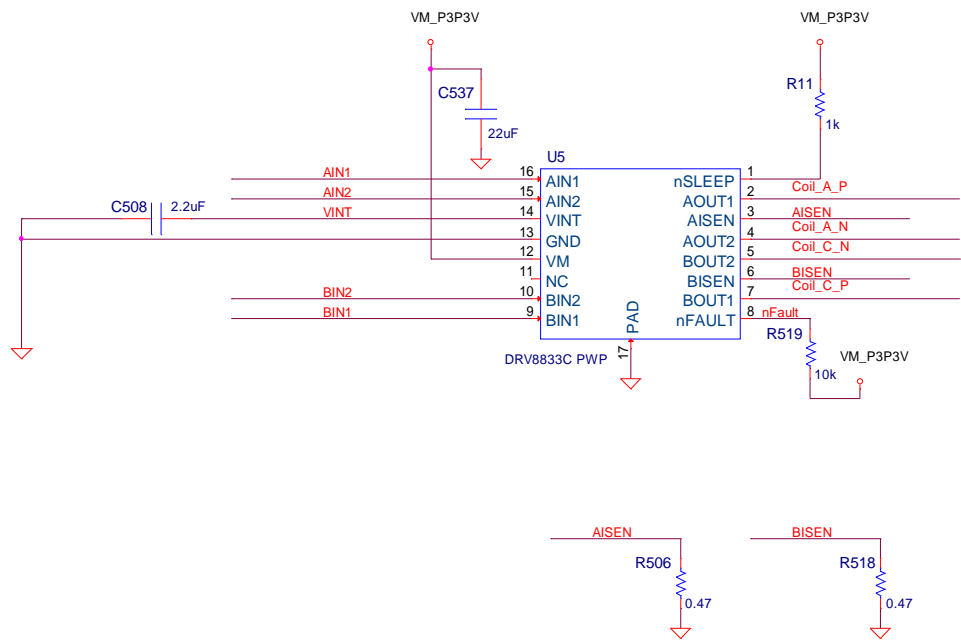
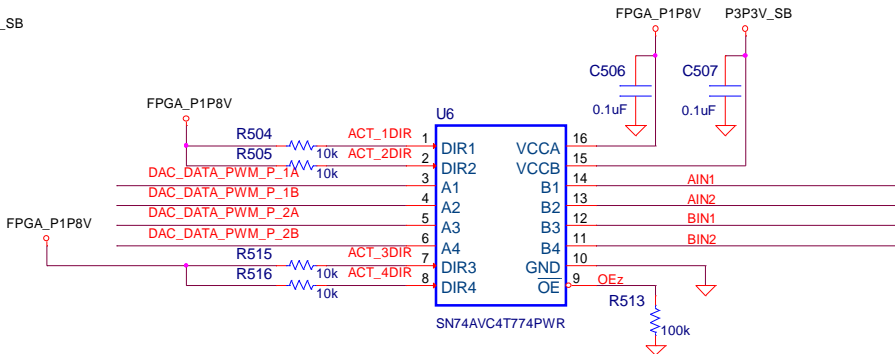
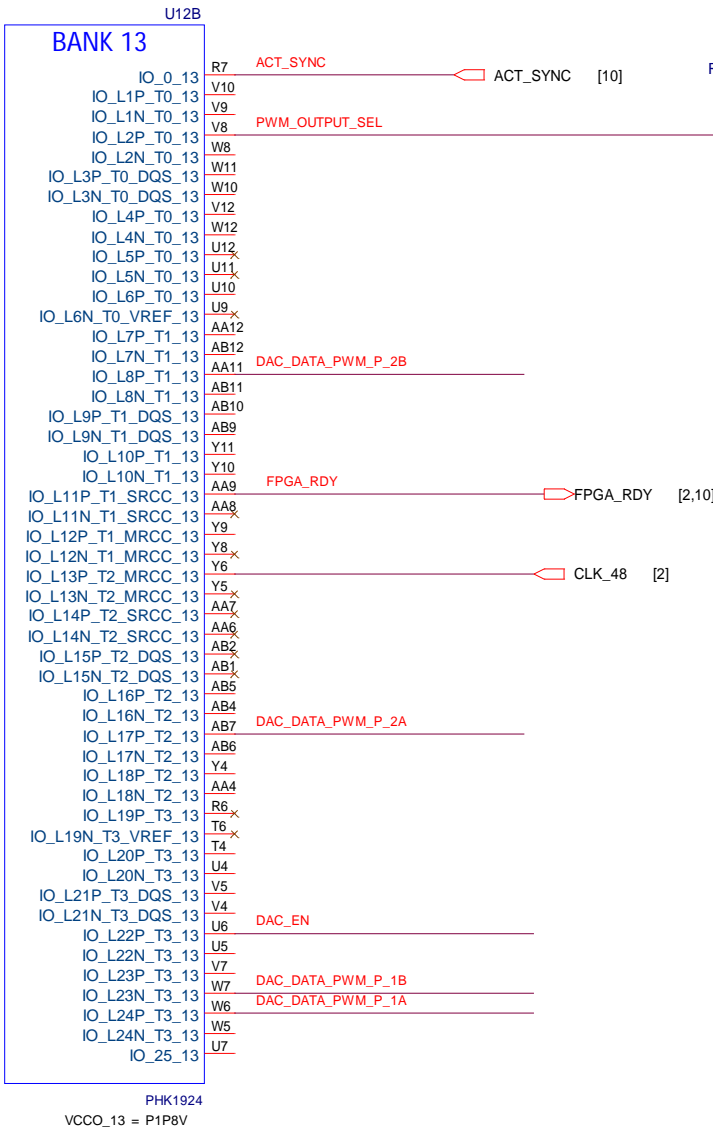
- SHUTDOWN WHEN FAST PARK DETECTED.
- PROJ_ON GOING HIGH ENABLES POWER TO FPGA



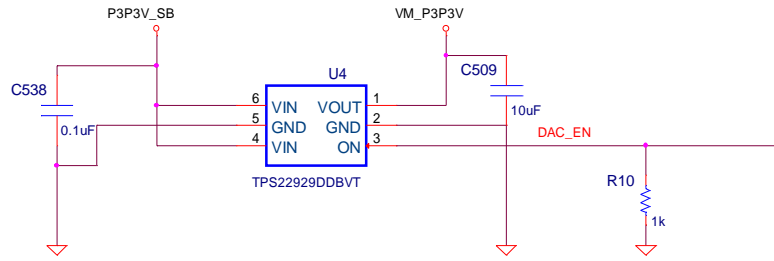
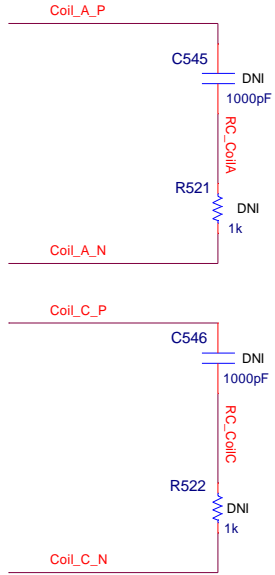
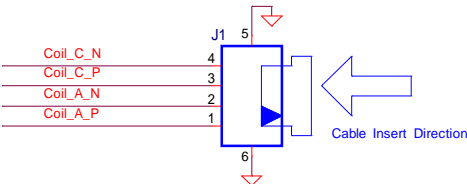
LOAD SWITCHES FOR I/O POWER



BANK 13



Note: The actuator driver design is only applicable for the actuator used with the DLPDLCR230NPEVM. Please contact your actuator manufacture for the appropriate driver solution.



ACTUATOR DRIVER

[illegible]