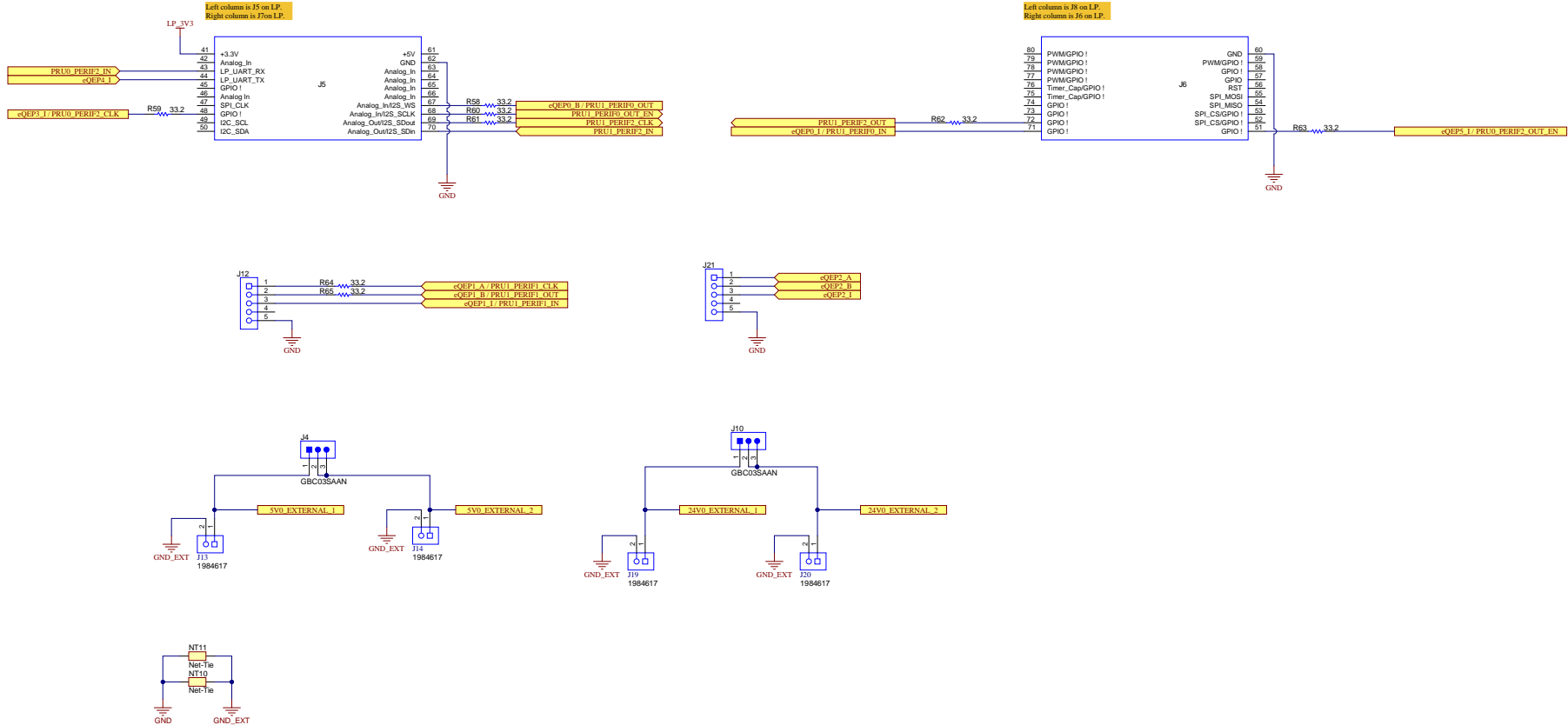
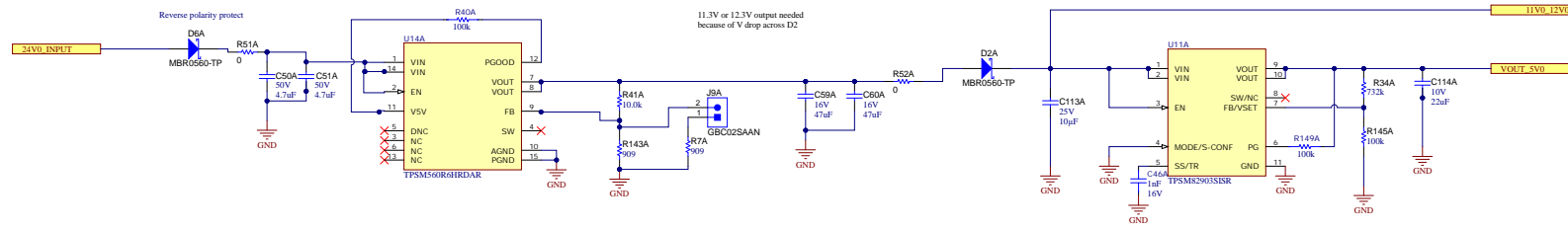


Boosterpack Header Site - 1

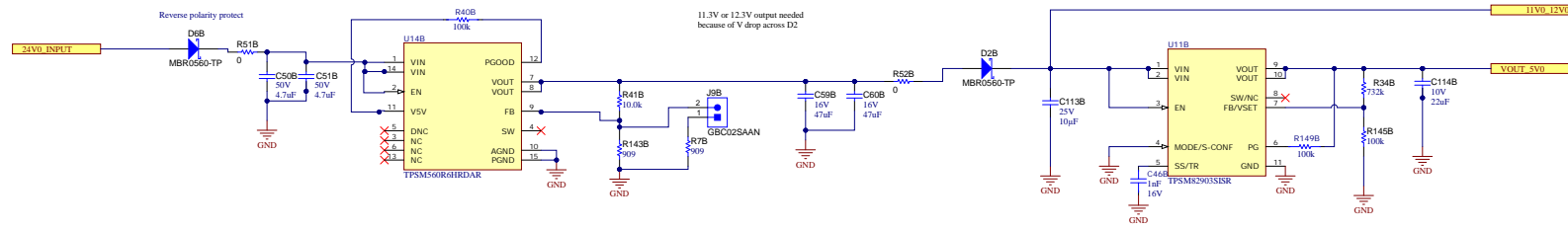


Boosterpack Header Site - 2

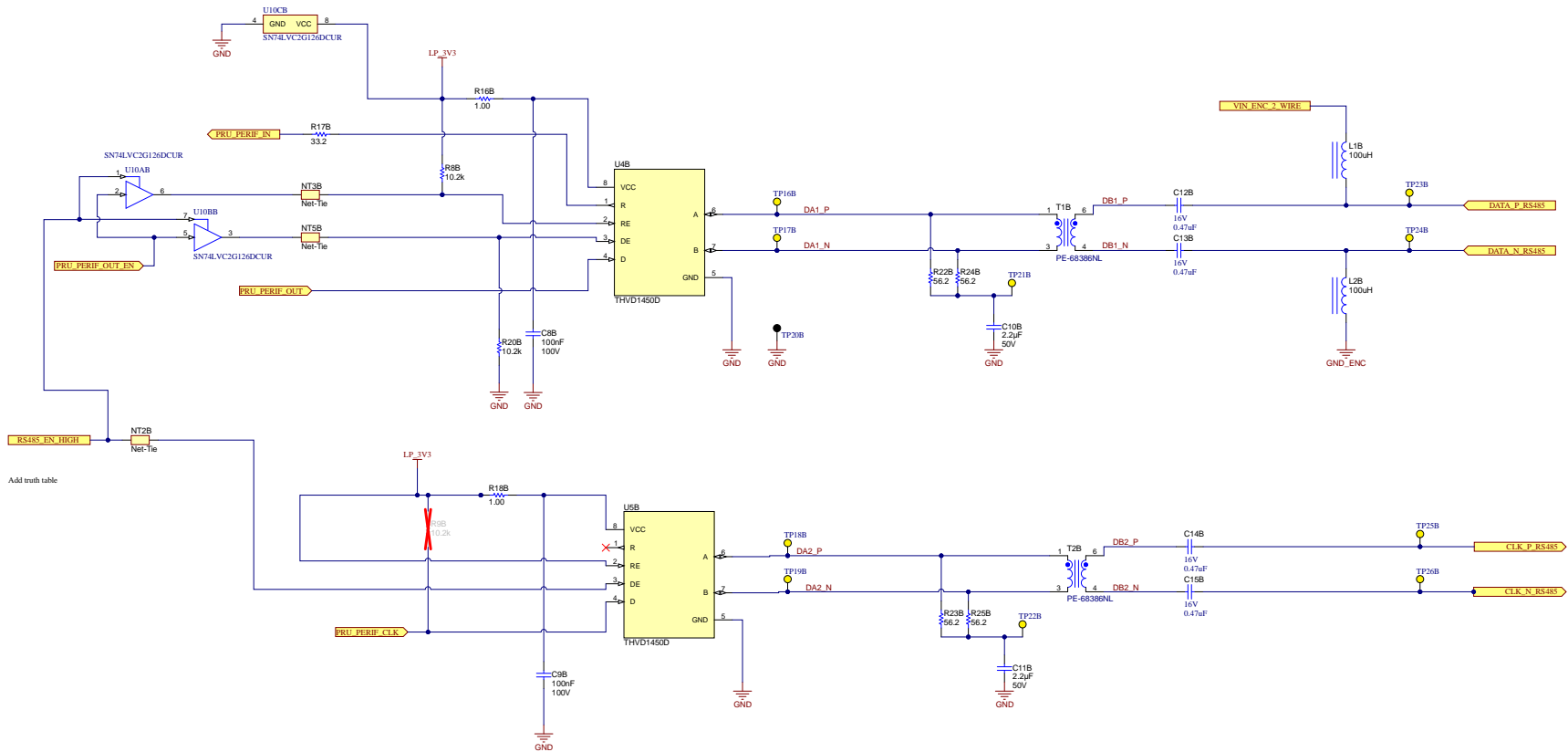




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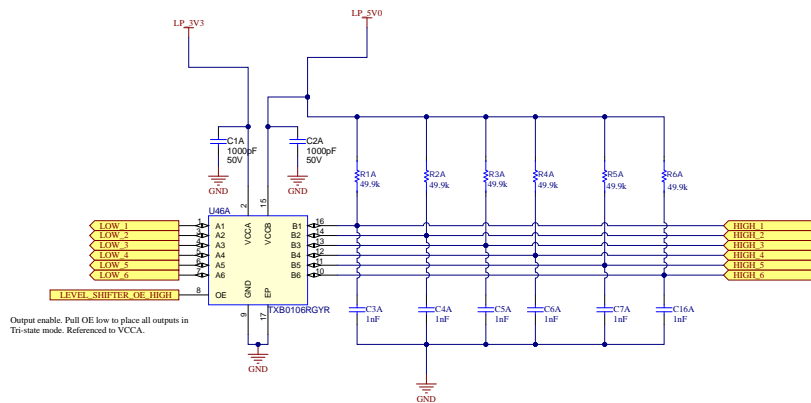


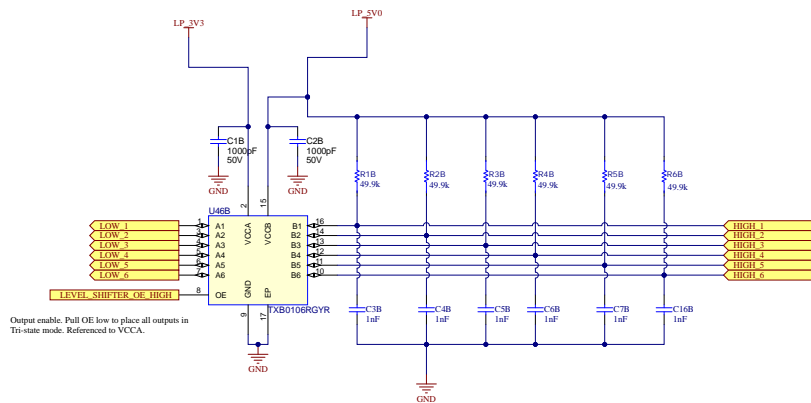
Add truth table

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TID # TIDA-010948-PB	Project Title: TIDA-010948-PB	
Number: TIDA-010948-PB Rev: E1	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet 6 of 9
Drawn By: Engineer: Saban Muthalagu	File: TIDA-010948-PB_Communication_SchDoc	Size: A2
	Cont'd: http://www.ti.com/support	© Texas Instruments 2024







Output enable. Pull OE low to place all outputs in Tri-state mode. Referenced to VCCA.

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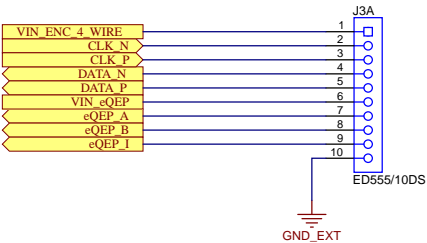
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Number: TIDA-010948-H Rev: E1	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 8 of 9
Drawn By:	File: TIDA-010948_PB_Encoder_connector.SchDoc	Size: A3
Engineer: Sabari Muthalagu	Contact: http://www.ti.com/support	



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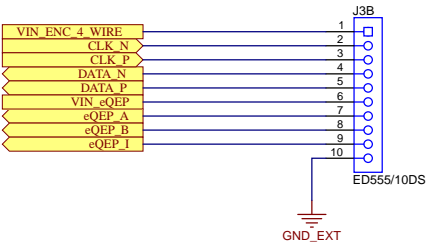
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Number: TIDA-010948-H Rev: E1	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 8 of 9
Drawn By:	File: TIDA-010948_PB_Encoder_connector.SchDoc	Size: A3
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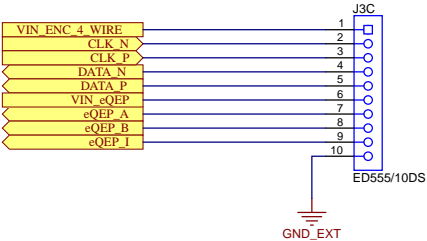
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SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 8 of 9
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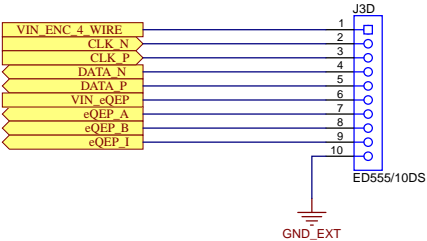
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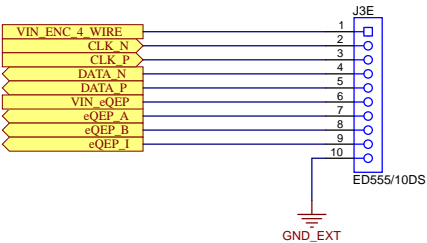
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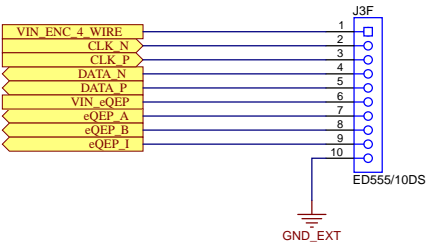
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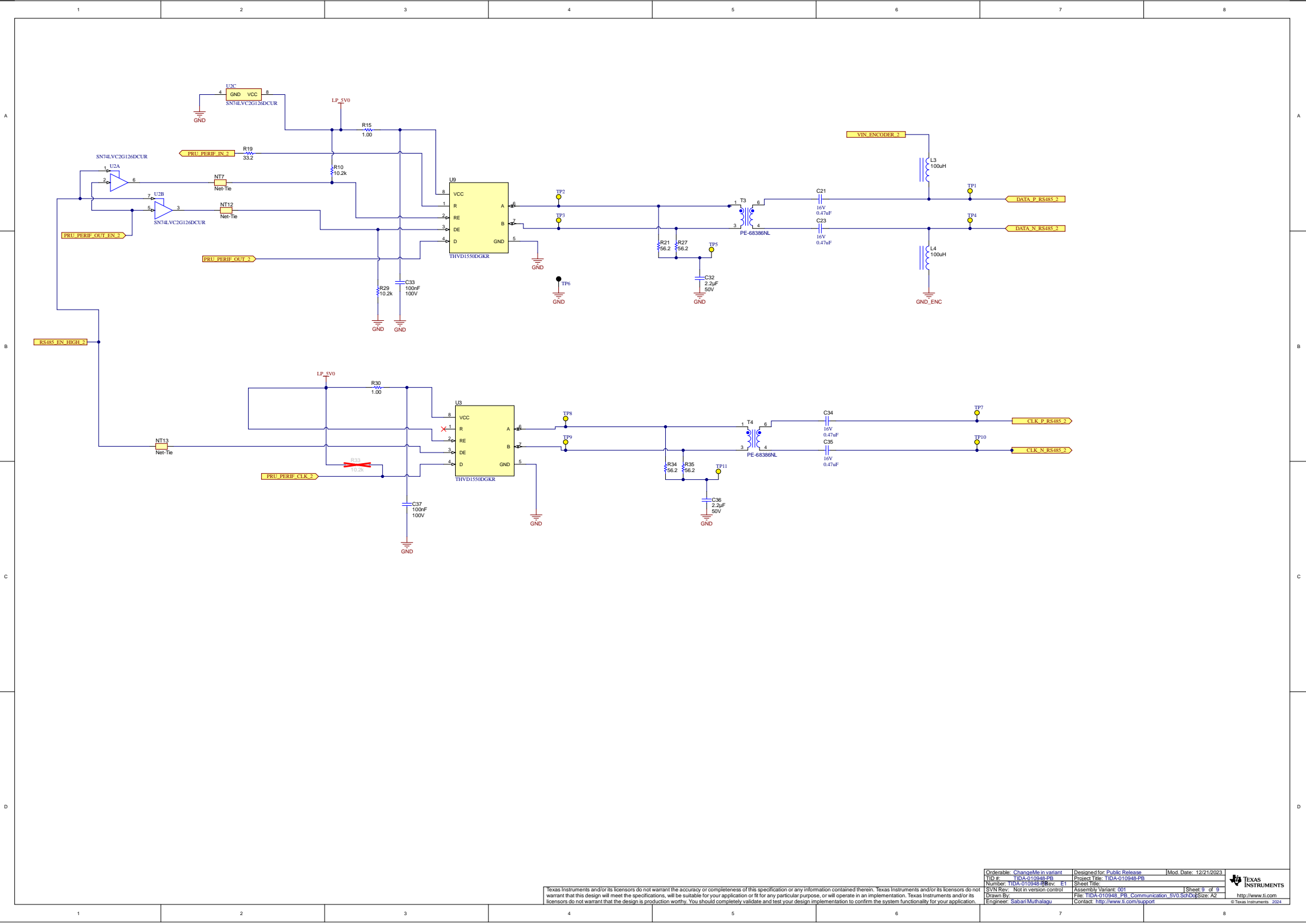
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